

LVR: 1-Trillion Polygon DRC in 11 Hours — Methods, Verification & Reproducibility

Ramtera Inc

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Ramtera Design Automation, Inc. 16192 Coastal Highway, Lewes, Delaware 19958, County of Sussex, USA.

1 A New Era of Trillion-Polygon DRC Verification

The trillion-polygon DRC verification experiment represents a landmark in electronic design automation, being the first of its kind to validate full-chip layouts at such an astronomical scale. Traditional verification tools typically struggle beyond tens of billions of polygons due to memory bottlenecks and runtime explosion. This experiment demonstrates a verified and complete trillion-polygon DRC run using LVR (Layout Verification Reporter), thereby crossing a threshold that was once deemed computationally impractical. By achieving this feat on commodity compute clusters, it redefines the scalability frontier of design rule verification.

More importantly, this success signifies a paradigm shift in the way large-scale physical verification can be perceived and executed. The experiment proves that distributed and multithreaded verification—when carefully balanced with memory partitioning and geometry handling—can achieve true trillion-scale sign-off. This opens a new era in DRC verification research, where runtime efficiency, deterministic scalability, and trillion-scale robustness become realistic engineering goals rather than theoretical aspirations.

2 Trillion-Polygon Design Creation Method

The trillion-polygon design was not a conventional integrated circuit layout but a synthetically generated geometric database engineered to stress-test verification capacity. A smaller verified layout, such as a standard block at 400 million polygons, was replicated systematically into a two-dimensional array using gdsy scripting and precise coordinate scaling. Each sub-cell was spaced with defined edge margins to ensure non-overlapping placement, thereby maintaining physical correctness while exponentially increasing the total polygon count.

Through recursive replication and controlled instancing, the base GDS was tiled into an ultra-large array that reached the trillion-polygon mark. This method allowed the creation of a realistic layout distribution pattern with both density and hierarchy, mimicking the challenges of modern SoC-level physical designs. The generated design thus served as a perfect benchmark for trillion-scale DRC—complex, massive, yet geometrically well-defined.

3 Design Area Computation

The total design area was calculated by considering the bounding box coordinates of the composite layout. For a design defined by (X_1, Y_1) as the lower-left corner and (X_2, Y_2) as the upper-right corner, the total area is given by $(X_2 - X_1) \times (Y_2 - Y_1)$. In the trillion-polygon experiment, this calculation provided an exact reference for die coverage and tile partitioning, ensuring that the workload was evenly distributed among compute nodes.

The area calculation was essential not only for geometry scaling but also for load-balancing across processing cores. Each tile's sub-area could then be derived by dividing the total design area by the number of partitions (e.g., 81 for a 9×9 grid). This uniform spatial partitioning allowed the DRC engine to maintain predictable runtime and memory behavior, enabling near-linear scalability across compute threads and nodes.

4 Technology Node Used

The trillion-polygon benchmark was derived from the Sky130HD technology node, a well-characterized open-source CMOS process from SkyWater. This node was selected for its availability, maturity, and robust layer definitions, which made it an ideal platform for large-scale replication. Its moderate complexity allowed DRC rules to be extensive enough for verification realism without introducing proprietary dependencies.

Using Sky130HD ensured that every layer, via, and spacing rule adhered to real-world design constraints. The corresponding DRC rule deck was fully compatible with LVR’s rule interpreter, enabling accurate and meaningful verification results. Thus, even though the trillion-scale dataset was synthetic, it remained rooted in a realistic process technology and verified against authentic design rule conditions.

5 9x9 Tile Dimensions

The trillion-polygon design was partitioned into a 9×9 grid, resulting in 81 rectangular tiles. Each tile corresponded to a specific sub-region of the die area and was defined with precise lower-left and upper-right coordinates. The tile width and height were obtained by dividing the total die width and height by nine, respectively, thereby ensuring equal geometric coverage across all partitions.

Such spatial segmentation was vital for parallel execution and memory management. Each tile could be independently processed by a DRC instance, minimizing inter-tile dependencies. The LVR framework efficiently handled boundary exchanges and ensured that global rule consistency was maintained across the entire 9x9 grid, even when executed on multiple cores or compute nodes.

6 Processing of 4x8 Node Array

The computational workload was mapped onto a 4x8 array of nodes, corresponding to 32 processing units. Each node was assigned a fixed number of tiles in round-robin fashion to balance the runtime load. The distributed controller orchestrated the assignment, dispatching geometric subsets and rule decks to worker nodes while maintaining synchronization checkpoints across the network.

This 4x8 configuration optimized resource utilization, combining horizontal and vertical data decomposition for efficient cache locality and throughput. The nodes operated in parallel yet communicated intermediate verification markers, enabling deterministic merging of results. The framework ensured that every tile's verification report was consolidated into a unified, coherent DRC summary at the master node.

7 Internal Handling of 8x4 Array by LVR

Internally, LVR treats the 8x4 array as a hierarchical set of compute domains rather than physical tiles. Each domain corresponds to a cluster of geometric regions assigned dynamically based on memory availability and thread workload. The scheduler continuously monitors completion rates and can remap unprocessed regions to idle threads to maximize CPU utilization.

This adaptive handling allows LVR to maintain balanced throughput even when some tiles contain denser geometries. Data transfer and result aggregation are fully asynchronous, ensuring minimal idle time between stages. Through this internal rebalancing mechanism, the 8x4 array behaves as a fluid, self-optimizing computation grid—key to achieving trillion-scale performance with deterministic accuracy.

8 Creating the environment

```
#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
# Version   : v1.0
# License   : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail

# ==== EDIT THESE ====
export AWS_REGION="${AWS_REGION:-us-east-1}"      # region of your EC2s
export EFS_NAME="${EFS_NAME:-lvr-efs}"            # name for the EFS
export EFS_MOUNT_POINT="${EFS_MOUNT_POINT:-/mnt/efs}" # where to mount inside instances

# SSH details (for connecting into EC2s)
export SSH_KEY_PATH="${SSH_KEY_PATH:-$HOME/monta.pem}" # path to your .pem
export SSH_USER_AL2="${SSH_USER_AL2:-ec2-user}"         # Amazon Linux default user
export SSH_USER_UBU="${SSH_USER_UBU:-ubuntu}"           # Ubuntu default user
export SSH_CONNECT_TIMEOUT=8

# ==== INPUT FILE ====
# File with instance IDs, one per line, e.g.:
# i-0abc123...
# i-0def456...
export INST_FILE="list_of_instances"
if [[ ! -s "$INST_FILE" ]]; then
    echo "[ERROR] $INST_FILE missing or empty (put one instance-id per line)" >&2
    exit 1
fi
```

9 Creating EFS and mounting

```
#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
```

```

# Function : [Describe the purpose of the script briefly here]
# Version  : v1.0
# License  : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website  : https://www.ramtera.com
# -----
# DISCLAIMER:
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# -----
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail
source ./00_env.sh

# --- Read instance IDs robustly ---
IIDS=(
while IFS= read -r line; do
    line=${line//$'\r'/}
    line=$(printf '%s' "$line" | awk '{ $1=$1; }')
    [[ -n "$line" ]] && IIDS+=("$line")
done < "$INST_FILE"
(( ${#IIDS[@]} > 0 )) || { echo "[ERROR] No instance IDs found in $INST_FILE"; exit 1; }

echo "[INFO] Region: $AWS_REGION"
DESC_JSON=$(aws ec2 describe-instances --region "$AWS_REGION" --instance-ids "${IIDS[@]}")

# --- Single VPC guard ---
VPCS=$(jq -r '.Reservations[].Instances[].VpcId' <<<"$DESC_JSON" | sort -u)
(( ${#VPCS[@]} == 1 )) || { echo "[ERROR] Instances span multiple VPCs: ${VPCS[*]}"; exit 1; }
VPC_ID="${VPCS[0]}"

# --- Subnets & SGs ---
SUBNETS=$(jq -r '.Reservations[].Instances[].SubnetId' <<<"$DESC_JSON" | sort -u)
EC2_SG_IDS=$(jq -r '.Reservations[].Instances[].SecurityGroups[].GroupId' <<<"$DESC_JSON" | sort -u)

# --- Per-instance meta ---
declare -A PUBIP PRIVIP PLAT NAME SUBNET_OF
for iid in "${IIDS[@]}; do
    j=$(jq -r --arg iid "$iid" '.Reservations[].Instances[] | select(.InstanceId==$iid)' <<<"$DESC_JSON")
    PUBIP["$iid"]=$(jq -r '.PublicIpAddress // empty' <<<"$j")
    PRIVIP["$iid"]=$(jq -r '.PrivateIpAddress // empty' <<<"$j")
    PLAT["$iid"]=$(jq -r '.PlatformDetails // ""' <<<"$j")
    NAME["$iid"]=$(jq -r '.Tags[]?|select(.Key=="Name")|.Value' <<<"$j" | head -n1)
    SUBNET_OF["$iid"]=$(jq -r '.SubnetId' <<<"$j")
done

echo "[OK] VPC: $VPC_ID"
echo "[OK] Subnets: ${SUBNETS[*]}"
echo "[OK] Instance SGs: ${EC2_SG_IDS[*]}"

# --- Create EFS ---
FS_ID=$(aws efs create-file-system \
    --region "$AWS_REGION" \
    --creation-token "$EFS_NAME-$(date +%s)" \
    --performance-mode generalPurpose \
    --throughput-mode bursting \
    --encrypted \

```

```

--tags Key=Name,Value="$EFS_NAME" \
--query 'FileSystemId' --output text)
echo "[INFO] EFS creating: $FS_ID"

# Wait for 'available'
echo "[INFO] Waiting for EFS to become available..."
for _ in {1..30}; do
    state=$(aws efs describe-file-systems --region "$AWS_REGION" \
        --file-system-id "$FS_ID" \
        --query 'FileSystems[0].LifeCycleState' --output text 2>/dev/null || true)
    [[ "$state" == "available" ]] && { echo "[OK] EFS is available"; break; }
    sleep 5
done
[[ "${state:-}" == "available" ]] || { echo "[ERROR] EFS did not reach 'available'"; exit 1; }

# --- EFS SG allow 2049 from instance SGs ---
EFS_SG_NAME="${EFS_NAME}-sg"
EFS_SG_ID=$(aws ec2 describe-security-groups \
    --region "$AWS_REGION" --filters Name=group-name,Values="$EFS_SG_NAME" Name=vpc-id,Values="$VPC_ID" \
    --query 'SecurityGroups[0].GroupId' --output text 2>/dev/null || true)
if [[ -z "$EFS_SG_ID" || "$EFS_SG_ID" == "None" ]]; then
    EFS_SG_ID=$(aws ec2 create-security-group \
        --region "$AWS_REGION" --vpc-id "$VPC_ID" \
        --group-name "$EFS_SG_NAME" \
        --description "EFS SG for $EFS_NAME (allow 2049 from EC2 SGs)" \
        --query 'GroupId' --output text)
fi
for sg in "${EC2_SG_IDS[@]}; do
    aws ec2 authorize-security-group-ingress \
        --region "$AWS_REGION" \
        --group-id "$EFS_SG_ID" \
        --protocol tcp --port 2049 \
        --source-group "$sg" 2>/dev/null || true
done
echo "[OK] EFS SG: $EFS_SG_ID (NFS 2049 from instance SGs)"

# --- Create mount targets & collect IPs ---
declare -A MTIP
for sn in "${SUBNETS[@]}; do
    aws efs create-mount-target \
        --region "$AWS_REGION" \
        --file-system-id "$FS_ID" \
        --subnet-id "$sn" \
        --security-groups "$EFS_SG_ID" 2>/dev/null || true
    echo "[INFO] Waiting for mount target IP in $sn ..."
    for _ in {1..36}; do
        ip=$(aws efs describe-mount-targets --region "$AWS_REGION" --file-system-id "$FS_ID" \
            --query "MountTargets[?SubnetId=='$sn'].IpAddress" --output text 2>/dev/null || true)
        if [[ -n "$ip" && "$ip" != "None" ]]; then MTIP["$sn"]="$ip"; break; fi
        sleep 5
    done
    [[ -n "${MTIP[$sn]:-}" ]] || { echo "[ERROR] No mount target IP for $sn"; exit 1; }
    echo "[OK] $sn mount-target IP: ${MTIP[$sn]}"
done

# --- SSH helper ---
ssh_run() {
    local host="$1" user="$2"
    shift 2
    ssh -i "$SSH_KEY_PATH" -o StrictHostKeyChecking=no -o ConnectTimeout="$SSH_CONNECT_TIMEOUT" \
        "$user@$host" "$@" </dev/null
}

```

```

echo
echo "===== INSTALL NFS, CLEAN FSTAB, MOUNT via IP, VERIFY ====="
for iid in "${IIDS[@]"; do
    name="${NAME[$iid]}-$iid"
    pub="${PUBIP[$iid]}"
    plat="${PLAT[$iid]}"
    subnet_id="${SUBNET_OF[$iid]}"
    mt_ip="${MTIP[$subnet_id]}"

    SSH_USER="$SSH_USER_AL2"
    [[ "$plat" =~ Ubuntu|ubuntu ]] && SSH_USER="$SSH_USER_UBU"

    echo "--- [name] $iid (subnet: $subnet_id, mt_ip: $mt_ip) ---"

    if [[ -n "$pub" ]]; then
        ssh_run "$pub" "$SSH_USER" "set -e
            # install NFS client
            if command -v dnf >/dev/null 2>&1; then sudo dnf -y install nfs-utils >/dev/null;
            elif command -v yum >/dev/null 2>&1; then sudo yum -y install nfs-utils >/dev/null;
            elif command -v apt-get >/dev/null 2>&1; then sudo apt-get update -y >/dev/null && sudo apt-get install -y nfs-common >/dev/null;
            else echo 'No known package manager found'; exit 1; fi

            sudo mkdir -p '$EFS_MOUNT_POINT'

            # SAFE CLEAN: rebuild /etc/fstab without any line whose mountpoint is $EFS_MOUNT_POINT
            sudo cp /etc/fstab /etc/fstab.bak || true
            sudo awk '\$2 != \"'$EFS_MOUNT_POINT'\" {print}' /etc/fstab | sudo tee /tmp/fstab.new >/dev/null
            sudo mv /tmp/fstab.new /etc/fstab

            # add fresh IP-based fstab line
            echo '$(mt_ip):/ $EFS_MOUNT_POINT nfs4 nfsvers=4.1,rsize=1048576,wsize=1048576,hard,timeo=600,retrans=2,_netdev,noresvport 0 0' | sudo tee -a /etc/fstab

            # mount and verify
            sudo mount -a
            if command -v mountpoint >/dev/null 2>&1; then
                mountpoint -q '$EFS_MOUNT_POINT' || { echo 'FAIL: not a mountpoint'; exit 2; }
            else
                df -h | grep -q '$EFS_MOUNT_POINT' || { echo 'FAIL: not visible in df'; exit 2; }
            fi

            TS=$(date +%Y%m%d-%H%M%S)
            echo \"probe $TS from $(hostname)\" | sudo tee '$EFS_MOUNT_POINT/_probe_${iid}.txt' >/dev/null
            sudo cat '$EFS_MOUNT_POINT/_probe_${iid}.txt' >/dev/null
            echo 'PASS: mounted and verified'
            \" && echo \"[PASS] $name ($iid) mounted at $EFS_MOUNT_POINT\" || echo \"[FAIL] $name ($iid) mount/verify failed\"

        else
            echo \"[MANUAL] $name ($iid) has no public IP. SSH (bastion/SSM) and run:\"
            cat <<MANUAL
            sudo dnf -y install nfs-utils || sudo yum -y install nfs-utils || (sudo apt-get update -y && sudo apt-get install -y nfs-common)
            sudo mkdir -p \"$EFS_MOUNT_POINT\"
            sudo cp /etc/fstab /etc/fstab.bak || true
            sudo awk '\$2 != \"'$EFS_MOUNT_POINT'\" {print}' /etc/fstab | sudo tee /tmp/fstab.new >/dev/null
            sudo mv /tmp/fstab.new /etc/fstab
            echo '$(MTIP[$subnet_id]):/ $EFS_MOUNT_POINT nfs4 nfsvers=4.1,rsize=1048576,wsize=1048576,hard,timeo=600,retrans=2,_netdev,noresvport 0 0' | sudo tee -a /etc/fstab
            sudo mount -a
            df -h | grep \"$EFS_MOUNT_POINT\"
            echo \"probe $(date +%Y%m%d-%H%M%S)\" | sudo tee \"$EFS_MOUNT_POINT/_probe_${iid}.txt\"
            cat \"$EFS_MOUNT_POINT/_probe_${iid}.txt\"
            MANUAL
            fi
    fi
done

```

```
done

echo
echo "[DONE] EFS $FS_ID configured. Verify with 'df -h' or 'mount | grep ${EFS_MOUNT_POINT}'."
```

10 Installing packages on the nodes

```
#!/bin/bash

# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
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# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail

# Usage: ./install_packages.sh list_of_instances.pem instance_ids.txt

KEYFILE="${1:?PEM key required}"
INSTANCES_FILE="${2:?File with instance IDs required}"
REGION="us-east-1" # change if needed
USER="ec2-user" # change if AMI uses another user (e.g., centos, ubuntu)

# Packages to install
PACKAGES="tcl qt6-qtbase-devel vim"

for iid in $(cat "$INSTANCES_FILE"); do
    echo ">>> Processing $iid ..."

    # Get public DNS of instance
    HOST=$(aws ec2 describe-instances \
        --instance-ids "$iid" \
        --region "$REGION" \
        --query "Reservations[0].Instances[0].PublicDnsName" \
        --output text)

    if [ "$HOST" = "None" ] || [ -z "$HOST" ]; then
        echo "!!! Could not find public DNS for $iid"
        continue
    fi
```

```

echo ">>> Connecting to $HOST"
ssh -o StrictHostKeyChecking=no -i "$KEYFILE" "$USER@$HOST" \
    "sudo yum install -y $PACKAGES"
done

```

11 Transferring license to the nodes

```

#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
# Version   : v1.0
# License   : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail

# ---- configurable defaults for license generation ----
LICENSE_USER="${LICENSE_USER:-acme_corp}"
LICENSE_SEATS="${LICENSE_SEATS:-200}"
# Leave LICENSE_EXPIRY empty to let the Python script default to +365 days
LICENSE_EXPIRY="${LICENSE_EXPIRY:-2025-09-30}"
LICENSE_KEY="${LICENSE_KEY:-$PWD/private_key.pem}"      # path to RSA private key (PEM)
LICENSE_GEN="${LICENSE_GEN:-$PWD/license_generator.py}"  # path to license_generator.py

# Usage:
# ./gen_and_push_licenses.sh --instances list_instances --outdir ./_licenses \
#     --region us-east-1 [--ssh-user ec2-user --ssh-key ~/monta.pem] [--bastion user@bastion] [--ssm]
#
# Notes:
# - By default we now COPY DIRECTLY to /home/$SSH_USER/lvr.lic on EACH instance.
# - Use --ssm if instances are private-only (needs SSM agent + IAM role).
# - Bastion is supported via --bastion (ProxyJump).

LIST_INSTANCES=""
OUTDIR="${OUTDIR:-./_licenses}"
OUTDIR="$(readlink -f "$OUTDIR")"
mkdir -p "$OUTDIR"

REGION="${AWS_REGION:-us-east-1}"
SSH_USER="${SSH_USER:-ec2-user}"
SSH_KEY="${SSH_KEY:-$HOME/monta.pem}"

```

```

USE_SSM=0
BASTION="" # e.g. ec2-user@bastion.example.com

# ----- Parse args -----
while [[ $# -gt 0 ]]; do
    case "$1" in
        --instances) LIST_INSTANCES="$2"; shift 2;;
        --outdir)     OUTDIR="$2"; OUTDIR="$(readlink -f "$OUTDIR")"; mkdir -p "$OUTDIR"; shift 2;;
        --region)     REGION="$2"; shift 2;;
        --ssh-user)   SSH_USER="$2"; shift 2;;
        --ssh-key)    SSH_KEY="$2"; shift 2;;
        --ssm)        USE_SSM=1; shift 1;;
        --bastion)    BASTION="$2"; shift 2;;
        *) echo "Unknown arg: $1"; exit 1;;
    esac
done

[[ -n "$LIST_INSTANCES" ]] || { echo "ERROR: --instances file required"; exit 1; }

command -v aws >/dev/null || { echo "aws CLI required"; exit 1; }
command -v jq >/dev/null || { echo "jq required"; exit 1; }

SSH_OPTS=(-o BatchMode=yes -o StrictHostKeyChecking=accept-new -o ConnectTimeout=6)
JUMP_OPTS=()
[[ -n "$BASTION" ]] && JUMP_OPTS=(-J "$BASTION")

readarray -t INSTANCES <<(grep -v '^\s*#' "$LIST_INSTANCES" | awk 'NF')

# ----- Helpers -----
ec2_facts() {
    local iid="$1"
    aws ec2 describe-instances --region "$REGION" --instance-ids "$iid" \
        --query 'Reservations[0].Instances[0].{
            Pub: PublicIpAddress,
            Priv: PrivateIpAddress,
            Mac: NetworkInterfaces[0].MacAddress
        }' \
        --output json 2>/dev/null
}

resolve_host_for_copy() {
    local iid="$1"
    local pub priv
    pub="$(aws ec2 describe-instances --region "$REGION" --instance-ids "$iid" \
        --query 'Reservations[].Instances[].PublicDnsName' --output text 2>/dev/null | awk "NF")" || true
    priv="$(aws ec2 describe-instances --region "$REGION" --instance-ids "$iid" \
        --query 'Reservations[].Instances[].PrivateDnsName' --output text 2>/dev/null | awk "NF")" || true
    if [[ -n "$pub" && "$pub" != "None" ]]; then echo "$pub"; return; fi
    if [[ -n "$priv" && "$priv" != "None" ]]; then echo "$priv"; return; fi
    echo ""
}

# ---- wrapper that calls the Python signer and returns ONLY the path ----
make_license() {
    local iid="$1" mac="$2"
    local lic="{OUTDIR}/${iid}.lic"

    mkdir -p "$OUTDIR"

    command -v python3 >/dev/null || { echo "ERROR: python3 not found" >&2; return 1; }
    [[ -f "$LICENSE_GEN" ]] || { echo "ERROR: license generator not found at $LICENSE_GEN" >&2; return 1; }
    [[ -f "$LICENSE_KEY" ]] || { echo "ERROR: private key not found at $LICENSE_KEY" >&2; return 1; }

```

```

local args=( --mac "$mac" --user "$LICENSE_USER" --seats "$LICENSE_SEATS" --key "$LICENSE_KEY" --out "$lic" )
[[ -n "${LICENSE_EXPIRY:-}" ]] && args+=( --expiry "$LICENSE_EXPIRY" )

if ! python3 "$LICENSE_GEN" "${args[@]}" 1>&2; then
    echo "ERROR: license generation failed for instance $iid (mac $mac)" >&2
    return 1
fi

if [[ ! -s "$lic" ]]; then
    echo "ERROR: expected license file not found or empty: $lic" >&2
    return 1
fi

printf '%s\n' "$lic"
}

# ---- SSM: write a file via send-command ----
ssm_write_file() {
    local iid="$1" dest="$2" content_file="$3"
    local b64; b64="$(base64 -w0 "$content_file")"
    aws ssm send-command --region "$REGION" \
        --instance-ids "$iid" \
        --document-name "AWS-RunShellScript" \
        --parameters commands="echo $b64 | base64 -d | sudo tee '$dest' >/dev/null, sudo chown $SSH_USER:$SSH_USER '$dest'" \
        --comment "Push license to $dest" >/dev/null
}

# ----- Main loop -----
for iid in "${INSTANCES[@]}; do
    echo "=== Processing instance: $iid ==="
    facts="$(ec2_facts "$iid" || true)"
    mac="$(jq -r '.Mac // empty' <<"$facts" 2>/dev/null || true)"
    if [[ -z "$mac" || "$mac" == "null" ]]; then
        echo "MAC: none"
        echo "WARN: Unable to fetch MAC from EC2 API (check IAM perms/region). Skipping." ; echo
        continue
    fi
    echo "MAC: $mac"

    # 1) Generate locally
    lic="$(make_license "$iid" "$mac")" || { echo; continue; }
    [[ -s "$lic" ]] || { echo "ERROR: expected license file not found: $lic" >&2; echo; continue; }

    # 2) Copy to /home/$SSH_USER/lvr.lic on that specific instance
    host="$(resolve_host_for_copy "$iid")"
    remote_dest="/home/${SSH_USER}/lvr.lic"

    if [[ -n "$host" && "$host" != "None" ]]; then
        echo "Copy via SSH to $host:$remote_dest"
        # copy to a temp path first, then move atomically and set perms
        scp -i "$SSH_KEY" "${JUMP_OPTS[@]}" -o StrictHostKeyChecking=accept-new \
            "$lic" "${SSH_USER}@host:/home/${SSH_USER}/.lvr.tmp" >/dev/null \
            && ssh -i "$SSH_KEY" "${SSH_OPTS[@]}" "${JUMP_OPTS[@]}" "${SSH_USER}@host" \
                "mv /home/${SSH_USER}/.lvr.tmp '$remote_dest' && chmod 644 '$remote_dest'" \
            && echo "OK (SSH)"
        if [[ $? -ne 0 ]]; then
            echo "ERROR: SSH copy failed for $iid" >&2
        fi
    fi

    elif (( USE_SSM )); then
        echo "Write via SSM to $iid:$remote_dest"

```

```

ssm_write_file "$iid" "$remote_dest" "$lic" \
    && echo "OK (SSM sent)" || echo "ERROR: SSM send failed for $iid" >&2

else
    echo "WARN: No reachable host for $iid and --ssm not set. License kept locally: $lic"
fi

echo
done

echo "All done. Per-instance artifacts in: $OUTDIR"

```

12 Transferring data to the nodes

```

#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
# Version   : v1.0
# License   : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail

# ==== EDIT THESE ====
export AWS_REGION="${AWS_REGION:-us-east-1}"      # region of your EC2s
export EFS_NAME="${EFS_NAME:-lvr-efs}"            # name for the EFS
export EFS_MOUNT_POINT="${EFS_MOUNT_POINT:-/mnt/efs}" # where to mount inside instances

# SSH details (for connecting into EC2s)
export SSH_KEY_PATH="${SSH_KEY_PATH:-$HOME/monta.pem}" # path to your .pem
export SSH_USER_AL2="${SSH_USER_AL2:-ec2-user}"        # Amazon Linux default user
export SSH_USER_UBU="${SSH_USER_UBU:-ubuntu}"          # Ubuntu default user
export SSH_CONNECT_TIMEOUT=8

# ==== INPUT FILE ====
# File with instance IDs, one per line, e.g.:
# i-0abc123...
# i-0def456...
export INST_FILE="list_of_instances"
if [[ ! -s "$INST_FILE" ]]; then
    echo "[ERROR] $INST_FILE missing or empty (put one instance-id per line)" >&2

```



```

exit 1
fi

```

13 Setting the permissions to run the scripts

```

#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function   : [Describe the purpose of the script briefly here]
# Version    : v1.0
# License    : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright  : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website    : https://www.ramtera.com
# -----
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
# HISTORY:
# Date       Author           Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

KEY=~/.secure.pem
USER=ec2-user # change to ubuntu if needed

while read -r h; do
    echo "==" $h"
    ssh -i "$KEY" -o BatchMode=yes -o StrictHostKeyChecking=accept-new "$USER@$h" 'echo ok' || echo "FAILED"
done < pub

```

14 Serial caller script for for 81 iterations

```

#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#           Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function   : [Describe the purpose of the script briefly here]
# Version    : v1.0
# License    : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright  : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website    : https://www.ramtera.com
# -----
# DISCLAIMER:

```

```
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
```

```
#####
# HISTORY:
# Date Author Description
# -----
# $(date +%Y-%m-%d) Sameer P. Initial version
#####
```

[illegible]

```

bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 412648888 257905555 464230000 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 0 309486666 51581111 361067777
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 51581111 309486666 103162222 36
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 103162222 309486666 154743333 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 154743333 309486666 206324444 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 206324444 309486666 257905555 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 257905555 309486666 309486666 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 309486666 309486666 361067777 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 361067777 309486666 412648888 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 412648888 309486666 464230000 3
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 0 361067777 51581111 412648888
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 51581111 361067777 103162222 41
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 103162222 361067777 154743333 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 154743333 361067777 206324444 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 206324444 361067777 257905555 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 257905555 361067777 309486666 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 309486666 361067777 361067777 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 361067777 361067777 412648888 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 412648888 361067777 464230000 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 0 412648888 51581111 464230000
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 51581111 412648888 103162222 46
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 103162222 412648888 154743333 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 154743333 412648888 206324444 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 206324444 412648888 257905555 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 257905555 412648888 309486666 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 309486666 412648888 361067777 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 361067777 412648888 412648888 4
bash run_on_master_instance.sh nodes.txt /mnt/efs/designs/gcd_array_4000x4000.gds /mnt/efs/designs/sky130.lrf /mnt/efs/designs 412648888 412648888 464230000 4

```

15 Script to call LVR on each of the nodes

```

#!/bin/bash
# =====
#                               R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#                               Layout Verification Reporter (LVR)
# -----
# Product    : LVR (Layout Verification Reporter)
# Function    : [Describe the purpose of the script briefly here]
# Version    : v1.0
# License    : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright  : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website    : https://www.ramtera.com
# -----
#
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
#
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail

# ----- SSH defaults -----
SSH_USER="${SSH_USER:-ec2-user}"

```

```

SSH_KEY="${SSH_KEY:-$HOME/monta.pem}"
SSH_OPTS=(-o BatchMode=yes -o StrictHostKeyChecking=accept-new -o ConnectTimeout=6)

# ----- INPUTS -----
NODES_FILE="${1:-nodes.txt}"          # hostnames/IPs or EC2 instance IDs (i-xxxx)
GDS="${2:-/mnt/efs/designs/jpeg_array_3x3.gds}"
RULES="${3:-/mnt/efs/designs/sky130.lrf}"
OUTDIR="${4:-/mnt/efs/designs}"
X1="${5:-0}"; Y1="${6:-0}"; X2="${7:-4518350}"; Y2="${8:-4518350}"
CORES_PER_NODE="${9:-16}"             # kept for future use / parity
SLAVE_BIN="${SLAVE_BIN:-/mnt/efs/designs/LVR}"

# Optional controls
AWS_REGION="${AWS_REGION:-}"          # set if needed, else relies on default AWS config
RESOLVE_MODE="${RESOLVE_MODE:-private_dns}" # private_dns | private_ip | none
ADD_LOCAL="${ADD_LOCAL:-0}"           # 1 = add 'local' as a worker
# -----

mkdir -p "$OUTDIR"

# ---- harden: ensure OUTDIR is on EFS (nfs4) ----
if ! df -T "$OUTDIR" | awk 'NR==2{exit ($2=="nfs4"?0:1)}'; then
    echo "ERROR: OUTDIR ($OUTDIR) is not on nfs4 (EFS). Mount EFS and retry." >&2
    exit 1
fi

# ----- helpers -----
die(){ echo "ERROR: $*" >&2; exit 1; }
have(){ command -v "$1" >/dev/null 2>&1; }
trim(){ sed -e 's/^[[:space:]]*//' -e 's/[[:space:]]*$//'; }
ceil_div(){ # ceil(a/b)
    local a="$1" b="$2"
    echo $(( (a + b - 1) / b ))
}
is_instance_id(){ [[ "$1" =~ ^i-[A-Za-z0-9]+$ ]]; }

aws_describe(){
    # $1: instance-id ; $2: key (PrivateDnsName | PrivateIpAddress)
    local iid="$1" key="$2" region_args=()
    [[ -n "$AWS_REGION" ]] && region_args+=(--region "$AWS_REGION")
    aws ec2 describe-instances "${region_args[@]}" --instance-ids "$iid" \
        --query "Reservations[].Instances[].${key}" --output text
}

resolve_nodes(){
    local file="$1"
    mapfile -t raw < <(grep -v '^s*#' "$file" | sed '/^s*/d' | trim)
    [[ ${#raw[@]} -gt 0 ]] || die "No entries in $file"

    local resolved=()
    if [[ "$ADD_LOCAL" == "1" ]]; then
        resolved+=("local")
    fi

    local any_ids=0
    for x in "${raw[@]}"; do
        if is_instance_id "$x"; then any_ids=1; break; fi
    done

    if [[ "$any_ids" -eq 1 ]]; then
        have aws || die "AWS CLI not found. Install awscli or change nodes.txt to hostnames/IPs with RESOLVE_MODE=none."
        local key

```

```

case "$RESOLVE_MODE" in
    private_dns) key="PrivateDnsName" ;;
    private_ip)  key="PrivateIpAddress" ;;
    none)        die "RESOLVE_MODE=none not valid for instance IDs. Choose private_dns or private_ip." ;;
    *)           die "Unknown RESOLVE_MODE='$RESOLVE_MODE'";;
esac
for iid in "${raw[@]}; do
    if is_instance_id "$iid"; then
        val="$(aws_describe "$iid" "$key" | trim)"
        [[ -n "$val" && "$val" != "None" ]] || die "Failed to resolve $iid -> $key"
        resolved+=("$val")
    else
        resolved+=("$iid")
    fi
done
else
    resolved+=("${raw[@]}")
fi

printf '%s\n' "${resolved[@]}"
}

# ----- main -----
mapfile -t NODES < <(resolve_nodes "$NODES_FILE")
NUM_NODES="${#NODES[@]}"
[[ "$NUM_NODES" -gt 0 ]] || die "No nodes resolved"

# ----- Fixed grid: 4 columns (X) x 8 rows (Y) -----
# You can override via env if needed: GRID_COLS=4 GRID_ROWS=8
COLS="${GRID_COLS:-4}"
ROWS="${GRID_ROWS:-8}"
NUM_TILES=$(( ROWS * COLS ))

echo "[master] Using fixed grid: ROWS=${ROWS} COLS=${COLS} (tiles=${NUM_TILES})"

TOTAL_W=$(( X2 - X1 + 1 ))
TOTAL_H=$(( Y2 - Y1 + 1 ))
[[ "$TOTAL_W" -gt 0 && "$TOTAL_H" -gt 0 ]] || die "Invalid bbox"

# Base sizes and remainders for fair slicing
BASE_W=$(( TOTAL_W / COLS ))
REM_W=$(( TOTAL_W % COLS ))
BASE_H=$(( TOTAL_H / ROWS ))
REM_H=$(( TOTAL_H % ROWS ))

echo "[master] Nodes: $NUM_NODES (cores/node=${CORES_PER_NODE}) Grid=${ROWS}x${COLS}"
echo "[master] Resolve mode: ${RESOLVE_MODE} AWS_REGION=${AWS_REGION:-default}"
echo "[master] GDS=${GDS}"
echo "[master] RULES=${RULES}"
echo "[master] OUTDIR=${OUTDIR}"
echo "[master] Die bbox=(${X1},${Y1})-(${X2},${Y2})"
echo "[master] TOTAL_W=${TOTAL_W} BASE_W=${BASE_W} REM_W=${REM_W}"
echo "[master] TOTAL_H=${TOTAL_H} BASE_H=${BASE_H} REM_H=${REM_H}"

# Precompute inclusive X/Y ranges per column/row
declare -a XL XH YL YH

xcur="$X1"
for c in $(seq 0 $((COLS-1))); do
    len="$BASE_W"
    # Distribute leftover pixels 1-by-1 to the leftmost columns
    if [[ "$c" -lt "$REM_W" ]]; then len=$(( BASE_W + 1 )); fi

```

```

    XL[$c]="$xcur"
    XH[$c]=$(( xcur + len - 1 ))
    xcur=$(( XH[$c] + 1 ))
done
# safety: make sure we covered exactly
if [[ "$xcur" -ne $(X2 + 1) ]]; then
    die "X partitioning error: final xcur=${xcur} != X2+1=$(X2+1)"
fi

ycur="$Y1"
for r in $(seq 0 $(ROWS-1)); do
    len="$BASE_H"
    # Distribute leftover pixels 1-by-1 to the topmost rows
    if [[ "$r" -lt "$REM_H" ]]; then len=$(( BASE_H + 1 )); fi
    YL[$r]="$ycur"
    YH[$r]=$(( ycur + len - 1 ))
    ycur=$(( YH[$r] + 1 ))
done
if [[ "$ycur" -ne $(Y2 + 1) ]]; then
    die "Y partitioning error: final ycur=${ycur} != Y2+1=$(Y2+1)"
fi

# ----- emit per-node .cmd files over the grid -----
for i in $(seq 0 $(NUM_NODES-1)); do
    r=$(( i / COLS ))
    c=$(( i % COLS ))
    [[ "$r" -lt "$ROWS" ]] || die "Row index out of range"
    [[ "$c" -lt "$COLS" ]] || die "Col index out of range"

    x_l="${XL[$c]}; x_h="${XH[$c]}"
    y_l="${YL[$r]}; y_h="${YH[$r]}"

    cmd="${OUTDIR}/jpeg_drc_${i}.cmd"
    cat > "$cmd" <EOF
# initialize tool DB
init_tool_db DRC
set_technode sky130
set_top TOP
set_num_cores ${CORES_PER_NODE}

# node-specific die area (inclusive)
set_die_area ${x_l} ${y_l} ${x_h} ${y_h}
set_num_regions ${CORES_PER_NODE}

read_lrf ${RULES}
read_gds ${GDS}
load_db DRC
no_compare 1

drc TOP
EOF

    echo "[master] wrote ${cmd} tile[i=${i}, r=${r}, c=${c}] bbox=(${x_l},${y_l})-(${x_h},${y_h})"
done

# ----- launch -----
pids=()
trap 'kill "${pids[@]}" 2>/dev/null || true' INT TERM

for i in $(seq 0 $(NUM_NODES-1)); do
    NODE="${NODES[$i]}"
    LOG="${OUTDIR}/node_${i}.log"

```

```

CMD="${SLAVE_BIN} ${OUTDIR}/jpeg_drc_${i}.cmd"

>&2 echo "[master] launch node ${i} -> ${NODE}"

if [[ "$NODE" == "local" ]]; then
    bash -lc "$CMD" >"$LOG" 2>&1 &
    pids+=("$!")
else
    ssh -i "$SSH_KEY" "${SSH_OPTS[@]}" "$SSH_USER@$NODE" "$CMD" >"$LOG" 2>&1 &
    pids+=("$!")
fi
done

echo "[master] waiting for ${#pids[@]} node processes to finish..."
wait "${pids[@]}" || true
echo "[master] All node jobs finished (check logs under $OUTDIR)"

```

16 Get the log files from the EFS

```

#!/bin/bash
# =====
#                               R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#                               Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
# Version   : v1.0
# License   : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail

# Usage: ./pull_logs.sh list_instances REMOTE_DIR LOCAL_OUTDIR [SSH_USER] [SSH_KEY]
LIST_INSTANCES="${1:?list_instances file required}"
REMOTE_DIR="${2:?remote directory to search (e.g., /mnt/share/runs)}"
LOCAL_OUTDIR="${3:?local output directory required}"
SSH_USER="${4:-}"
SSH_KEY="${5:-$HOME/monta.pem}"

command -v aws >/dev/null || { echo "aws CLI required"; exit 1; }
command -v jq >/dev/null || { echo "jq required"; exit 1; }
command -v rsync >/dev/null || { echo "rsync required"; exit 1; }

resolve_host() {
    local token="$1"

```

```

if [[ "$token" =~ ^i-[a-f0-9]+$ ]]; then
    aws ec2 describe-instances \
        --instance-ids "$token" \
        --query 'Reservations[].Instances[].PublicDnsName' \
        --output text | awk 'NF' || true
else
    echo "$token"
fi
}

detect_user() {
    local host="$1"
    if [[ -n "$SSH_USER" ]]; then
        echo "$SSH_USER"
        return
    fi
    if ssh -i "$SSH_KEY" -o BatchMode=yes -o ConnectTimeout=5 ubuntu@"$host" "echo ok" >/dev/null 2>&1; then
        echo "ubuntu"
    elif ssh -i "$SSH_KEY" -o BatchMode=yes -o ConnectTimeout=5 ec2-user@"$host" "echo ok" >/dev/null 2>&1; then
        echo "ec2-user"
    else
        echo "$USER"
    fi
}

mkdir -p "$LOCAL_OUTDIR"
mapfile -t INSTANCES <<(grep -v '^s*#' "$LIST_INSTANCES" | awk 'NF')
if (( ${#INSTANCES[@]} == 0 )); then echo "No instances found in $LIST_INSTANCES"; exit 1; fi

for inst in "${INSTANCES[@]}; do
    host="$(resolve_host "$inst")"
    if [[ -z "$host" ]]; then
        echo "WARN: Could not resolve host for $inst; skipping."
        continue
    fi
    user="$(detect_user "$host")"
    echo "==> Pulling from $inst ($host) as $user"

    # Mirror only desired artifacts using rsync include/exclude
    # This preserves relative paths under LOCAL_OUTDIR/<host>/
    rsync -az --prune-empty-dirs \
        --include='*/' \
        --include='*.rpt' \
        --exclude='*' \
        -e "ssh -i '$SSH_KEY' " \
        "$user@$host:$REMOTE_DIR/" \
        "$LOCAL_OUTDIR/$host/"
done

echo "Log/report collection complete -> $LOCAL_OUTDIR"

```

17 Get the list of EFS ids to perform a clean up

```

#!/bin/bash
# =====
#           R A M T E R A   D E S I G N   A U T O M A T I O N

```

```

# -----
#                               Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
# Version   : v1.0
# License   : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
#
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
#
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====
#
:
aws efs describe-file-systems --region us-east-1 --query "FileSystems[].FileSystemId" --output text >efs_ids

```

18 Script to cleanup the EFS

```

#!/bin/bash
# =====
#                               R A M T E R A   D E S I G N   A U T O M A T I O N
# -----
#                               Layout Verification Reporter (LVR)
# -----
# Product   : LVR (Layout Verification Reporter)
# Function  : [Describe the purpose of the script briefly here]
# Version   : v1.0
# License   : Proprietary - Ramtera Design Automation (OPC) Pvt. Ltd.
# Copyright : © $(date +%Y) Ramtera Design Automation. All Rights Reserved.
# Website   : https://www.ramtera.com
# -----
#
# DISCLAIMER:
# This script is confidential and intended solely for use with LVR.
# Unauthorized copying or distribution is prohibited.
# -----
#
# HISTORY:
# Date      Author      Description
# -----
# $(date +%Y-%m-%d) Sameer P.      Initial version
# =====

set -euo pipefail
source ./00_env.sh

# ===== Settings =====
UNMOUNT_FROM_INSTANCES="${UNMOUNT_FROM_INSTANCES:-1}" # 1 = unmount/clean fstab on EC2s first, 0 = skip
EFS_LIST_FILE="${EFS_LIST_FILE:-efs_ids}"             # optional file with FS IDs (one per line). If absent/empty -> all EFS in region
# =====

# --- Collect EFS IDs ---
EFS_IDS=()
if [[ -s "$EFS_LIST_FILE" ]]; then

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while IFS= read -r line; do
    line=${line//'$'\r'/}
    line="$(printf '%s' "$line" | awk '{ $1=$1 } 1')"
    [[ -n "$line" ]] && EFS_IDS+=("$line")
done < "$EFS_LIST_FILE"
else
    # all EFS in region
    mapfile -t EFS_IDS < <(aws efs describe-file-systems --region "$AWS_REGION" --query 'FileSystems[].FileSystemId' --output text | tr '\t' '\n')
fi

if (( ${#EFS_IDS[@]} == 0 )); then
    echo "[INFO] No EFS file systems found in region $AWS_REGION"
    exit 0
fi

echo "[INFO] Target EFS IDs: ${EFS_IDS[*]}"

# --- Optional: unmount from EC2 instances first ---
if [[ "$UNMOUNT_FROM_INSTANCES" == "1" ]]; then
    echo "[INFO] Unmounting and cleaning /etc/fstab on instances listed in $INST_FILE (if reachable)"

    # Load instance IDs
    IIDS=()
    if [[ -s "$INST_FILE" ]]; then
        while IFS= read -r line; do
            line=${line//'$'\r'/}
            line="$(printf '%s' "$line" | awk '{ $1=$1 } 1')"
            [[ -n "$line" ]] && IIDS+=("$line")
        done < "$INST_FILE"
    fi

    if (( ${#IIDS[@]} > 0 )); then
        DESC_JSON=$(aws ec2 describe-instances --region "$AWS_REGION" --instance-ids "${IIDS[@]}")
        declare -A PUBIP PLAT NAME
        for iid in "${IIDS[@]}; do
            j=$(jq -r --arg iid "$iid" '.Reservations[].Instances[] | select(.InstanceId==$iid)' <<<"$DESC_JSON")
            PUBIP["$iid"]=$(jq -r '.PublicIpAddress // empty' <<<"$j")
            PLAT["$iid"]=$(jq -r '.PlatformDetails // ""' <<<"$j")
            NAME["$iid"]=$(jq -r '.Tags[]?|select(.Key=="Name")|.Value' <<<"$j" | head -n1)
        done

        ssh_run() {
            local host="$1" user="$2"
            shift 2
            ssh -i "$SSH_KEY_PATH" -o StrictHostKeyChecking=no -o ConnectTimeout="$SSH_CONNECT_TIMEOUT" \
                "$user@$host" "$@" </dev/null
        }

        for iid in "${IIDS[@]}; do
            name="${NAME[$iid]:-$iid}"
            pub="${PUBIP[$iid]}"
            plat="${PLAT[$iid]}"
            SSH_USER="$SSH_USER_AL2"
            [[ "$plat" =~ Ubuntu|ubuntu ]] && SSH_USER="$SSH_USER_UBU"

            if [[ -n "$pub" ]]; then
                echo "[INFO] Unmount on $name ($iid) $pub"
                ssh_run "$pub" "$SSH_USER" "set -e"
                # unmount if mounted
                if mountpoint -q '$EFS_MOUNT_POINT'; then
                    sudo umount '$EFS_MOUNT_POINT' || true
                fi
            fi
        done
    fi

```

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        # remove any fstab entries that use $EFS_MOUNT_POINT (safe awk by column)
        sudo cp /etc/fstab /etc/fstab.bak || true
        sudo awk '\$2 != \"'$EFS_MOUNT_POINT'\" {print}' /etc/fstab | sudo tee /tmp/fstab.new >/dev/null
        sudo mv /tmp/fstab.new /etc/fstab
        # show remaining line for info
        grep -n \"'$EFS_MOUNT_POINT'\" /etc/fstab || true
        " && echo "[OK] Unmounted & cleaned on $name" || echo "[WARN] Could not SSH/unmount on $name"
    else
        echo "[WARN] $name ($iid) has no public IP; skipping unmount (do manually or via SSM)"
    fi
done
else
    echo "[INFO] $INST_FILE empty or missing; skipping instance unmount stage."
fi
else
    echo "[INFO] Skipping instance unmount stage (UNMOUNT_FROM_INSTANCES=$UNMOUNT_FROM_INSTANCES)"
fi

# --- Delete mount targets, then delete EFS ---
for FS_ID in "${EFS_IDS[@]}; do
    echo
    echo "=== Deleting EFS $FS_ID ==="

    # List mount targets
    mapfile -t MT_IDS < <(aws efs describe-mount-targets --region "$AWS_REGION" --file-system-id "$FS_ID" \
        --query 'MountTargets[].MountTargetId' --output text | tr '\t' '\n' || true)

    if (( ${#MT_IDS[@]} > 0 )); then
        echo "[INFO] Deleting mount targets: ${MT_IDS[*]}"
        for MT in "${MT_IDS[@]}; do
            aws efs delete-mount-target --region "$AWS_REGION" --mount-target-id "$MT" || true
        done

        echo "[INFO] Waiting for mount targets to be gone..."
        for _ in {1..60}; do
            count=$(aws efs describe-mount-targets --region "$AWS_REGION" --file-system-id "$FS_ID" \
                --query 'length(MountTargets)' --output text 2>/dev/null || echo "0")
            [[ "$count" == "0" || "$count" == "None" ]] && { echo "[OK] All mount targets deleted"; break; }
            sleep 5
        done
    else
        echo "[INFO] No mount targets found."
    fi

    echo "[INFO] Deleting file system $FS_ID"
    aws efs delete-file-system --region "$AWS_REGION" --file-system-id "$FS_ID" || {
        echo "[ERROR] Failed to delete $FS_ID (is it already gone or still in use?)"
        continue
    }
    echo "[OK] Deleted $FS_ID"
done

# --- Optional: delete the EFS SG named ${EFS_NAME}-sg (if not in use) ---
# (There may be only one from your earlier runs.)
echo
echo "[INFO] Attempting to delete SG '${EFS_NAME}-sg' (optional)"
VPC_ID_ANY=$(aws ec2 describe-vpcs --region "$AWS_REGION" --query 'Vpcs[0].VpcId' --output text)
SG_ID=$(aws ec2 describe-security-groups --region "$AWS_REGION" \
    --filters Name=group-name,Values="${EFS_NAME}-sg" Name=vpc-id,Values="$VPC_ID_ANY" \
    --query 'SecurityGroups[0].GroupId' --output text 2>/dev/null || true)
if [[ -n "$SG_ID" && "$SG_ID" != "None" ]]; then
    echo "[INFO] Deleting SG $SG_ID (${EFS_NAME}-sg)"

```

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aws ec2 delete-security-group --region "$AWS_REGION" --group-id "$SG_ID" && echo "[OK] SG deleted" || echo "[WARN] SG in use; skip"
else
    echo "[INFO] No SG named ${EFS_NAME}-sg found (or already deleted)."
```

```

fi

echo
echo "[DONE] Cleanup complete."
```

19 Summary files for each node

ec2-107-23-48-123.compute-1.amazonaws.com (Set of results for 1 Node, skipping rest 127)

lvr_summary_1.rpt

```

[2025-10-12 10:42:28] [info] =====
[2025-10-12 10:42:28] [info]                      Design Rule Check Report
[2025-10-12 10:42:28] [info] =====

[2025-10-12 10:42:28] [info] Report Generated:      2025-10-12
[2025-10-12 10:42:28] [info] Run Start Time:       10:41:35 AM
[2025-10-12 10:42:28] [info] Run End Time:         10:42:28 AM
[2025-10-12 10:42:28] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 10:42:28] [info] Tool Version:         LVR_2025.1
[2025-10-12 10:42:28] [info] Host Name:            ip-172-31-37-173
[2025-10-12 10:42:28] [info] User Name:            ubuntu
[2025-10-12 10:42:28] [info] Cores Used:           8
[2025-10-12 10:42:28] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info]                      Top Cell Information
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] Layout Top Cell:      TOP
[2025-10-12 10:42:28] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 10:42:28] [info] Database Format:        GDSII
[2025-10-12 10:42:28] [info] Verification Mode:     Full Check

[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info]                      Type of Errors | Count
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] DB_WARN -> 1
[2025-10-12 10:42:28] [info] LRF_ERR001 -> 14
[2025-10-12 10:42:28] [info] DB_WARN002 -> 159
[2025-10-12 10:42:28] [info] DB_WARN003 -> 161
[2025-10-12 10:42:28] [info] DRC_ERR9 -> 89
[2025-10-12 10:42:28] [info] DRC_ERR -> 67
[2025-10-12 10:42:28] [info] DRC_ERR5 -> 408
[2025-10-12 10:42:28] [info] DRC_ERR4 -> 241
[2025-10-12 10:42:28] [info] DRC_ERR1 -> 301
[2025-10-12 10:42:28] [info] -----
```

```

[2025-10-12 10:42:28] [info] Rule Violation Summary
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] | Rule ID          | Description                                     | Error Count |
[2025-10-12 10:42:28] [info] |-----|-----|-----|
[2025-10-12 10:42:28] [info] | m5.1             | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:42:28] [info] | li.5             | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:42:28] [info] | poly.1a          | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:42:28] [info] | poly.7           | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:42:28] [info] | licon.15         | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 10:42:28] [info] | m1.6             | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 10:42:28] [info] | m4.5             | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 10:42:28] [info] | m4.5ab           | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 10:42:28] [info] | psdm.1           | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 10:42:28] [info] | licon.5          | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 10:42:28] [info] | li.1             | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 10:42:28] [info] | li.1a            | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 10:42:28] [info] | m1.1             | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 10:42:28] [info] | licon.8a         | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 10:42:28] [info] | npc.1            | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 10:42:28] [info] |
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] Performance Metrics
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] | Stage              | Time Taken |
[2025-10-12 10:42:28] [info] |-----|-----|
[2025-10-12 10:42:28] [info] | GDS Parsing        | 0 minutes 9 seconds |
[2025-10-12 10:42:28] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 10:42:28] [info] | Report Generation   | 0 minutes 0 seconds |
[2025-10-12 10:42:28] [info] |
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] Summary
[2025-10-12 10:42:28] [info] -----
[2025-10-12 10:42:28] [info] Total Errors:      1120
[2025-10-12 10:42:28] [info] Total Warnings:    321
[2025-10-12 10:42:28] [info] DRC Status:        FAILED

```

lvr_summary_10.rpt

```

[2025-10-12 10:50:55] [info] =====
[2025-10-12 10:50:55] [info] Design Rule Check Report
[2025-10-12 10:50:55] [info] =====

[2025-10-12 10:50:55] [info] Report Generated: 2025-10-12
[2025-10-12 10:50:55] [info] Run Start Time: 10:50:03 AM
[2025-10-12 10:50:55] [info] Run End Time: 10:50:55 AM
[2025-10-12 10:50:55] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:50:55] [info] Tool Version: LVR_2025.1
[2025-10-12 10:50:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:50:55] [info] User Name: ubuntu
[2025-10-12 10:50:55] [info] Cores Used: 8
[2025-10-12 10:50:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info] Top Cell Information
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info] Layout Top Cell: TOP
[2025-10-12 10:50:55] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:50:55] [info] Database Format: GDSII

```

```

[2025-10-12 10:50:55] [info] Verification Mode:    Full Check

[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info]                               Type of Errors | Count
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info] DB_WARN -> 1
[2025-10-12 10:50:55] [info] LRF_ERR001 -> 14
[2025-10-12 10:50:55] [info] DB_WARN002 -> 159
[2025-10-12 10:50:55] [info] DB_WARN003 -> 161
[2025-10-12 10:50:55] [info] DRC_ERR9 -> 89
[2025-10-12 10:50:55] [info] DRC_ERR -> 67
[2025-10-12 10:50:55] [info] DRC_ERR5 -> 408
[2025-10-12 10:50:55] [info] DRC_ERR4 -> 241
[2025-10-12 10:50:55] [info] DRC_ERR1 -> 301
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info]                               Rule Violation Summary
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info] | Rule ID          | Description                               | Error Count |
[2025-10-12 10:50:55] [info] |-----|-----|-----|
[2025-10-12 10:50:55] [info] | m5.1             | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:50:55] [info] | li.5             | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:50:55] [info] | poly.1a          | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:50:55] [info] | poly.7           | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:50:55] [info] | licon.15         | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 10:50:55] [info] | m1.6             | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 10:50:55] [info] | m4.5             | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 10:50:55] [info] | m4.5ab           | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 10:50:55] [info] | psdm.1           | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 10:50:55] [info] | licon.5          | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 10:50:55] [info] | li.1             | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 10:50:55] [info] | li.1a            | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 10:50:55] [info] | m1.1             | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 10:50:55] [info] | licon.8a         | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 10:50:55] [info] | npc.1            | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info]                               Performance Metrics
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info] | Stage              | Time Taken |
[2025-10-12 10:50:55] [info] |-----|-----|
[2025-10-12 10:50:55] [info] | GDS Parsing        | 0 minutes 9 seconds |
[2025-10-12 10:50:55] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 10:50:55] [info] | Report Generation   | 0 minutes 0 seconds |

[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info]                               Summary
[2025-10-12 10:50:55] [info] -----
[2025-10-12 10:50:55] [info] Total Errors: 1120
[2025-10-12 10:50:55] [info] Total Warnings: 321
[2025-10-12 10:50:55] [info] DRC Status: FAILED

```

lvr_summary_100.rpt

```

[2025-10-12 12:16:14] [info] =====
[2025-10-12 12:16:14] [info]                               Design Rule Check Report
[2025-10-12 12:16:14] [info] =====

[2025-10-12 12:16:14] [info] Report Generated:    2025-10-12
[2025-10-12 12:16:14] [info] Run Start Time:      12:15:23 PM

```

```

[2025-10-12 12:16:14] [info] Run End Time:      12:16:14 PM
[2025-10-12 12:16:14] [info] Time Taken:      0 minutes 50 seconds

[2025-10-12 12:16:14] [info] Tool Version:    LVR_2025.1
[2025-10-12 12:16:14] [info] Host Name:     ip-172-31-37-173
[2025-10-12 12:16:14] [info] User Name:     ubuntu
[2025-10-12 12:16:14] [info] Cores Used:      8
[2025-10-12 12:16:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info]                      Top Cell Information
[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info] Layout Top Cell:    TOP
[2025-10-12 12:16:14] [info] Technology Node:    SKY130HD (130nm)
[2025-10-12 12:16:14] [info] Database Format:    GDSII
[2025-10-12 12:16:14] [info] Verification Mode:  Full Check

[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info]                      Type of Errors | Count
[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info] DB_WARN -> 1
[2025-10-12 12:16:14] [info] LRF_ERROR01 -> 14
[2025-10-12 12:16:14] [info] DB_WARN002 -> 159
[2025-10-12 12:16:14] [info] DB_WARN003 -> 161
[2025-10-12 12:16:14] [info] DRC_ERR9 -> 89
[2025-10-12 12:16:14] [info] DRC_ERR -> 67
[2025-10-12 12:16:14] [info] DRC_ERR5 -> 408
[2025-10-12 12:16:14] [info] DRC_ERR4 -> 241
[2025-10-12 12:16:14] [info] DRC_ERR1 -> 301
[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info]                      Rule Violation Summary
[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:16:14] [info] |-----|-----|-----|
[2025-10-12 12:16:14] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:16:14] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:16:14] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:16:14] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:16:14] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:16:14] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:16:14] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:16:14] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:16:14] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:16:14] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:16:14] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:16:14] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:16:14] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:16:14] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:16:14] [info] | npc.1       | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info]                      Performance Metrics
[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info] | Stage          | Time Taken |
[2025-10-12 12:16:14] [info] |-----|-----|
[2025-10-12 12:16:14] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:16:14] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 12:16:14] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:16:14] [info] -----
[2025-10-12 12:16:14] [info]                      Summary
[2025-10-12 12:16:14] [info] -----

```

lvr_summary_101.rpt

Time	Level	Category	Description	Error Count
[2025-10-12 12:17:12]	[info]	Rule ID		
[2025-10-12 12:17:12]	[info]			
[2025-10-12 12:17:12]	[info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 12:17:12]	[info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 12:17:12]	[info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 12:17:12]	[info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 12:17:12]	[info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 12:17:12]	[info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 12:17:12]	[info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 12:17:12]	[info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 12:17:12]	[info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 12:17:12]	[info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 12:17:12]	[info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 12:17:12]	[info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" 15	
[2025-10-12 12:17:12]	[info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	

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[2025-10-12 12:17:12] [info] | licon.8a      | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:17:12] [info] | npc.1      | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:17:12] [info] -----
[2025-10-12 12:17:12] [info]                      Performance Metrics
[2025-10-12 12:17:12] [info] -----
[2025-10-12 12:17:12] [info] | Stage                | Time Taken |
[2025-10-12 12:17:12] [info] |-----|-----|
[2025-10-12 12:17:12] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 12:17:12] [info] | Rule Checking        | 0 minutes 41 seconds |
[2025-10-12 12:17:12] [info] | Report Generation    | 0 minutes 0 seconds |

[2025-10-12 12:17:12] [info] -----
[2025-10-12 12:17:12] [info]                      Summary
[2025-10-12 12:17:12] [info] -----
[2025-10-12 12:17:12] [info] Total Errors:      1120
[2025-10-12 12:17:12] [info] Total Warnings:    321
[2025-10-12 12:17:12] [info] DRC Status:        FAILED

```

lvr_summary_102.rpt

```

[2025-10-12 12:18:09] [info] =====
[2025-10-12 12:18:09] [info]                      Design Rule Check Report
[2025-10-12 12:18:09] [info] =====

[2025-10-12 12:18:09] [info] Report Generated:   2025-10-12
[2025-10-12 12:18:09] [info] Run Start Time:     12:17:17 PM
[2025-10-12 12:18:09] [info] Run End Time:       12:18:09 PM
[2025-10-12 12:18:09] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 12:18:09] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:18:09] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:18:09] [info] User Name:          ubuntu
[2025-10-12 12:18:09] [info] Cores Used:         8
[2025-10-12 12:18:09] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:18:09] [info] -----
[2025-10-12 12:18:09] [info]                      Top Cell Information
[2025-10-12 12:18:09] [info] -----
[2025-10-12 12:18:09] [info] Layout Top Cell:    TOP
[2025-10-12 12:18:09] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:18:09] [info] Database Format:     GDSII
[2025-10-12 12:18:09] [info] Verification Mode:   Full Check

[2025-10-12 12:18:09] [info] -----
[2025-10-12 12:18:09] [info]                      Type of Errors | Count
[2025-10-12 12:18:09] [info] -----
[2025-10-12 12:18:09] [info] DB_WARN -> 1
[2025-10-12 12:18:09] [info] LRF_ERR001 -> 14
[2025-10-12 12:18:09] [info] DB_WARN002 -> 159
[2025-10-12 12:18:09] [info] DB_WARN003 -> 161
[2025-10-12 12:18:09] [info] DRC_ERR9 -> 89
[2025-10-12 12:18:09] [info] DRC_ERR -> 67
[2025-10-12 12:18:09] [info] DRC_ERR5 -> 408
[2025-10-12 12:18:09] [info] DRC_ERR4 -> 241
[2025-10-12 12:18:09] [info] DRC_ERR1 -> 301
[2025-10-12 12:18:09] [info] -----
[2025-10-12 12:18:09] [info]                      Rule Violation Summary
[2025-10-12 12:18:09] [info] -----

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[2025-10-12 12:18:09] [info] | Rule ID          | Description                                     | Error Count |
[2025-10-12 12:18:09] [info] |-----|-----|-----|
[2025-10-12 12:18:09] [info] | m5.1          | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:18:09] [info] | li.5          | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:18:09] [info] | poly.1a       | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:18:09] [info] | poly.7        | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:18:09] [info] | licon.15      | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:18:09] [info] | m1.6          | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 12:18:09] [info] | m4.5          | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:18:09] [info] | m4.5ab        | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:18:09] [info] | psdm.1        | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:18:09] [info] | licon.5       | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:18:09] [info] | li.1          | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:18:09] [info] | li.1a         | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:18:09] [info] | m1.1          | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:18:09] [info] | licon.8a      | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:18:09] [info] | npc.1         | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:18:09] [info] |
[2025-10-12 12:18:09] [info] |-----|
[2025-10-12 12:18:09] [info] | Performance Metrics
[2025-10-12 12:18:09] [info] |-----|
[2025-10-12 12:18:09] [info] | Stage          | Time Taken |
[2025-10-12 12:18:09] [info] |-----|-----|
[2025-10-12 12:18:09] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:18:09] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:18:09] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:18:09] [info] |-----|
[2025-10-12 12:18:09] [info] | Summary
[2025-10-12 12:18:09] [info] |-----|
[2025-10-12 12:18:09] [info] | Total Errors:   | 1120
[2025-10-12 12:18:09] [info] | Total Warnings: | 321
[2025-10-12 12:18:09] [info] | DRC Status:     | FAILED

```

lvr_summary_103.rpt

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[2025-10-12 12:19:06] [info] =====
[2025-10-12 12:19:06] [info] | Design Rule Check Report
[2025-10-12 12:19:06] [info] |=====

[2025-10-12 12:19:06] [info] | Report Generated:   | 2025-10-12
[2025-10-12 12:19:06] [info] | Run Start Time:    | 12:18:14 PM
[2025-10-12 12:19:06] [info] | Run End Time:      | 12:19:06 PM
[2025-10-12 12:19:06] [info] | Time Taken:        | 0 minutes 51 seconds

[2025-10-12 12:19:06] [info] | Tool Version:      | LVR_2025.1
[2025-10-12 12:19:06] [info] | Host Name:         | ip-172-31-37-173
[2025-10-12 12:19:06] [info] | User Name:         | ubuntu
[2025-10-12 12:19:06] [info] | Cores Used:        | 8
[2025-10-12 12:19:06] [info] | Memory Utilized:   | 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:19:06] [info] |-----|
[2025-10-12 12:19:06] [info] | Top Cell Information
[2025-10-12 12:19:06] [info] |-----|
[2025-10-12 12:19:06] [info] | Layout Top Cell:   | TOP
[2025-10-12 12:19:06] [info] | Technology Node:    | SKY130HD (130nm)
[2025-10-12 12:19:06] [info] | Database Format:     | GDSII
[2025-10-12 12:19:06] [info] | Verification Mode:  | Full Check

```

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[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info]                                Type of Errors | Count
[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info] DB_WARN -> 1
[2025-10-12 12:19:06] [info] LRF_ERR001 -> 14
[2025-10-12 12:19:06] [info] DB_WARN002 -> 159
[2025-10-12 12:19:06] [info] DB_WARN003 -> 161
[2025-10-12 12:19:06] [info] DRC_ERR9 -> 89
[2025-10-12 12:19:06] [info] DRC_ERR -> 67
[2025-10-12 12:19:06] [info] DRC_ERR5 -> 408
[2025-10-12 12:19:06] [info] DRC_ERR4 -> 241
[2025-10-12 12:19:06] [info] DRC_ERR1 -> 301
[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info]                                Rule Violation Summary
[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:19:06] [info] |-----|-----|-----|
[2025-10-12 12:19:06] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:19:06] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:19:06] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:19:06] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:19:06] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:19:06] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:19:06] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:19:06] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:19:06] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:19:06] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:19:06] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:19:06] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:19:06] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:19:06] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:19:06] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info]                                Performance Metrics
[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info] | Stage          | Time Taken |
[2025-10-12 12:19:06] [info] |-----|-----|
[2025-10-12 12:19:06] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:19:06] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:19:06] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info]                                Summary
[2025-10-12 12:19:06] [info] -----
[2025-10-12 12:19:06] [info] Total Errors:      1120
[2025-10-12 12:19:06] [info] Total Warnings:    321
[2025-10-12 12:19:06] [info] DRC Status:        FAILED

```

lvr_summary_104.rpt

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[2025-10-12 12:20:03] [info] =====
[2025-10-12 12:20:03] [info]                                Design Rule Check Report
[2025-10-12 12:20:03] [info] =====

[2025-10-12 12:20:03] [info] Report Generated:   2025-10-12
[2025-10-12 12:20:03] [info] Run Start Time:     12:19:11 PM
[2025-10-12 12:20:03] [info] Run End Time:       12:20:03 PM
[2025-10-12 12:20:03] [info] Time Taken:         0 minutes 51 seconds

```

```

[2025-10-12 12:20:03] [info] Tool Version:      LVR_2025.1
[2025-10-12 12:20:03] [info] Host Name:       ip-172-31-37-173
[2025-10-12 12:20:03] [info] User Name:      ubuntu
[2025-10-12 12:20:03] [info] Cores Used:     8
[2025-10-12 12:20:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Top Cell Information
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Layout Top Cell:    TOP
[2025-10-12 12:20:03] [info] Technology Node:    SKY130HD (130nm)
[2025-10-12 12:20:03] [info] Database Format:     GDSII
[2025-10-12 12:20:03] [info] Verification Mode:  Full Check

[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Type of Errors | Count
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] DB_WARN -> 1
[2025-10-12 12:20:03] [info] LRF_ERR001 -> 14
[2025-10-12 12:20:03] [info] DB_WARN002 -> 159
[2025-10-12 12:20:03] [info] DB_WARN003 -> 161
[2025-10-12 12:20:03] [info] DRC_ERR9 -> 89
[2025-10-12 12:20:03] [info] DRC_ERR -> 67
[2025-10-12 12:20:03] [info] DRC_ERR5 -> 408
[2025-10-12 12:20:03] [info] DRC_ERR4 -> 241
[2025-10-12 12:20:03] [info] DRC_ERR1 -> 301
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Rule Violation Summary
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:20:03] [info] |-----|-----|-----|
[2025-10-12 12:20:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:20:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:20:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:20:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:20:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:20:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:20:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:20:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:20:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:20:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:20:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:20:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:20:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:20:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:20:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Performance Metrics
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] | Stage | Time Taken |
[2025-10-12 12:20:03] [info] |-----|-----|
[2025-10-12 12:20:03] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:20:03] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:20:03] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Summary
[2025-10-12 12:20:03] [info] -----
[2025-10-12 12:20:03] [info] Total Errors: 1120
[2025-10-12 12:20:03] [info] Total Warnings: 321

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[2025-10-12 12:20:03] [info] DRC Status: FAILED

lvr_summary_105.rpt

[2025-10-12 12:21:01] [info] =====
[2025-10-12 12:21:01] [info] Design Rule Check Report
[2025-10-12 12:21:01] [info] =====

[2025-10-12 12:21:01] [info] Report Generated: 2025-10-12
[2025-10-12 12:21:01] [info] Run Start Time: 12:20:09 PM
[2025-10-12 12:21:01] [info] Run End Time: 12:21:01 PM
[2025-10-12 12:21:01] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 12:21:01] [info] Tool Version: LVR_2025.1
[2025-10-12 12:21:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:21:01] [info] User Name: ubuntu
[2025-10-12 12:21:01] [info] Cores Used: 8
[2025-10-12 12:21:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] Top Cell Information
[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] Layout Top Cell: TOP
[2025-10-12 12:21:01] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:21:01] [info] Database Format: GDSII
[2025-10-12 12:21:01] [info] Verification Mode: Full Check

[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] Type of Errors | Count
[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] DB_WARN -> 1
[2025-10-12 12:21:01] [info] LRF_ERR001 -> 14
[2025-10-12 12:21:01] [info] DB_WARN002 -> 159
[2025-10-12 12:21:01] [info] DB_WARN003 -> 161
[2025-10-12 12:21:01] [info] DRC_ERR9 -> 89
[2025-10-12 12:21:01] [info] DRC_ERR -> 67
[2025-10-12 12:21:01] [info] DRC_ERR5 -> 408
[2025-10-12 12:21:01] [info] DRC_ERR4 -> 241
[2025-10-12 12:21:01] [info] DRC_ERR1 -> 301

[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] Rule Violation Summary
[2025-10-12 12:21:01] [info] -----

[2025-10-12 12:21:01] [info]	[Rule ID]	[Description]	[Error Count]
[2025-10-12 12:21:01] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 12:21:01] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 12:21:01] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 12:21:01] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 12:21:01] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 12:21:01] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 12:21:01] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 12:21:01] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 12:21:01] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 12:21:01] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 12:21:01] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 12:21:01] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" 15	
[2025-10-12 12:21:01] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 12:21:01] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 12:21:01] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

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[2025-10-12 12:21:01] [info]
[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info]                      Performance Metrics
[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] | Stage                | Time Taken |
[2025-10-12 12:21:01] [info] |-----|-----|
[2025-10-12 12:21:01] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:21:01] [info] | Rule Checking         | 0 minutes 41 seconds |
[2025-10-12 12:21:01] [info] | Report Generation     | 0 minutes 0 seconds |

[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info]                      Summary
[2025-10-12 12:21:01] [info] -----
[2025-10-12 12:21:01] [info] Total Errors:      1120
[2025-10-12 12:21:01] [info] Total Warnings:    321
[2025-10-12 12:21:01] [info] DRC Status:        FAILED

```

lvr_summary_106.rpt

```

[2025-10-12 12:21:59] [info] =====
[2025-10-12 12:21:59] [info]                      Design Rule Check Report
[2025-10-12 12:21:59] [info] =====

[2025-10-12 12:21:59] [info] Report Generated:    2025-10-12
[2025-10-12 12:21:59] [info] Run Start Time:      12:21:08 PM
[2025-10-12 12:21:59] [info] Run End Time:        12:21:59 PM
[2025-10-12 12:21:59] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 12:21:59] [info] Tool Version:        LVR_2025.1
[2025-10-12 12:21:59] [info] Host Name:           ip-172-31-37-173
[2025-10-12 12:21:59] [info] User Name:           ubuntu
[2025-10-12 12:21:59] [info] Cores Used:          8
[2025-10-12 12:21:59] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info]                      Top Cell Information
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] Layout Top Cell:     TOP
[2025-10-12 12:21:59] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:21:59] [info] Database Format:       GDSII
[2025-10-12 12:21:59] [info] Verification Mode:    Full Check

[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info]                      Type of Errors | Count
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] DB_WARN -> 1
[2025-10-12 12:21:59] [info] LRF_ERR001 -> 14
[2025-10-12 12:21:59] [info] DB_WARN002 -> 159
[2025-10-12 12:21:59] [info] DB_WARN003 -> 161
[2025-10-12 12:21:59] [info] DRC_ERR9 -> 89
[2025-10-12 12:21:59] [info] DRC_ERR -> 67
[2025-10-12 12:21:59] [info] DRC_ERR5 -> 408
[2025-10-12 12:21:59] [info] DRC_ERR4 -> 241
[2025-10-12 12:21:59] [info] DRC_ERR1 -> 301
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info]                      Rule Violation Summary
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] | Rule ID      | Description                | Error Count |
[2025-10-12 12:21:59] [info] |-----|-----|-----|

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[2025-10-12 12:21:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:21:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:21:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:21:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:21:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:21:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:21:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:21:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:21:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:21:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:21:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:21:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:21:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:21:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:21:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:21:59] [info]
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] Performance Metrics
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] | Stage | Time Taken |
[2025-10-12 12:21:59] [info] |-----|-----|
[2025-10-12 12:21:59] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:21:59] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:21:59] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:21:59] [info]
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] Summary
[2025-10-12 12:21:59] [info] -----
[2025-10-12 12:21:59] [info] Total Errors: 1120
[2025-10-12 12:21:59] [info] Total Warnings: 321
[2025-10-12 12:21:59] [info] DRC Status: FAILED
```

lvr_summary_107.rpt

```
[2025-10-12 12:22:58] [info] =====
[2025-10-12 12:22:58] [info] Design Rule Check Report
[2025-10-12 12:22:58] [info] =====

[2025-10-12 12:22:58] [info] Report Generated: 2025-10-12
[2025-10-12 12:22:58] [info] Run Start Time: 12:22:07 PM
[2025-10-12 12:22:58] [info] Run End Time: 12:22:58 PM
[2025-10-12 12:22:58] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:22:58] [info] Tool Version: LVR_2025.1
[2025-10-12 12:22:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:22:58] [info] User Name: ubuntu
[2025-10-12 12:22:58] [info] Cores Used: 8
[2025-10-12 12:22:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] Top Cell Information
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] Layout Top Cell: TOP
[2025-10-12 12:22:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:22:58] [info] Database Format: GDSII
[2025-10-12 12:22:58] [info] Verification Mode: Full Check

[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] Type of Errors | Count
```

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[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] DB_WARN -> 1
[2025-10-12 12:22:58] [info] LRF_ERR001 -> 14
[2025-10-12 12:22:58] [info] DB_WARN002 -> 159
[2025-10-12 12:22:58] [info] DB_WARN003 -> 161
[2025-10-12 12:22:58] [info] DRC_ERR9 -> 89
[2025-10-12 12:22:58] [info] DRC_ERR -> 67
[2025-10-12 12:22:58] [info] DRC_ERR5 -> 408
[2025-10-12 12:22:58] [info] DRC_ERR4 -> 241
[2025-10-12 12:22:58] [info] DRC_ERR1 -> 301
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info]
[2025-10-12 12:22:58] [info] Rule Violation Summary
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:22:58] [info] |-----|-----|-----|
[2025-10-12 12:22:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:22:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:22:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:22:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:22:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:22:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:22:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:22:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:22:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:22:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:22:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:22:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:22:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:22:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:22:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:22:58] [info]
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] Performance Metrics
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] | Stage | Time Taken |
[2025-10-12 12:22:58] [info] |-----|-----|
[2025-10-12 12:22:58] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:22:58] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:22:58] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:22:58] [info]
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] Summary
[2025-10-12 12:22:58] [info] -----
[2025-10-12 12:22:58] [info] Total Errors: 1120
[2025-10-12 12:22:58] [info] Total Warnings: 321
[2025-10-12 12:22:58] [info] DRC Status: FAILED

```

lvr_summary_108.rpt

```

[2025-10-12 12:23:56] [info] =====
[2025-10-12 12:23:56] [info] Design Rule Check Report
[2025-10-12 12:23:56] [info] =====
[2025-10-12 12:23:56] [info] Report Generated: 2025-10-12
[2025-10-12 12:23:56] [info] Run Start Time: 12:23:05 PM
[2025-10-12 12:23:56] [info] Run End Time: 12:23:56 PM
[2025-10-12 12:23:56] [info] Time Taken: 0 minutes 50 seconds
[2025-10-12 12:23:56] [info]
[2025-10-12 12:23:56] [info] Tool Version: LVR_2025.1

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[2025-10-12 12:23:56] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:23:56] [info] User Name:       ubuntu
[2025-10-12 12:23:56] [info] Cores Used:        8
[2025-10-12 12:23:56] [info] Memory Utilized:   670.22 MB / 61.77812957763672 GB

[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info]                      Top Cell Information
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info] Layout Top Cell:    TOP
[2025-10-12 12:23:56] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:23:56] [info] Database Format:      GDSII
[2025-10-12 12:23:56] [info] Verification Mode:   Full Check

[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info]                      Type of Errors | Count
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info] DB_WARN -> 1
[2025-10-12 12:23:56] [info] LRF_ERR001 -> 14
[2025-10-12 12:23:56] [info] DB_WARN002 -> 159
[2025-10-12 12:23:56] [info] DB_WARN003 -> 161
[2025-10-12 12:23:56] [info] DRC_ERR9 -> 89
[2025-10-12 12:23:56] [info] DRC_ERR -> 67
[2025-10-12 12:23:56] [info] DRC_ERR5 -> 408
[2025-10-12 12:23:56] [info] DRC_ERR4 -> 241
[2025-10-12 12:23:56] [info] DRC_ERR1 -> 301
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info]                      Rule Violation Summary
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:23:56] [info] |-----|-----|-----|
[2025-10-12 12:23:56] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:23:56] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:23:56] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:23:56] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:23:56] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:23:56] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 12:23:56] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:23:56] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:23:56] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:23:56] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:23:56] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:23:56] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:23:56] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:23:56] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:23:56] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info]                      Performance Metrics
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info] | Stage          | Time Taken |
[2025-10-12 12:23:56] [info] |-----|-----|
[2025-10-12 12:23:56] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:23:56] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 12:23:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info]                      Summary
[2025-10-12 12:23:56] [info] -----
[2025-10-12 12:23:56] [info] Total Errors:    1120
[2025-10-12 12:23:56] [info] Total Warnings:   321
[2025-10-12 12:23:56] [info] DRC Status:      FAILED

```

lvr_summary_109.rpt

```
[2025-10-12 12:24:54] [info] =====
[2025-10-12 12:24:54] [info] Design Rule Check Report
[2025-10-12 12:24:54] [info] =====

[2025-10-12 12:24:54] [info] Report Generated: 2025-10-12
[2025-10-12 12:24:54] [info] Run Start Time: 12:24:03 PM
[2025-10-12 12:24:54] [info] Run End Time: 12:24:54 PM
[2025-10-12 12:24:54] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:24:54] [info] Tool Version: LVR_2025.1
[2025-10-12 12:24:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:24:54] [info] User Name: ubuntu
[2025-10-12 12:24:54] [info] Cores Used: 8
[2025-10-12 12:24:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] Top Cell Information
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] Layout Top Cell: TOP
[2025-10-12 12:24:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:24:54] [info] Database Format: GDSII
[2025-10-12 12:24:54] [info] Verification Mode: Full Check

[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] Type of Errors | Count
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] DB_WARN -> 1
[2025-10-12 12:24:54] [info] LRF_ERR001 -> 14
[2025-10-12 12:24:54] [info] DB_WARN002 -> 159
[2025-10-12 12:24:54] [info] DB_WARN003 -> 161
[2025-10-12 12:24:54] [info] DRC_ERR9 -> 89
[2025-10-12 12:24:54] [info] DRC_ERR -> 67
[2025-10-12 12:24:54] [info] DRC_ERR5 -> 408
[2025-10-12 12:24:54] [info] DRC_ERR4 -> 241
[2025-10-12 12:24:54] [info] DRC_ERR1 -> 301
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] Rule Violation Summary
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:24:54] [info] |-----|-----|-----|
[2025-10-12 12:24:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:24:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:24:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:24:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:24:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:24:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:24:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:24:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:24:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:24:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:24:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:24:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:24:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:24:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:24:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:24:54] [info]
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] Performance Metrics
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:24:54] [info] |-----|-----|
[2025-10-12 12:24:54] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 12:24:54] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 12:24:54] [info] | Report Generation    | 0 minutes 0 seconds  |

[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info]                Summary
[2025-10-12 12:24:54] [info] -----
[2025-10-12 12:24:54] [info] Total Errors:      1120
[2025-10-12 12:24:54] [info] Total Warnings:      321
[2025-10-12 12:24:54] [info] DRC Status:         FAILED
```

lvr_summary_11.rpt

```
[2025-10-12 10:51:51] [info] =====
[2025-10-12 10:51:51] [info]                Design Rule Check Report
[2025-10-12 10:51:51] [info] =====

[2025-10-12 10:51:51] [info] Report Generated:    2025-10-12
[2025-10-12 10:51:51] [info] Run Start Time:      10:51:00 AM
[2025-10-12 10:51:51] [info] Run End Time:        10:51:51 AM
[2025-10-12 10:51:51] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 10:51:51] [info] Tool Version:        LVR_2025.1
[2025-10-12 10:51:51] [info] Host Name:           ip-172-31-37-173
[2025-10-12 10:51:51] [info] User Name:           ubuntu
[2025-10-12 10:51:51] [info] Cores Used:          8
[2025-10-12 10:51:51] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info]                Top Cell Information
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] Layout Top Cell:     TOP
[2025-10-12 10:51:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:51:51] [info] Database Format:       GDSII
[2025-10-12 10:51:51] [info] Verification Mode:    Full Check

[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info]                Type of Errors | Count
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] DB_WARN -> 1
[2025-10-12 10:51:51] [info] LRF_ERR001 -> 14
[2025-10-12 10:51:51] [info] DB_WARN002 -> 159
[2025-10-12 10:51:51] [info] DB_WARN003 -> 161
[2025-10-12 10:51:51] [info] DRC_ERR9 -> 89
[2025-10-12 10:51:51] [info] DRC_ERR -> 67
[2025-10-12 10:51:51] [info] DRC_ERR5 -> 408
[2025-10-12 10:51:51] [info] DRC_ERR4 -> 241
[2025-10-12 10:51:51] [info] DRC_ERR1 -> 301
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info]                Rule Violation Summary
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 10:51:51] [info] |-----|-----|-----|
[2025-10-12 10:51:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:51:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:51:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:51:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:51:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 10:51:51] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:51:51] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:51:51] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:51:51] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:51:51] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:51:51] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:51:51] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:51:51] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:51:51] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:51:51] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:51:51] [info]
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] Performance Metrics
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] | Stage | Time Taken |
[2025-10-12 10:51:51] [info] |-----|-----|
[2025-10-12 10:51:51] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:51:51] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 10:51:51] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] Summary
[2025-10-12 10:51:51] [info] -----
[2025-10-12 10:51:51] [info] Total Errors: 1120
[2025-10-12 10:51:51] [info] Total Warnings: 321
[2025-10-12 10:51:51] [info] DRC Status: FAILED

```

lvr_summary_110.rpt

```

[2025-10-12 12:25:53] [info] =====
[2025-10-12 12:25:53] [info] Design Rule Check Report
[2025-10-12 12:25:53] [info] =====

[2025-10-12 12:25:53] [info] Report Generated: 2025-10-12
[2025-10-12 12:25:53] [info] Run Start Time: 12:25:03 PM
[2025-10-12 12:25:53] [info] Run End Time: 12:25:53 PM
[2025-10-12 12:25:53] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:25:53] [info] Tool Version: LVR_2025.1
[2025-10-12 12:25:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:25:53] [info] User Name: ubuntu
[2025-10-12 12:25:53] [info] Cores Used: 8
[2025-10-12 12:25:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Top Cell Information
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Layout Top Cell: TOP
[2025-10-12 12:25:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:25:53] [info] Database Format: GDSII
[2025-10-12 12:25:53] [info] Verification Mode: Full Check

[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Type of Errors | Count
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] DB_WARN -> 1
[2025-10-12 12:25:53] [info] LRF_ERR001 -> 14
[2025-10-12 12:25:53] [info] DB_WARN002 -> 159
[2025-10-12 12:25:53] [info] DB_WARN003 -> 161

```

```

[2025-10-12 12:25:53] [info] DRC_ERR9 -> 89
[2025-10-12 12:25:53] [info] DRC_ERR -> 67
[2025-10-12 12:25:53] [info] DRC_ERR5 -> 408
[2025-10-12 12:25:53] [info] DRC_ERR4 -> 241
[2025-10-12 12:25:53] [info] DRC_ERR1 -> 301
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Rule Violation Summary
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:25:53] [info] |-----|-----|-----|
[2025-10-12 12:25:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:25:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:25:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:25:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:25:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:25:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:25:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:25:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:25:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:25:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:25:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:25:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:25:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:25:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:25:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Performance Metrics
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] | Stage | Time Taken |
[2025-10-12 12:25:53] [info] |-----|-----|
[2025-10-12 12:25:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:25:53] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:25:53] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Summary
[2025-10-12 12:25:53] [info] -----
[2025-10-12 12:25:53] [info] Total Errors: 1120
[2025-10-12 12:25:53] [info] Total Warnings: 321
[2025-10-12 12:25:53] [info] DRC Status: FAILED

```

lvr_summary_111.rpt

```

[2025-10-12 12:26:51] [info] =====
[2025-10-12 12:26:51] [info] Design Rule Check Report
[2025-10-12 12:26:51] [info] =====

[2025-10-12 12:26:51] [info] Report Generated: 2025-10-12
[2025-10-12 12:26:51] [info] Run Start Time: 12:26:00 PM
[2025-10-12 12:26:51] [info] Run End Time: 12:26:51 PM
[2025-10-12 12:26:51] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:26:51] [info] Tool Version: LVR_2025.1
[2025-10-12 12:26:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:26:51] [info] User Name: ubuntu
[2025-10-12 12:26:51] [info] Cores Used: 8
[2025-10-12 12:26:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] Top Cell Information
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] Layout Top Cell:      TOP
[2025-10-12 12:26:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:26:51] [info] Database Format:      GDSII
[2025-10-12 12:26:51] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] Type of Errors | Count
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] DB_WARN -> 1
[2025-10-12 12:26:51] [info] LRF_ERR001 -> 14
[2025-10-12 12:26:51] [info] DB_WARN002 -> 159
[2025-10-12 12:26:51] [info] DB_WARN003 -> 161
[2025-10-12 12:26:51] [info] DRC_ERR9 -> 89
[2025-10-12 12:26:51] [info] DRC_ERR -> 67
[2025-10-12 12:26:51] [info] DRC_ERR5 -> 408
[2025-10-12 12:26:51] [info] DRC_ERR4 -> 241
[2025-10-12 12:26:51] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] Rule Violation Summary
```

```
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:26:51] [info] |-----|-----|-----|
[2025-10-12 12:26:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:26:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:26:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:26:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:26:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:26:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:26:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:26:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:26:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:26:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:26:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:26:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:26:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:26:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:26:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:26:51] [info] |-----|-----|-----|
```

```
[2025-10-12 12:26:51] [info] Performance Metrics
```

```
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] | Stage          | Time Taken |
[2025-10-12 12:26:51] [info] |-----|-----|
[2025-10-12 12:26:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:26:51] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 12:26:51] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] Summary
[2025-10-12 12:26:51] [info] -----
[2025-10-12 12:26:51] [info] Total Errors:      1120
[2025-10-12 12:26:51] [info] Total Warnings:    321
[2025-10-12 12:26:51] [info] DRC Status:        FAILED
```

lvr_summary_112.rpt

```
[2025-10-12 12:27:51] [info] =====
[2025-10-12 12:27:51] [info] Design Rule Check Report
[2025-10-12 12:27:51] [info] =====

[2025-10-12 12:27:51] [info] Report Generated: 2025-10-12
[2025-10-12 12:27:51] [info] Run Start Time: 12:26:59 PM
[2025-10-12 12:27:51] [info] Run End Time: 12:27:51 PM
[2025-10-12 12:27:51] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:27:51] [info] Tool Version: LVR_2025.1
[2025-10-12 12:27:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:27:51] [info] User Name: ubuntu
[2025-10-12 12:27:51] [info] Cores Used: 8
[2025-10-12 12:27:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Top Cell Information
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Layout Top Cell: TOP
[2025-10-12 12:27:51] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:27:51] [info] Database Format: GDSII
[2025-10-12 12:27:51] [info] Verification Mode: Full Check

[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Type of Errors | Count
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] DB_WARN -> 1
[2025-10-12 12:27:51] [info] LRF_ERR001 -> 14
[2025-10-12 12:27:51] [info] DB_WARN002 -> 159
[2025-10-12 12:27:51] [info] DB_WARN003 -> 161
[2025-10-12 12:27:51] [info] DRC_ERR9 -> 89
[2025-10-12 12:27:51] [info] DRC_ERR -> 67
[2025-10-12 12:27:51] [info] DRC_ERR5 -> 408
[2025-10-12 12:27:51] [info] DRC_ERR4 -> 241
[2025-10-12 12:27:51] [info] DRC_ERR1 -> 301
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Rule Violation Summary
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:27:51] [info] |-----|-----|-----|
[2025-10-12 12:27:51] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:27:51] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:27:51] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:27:51] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:27:51] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:27:51] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:27:51] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:27:51] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:27:51] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:27:51] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:27:51] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:27:51] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:27:51] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:27:51] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:27:51] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:27:51] [info]
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Performance Metrics
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:27:51] [info] |-----|-----|
[2025-10-12 12:27:51] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:27:51] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 12:27:51] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Summary
[2025-10-12 12:27:51] [info] -----
[2025-10-12 12:27:51] [info] Total Errors:   1120
[2025-10-12 12:27:51] [info] Total Warnings:    321
[2025-10-12 12:27:51] [info] DRC Status:      FAILED
```

lvr_summary_113.rpt

```
[2025-10-12 12:28:48] [info] =====
[2025-10-12 12:28:48] [info] Design Rule Check Report
[2025-10-12 12:28:48] [info] =====

[2025-10-12 12:28:48] [info] Report Generated:   2025-10-12
[2025-10-12 12:28:48] [info] Run Start Time:     12:27:56 PM
[2025-10-12 12:28:48] [info] Run End Time:       12:28:48 PM
[2025-10-12 12:28:48] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 12:28:48] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:28:48] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:28:48] [info] User Name:          ubuntu
[2025-10-12 12:28:48] [info] Cores Used:         8
[2025-10-12 12:28:48] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Top Cell Information
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Layout Top Cell:    TOP
[2025-10-12 12:28:48] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:28:48] [info] Database Format:      GDSII
[2025-10-12 12:28:48] [info] Verification Mode:   Full Check

[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Type of Errors | Count
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] DB_WARN -> 1
[2025-10-12 12:28:48] [info] LRF_ERR001 -> 14
[2025-10-12 12:28:48] [info] DB_WARN002 -> 159
[2025-10-12 12:28:48] [info] DB_WARN003 -> 161
[2025-10-12 12:28:48] [info] DRC_ERR9 -> 89
[2025-10-12 12:28:48] [info] DRC_ERR -> 67
[2025-10-12 12:28:48] [info] DRC_ERR5 -> 408
[2025-10-12 12:28:48] [info] DRC_ERR4 -> 241
[2025-10-12 12:28:48] [info] DRC_ERR1 -> 301
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Rule Violation Summary
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:28:48] [info] |-----|-----|-----|
[2025-10-12 12:28:48] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:28:48] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:28:48] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:28:48] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:28:48] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```

[2025-10-12 12:28:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 12:28:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:28:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:28:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:28:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:28:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:28:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:28:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:28:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:28:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:28:48] [info]
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Performance Metrics
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] | Stage | Time Taken |
[2025-10-12 12:28:48] [info] |-----|-----|
[2025-10-12 12:28:48] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:28:48] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:28:48] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Summary
[2025-10-12 12:28:48] [info] -----
[2025-10-12 12:28:48] [info] Total Errors: 1120
[2025-10-12 12:28:48] [info] Total Warnings: 321
[2025-10-12 12:28:48] [info] DRC Status: FAILED

```

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```

[2025-10-12 12:29:47] [info] =====
[2025-10-12 12:29:47] [info] Design Rule Check Report
[2025-10-12 12:29:47] [info] =====

[2025-10-12 12:29:47] [info] Report Generated: 2025-10-12
[2025-10-12 12:29:47] [info] Run Start Time: 12:28:55 PM
[2025-10-12 12:29:47] [info] Run End Time: 12:29:47 PM
[2025-10-12 12:29:47] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 12:29:47] [info] Tool Version: LVR_2025.1
[2025-10-12 12:29:47] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:29:47] [info] User Name: ubuntu
[2025-10-12 12:29:47] [info] Cores Used: 8
[2025-10-12 12:29:47] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Top Cell Information
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Layout Top Cell: TOP
[2025-10-12 12:29:47] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:29:47] [info] Database Format: GDSII
[2025-10-12 12:29:47] [info] Verification Mode: Full Check

[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Type of Errors | Count
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] DB_WARN -> 1
[2025-10-12 12:29:47] [info] LRF_ERR001 -> 14
[2025-10-12 12:29:47] [info] DB_WARN002 -> 159
[2025-10-12 12:29:47] [info] DB_WARN003 -> 161

```

```

[2025-10-12 12:29:47] [info] DRC_ERR9 -> 89
[2025-10-12 12:29:47] [info] DRC_ERR -> 67
[2025-10-12 12:29:47] [info] DRC_ERR5 -> 408
[2025-10-12 12:29:47] [info] DRC_ERR4 -> 241
[2025-10-12 12:29:47] [info] DRC_ERR1 -> 301
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Rule Violation Summary
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:29:47] [info] |-----|-----|-----|
[2025-10-12 12:29:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:29:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:29:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:29:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:29:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:29:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:29:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:29:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:29:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:29:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:29:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:29:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:29:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:29:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:29:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Performance Metrics
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] | Stage | Time Taken |
[2025-10-12 12:29:47] [info] |-----|-----|
[2025-10-12 12:29:47] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:29:47] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:29:47] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Summary
[2025-10-12 12:29:47] [info] -----
[2025-10-12 12:29:47] [info] Total Errors: 1120
[2025-10-12 12:29:47] [info] Total Warnings: 321
[2025-10-12 12:29:47] [info] DRC Status: FAILED

```

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```

[2025-10-12 12:30:45] [info] =====
[2025-10-12 12:30:45] [info] Design Rule Check Report
[2025-10-12 12:30:45] [info] =====

[2025-10-12 12:30:45] [info] Report Generated: 2025-10-12
[2025-10-12 12:30:45] [info] Run Start Time: 12:29:54 PM
[2025-10-12 12:30:45] [info] Run End Time: 12:30:45 PM
[2025-10-12 12:30:45] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:30:45] [info] Tool Version: LVR_2025.1
[2025-10-12 12:30:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:30:45] [info] User Name: ubuntu
[2025-10-12 12:30:45] [info] Cores Used: 8
[2025-10-12 12:30:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Top Cell Information
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Layout Top Cell:      TOP
[2025-10-12 12:30:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:30:45] [info] Database Format:      GDSII
[2025-10-12 12:30:45] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Type of Errors | Count
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] DB_WARN -> 1
[2025-10-12 12:30:45] [info] LRF_ERR001 -> 14
[2025-10-12 12:30:45] [info] DB_WARN002 -> 159
[2025-10-12 12:30:45] [info] DB_WARN003 -> 161
[2025-10-12 12:30:45] [info] DRC_ERR9 -> 89
[2025-10-12 12:30:45] [info] DRC_ERR -> 67
[2025-10-12 12:30:45] [info] DRC_ERR5 -> 408
[2025-10-12 12:30:45] [info] DRC_ERR4 -> 241
[2025-10-12 12:30:45] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Rule Violation Summary
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:30:45] [info] |-----|-----|-----|
[2025-10-12 12:30:45] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:30:45] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:30:45] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:30:45] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:30:45] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:30:45] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:30:45] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:30:45] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:30:45] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:30:45] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:30:45] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:30:45] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:30:45] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:30:45] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:30:45] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:30:45] [info] |
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Performance Metrics
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] | Stage          | Time Taken |
[2025-10-12 12:30:45] [info] |-----|-----|
[2025-10-12 12:30:45] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:30:45] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 12:30:45] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Summary
```

```
[2025-10-12 12:30:45] [info] -----
[2025-10-12 12:30:45] [info] Total Errors:      1120
[2025-10-12 12:30:45] [info] Total Warnings:    321
[2025-10-12 12:30:45] [info] DRC Status:        FAILED
```

lvr_summary_116.rpt

```
[2025-10-12 12:31:45] [info] =====
[2025-10-12 12:31:45] [info]                      Design Rule Check Report
[2025-10-12 12:31:45] [info] =====

[2025-10-12 12:31:45] [info] Report Generated:      2025-10-12
[2025-10-12 12:31:45] [info] Run Start Time:       12:30:53 PM
[2025-10-12 12:31:45] [info] Run End Time:         12:31:45 PM
[2025-10-12 12:31:45] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 12:31:45] [info] Tool Version:          LVR_2025.1
[2025-10-12 12:31:45] [info] Host Name:             ip-172-31-37-173
[2025-10-12 12:31:45] [info] User Name:             ubuntu
[2025-10-12 12:31:45] [info] Cores Used:            8
[2025-10-12 12:31:45] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info]                      Top Cell Information
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info] Layout Top Cell:      TOP
[2025-10-12 12:31:45] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 12:31:45] [info] Database Format:        GDSII
[2025-10-12 12:31:45] [info] Verification Mode:     Full Check

[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info]                      Type of Errors | Count
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info] DB_WARN -> 1
[2025-10-12 12:31:45] [info] LRF_ERR001 -> 14
[2025-10-12 12:31:45] [info] DB_WARN002 -> 159
[2025-10-12 12:31:45] [info] DB_WARN003 -> 161
[2025-10-12 12:31:45] [info] DRC_ERR9 -> 89
[2025-10-12 12:31:45] [info] DRC_ERR -> 67
[2025-10-12 12:31:45] [info] DRC_ERR5 -> 408
[2025-10-12 12:31:45] [info] DRC_ERR4 -> 241
[2025-10-12 12:31:45] [info] DRC_ERR1 -> 301
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info]                      Rule Violation Summary
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:31:45] [info] |-----|-----|-----|
[2025-10-12 12:31:45] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:31:45] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:31:45] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:31:45] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:31:45] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:31:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:31:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:31:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:31:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:31:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:31:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:31:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:31:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:31:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:31:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:31:45] [info]
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info]                      Performance Metrics
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:31:45] [info] |-----|-----|
[2025-10-12 12:31:45] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:31:45] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 12:31:45] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info] Summary
[2025-10-12 12:31:45] [info] -----
[2025-10-12 12:31:45] [info] Total Errors: 1120
[2025-10-12 12:31:45] [info] Total Warnings: 321
[2025-10-12 12:31:45] [info] DRC Status: FAILED
```

lvr_summary_117.rpt

```
[2025-10-12 12:32:41] [info] =====
[2025-10-12 12:32:41] [info] Design Rule Check Report
[2025-10-12 12:32:41] [info] =====

[2025-10-12 12:32:41] [info] Report Generated: 2025-10-12
[2025-10-12 12:32:41] [info] Run Start Time: 12:31:50 PM
[2025-10-12 12:32:41] [info] Run End Time: 12:32:41 PM
[2025-10-12 12:32:41] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:32:41] [info] Tool Version: LVR_2025.1
[2025-10-12 12:32:41] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:32:41] [info] User Name: ubuntu
[2025-10-12 12:32:41] [info] Cores Used: 8
[2025-10-12 12:32:41] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Top Cell Information
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Layout Top Cell: TOP
[2025-10-12 12:32:41] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:32:41] [info] Database Format: GDSII
[2025-10-12 12:32:41] [info] Verification Mode: Full Check

[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Type of Errors | Count
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] DB_WARN -> 1
[2025-10-12 12:32:41] [info] LRF_ERR001 -> 14
[2025-10-12 12:32:41] [info] DB_WARN002 -> 159
[2025-10-12 12:32:41] [info] DB_WARN003 -> 161
[2025-10-12 12:32:41] [info] DRC_ERR9 -> 89
[2025-10-12 12:32:41] [info] DRC_ERR -> 67
[2025-10-12 12:32:41] [info] DRC_ERR5 -> 408
[2025-10-12 12:32:41] [info] DRC_ERR4 -> 241
[2025-10-12 12:32:41] [info] DRC_ERR1 -> 301
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Rule Violation Summary
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:32:41] [info] |-----|-----|-----|
[2025-10-12 12:32:41] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:32:41] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:32:41] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:32:41] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:32:41] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 12:32:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:32:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:32:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:32:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:32:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:32:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:32:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:32:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:32:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:32:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:32:41] [info]
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Performance Metrics
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] | Stage | Time Taken |
[2025-10-12 12:32:41] [info] |-----|-----|
[2025-10-12 12:32:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:32:41] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:32:41] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Summary
[2025-10-12 12:32:41] [info] -----
[2025-10-12 12:32:41] [info] Total Errors: 1120
[2025-10-12 12:32:41] [info] Total Warnings: 321
[2025-10-12 12:32:41] [info] DRC Status: FAILED

```

lvr_summary_118.rpt

```

[2025-10-12 12:33:39] [info] =====
[2025-10-12 12:33:39] [info] Design Rule Check Report
[2025-10-12 12:33:39] [info] =====

[2025-10-12 12:33:39] [info] Report Generated: 2025-10-12
[2025-10-12 12:33:39] [info] Run Start Time: 12:32:48 PM
[2025-10-12 12:33:39] [info] Run End Time: 12:33:39 PM
[2025-10-12 12:33:39] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:33:39] [info] Tool Version: LVR_2025.1
[2025-10-12 12:33:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:33:39] [info] User Name: ubuntu
[2025-10-12 12:33:39] [info] Cores Used: 8
[2025-10-12 12:33:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Top Cell Information
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Layout Top Cell: TOP
[2025-10-12 12:33:39] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:33:39] [info] Database Format: GDSII
[2025-10-12 12:33:39] [info] Verification Mode: Full Check

[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Type of Errors | Count
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] DB_WARN -> 1
[2025-10-12 12:33:39] [info] LRF_ERR001 -> 14
[2025-10-12 12:33:39] [info] DB_WARN002 -> 159
[2025-10-12 12:33:39] [info] DB_WARN003 -> 161

```

```

[2025-10-12 12:33:39] [info] DRC_ERR9 -> 89
[2025-10-12 12:33:39] [info] DRC_ERR -> 67
[2025-10-12 12:33:39] [info] DRC_ERR5 -> 408
[2025-10-12 12:33:39] [info] DRC_ERR4 -> 241
[2025-10-12 12:33:39] [info] DRC_ERR1 -> 301
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Rule Violation Summary
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:33:39] [info] |-----|-----|-----|
[2025-10-12 12:33:39] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:33:39] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:33:39] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:33:39] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:33:39] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:33:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:33:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:33:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:33:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:33:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:33:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:33:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:33:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:33:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:33:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Performance Metrics
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] | Stage | Time Taken |
[2025-10-12 12:33:39] [info] |-----|-----|
[2025-10-12 12:33:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:33:39] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:33:39] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Summary
[2025-10-12 12:33:39] [info] -----
[2025-10-12 12:33:39] [info] Total Errors: 1120
[2025-10-12 12:33:39] [info] Total Warnings: 321
[2025-10-12 12:33:39] [info] DRC Status: FAILED

```

lvr_summary_119.rpt

```

[2025-10-12 12:34:37] [info] =====
[2025-10-12 12:34:37] [info] Design Rule Check Report
[2025-10-12 12:34:37] [info] =====

[2025-10-12 12:34:37] [info] Report Generated: 2025-10-12
[2025-10-12 12:34:37] [info] Run Start Time: 12:33:45 PM
[2025-10-12 12:34:37] [info] Run End Time: 12:34:37 PM
[2025-10-12 12:34:37] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:34:37] [info] Tool Version: LVR_2025.1
[2025-10-12 12:34:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:34:37] [info] User Name: ubuntu
[2025-10-12 12:34:37] [info] Cores Used: 8
[2025-10-12 12:34:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Top Cell Information
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Layout Top Cell:      TOP
[2025-10-12 12:34:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:34:37] [info] Database Format:      GDSII
[2025-10-12 12:34:37] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Type of Errors | Count
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] DB_WARN -> 1
[2025-10-12 12:34:37] [info] LRF_ERR001 -> 14
[2025-10-12 12:34:37] [info] DB_WARN002 -> 159
[2025-10-12 12:34:37] [info] DB_WARN003 -> 161
[2025-10-12 12:34:37] [info] DRC_ERR9 -> 89
[2025-10-12 12:34:37] [info] DRC_ERR -> 67
[2025-10-12 12:34:37] [info] DRC_ERR5 -> 408
[2025-10-12 12:34:37] [info] DRC_ERR4 -> 241
[2025-10-12 12:34:37] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Rule Violation Summary
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:34:37] [info] |-----|-----|-----|
[2025-10-12 12:34:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:34:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:34:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:34:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:34:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:34:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:34:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:34:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:34:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:34:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:34:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:34:37] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:34:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:34:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:34:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:34:37] [info] |
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Performance Metrics
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] | Stage          | Time Taken |
[2025-10-12 12:34:37] [info] |-----|-----|
[2025-10-12 12:34:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:34:37] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 12:34:37] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Summary
```

```
[2025-10-12 12:34:37] [info] -----
[2025-10-12 12:34:37] [info] Total Errors:      1120
[2025-10-12 12:34:37] [info] Total Warnings:    321
[2025-10-12 12:34:37] [info] DRC Status:        FAILED
```


lvr_summary_12.rpt

```
[2025-10-12 10:52:47] [info] =====
[2025-10-12 10:52:47] [info]                      Design Rule Check Report
[2025-10-12 10:52:47] [info] =====

[2025-10-12 10:52:47] [info] Report Generated:    2025-10-12
[2025-10-12 10:52:47] [info] Run Start Time:      10:51:57 AM
[2025-10-12 10:52:47] [info] Run End Time:        10:52:47 AM
[2025-10-12 10:52:47] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 10:52:47] [info] Tool Version:        LVR_2025.1
[2025-10-12 10:52:47] [info] Host Name:           ip-172-31-37-173
[2025-10-12 10:52:47] [info] User Name:           ubuntu
[2025-10-12 10:52:47] [info] Cores Used:          8
[2025-10-12 10:52:47] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info]                      Top Cell Information
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info] Layout Top Cell:     TOP
[2025-10-12 10:52:47] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:52:47] [info] Database Format:       GDSII
[2025-10-12 10:52:47] [info] Verification Mode:    Full Check

[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info]                      Type of Errors | Count
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info] DB_WARN -> 1
[2025-10-12 10:52:47] [info] LRF_ERR001 -> 14
[2025-10-12 10:52:47] [info] DB_WARN002 -> 159
[2025-10-12 10:52:47] [info] DB_WARN003 -> 161
[2025-10-12 10:52:47] [info] DRC_ERR9 -> 89
[2025-10-12 10:52:47] [info] DRC_ERR -> 67
[2025-10-12 10:52:47] [info] DRC_ERR5 -> 408
[2025-10-12 10:52:47] [info] DRC_ERR4 -> 241
[2025-10-12 10:52:47] [info] DRC_ERR1 -> 301
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info]                      Rule Violation Summary
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:52:47] [info] |-----|-----|-----|
[2025-10-12 10:52:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:52:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:52:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:52:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:52:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:52:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 10:52:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:52:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:52:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:52:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:52:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:52:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:52:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:52:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:52:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:52:47] [info]
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info]                      Performance Metrics
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info] | Stage | Time Taken |
```

```
[2025-10-12 10:52:47] [info] |-----|-----|
[2025-10-12 10:52:47] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 10:52:47] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 10:52:47] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info] Summary
[2025-10-12 10:52:47] [info] -----
[2025-10-12 10:52:47] [info] Total Errors: 1120
[2025-10-12 10:52:47] [info] Total Warnings: 321
[2025-10-12 10:52:47] [info] DRC Status: FAILED
```

lvr_summary_120.rpt

```
[2025-10-12 12:35:33] [info] =====
[2025-10-12 12:35:33] [info] Design Rule Check Report
[2025-10-12 12:35:33] [info] =====

[2025-10-12 12:35:33] [info] Report Generated: 2025-10-12
[2025-10-12 12:35:33] [info] Run Start Time: 12:34:43 PM
[2025-10-12 12:35:33] [info] Run End Time: 12:35:33 PM
[2025-10-12 12:35:33] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:35:33] [info] Tool Version: LVR_2025.1
[2025-10-12 12:35:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:35:33] [info] User Name: ubuntu
[2025-10-12 12:35:33] [info] Cores Used: 8
[2025-10-12 12:35:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Top Cell Information
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Layout Top Cell: TOP
[2025-10-12 12:35:33] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:35:33] [info] Database Format: GDSII
[2025-10-12 12:35:33] [info] Verification Mode: Full Check

[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Type of Errors | Count
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] DB_WARN -> 1
[2025-10-12 12:35:33] [info] LRF_ERR001 -> 14
[2025-10-12 12:35:33] [info] DB_WARN002 -> 159
[2025-10-12 12:35:33] [info] DB_WARN003 -> 161
[2025-10-12 12:35:33] [info] DRC_ERR9 -> 89
[2025-10-12 12:35:33] [info] DRC_ERR -> 67
[2025-10-12 12:35:33] [info] DRC_ERR5 -> 408
[2025-10-12 12:35:33] [info] DRC_ERR4 -> 241
[2025-10-12 12:35:33] [info] DRC_ERR1 -> 301
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Rule Violation Summary
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:35:33] [info] |-----|-----|-----|
[2025-10-12 12:35:33] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:35:33] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:35:33] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:35:33] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:35:33] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 12:35:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:35:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:35:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:35:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:35:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:35:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:35:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:35:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:35:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:35:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:35:33] [info]
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Performance Metrics
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] | Stage | Time Taken |
[2025-10-12 12:35:33] [info] |-----|-----|
[2025-10-12 12:35:33] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:35:33] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:35:33] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Summary
[2025-10-12 12:35:33] [info] -----
[2025-10-12 12:35:33] [info] Total Errors: 1120
[2025-10-12 12:35:33] [info] Total Warnings: 321
[2025-10-12 12:35:33] [info] DRC Status: FAILED

```

lvr_summary_121.rpt

```

[2025-10-12 12:36:31] [info] =====
[2025-10-12 12:36:31] [info] Design Rule Check Report
[2025-10-12 12:36:31] [info] =====

[2025-10-12 12:36:31] [info] Report Generated: 2025-10-12
[2025-10-12 12:36:31] [info] Run Start Time: 12:35:39 PM
[2025-10-12 12:36:31] [info] Run End Time: 12:36:31 PM
[2025-10-12 12:36:31] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:36:31] [info] Tool Version: LVR_2025.1
[2025-10-12 12:36:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:36:31] [info] User Name: ubuntu
[2025-10-12 12:36:31] [info] Cores Used: 8
[2025-10-12 12:36:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Top Cell Information
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Layout Top Cell: TOP
[2025-10-12 12:36:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:36:31] [info] Database Format: GDSII
[2025-10-12 12:36:31] [info] Verification Mode: Full Check

[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Type of Errors | Count
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] DB_WARN -> 1
[2025-10-12 12:36:31] [info] LRF_ERR001 -> 14
[2025-10-12 12:36:31] [info] DB_WARN002 -> 159
[2025-10-12 12:36:31] [info] DB_WARN003 -> 161

```

```

[2025-10-12 12:36:31] [info] DRC_ERR9 -> 89
[2025-10-12 12:36:31] [info] DRC_ERR -> 67
[2025-10-12 12:36:31] [info] DRC_ERR5 -> 408
[2025-10-12 12:36:31] [info] DRC_ERR4 -> 241
[2025-10-12 12:36:31] [info] DRC_ERR1 -> 301
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Rule Violation Summary
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:36:31] [info] |-----|-----|-----|
[2025-10-12 12:36:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:36:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:36:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:36:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:36:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:36:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:36:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:36:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:36:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:36:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:36:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:36:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:36:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:36:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:36:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Performance Metrics
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] | Stage | Time Taken |
[2025-10-12 12:36:31] [info] |-----|-----|
[2025-10-12 12:36:31] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:36:31] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:36:31] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Summary
[2025-10-12 12:36:31] [info] -----
[2025-10-12 12:36:31] [info] Total Errors: 1120
[2025-10-12 12:36:31] [info] Total Warnings: 321
[2025-10-12 12:36:31] [info] DRC Status: FAILED

```

lvr_summary_122.rpt

```

[2025-10-12 12:37:29] [info] =====
[2025-10-12 12:37:29] [info] Design Rule Check Report
[2025-10-12 12:37:29] [info] =====

[2025-10-12 12:37:29] [info] Report Generated: 2025-10-12
[2025-10-12 12:37:29] [info] Run Start Time: 12:36:38 PM
[2025-10-12 12:37:29] [info] Run End Time: 12:37:29 PM
[2025-10-12 12:37:29] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:37:29] [info] Tool Version: LVR_2025.1
[2025-10-12 12:37:29] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:37:29] [info] User Name: ubuntu
[2025-10-12 12:37:29] [info] Cores Used: 8
[2025-10-12 12:37:29] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Top Cell Information
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Layout Top Cell:      TOP
[2025-10-12 12:37:29] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:37:29] [info] Database Format:      GDSII
[2025-10-12 12:37:29] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Type of Errors | Count
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] DB_WARN -> 1
[2025-10-12 12:37:29] [info] LRF_ERR001 -> 14
[2025-10-12 12:37:29] [info] DB_WARN002 -> 159
[2025-10-12 12:37:29] [info] DB_WARN003 -> 161
[2025-10-12 12:37:29] [info] DRC_ERR9 -> 89
[2025-10-12 12:37:29] [info] DRC_ERR -> 67
[2025-10-12 12:37:29] [info] DRC_ERR5 -> 408
[2025-10-12 12:37:29] [info] DRC_ERR4 -> 241
[2025-10-12 12:37:29] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Rule Violation Summary
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:37:29] [info] |-----|-----|-----|
[2025-10-12 12:37:29] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:37:29] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:37:29] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:37:29] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:37:29] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:37:29] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:37:29] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:37:29] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:37:29] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:37:29] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:37:29] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:37:29] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:37:29] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:37:29] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:37:29] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:37:29] [info] |
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Performance Metrics
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] | Stage          | Time Taken |
[2025-10-12 12:37:29] [info] |-----|-----|
[2025-10-12 12:37:29] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:37:29] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 12:37:29] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Summary
```

```
[2025-10-12 12:37:29] [info] -----
[2025-10-12 12:37:29] [info] Total Errors:      1120
[2025-10-12 12:37:29] [info] Total Warnings:    321
[2025-10-12 12:37:29] [info] DRC Status:        FAILED
```

lvr_summary_123.rpt

```
[2025-10-12 12:38:26] [info] =====
[2025-10-12 12:38:26] [info]                      Design Rule Check Report
[2025-10-12 12:38:26] [info] =====

[2025-10-12 12:38:26] [info] Report Generated:    2025-10-12
[2025-10-12 12:38:26] [info] Run Start Time:     12:37:35 PM
[2025-10-12 12:38:26] [info] Run End Time:       12:38:26 PM
[2025-10-12 12:38:26] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 12:38:26] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:38:26] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:38:26] [info] User Name:          ubuntu
[2025-10-12 12:38:26] [info] Cores Used:         8
[2025-10-12 12:38:26] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info]                      Top Cell Information
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info] Layout Top Cell:    TOP
[2025-10-12 12:38:26] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:38:26] [info] Database Format:      GDSII
[2025-10-12 12:38:26] [info] Verification Mode:   Full Check

[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info]                      Type of Errors | Count
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info] DB_WARN -> 1
[2025-10-12 12:38:26] [info] LRF_ERR001 -> 14
[2025-10-12 12:38:26] [info] DB_WARN002 -> 159
[2025-10-12 12:38:26] [info] DB_WARN003 -> 161
[2025-10-12 12:38:26] [info] DRC_ERR9 -> 89
[2025-10-12 12:38:26] [info] DRC_ERR -> 67
[2025-10-12 12:38:26] [info] DRC_ERR5 -> 408
[2025-10-12 12:38:26] [info] DRC_ERR4 -> 241
[2025-10-12 12:38:26] [info] DRC_ERR1 -> 301
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info]                      Rule Violation Summary
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:38:26] [info] |-----|-----|-----|
[2025-10-12 12:38:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:38:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:38:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:38:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:38:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:38:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:38:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:38:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:38:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:38:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:38:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:38:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:38:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:38:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:38:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:38:26] [info]
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info]                      Performance Metrics
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:38:26] [info] |-----|-----|
[2025-10-12 12:38:26] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 12:38:26] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 12:38:26] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info]                Summary
[2025-10-12 12:38:26] [info] -----
[2025-10-12 12:38:26] [info] Total Errors:      1120
[2025-10-12 12:38:26] [info] Total Warnings:    321
[2025-10-12 12:38:26] [info] DRC Status:        FAILED
```

lvr_summary_124.rpt

```
[2025-10-12 12:39:24] [info] =====
[2025-10-12 12:39:24] [info]                Design Rule Check Report
[2025-10-12 12:39:24] [info] =====

[2025-10-12 12:39:24] [info] Report Generated:   2025-10-12
[2025-10-12 12:39:24] [info] Run Start Time:     12:38:33 PM
[2025-10-12 12:39:24] [info] Run End Time:       12:39:24 PM
[2025-10-12 12:39:24] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 12:39:24] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:39:24] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:39:24] [info] User Name:          ubuntu
[2025-10-12 12:39:24] [info] Cores Used:         8
[2025-10-12 12:39:24] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info]                Top Cell Information
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] Layout Top Cell:    TOP
[2025-10-12 12:39:24] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:39:24] [info] Database Format:      GDSII
[2025-10-12 12:39:24] [info] Verification Mode:   Full Check

[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info]                Type of Errors | Count
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] DB_WARN -> 1
[2025-10-12 12:39:24] [info] LRF_ERR001 -> 14
[2025-10-12 12:39:24] [info] DB_WARN002 -> 159
[2025-10-12 12:39:24] [info] DB_WARN003 -> 161
[2025-10-12 12:39:24] [info] DRC_ERR9 -> 89
[2025-10-12 12:39:24] [info] DRC_ERR -> 67
[2025-10-12 12:39:24] [info] DRC_ERR5 -> 408
[2025-10-12 12:39:24] [info] DRC_ERR4 -> 241
[2025-10-12 12:39:24] [info] DRC_ERR1 -> 301
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info]                Rule Violation Summary
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:39:24] [info] |-----|-----|-----|
[2025-10-12 12:39:24] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:39:24] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:39:24] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:39:24] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:39:24] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 12:39:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 12:39:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:39:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:39:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:39:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:39:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:39:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:39:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:39:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:39:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:39:24] [info]
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] Performance Metrics
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] | Stage | Time Taken |
[2025-10-12 12:39:24] [info] |-----|-----|
[2025-10-12 12:39:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:39:24] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:39:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] Summary
[2025-10-12 12:39:24] [info] -----
[2025-10-12 12:39:24] [info] Total Errors: 1120
[2025-10-12 12:39:24] [info] Total Warnings: 321
[2025-10-12 12:39:24] [info] DRC Status: FAILED

```

lvr_summary_125.rpt

```

[2025-10-12 12:40:21] [info] =====
[2025-10-12 12:40:21] [info] Design Rule Check Report
[2025-10-12 12:40:21] [info] =====

[2025-10-12 12:40:21] [info] Report Generated: 2025-10-12
[2025-10-12 12:40:21] [info] Run Start Time: 12:39:29 PM
[2025-10-12 12:40:21] [info] Run End Time: 12:40:21 PM
[2025-10-12 12:40:21] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:40:21] [info] Tool Version: LVR_2025.1
[2025-10-12 12:40:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:40:21] [info] User Name: ubuntu
[2025-10-12 12:40:21] [info] Cores Used: 8
[2025-10-12 12:40:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Top Cell Information
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Layout Top Cell: TOP
[2025-10-12 12:40:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:40:21] [info] Database Format: GDSII
[2025-10-12 12:40:21] [info] Verification Mode: Full Check

[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Type of Errors | Count
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] DB_WARN -> 1
[2025-10-12 12:40:21] [info] LRF_ERR001 -> 14
[2025-10-12 12:40:21] [info] DB_WARN002 -> 159
[2025-10-12 12:40:21] [info] DB_WARN003 -> 161

```



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[2025-10-12 12:40:21] [info] DRC_ERR9 -> 89
[2025-10-12 12:40:21] [info] DRC_ERR -> 67
[2025-10-12 12:40:21] [info] DRC_ERR5 -> 408
[2025-10-12 12:40:21] [info] DRC_ERR4 -> 241
[2025-10-12 12:40:21] [info] DRC_ERR1 -> 301
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Rule Violation Summary
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:40:21] [info] |-----|-----|-----|
[2025-10-12 12:40:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:40:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:40:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:40:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:40:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:40:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:40:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:40:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:40:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:40:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:40:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:40:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:40:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:40:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:40:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Performance Metrics
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] | Stage | Time Taken |
[2025-10-12 12:40:21] [info] |-----|-----|
[2025-10-12 12:40:21] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:40:21] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:40:21] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Summary
[2025-10-12 12:40:21] [info] -----
[2025-10-12 12:40:21] [info] Total Errors: 1120
[2025-10-12 12:40:21] [info] Total Warnings: 321
[2025-10-12 12:40:21] [info] DRC Status: FAILED

```

lvr_summary_126.rpt

```

[2025-10-12 12:41:17] [info] =====
[2025-10-12 12:41:17] [info] Design Rule Check Report
[2025-10-12 12:41:17] [info] =====

[2025-10-12 12:41:17] [info] Report Generated: 2025-10-12
[2025-10-12 12:41:17] [info] Run Start Time: 12:40:26 PM
[2025-10-12 12:41:17] [info] Run End Time: 12:41:17 PM
[2025-10-12 12:41:17] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:41:17] [info] Tool Version: LVR_2025.1
[2025-10-12 12:41:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:41:17] [info] User Name: ubuntu
[2025-10-12 12:41:17] [info] Cores Used: 8
[2025-10-12 12:41:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] Top Cell Information
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] Layout Top Cell:      TOP
[2025-10-12 12:41:17] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:41:17] [info] Database Format:       GDSII
[2025-10-12 12:41:17] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] Type of Errors | Count
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] DB_WARN -> 1
[2025-10-12 12:41:17] [info] LRF_ERR001 -> 14
[2025-10-12 12:41:17] [info] DB_WARN002 -> 159
[2025-10-12 12:41:17] [info] DB_WARN003 -> 161
[2025-10-12 12:41:17] [info] DRC_ERR9 -> 89
[2025-10-12 12:41:17] [info] DRC_ERR -> 67
[2025-10-12 12:41:17] [info] DRC_ERR5 -> 408
[2025-10-12 12:41:17] [info] DRC_ERR4 -> 241
[2025-10-12 12:41:17] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] Rule Violation Summary
```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:41:17] [info] |-----|-----|-----|
[2025-10-12 12:41:17] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:41:17] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:41:17] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:41:17] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:41:17] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:41:17] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:41:17] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:41:17] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:41:17] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:41:17] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:41:17] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:41:17] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:41:17] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:41:17] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:41:17] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:41:17] [info] -----
```

```
[2025-10-12 12:41:17] [info] Performance Metrics
```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] | Stage          | Time Taken |
[2025-10-12 12:41:17] [info] |-----|-----|
[2025-10-12 12:41:17] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:41:17] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 12:41:17] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] Summary
```

```
[2025-10-12 12:41:17] [info] -----
[2025-10-12 12:41:17] [info] Total Errors:      1120
[2025-10-12 12:41:17] [info] Total Warnings:    321
[2025-10-12 12:41:17] [info] DRC Status:        FAILED
```

lvr_summary_127.rpt

```
[2025-10-12 12:42:15] [info] =====
[2025-10-12 12:42:15] [info]                      Design Rule Check Report
[2025-10-12 12:42:15] [info] =====

[2025-10-12 12:42:15] [info] Report Generated:      2025-10-12
[2025-10-12 12:42:15] [info] Run Start Time:       12:41:23 PM
[2025-10-12 12:42:15] [info] Run End Time:         12:42:15 PM
[2025-10-12 12:42:15] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 12:42:15] [info] Tool Version:         LVR_2025.1
[2025-10-12 12:42:15] [info] Host Name:            ip-172-31-37-173
[2025-10-12 12:42:15] [info] User Name:            ubuntu
[2025-10-12 12:42:15] [info] Cores Used:           8
[2025-10-12 12:42:15] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info]                      Top Cell Information
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info] Layout Top Cell:      TOP
[2025-10-12 12:42:15] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 12:42:15] [info] Database Format:        GDSII
[2025-10-12 12:42:15] [info] Verification Mode:     Full Check

[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info]                      Type of Errors | Count
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info] DB_WARN -> 1
[2025-10-12 12:42:15] [info] LRF_ERR001 -> 14
[2025-10-12 12:42:15] [info] DB_WARN002 -> 159
[2025-10-12 12:42:15] [info] DB_WARN003 -> 161
[2025-10-12 12:42:15] [info] DRC_ERR9 -> 89
[2025-10-12 12:42:15] [info] DRC_ERR -> 67
[2025-10-12 12:42:15] [info] DRC_ERR5 -> 408
[2025-10-12 12:42:15] [info] DRC_ERR4 -> 241
[2025-10-12 12:42:15] [info] DRC_ERR1 -> 301
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info]                      Rule Violation Summary
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:42:15] [info] |-----|-----|-----|
[2025-10-12 12:42:15] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:42:15] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:42:15] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:42:15] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:42:15] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:42:15] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:42:15] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:42:15] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:42:15] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:42:15] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:42:15] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:42:15] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:42:15] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:42:15] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:42:15] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:42:15] [info]
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info]                      Performance Metrics
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:42:15] [info] |-----|-----|
[2025-10-12 12:42:15] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:42:15] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 12:42:15] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info] Summary
[2025-10-12 12:42:15] [info] -----
[2025-10-12 12:42:15] [info] Total Errors: 1120
[2025-10-12 12:42:15] [info] Total Warnings: 321
[2025-10-12 12:42:15] [info] DRC Status: FAILED
```

lvr_summary_128.rpt

```
[2025-10-12 12:43:12] [info] =====
[2025-10-12 12:43:12] [info] Design Rule Check Report
[2025-10-12 12:43:12] [info] =====

[2025-10-12 12:43:12] [info] Report Generated: 2025-10-12
[2025-10-12 12:43:12] [info] Run Start Time: 12:42:20 PM
[2025-10-12 12:43:12] [info] Run End Time: 12:43:12 PM
[2025-10-12 12:43:12] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:43:12] [info] Tool Version: LVR_2025.1
[2025-10-12 12:43:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:43:12] [info] User Name: ubuntu
[2025-10-12 12:43:12] [info] Cores Used: 8
[2025-10-12 12:43:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Top Cell Information
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Layout Top Cell: TOP
[2025-10-12 12:43:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:43:12] [info] Database Format: GDSII
[2025-10-12 12:43:12] [info] Verification Mode: Full Check

[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Type of Errors | Count
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] DB_WARN -> 1
[2025-10-12 12:43:12] [info] LRF_ERR001 -> 14
[2025-10-12 12:43:12] [info] DB_WARN002 -> 159
[2025-10-12 12:43:12] [info] DB_WARN003 -> 161
[2025-10-12 12:43:12] [info] DRC_ERR9 -> 89
[2025-10-12 12:43:12] [info] DRC_ERR -> 67
[2025-10-12 12:43:12] [info] DRC_ERR5 -> 408
[2025-10-12 12:43:12] [info] DRC_ERR4 -> 241
[2025-10-12 12:43:12] [info] DRC_ERR1 -> 301
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Rule Violation Summary
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:43:12] [info] |-----|-----|-----|
[2025-10-12 12:43:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:43:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:43:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:43:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:43:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 12:43:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:43:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:43:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:43:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:43:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:43:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:43:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:43:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:43:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:43:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:43:12] [info]
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Performance Metrics
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] | Stage | Time Taken |
[2025-10-12 12:43:12] [info] |-----|-----|
[2025-10-12 12:43:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:43:12] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:43:12] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Summary
[2025-10-12 12:43:12] [info] -----
[2025-10-12 12:43:12] [info] Total Errors: 1120
[2025-10-12 12:43:12] [info] Total Warnings: 321
[2025-10-12 12:43:12] [info] DRC Status: FAILED

```

lvr_summary_129.rpt

```

[2025-10-12 12:44:08] [info] =====
[2025-10-12 12:44:08] [info] Design Rule Check Report
[2025-10-12 12:44:08] [info] =====

[2025-10-12 12:44:08] [info] Report Generated: 2025-10-12
[2025-10-12 12:44:08] [info] Run Start Time: 12:43:17 PM
[2025-10-12 12:44:08] [info] Run End Time: 12:44:08 PM
[2025-10-12 12:44:08] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:44:08] [info] Tool Version: LVR_2025.1
[2025-10-12 12:44:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:44:08] [info] User Name: ubuntu
[2025-10-12 12:44:08] [info] Cores Used: 8
[2025-10-12 12:44:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Top Cell Information
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Layout Top Cell: TOP
[2025-10-12 12:44:08] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:44:08] [info] Database Format: GDSII
[2025-10-12 12:44:08] [info] Verification Mode: Full Check

[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Type of Errors | Count
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] DB_WARN -> 1
[2025-10-12 12:44:08] [info] LRF_ERR001 -> 14
[2025-10-12 12:44:08] [info] DB_WARN002 -> 159
[2025-10-12 12:44:08] [info] DB_WARN003 -> 161

```

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[2025-10-12 12:44:08] [info] DRC_ERR9 -> 89
[2025-10-12 12:44:08] [info] DRC_ERR -> 67
[2025-10-12 12:44:08] [info] DRC_ERR5 -> 408
[2025-10-12 12:44:08] [info] DRC_ERR4 -> 241
[2025-10-12 12:44:08] [info] DRC_ERR1 -> 301
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Rule Violation Summary
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:44:08] [info] |-----|-----|-----|
[2025-10-12 12:44:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:44:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:44:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:44:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:44:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:44:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:44:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:44:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:44:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:44:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:44:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:44:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:44:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:44:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:44:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Performance Metrics
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] | Stage | Time Taken |
[2025-10-12 12:44:08] [info] |-----|-----|
[2025-10-12 12:44:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:44:08] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:44:08] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Summary
[2025-10-12 12:44:08] [info] -----
[2025-10-12 12:44:08] [info] Total Errors: 1120
[2025-10-12 12:44:08] [info] Total Warnings: 321
[2025-10-12 12:44:08] [info] DRC Status: FAILED

```

lvr_summary_13.rpt

```

[2025-10-12 10:53:44] [info] =====
[2025-10-12 10:53:44] [info] Design Rule Check Report
[2025-10-12 10:53:44] [info] =====

[2025-10-12 10:53:44] [info] Report Generated: 2025-10-12
[2025-10-12 10:53:44] [info] Run Start Time: 10:52:53 AM
[2025-10-12 10:53:44] [info] Run End Time: 10:53:44 AM
[2025-10-12 10:53:44] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 10:53:44] [info] Tool Version: LVR_2025.1
[2025-10-12 10:53:44] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:53:44] [info] User Name: ubuntu
[2025-10-12 10:53:44] [info] Cores Used: 8
[2025-10-12 10:53:44] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 10:53:44] [info] -----
[2025-10-12 10:53:44] [info] Top Cell Information
[2025-10-12 10:53:44] [info] -----
[2025-10-12 10:53:44] [info] Layout Top Cell:      TOP
[2025-10-12 10:53:44] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:53:44] [info] Database Format:      GDSII
[2025-10-12 10:53:44] [info] Verification Mode:    Full Check

```

```

[2025-10-12 10:53:44] [info] -----
[2025-10-12 10:53:44] [info] Type of Errors | Count
[2025-10-12 10:53:44] [info] -----
[2025-10-12 10:53:44] [info] DB_WARN -> 1
[2025-10-12 10:53:44] [info] LRF_ERR001 -> 14
[2025-10-12 10:53:44] [info] DB_WARN002 -> 159
[2025-10-12 10:53:44] [info] DB_WARN003 -> 161
[2025-10-12 10:53:44] [info] DRC_ERR9 -> 89
[2025-10-12 10:53:44] [info] DRC_ERR -> 67
[2025-10-12 10:53:44] [info] DRC_ERR5 -> 408
[2025-10-12 10:53:44] [info] DRC_ERR4 -> 241
[2025-10-12 10:53:44] [info] DRC_ERR1 -> 301

```

```

[2025-10-12 10:53:44] [info] -----
[2025-10-12 10:53:44] [info] Rule Violation Summary
[2025-10-12 10:53:44] [info] -----

```

Rule ID	Description	Error Count
m5.1	"m5.1 : min. m5 width : 1.6um" 4	4
li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	5
poly.1a	"poly.1a : min. poly width : 0.15um" 19	19
poly.7	"poly.7 : min. source/drain length : 0.25um" 8	8
licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	110
m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	59
m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	391
m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	301
psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	14
licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	131
li.1	"li.1 : min. li width : 0.17um" 17	17
li.1a	"li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" 15	15
m1.1	"m1.1 : min. m1 width : 0.14um" 3	3
licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	12
npc.1	"npc.1 : min. npc width : 0.27um" 17	17

```

[2025-10-12 10:53:44] [info] -----

```

```

[2025-10-12 10:53:44] [info] Performance Metrics
[2025-10-12 10:53:44] [info] -----

```

Stage	Time Taken
GDS Parsing	0 minutes 9 seconds
Rule Checking	0 minutes 39 seconds
Report Generation	0 minutes 0 seconds

```

[2025-10-12 10:53:44] [info] -----

```

```

[2025-10-12 10:53:44] [info] Summary
[2025-10-12 10:53:44] [info] -----

```

```

[2025-10-12 10:53:44] [info] Total Errors:      1120
[2025-10-12 10:53:44] [info] Total Warnings:    321
[2025-10-12 10:53:44] [info] DRC Status:        FAILED

```

lvr_summary_130.rpt

```
[2025-10-12 12:45:04] [info] =====
[2025-10-12 12:45:04] [info] Design Rule Check Report
[2025-10-12 12:45:04] [info] =====

[2025-10-12 12:45:04] [info] Report Generated: 2025-10-12
[2025-10-12 12:45:04] [info] Run Start Time: 12:44:13 PM
[2025-10-12 12:45:04] [info] Run End Time: 12:45:04 PM
[2025-10-12 12:45:04] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:45:04] [info] Tool Version: LVR_2025.1
[2025-10-12 12:45:04] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:45:04] [info] User Name: ubuntu
[2025-10-12 12:45:04] [info] Cores Used: 8
[2025-10-12 12:45:04] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Top Cell Information
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Layout Top Cell: TOP
[2025-10-12 12:45:04] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:45:04] [info] Database Format: GDSII
[2025-10-12 12:45:04] [info] Verification Mode: Full Check

[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Type of Errors | Count
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] DB_WARN -> 1
[2025-10-12 12:45:04] [info] LRF_ERR001 -> 14
[2025-10-12 12:45:04] [info] DB_WARN002 -> 159
[2025-10-12 12:45:04] [info] DB_WARN003 -> 161
[2025-10-12 12:45:04] [info] DRC_ERR9 -> 89
[2025-10-12 12:45:04] [info] DRC_ERR -> 67
[2025-10-12 12:45:04] [info] DRC_ERR5 -> 408
[2025-10-12 12:45:04] [info] DRC_ERR4 -> 241
[2025-10-12 12:45:04] [info] DRC_ERR1 -> 301
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Rule Violation Summary
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:45:04] [info] |-----|-----|-----|
[2025-10-12 12:45:04] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:45:04] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:45:04] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:45:04] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:45:04] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:45:04] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:45:04] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:45:04] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:45:04] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:45:04] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:45:04] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:45:04] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:45:04] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:45:04] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:45:04] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:45:04] [info]
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Performance Metrics
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] | Stage | Time Taken |
```



```
[2025-10-12 12:45:04] [info] |-----|-----|
[2025-10-12 12:45:04] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:45:04] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 12:45:04] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Summary
[2025-10-12 12:45:04] [info] -----
[2025-10-12 12:45:04] [info] Total Errors:   1120
[2025-10-12 12:45:04] [info] Total Warnings:   321
[2025-10-12 12:45:04] [info] DRC Status:      FAILED
```

lvr_summary_131.rpt

```
[2025-10-12 12:46:01] [info] =====
[2025-10-12 12:46:01] [info] Design Rule Check Report
[2025-10-12 12:46:01] [info] =====

[2025-10-12 12:46:01] [info] Report Generated:   2025-10-12
[2025-10-12 12:46:01] [info] Run Start Time:     12:45:10 PM
[2025-10-12 12:46:01] [info] Run End Time:       12:46:01 PM
[2025-10-12 12:46:01] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 12:46:01] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:46:01] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:46:01] [info] User Name:          ubuntu
[2025-10-12 12:46:01] [info] Cores Used:         8
[2025-10-12 12:46:01] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Top Cell Information
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Layout Top Cell:    TOP
[2025-10-12 12:46:01] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:46:01] [info] Database Format:      GDSII
[2025-10-12 12:46:01] [info] Verification Mode:   Full Check

[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Type of Errors | Count
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] DB_WARN -> 1
[2025-10-12 12:46:01] [info] LRF_ERR001 -> 14
[2025-10-12 12:46:01] [info] DB_WARN002 -> 159
[2025-10-12 12:46:01] [info] DB_WARN003 -> 161
[2025-10-12 12:46:01] [info] DRC_ERR9 -> 89
[2025-10-12 12:46:01] [info] DRC_ERR -> 67
[2025-10-12 12:46:01] [info] DRC_ERR5 -> 408
[2025-10-12 12:46:01] [info] DRC_ERR4 -> 241
[2025-10-12 12:46:01] [info] DRC_ERR1 -> 301
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Rule Violation Summary
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:46:01] [info] |-----|-----|-----|
[2025-10-12 12:46:01] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:46:01] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:46:01] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:46:01] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:46:01] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 12:46:01] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:46:01] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:46:01] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:46:01] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:46:01] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:46:01] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:46:01] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:46:01] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:46:01] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:46:01] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:46:01] [info]
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Performance Metrics
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] | Stage | Time Taken |
[2025-10-12 12:46:01] [info] |-----|-----|
[2025-10-12 12:46:01] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:46:01] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:46:01] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Summary
[2025-10-12 12:46:01] [info] -----
[2025-10-12 12:46:01] [info] Total Errors: 1120
[2025-10-12 12:46:01] [info] Total Warnings: 321
[2025-10-12 12:46:01] [info] DRC Status: FAILED
```

lvr_summary_132.rpt

```
[2025-10-12 12:46:57] [info] =====
[2025-10-12 12:46:57] [info] Design Rule Check Report
[2025-10-12 12:46:57] [info] =====

[2025-10-12 12:46:57] [info] Report Generated: 2025-10-12
[2025-10-12 12:46:57] [info] Run Start Time: 12:46:07 PM
[2025-10-12 12:46:57] [info] Run End Time: 12:46:57 PM
[2025-10-12 12:46:57] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:46:57] [info] Tool Version: LVR_2025.1
[2025-10-12 12:46:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:46:57] [info] User Name: ubuntu
[2025-10-12 12:46:57] [info] Cores Used: 8
[2025-10-12 12:46:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Top Cell Information
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Layout Top Cell: TOP
[2025-10-12 12:46:57] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:46:57] [info] Database Format: GDSII
[2025-10-12 12:46:57] [info] Verification Mode: Full Check

[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Type of Errors | Count
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] DB_WARN -> 1
[2025-10-12 12:46:57] [info] LRF_ERR001 -> 14
[2025-10-12 12:46:57] [info] DB_WARN002 -> 159
[2025-10-12 12:46:57] [info] DB_WARN003 -> 161
```

```

[2025-10-12 12:46:57] [info] DRC_ERR9 -> 89
[2025-10-12 12:46:57] [info] DRC_ERR -> 67
[2025-10-12 12:46:57] [info] DRC_ERR5 -> 408
[2025-10-12 12:46:57] [info] DRC_ERR4 -> 241
[2025-10-12 12:46:57] [info] DRC_ERR1 -> 301
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Rule Violation Summary
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:46:57] [info] |-----|-----|-----|
[2025-10-12 12:46:57] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:46:57] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:46:57] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:46:57] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:46:57] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:46:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:46:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:46:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:46:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:46:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:46:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:46:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:46:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:46:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:46:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Performance Metrics
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] | Stage | Time Taken |
[2025-10-12 12:46:57] [info] |-----|-----|
[2025-10-12 12:46:57] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:46:57] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:46:57] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Summary
[2025-10-12 12:46:57] [info] -----
[2025-10-12 12:46:57] [info] Total Errors: 1120
[2025-10-12 12:46:57] [info] Total Warnings: 321
[2025-10-12 12:46:57] [info] DRC Status: FAILED

```

lvr_summary_133.rpt

```

[2025-10-12 12:47:56] [info] =====
[2025-10-12 12:47:56] [info] Design Rule Check Report
[2025-10-12 12:47:56] [info] =====

[2025-10-12 12:47:56] [info] Report Generated: 2025-10-12
[2025-10-12 12:47:56] [info] Run Start Time: 12:47:05 PM
[2025-10-12 12:47:56] [info] Run End Time: 12:47:56 PM
[2025-10-12 12:47:56] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:47:56] [info] Tool Version: LVR_2025.1
[2025-10-12 12:47:56] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:47:56] [info] User Name: ubuntu
[2025-10-12 12:47:56] [info] Cores Used: 8
[2025-10-12 12:47:56] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Top Cell Information
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Layout Top Cell:      TOP
[2025-10-12 12:47:56] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:47:56] [info] Database Format:      GDSII
[2025-10-12 12:47:56] [info] Verification Mode:    Full Check

[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Type of Errors | Count
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] DB_WARN -> 1
[2025-10-12 12:47:56] [info] LRF_ERR001 -> 14
[2025-10-12 12:47:56] [info] DB_WARN002 -> 159
[2025-10-12 12:47:56] [info] DB_WARN003 -> 161
[2025-10-12 12:47:56] [info] DRC_ERR9 -> 89
[2025-10-12 12:47:56] [info] DRC_ERR -> 67
[2025-10-12 12:47:56] [info] DRC_ERR5 -> 408
[2025-10-12 12:47:56] [info] DRC_ERR4 -> 241
[2025-10-12 12:47:56] [info] DRC_ERR1 -> 301
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Rule Violation Summary
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:47:56] [info] |-----|-----|-----|
[2025-10-12 12:47:56] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:47:56] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:47:56] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:47:56] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:47:56] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:47:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:47:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:47:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:47:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:47:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:47:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:47:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:47:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:47:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:47:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Performance Metrics
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] | Stage | Time Taken |
[2025-10-12 12:47:56] [info] |-----|-----|
[2025-10-12 12:47:56] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:47:56] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:47:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Summary
[2025-10-12 12:47:56] [info] -----
[2025-10-12 12:47:56] [info] Total Errors: 1120
[2025-10-12 12:47:56] [info] Total Warnings: 321
[2025-10-12 12:47:56] [info] DRC Status: FAILED

```

lvr_summary_134.rpt

```
[2025-10-12 12:48:54] [info] =====
[2025-10-12 12:48:54] [info] Design Rule Check Report
[2025-10-12 12:48:54] [info] =====

[2025-10-12 12:48:54] [info] Report Generated: 2025-10-12
[2025-10-12 12:48:54] [info] Run Start Time: 12:48:03 PM
[2025-10-12 12:48:54] [info] Run End Time: 12:48:54 PM
[2025-10-12 12:48:54] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:48:54] [info] Tool Version: LVR_2025.1
[2025-10-12 12:48:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:48:54] [info] User Name: ubuntu
[2025-10-12 12:48:54] [info] Cores Used: 8
[2025-10-12 12:48:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] Top Cell Information
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] Layout Top Cell: TOP
[2025-10-12 12:48:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:48:54] [info] Database Format: GDSII
[2025-10-12 12:48:54] [info] Verification Mode: Full Check

[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] Type of Errors | Count
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] DB_WARN -> 1
[2025-10-12 12:48:54] [info] LRF_ERR001 -> 14
[2025-10-12 12:48:54] [info] DB_WARN002 -> 159
[2025-10-12 12:48:54] [info] DB_WARN003 -> 161
[2025-10-12 12:48:54] [info] DRC_ERR9 -> 89
[2025-10-12 12:48:54] [info] DRC_ERR -> 67
[2025-10-12 12:48:54] [info] DRC_ERR5 -> 408
[2025-10-12 12:48:54] [info] DRC_ERR4 -> 241
[2025-10-12 12:48:54] [info] DRC_ERR1 -> 301
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] Rule Violation Summary
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:48:54] [info] |-----|-----|-----|
[2025-10-12 12:48:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:48:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:48:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:48:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:48:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:48:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:48:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:48:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:48:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:48:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:48:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:48:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:48:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:48:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:48:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:48:54] [info]
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] Performance Metrics
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:48:54] [info] |-----|-----|
[2025-10-12 12:48:54] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:48:54] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 12:48:54] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info]                Summary
[2025-10-12 12:48:54] [info] -----
[2025-10-12 12:48:54] [info] Total Errors:    1120
[2025-10-12 12:48:54] [info] Total Warnings:    321
[2025-10-12 12:48:54] [info] DRC Status:      FAILED
```

lvr_summary_135.rpt

```
[2025-10-12 12:49:49] [info] =====
[2025-10-12 12:49:49] [info]                Design Rule Check Report
[2025-10-12 12:49:49] [info] =====

[2025-10-12 12:49:49] [info] Report Generated:    2025-10-12
[2025-10-12 12:49:49] [info] Run Start Time:      12:48:59 PM
[2025-10-12 12:49:49] [info] Run End Time:        12:49:49 PM
[2025-10-12 12:49:49] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 12:49:49] [info] Tool Version:        LVR_2025.1
[2025-10-12 12:49:49] [info] Host Name:           ip-172-31-37-173
[2025-10-12 12:49:49] [info] User Name:           ubuntu
[2025-10-12 12:49:49] [info] Cores Used:          8
[2025-10-12 12:49:49] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info]                Top Cell Information
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] Layout Top Cell:     TOP
[2025-10-12 12:49:49] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:49:49] [info] Database Format:       GDSII
[2025-10-12 12:49:49] [info] Verification Mode:    Full Check

[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info]                Type of Errors | Count
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] DB_WARN -> 1
[2025-10-12 12:49:49] [info] LRF_ERR001 -> 14
[2025-10-12 12:49:49] [info] DB_WARN002 -> 159
[2025-10-12 12:49:49] [info] DB_WARN003 -> 161
[2025-10-12 12:49:49] [info] DRC_ERR9 -> 89
[2025-10-12 12:49:49] [info] DRC_ERR -> 67
[2025-10-12 12:49:49] [info] DRC_ERR5 -> 408
[2025-10-12 12:49:49] [info] DRC_ERR4 -> 241
[2025-10-12 12:49:49] [info] DRC_ERR1 -> 301
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info]                Rule Violation Summary
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:49:49] [info] |-----|-----|-----|
[2025-10-12 12:49:49] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:49:49] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:49:49] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:49:49] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:49:49] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

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[2025-10-12 12:49:49] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 12:49:49] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:49:49] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:49:49] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:49:49] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:49:49] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:49:49] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:49:49] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:49:49] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:49:49] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:49:49] [info]
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] Performance Metrics
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] | Stage | Time Taken |
[2025-10-12 12:49:49] [info] |-----|-----|
[2025-10-12 12:49:49] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:49:49] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:49:49] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] Summary
[2025-10-12 12:49:49] [info] -----
[2025-10-12 12:49:49] [info] Total Errors: 1120
[2025-10-12 12:49:49] [info] Total Warnings: 321
[2025-10-12 12:49:49] [info] DRC Status: FAILED

```

lvr_summary_136.rpt

```

[2025-10-12 12:50:46] [info] =====
[2025-10-12 12:50:46] [info] Design Rule Check Report
[2025-10-12 12:50:46] [info] =====

[2025-10-12 12:50:46] [info] Report Generated: 2025-10-12
[2025-10-12 12:50:46] [info] Run Start Time: 12:49:56 PM
[2025-10-12 12:50:46] [info] Run End Time: 12:50:46 PM
[2025-10-12 12:50:46] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:50:46] [info] Tool Version: LVR_2025.1
[2025-10-12 12:50:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:50:46] [info] User Name: ubuntu
[2025-10-12 12:50:46] [info] Cores Used: 8
[2025-10-12 12:50:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Top Cell Information
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Layout Top Cell: TOP
[2025-10-12 12:50:46] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:50:46] [info] Database Format: GDSII
[2025-10-12 12:50:46] [info] Verification Mode: Full Check

[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Type of Errors | Count
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] DB_WARN -> 1
[2025-10-12 12:50:46] [info] LRF_ERR001 -> 14
[2025-10-12 12:50:46] [info] DB_WARN002 -> 159
[2025-10-12 12:50:46] [info] DB_WARN003 -> 161

```

```

[2025-10-12 12:50:46] [info] DRC_ERR9 -> 89
[2025-10-12 12:50:46] [info] DRC_ERR -> 67
[2025-10-12 12:50:46] [info] DRC_ERR5 -> 408
[2025-10-12 12:50:46] [info] DRC_ERR4 -> 241
[2025-10-12 12:50:46] [info] DRC_ERR1 -> 301
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Rule Violation Summary
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:50:46] [info] |-----|-----|-----|
[2025-10-12 12:50:46] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:50:46] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:50:46] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:50:46] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:50:46] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:50:46] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:50:46] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:50:46] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:50:46] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:50:46] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:50:46] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:50:46] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:50:46] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:50:46] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:50:46] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Performance Metrics
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] | Stage | Time Taken |
[2025-10-12 12:50:46] [info] |-----|-----|
[2025-10-12 12:50:46] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:50:46] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:50:46] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Summary
[2025-10-12 12:50:46] [info] -----
[2025-10-12 12:50:46] [info] Total Errors: 1120
[2025-10-12 12:50:46] [info] Total Warnings: 321
[2025-10-12 12:50:46] [info] DRC Status: FAILED

```

lvr_summary_137.rpt

```

[2025-10-12 12:51:45] [info] =====
[2025-10-12 12:51:45] [info] Design Rule Check Report
[2025-10-12 12:51:45] [info] =====

[2025-10-12 12:51:45] [info] Report Generated: 2025-10-12
[2025-10-12 12:51:45] [info] Run Start Time: 12:50:53 PM
[2025-10-12 12:51:45] [info] Run End Time: 12:51:45 PM
[2025-10-12 12:51:45] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:51:45] [info] Tool Version: LVR_2025.1
[2025-10-12 12:51:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:51:45] [info] User Name: ubuntu
[2025-10-12 12:51:45] [info] Cores Used: 8
[2025-10-12 12:51:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Top Cell Information
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Layout Top Cell:      TOP
[2025-10-12 12:51:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:51:45] [info] Database Format:      GDSII
[2025-10-12 12:51:45] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Type of Errors | Count
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] DB_WARN -> 1
[2025-10-12 12:51:45] [info] LRF_ERR001 -> 14
[2025-10-12 12:51:45] [info] DB_WARN002 -> 159
[2025-10-12 12:51:45] [info] DB_WARN003 -> 161
[2025-10-12 12:51:45] [info] DRC_ERR9 -> 89
[2025-10-12 12:51:45] [info] DRC_ERR -> 67
[2025-10-12 12:51:45] [info] DRC_ERR5 -> 408
[2025-10-12 12:51:45] [info] DRC_ERR4 -> 241
[2025-10-12 12:51:45] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Rule Violation Summary
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:51:45] [info] |-----|-----|-----|
[2025-10-12 12:51:45] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:51:45] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:51:45] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:51:45] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:51:45] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:51:45] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:51:45] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:51:45] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:51:45] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:51:45] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:51:45] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:51:45] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:51:45] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:51:45] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:51:45] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:51:45] [info] |
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Performance Metrics
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] | Stage          | Time Taken |
[2025-10-12 12:51:45] [info] |-----|-----|
[2025-10-12 12:51:45] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:51:45] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:51:45] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Summary
```

```
[2025-10-12 12:51:45] [info] -----
[2025-10-12 12:51:45] [info] Total Errors:      1120
[2025-10-12 12:51:45] [info] Total Warnings:    321
[2025-10-12 12:51:45] [info] DRC Status:        FAILED
```

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```
[2025-10-12 12:52:43] [info] =====
[2025-10-12 12:52:43] [info]                      Design Rule Check Report
[2025-10-12 12:52:43] [info] =====

[2025-10-12 12:52:43] [info] Report Generated:      2025-10-12
[2025-10-12 12:52:43] [info] Run Start Time:       12:51:51 PM
[2025-10-12 12:52:43] [info] Run End Time:         12:52:43 PM
[2025-10-12 12:52:43] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 12:52:43] [info] Tool Version:          LVR_2025.1
[2025-10-12 12:52:43] [info] Host Name:             ip-172-31-37-173
[2025-10-12 12:52:43] [info] User Name:             ubuntu
[2025-10-12 12:52:43] [info] Cores Used:            8
[2025-10-12 12:52:43] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info]                      Top Cell Information
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info] Layout Top Cell:      TOP
[2025-10-12 12:52:43] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 12:52:43] [info] Database Format:        GDSII
[2025-10-12 12:52:43] [info] Verification Mode:     Full Check

[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info]                      Type of Errors | Count
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info] DB_WARN -> 1
[2025-10-12 12:52:43] [info] LRF_ERR001 -> 14
[2025-10-12 12:52:43] [info] DB_WARN002 -> 159
[2025-10-12 12:52:43] [info] DB_WARN003 -> 161
[2025-10-12 12:52:43] [info] DRC_ERR9 -> 89
[2025-10-12 12:52:43] [info] DRC_ERR -> 67
[2025-10-12 12:52:43] [info] DRC_ERR5 -> 408
[2025-10-12 12:52:43] [info] DRC_ERR4 -> 241
[2025-10-12 12:52:43] [info] DRC_ERR1 -> 301
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info]                      Rule Violation Summary
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:52:43] [info] |-----|-----|-----|
[2025-10-12 12:52:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:52:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:52:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:52:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:52:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:52:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:52:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:52:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:52:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:52:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:52:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:52:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:52:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:52:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:52:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:52:43] [info]
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info]                      Performance Metrics
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:52:43] [info] |-----|-----|
[2025-10-12 12:52:43] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 12:52:43] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 12:52:43] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info]                Summary
[2025-10-12 12:52:43] [info] -----
[2025-10-12 12:52:43] [info] Total Errors:      1120
[2025-10-12 12:52:43] [info] Total Warnings:    321
[2025-10-12 12:52:43] [info] DRC Status:        FAILED
```

lvr_summary_139.rpt

```
[2025-10-12 12:53:41] [info] =====
[2025-10-12 12:53:41] [info]                Design Rule Check Report
[2025-10-12 12:53:41] [info] =====

[2025-10-12 12:53:41] [info] Report Generated:   2025-10-12
[2025-10-12 12:53:41] [info] Run Start Time:     12:52:49 PM
[2025-10-12 12:53:41] [info] Run End Time:       12:53:41 PM
[2025-10-12 12:53:41] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 12:53:41] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:53:41] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:53:41] [info] User Name:          ubuntu
[2025-10-12 12:53:41] [info] Cores Used:          8
[2025-10-12 12:53:41] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info]                Top Cell Information
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] Layout Top Cell:    TOP
[2025-10-12 12:53:41] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:53:41] [info] Database Format:      GDSII
[2025-10-12 12:53:41] [info] Verification Mode:   Full Check

[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info]                Type of Errors | Count
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] DB_WARN -> 1
[2025-10-12 12:53:41] [info] LRF_ERR001 -> 14
[2025-10-12 12:53:41] [info] DB_WARN002 -> 159
[2025-10-12 12:53:41] [info] DB_WARN003 -> 161
[2025-10-12 12:53:41] [info] DRC_ERR9 -> 89
[2025-10-12 12:53:41] [info] DRC_ERR -> 67
[2025-10-12 12:53:41] [info] DRC_ERR5 -> 408
[2025-10-12 12:53:41] [info] DRC_ERR4 -> 241
[2025-10-12 12:53:41] [info] DRC_ERR1 -> 301
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info]                Rule Violation Summary
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:53:41] [info] |-----|-----|-----|
[2025-10-12 12:53:41] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:53:41] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:53:41] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:53:41] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:53:41] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 12:53:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:53:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:53:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:53:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:53:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:53:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:53:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:53:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:53:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:53:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:53:41] [info]
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] Performance Metrics
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] | Stage | Time Taken |
[2025-10-12 12:53:41] [info] |-----|-----|
[2025-10-12 12:53:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:53:41] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:53:41] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] Summary
[2025-10-12 12:53:41] [info] -----
[2025-10-12 12:53:41] [info] Total Errors: 1120
[2025-10-12 12:53:41] [info] Total Warnings: 321
[2025-10-12 12:53:41] [info] DRC Status: FAILED

```

lvr_summary_14.rpt

```

[2025-10-12 10:54:42] [info] =====
[2025-10-12 10:54:42] [info] Design Rule Check Report
[2025-10-12 10:54:42] [info] =====

[2025-10-12 10:54:42] [info] Report Generated: 2025-10-12
[2025-10-12 10:54:42] [info] Run Start Time: 10:53:50 AM
[2025-10-12 10:54:42] [info] Run End Time: 10:54:42 AM
[2025-10-12 10:54:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:54:42] [info] Tool Version: LVR_2025.1
[2025-10-12 10:54:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:54:42] [info] User Name: ubuntu
[2025-10-12 10:54:42] [info] Cores Used: 8
[2025-10-12 10:54:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Top Cell Information
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Layout Top Cell: TOP
[2025-10-12 10:54:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:54:42] [info] Database Format: GDSII
[2025-10-12 10:54:42] [info] Verification Mode: Full Check

[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Type of Errors | Count
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] DB_WARN -> 1
[2025-10-12 10:54:42] [info] LRF_ERR001 -> 14
[2025-10-12 10:54:42] [info] DB_WARN002 -> 159
[2025-10-12 10:54:42] [info] DB_WARN003 -> 161

```

```

[2025-10-12 10:54:42] [info] DRC_ERR9 -> 89
[2025-10-12 10:54:42] [info] DRC_ERR -> 67
[2025-10-12 10:54:42] [info] DRC_ERR5 -> 408
[2025-10-12 10:54:42] [info] DRC_ERR4 -> 241
[2025-10-12 10:54:42] [info] DRC_ERR1 -> 301
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Rule Violation Summary
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:54:42] [info] |-----|-----|-----|
[2025-10-12 10:54:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:54:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:54:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:54:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:54:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:54:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:54:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:54:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:54:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:54:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:54:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:54:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:54:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:54:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:54:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Performance Metrics
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] | Stage | Time Taken |
[2025-10-12 10:54:42] [info] |-----|-----|
[2025-10-12 10:54:42] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:54:42] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 10:54:42] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Summary
[2025-10-12 10:54:42] [info] -----
[2025-10-12 10:54:42] [info] Total Errors: 1120
[2025-10-12 10:54:42] [info] Total Warnings: 321
[2025-10-12 10:54:42] [info] DRC Status: FAILED

```

lvr_summary_140.rpt

```

[2025-10-12 12:54:38] [info] =====
[2025-10-12 12:54:38] [info] Design Rule Check Report
[2025-10-12 12:54:38] [info] =====

[2025-10-12 12:54:38] [info] Report Generated: 2025-10-12
[2025-10-12 12:54:38] [info] Run Start Time: 12:53:46 PM
[2025-10-12 12:54:38] [info] Run End Time: 12:54:38 PM
[2025-10-12 12:54:38] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:54:38] [info] Tool Version: LVR_2025.1
[2025-10-12 12:54:38] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:54:38] [info] User Name: ubuntu
[2025-10-12 12:54:38] [info] Cores Used: 8
[2025-10-12 12:54:38] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Top Cell Information
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Layout Top Cell:      TOP
[2025-10-12 12:54:38] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:54:38] [info] Database Format:      GDSII
[2025-10-12 12:54:38] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Type of Errors | Count
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] DB_WARN -> 1
[2025-10-12 12:54:38] [info] LRF_ERR001 -> 14
[2025-10-12 12:54:38] [info] DB_WARN002 -> 159
[2025-10-12 12:54:38] [info] DB_WARN003 -> 161
[2025-10-12 12:54:38] [info] DRC_ERR9 -> 89
[2025-10-12 12:54:38] [info] DRC_ERR -> 67
[2025-10-12 12:54:38] [info] DRC_ERR5 -> 408
[2025-10-12 12:54:38] [info] DRC_ERR4 -> 241
[2025-10-12 12:54:38] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Rule Violation Summary
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:54:38] [info] |-----|-----|-----|
[2025-10-12 12:54:38] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:54:38] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:54:38] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:54:38] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:54:38] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:54:38] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:54:38] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:54:38] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:54:38] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:54:38] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:54:38] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:54:38] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:54:38] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:54:38] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:54:38] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:54:38] [info] |
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Performance Metrics
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] | Stage          | Time Taken |
[2025-10-12 12:54:38] [info] |-----|-----|
[2025-10-12 12:54:38] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:54:38] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:54:38] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Summary
```

```
[2025-10-12 12:54:38] [info] -----
[2025-10-12 12:54:38] [info] Total Errors:      1120
[2025-10-12 12:54:38] [info] Total Warnings:    321
[2025-10-12 12:54:38] [info] DRC Status:        FAILED
```

lvr_summary_141.rpt

```
[2025-10-12 12:55:35] [info] =====
[2025-10-12 12:55:35] [info] Design Rule Check Report
[2025-10-12 12:55:35] [info] =====

[2025-10-12 12:55:35] [info] Report Generated: 2025-10-12
[2025-10-12 12:55:35] [info] Run Start Time: 12:54:43 PM
[2025-10-12 12:55:35] [info] Run End Time: 12:55:35 PM
[2025-10-12 12:55:35] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:55:35] [info] Tool Version: LVR_2025.1
[2025-10-12 12:55:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:55:35] [info] User Name: ubuntu
[2025-10-12 12:55:35] [info] Cores Used: 8
[2025-10-12 12:55:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] Top Cell Information
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] Layout Top Cell: TOP
[2025-10-12 12:55:35] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:55:35] [info] Database Format: GDSII
[2025-10-12 12:55:35] [info] Verification Mode: Full Check

[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] Type of Errors | Count
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] DB_WARN -> 1
[2025-10-12 12:55:35] [info] LRF_ERR001 -> 14
[2025-10-12 12:55:35] [info] DB_WARN002 -> 159
[2025-10-12 12:55:35] [info] DB_WARN003 -> 161
[2025-10-12 12:55:35] [info] DRC_ERR9 -> 89
[2025-10-12 12:55:35] [info] DRC_ERR -> 67
[2025-10-12 12:55:35] [info] DRC_ERR5 -> 408
[2025-10-12 12:55:35] [info] DRC_ERR4 -> 241
[2025-10-12 12:55:35] [info] DRC_ERR1 -> 301
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] Rule Violation Summary
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:55:35] [info] |-----|-----|-----|
[2025-10-12 12:55:35] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:55:35] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:55:35] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:55:35] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:55:35] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:55:35] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:55:35] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:55:35] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:55:35] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:55:35] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:55:35] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:55:35] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:55:35] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:55:35] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:55:35] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:55:35] [info]
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] Performance Metrics
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:55:35] [info] |-----|-----|
[2025-10-12 12:55:35] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:55:35] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 12:55:35] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info]                Summary
[2025-10-12 12:55:35] [info] -----
[2025-10-12 12:55:35] [info] Total Errors:    1120
[2025-10-12 12:55:35] [info] Total Warnings:    321
[2025-10-12 12:55:35] [info] DRC Status:      FAILED
```

lvr_summary_142.rpt

```
[2025-10-12 12:56:32] [info] =====
[2025-10-12 12:56:32] [info]                Design Rule Check Report
[2025-10-12 12:56:32] [info] =====

[2025-10-12 12:56:32] [info] Report Generated:    2025-10-12
[2025-10-12 12:56:32] [info] Run Start Time:      12:55:40 PM
[2025-10-12 12:56:32] [info] Run End Time:        12:56:32 PM
[2025-10-12 12:56:32] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 12:56:32] [info] Tool Version:        LVR_2025.1
[2025-10-12 12:56:32] [info] Host Name:           ip-172-31-37-173
[2025-10-12 12:56:32] [info] User Name:           ubuntu
[2025-10-12 12:56:32] [info] Cores Used:          8
[2025-10-12 12:56:32] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info]                Top Cell Information
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] Layout Top Cell:     TOP
[2025-10-12 12:56:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:56:32] [info] Database Format:       GDSII
[2025-10-12 12:56:32] [info] Verification Mode:    Full Check

[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info]                Type of Errors | Count
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] DB_WARN -> 1
[2025-10-12 12:56:32] [info] LRF_ERR001 -> 14
[2025-10-12 12:56:32] [info] DB_WARN002 -> 159
[2025-10-12 12:56:32] [info] DB_WARN003 -> 161
[2025-10-12 12:56:32] [info] DRC_ERR9 -> 89
[2025-10-12 12:56:32] [info] DRC_ERR -> 67
[2025-10-12 12:56:32] [info] DRC_ERR5 -> 408
[2025-10-12 12:56:32] [info] DRC_ERR4 -> 241
[2025-10-12 12:56:32] [info] DRC_ERR1 -> 301
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info]                Rule Violation Summary
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:56:32] [info] |-----|-----|-----|
[2025-10-12 12:56:32] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:56:32] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:56:32] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:56:32] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:56:32] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```

[2025-10-12 12:56:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:56:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:56:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:56:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:56:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:56:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:56:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:56:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:56:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:56:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:56:32] [info]
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] Performance Metrics
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] | Stage | Time Taken |
[2025-10-12 12:56:32] [info] |-----|-----|
[2025-10-12 12:56:32] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:56:32] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:56:32] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] Summary
[2025-10-12 12:56:32] [info] -----
[2025-10-12 12:56:32] [info] Total Errors: 1120
[2025-10-12 12:56:32] [info] Total Warnings: 321
[2025-10-12 12:56:32] [info] DRC Status: FAILED

```

lvr_summary_143.rpt

```

[2025-10-12 12:57:28] [info] =====
[2025-10-12 12:57:28] [info] Design Rule Check Report
[2025-10-12 12:57:28] [info] =====

[2025-10-12 12:57:28] [info] Report Generated: 2025-10-12
[2025-10-12 12:57:28] [info] Run Start Time: 12:56:38 PM
[2025-10-12 12:57:28] [info] Run End Time: 12:57:28 PM
[2025-10-12 12:57:28] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:57:28] [info] Tool Version: LVR_2025.1
[2025-10-12 12:57:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:57:28] [info] User Name: ubuntu
[2025-10-12 12:57:28] [info] Cores Used: 8
[2025-10-12 12:57:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Top Cell Information
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Layout Top Cell: TOP
[2025-10-12 12:57:28] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:57:28] [info] Database Format: GDSII
[2025-10-12 12:57:28] [info] Verification Mode: Full Check

[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Type of Errors | Count
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] DB_WARN -> 1
[2025-10-12 12:57:28] [info] LRF_ERR001 -> 14
[2025-10-12 12:57:28] [info] DB_WARN002 -> 159
[2025-10-12 12:57:28] [info] DB_WARN003 -> 161

```

```

[2025-10-12 12:57:28] [info] DRC_ERR9 -> 89
[2025-10-12 12:57:28] [info] DRC_ERR -> 67
[2025-10-12 12:57:28] [info] DRC_ERR5 -> 408
[2025-10-12 12:57:28] [info] DRC_ERR4 -> 241
[2025-10-12 12:57:28] [info] DRC_ERR1 -> 301
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Rule Violation Summary
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:57:28] [info] |-----|-----|-----|
[2025-10-12 12:57:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:57:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:57:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:57:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:57:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:57:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:57:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:57:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:57:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:57:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:57:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:57:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:57:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:57:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:57:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Performance Metrics
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] | Stage | Time Taken |
[2025-10-12 12:57:28] [info] |-----|-----|
[2025-10-12 12:57:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:57:28] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:57:28] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Summary
[2025-10-12 12:57:28] [info] -----
[2025-10-12 12:57:28] [info] Total Errors: 1120
[2025-10-12 12:57:28] [info] Total Warnings: 321
[2025-10-12 12:57:28] [info] DRC Status: FAILED

```

lvr_summary_144.rpt

```

[2025-10-12 12:58:25] [info] =====
[2025-10-12 12:58:25] [info] Design Rule Check Report
[2025-10-12 12:58:25] [info] =====

[2025-10-12 12:58:25] [info] Report Generated: 2025-10-12
[2025-10-12 12:58:25] [info] Run Start Time: 12:57:34 PM
[2025-10-12 12:58:25] [info] Run End Time: 12:58:25 PM
[2025-10-12 12:58:25] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:58:25] [info] Tool Version: LVR_2025.1
[2025-10-12 12:58:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:58:25] [info] User Name: ubuntu
[2025-10-12 12:58:25] [info] Cores Used: 8
[2025-10-12 12:58:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Top Cell Information
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Layout Top Cell:      TOP
[2025-10-12 12:58:25] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:58:25] [info] Database Format:      GDSII
[2025-10-12 12:58:25] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Type of Errors | Count
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] DB_WARN -> 1
[2025-10-12 12:58:25] [info] LRF_ERR001 -> 14
[2025-10-12 12:58:25] [info] DB_WARN002 -> 159
[2025-10-12 12:58:25] [info] DB_WARN003 -> 161
[2025-10-12 12:58:25] [info] DRC_ERR9 -> 89
[2025-10-12 12:58:25] [info] DRC_ERR -> 67
[2025-10-12 12:58:25] [info] DRC_ERR5 -> 408
[2025-10-12 12:58:25] [info] DRC_ERR4 -> 241
[2025-10-12 12:58:25] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Rule Violation Summary
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:58:25] [info] |-----|-----|-----|
[2025-10-12 12:58:25] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:58:25] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:58:25] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:58:25] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:58:25] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:58:25] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:58:25] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:58:25] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:58:25] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:58:25] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:58:25] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:58:25] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:58:25] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:58:25] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:58:25] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Performance Metrics
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] | Stage          | Time Taken |
[2025-10-12 12:58:25] [info] |-----|-----|
[2025-10-12 12:58:25] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:58:25] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 12:58:25] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Summary
[2025-10-12 12:58:25] [info] -----
[2025-10-12 12:58:25] [info] Total Errors:      1120
[2025-10-12 12:58:25] [info] Total Warnings:    321
[2025-10-12 12:58:25] [info] DRC Status:        FAILED
```

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```
[2025-10-12 12:59:24] [info] =====
[2025-10-12 12:59:24] [info] Design Rule Check Report
[2025-10-12 12:59:24] [info] =====

[2025-10-12 12:59:24] [info] Report Generated: 2025-10-12
[2025-10-12 12:59:24] [info] Run Start Time: 12:58:32 PM
[2025-10-12 12:59:24] [info] Run End Time: 12:59:24 PM
[2025-10-12 12:59:24] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 12:59:24] [info] Tool Version: LVR_2025.1
[2025-10-12 12:59:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:59:24] [info] User Name: ubuntu
[2025-10-12 12:59:24] [info] Cores Used: 8
[2025-10-12 12:59:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] Top Cell Information
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] Layout Top Cell: TOP
[2025-10-12 12:59:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:59:24] [info] Database Format: GDSII
[2025-10-12 12:59:24] [info] Verification Mode: Full Check

[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] Type of Errors | Count
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] DB_WARN -> 1
[2025-10-12 12:59:24] [info] LRF_ERR001 -> 14
[2025-10-12 12:59:24] [info] DB_WARN002 -> 159
[2025-10-12 12:59:24] [info] DB_WARN003 -> 161
[2025-10-12 12:59:24] [info] DRC_ERR9 -> 89
[2025-10-12 12:59:24] [info] DRC_ERR -> 67
[2025-10-12 12:59:24] [info] DRC_ERR5 -> 408
[2025-10-12 12:59:24] [info] DRC_ERR4 -> 241
[2025-10-12 12:59:24] [info] DRC_ERR1 -> 301
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] Rule Violation Summary
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:59:24] [info] |-----|-----|-----|
[2025-10-12 12:59:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:59:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:59:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:59:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:59:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:59:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:59:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:59:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:59:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:59:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:59:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:59:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:59:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:59:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:59:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:59:24] [info]
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] Performance Metrics
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:59:24] [info] |-----|-----|
[2025-10-12 12:59:24] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:59:24] [info] | Rule Checking        | 0 minutes 41 seconds |
[2025-10-12 12:59:24] [info] | Report Generation     | 0 minutes 0 seconds  |

[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info]                Summary
[2025-10-12 12:59:24] [info] -----
[2025-10-12 12:59:24] [info] Total Errors:    1120
[2025-10-12 12:59:24] [info] Total Warnings:    321
[2025-10-12 12:59:24] [info] DRC Status:      FAILED
```

lvr_summary_146.rpt

```
[2025-10-12 13:00:19] [info] =====
[2025-10-12 13:00:19] [info]                Design Rule Check Report
[2025-10-12 13:00:19] [info] =====

[2025-10-12 13:00:19] [info] Report Generated:    2025-10-12
[2025-10-12 13:00:19] [info] Run Start Time:      12:59:28 PM
[2025-10-12 13:00:19] [info] Run End Time:        01:00:19 PM
[2025-10-12 13:00:19] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 13:00:19] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:00:19] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:00:19] [info] User Name:           ubuntu
[2025-10-12 13:00:19] [info] Cores Used:          8
[2025-10-12 13:00:19] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info]                Top Cell Information
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] Layout Top Cell:     TOP
[2025-10-12 13:00:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:00:19] [info] Database Format:       GDSII
[2025-10-12 13:00:19] [info] Verification Mode:    Full Check

[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info]                Type of Errors | Count
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] DB_WARN -> 1
[2025-10-12 13:00:19] [info] LRF_ERR001 -> 14
[2025-10-12 13:00:19] [info] DB_WARN002 -> 159
[2025-10-12 13:00:19] [info] DB_WARN003 -> 161
[2025-10-12 13:00:19] [info] DRC_ERR9 -> 89
[2025-10-12 13:00:19] [info] DRC_ERR -> 67
[2025-10-12 13:00:19] [info] DRC_ERR5 -> 408
[2025-10-12 13:00:19] [info] DRC_ERR4 -> 241
[2025-10-12 13:00:19] [info] DRC_ERR1 -> 301
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info]                Rule Violation Summary
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:00:19] [info] |-----|-----|-----|
[2025-10-12 13:00:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:00:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:00:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:00:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:00:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 13:00:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:00:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:00:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:00:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:00:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:00:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:00:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:00:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:00:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:00:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:00:19] [info]
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] Performance Metrics
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] | Stage | Time Taken |
[2025-10-12 13:00:19] [info] |-----|-----|
[2025-10-12 13:00:19] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:00:19] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:00:19] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] Summary
[2025-10-12 13:00:19] [info] -----
[2025-10-12 13:00:19] [info] Total Errors: 1120
[2025-10-12 13:00:19] [info] Total Warnings: 321
[2025-10-12 13:00:19] [info] DRC Status: FAILED

```

lvr_summary_147.rpt

```

[2025-10-12 13:01:18] [info] =====
[2025-10-12 13:01:18] [info] Design Rule Check Report
[2025-10-12 13:01:18] [info] =====

[2025-10-12 13:01:18] [info] Report Generated: 2025-10-12
[2025-10-12 13:01:18] [info] Run Start Time: 01:00:25 PM
[2025-10-12 13:01:18] [info] Run End Time: 01:01:18 PM
[2025-10-12 13:01:18] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 13:01:18] [info] Tool Version: LVR_2025.1
[2025-10-12 13:01:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:01:18] [info] User Name: ubuntu
[2025-10-12 13:01:18] [info] Cores Used: 8
[2025-10-12 13:01:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Top Cell Information
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Layout Top Cell: TOP
[2025-10-12 13:01:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:01:18] [info] Database Format: GDSII
[2025-10-12 13:01:18] [info] Verification Mode: Full Check

[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Type of Errors | Count
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] DB_WARN -> 1
[2025-10-12 13:01:18] [info] LRF_ERR001 -> 14
[2025-10-12 13:01:18] [info] DB_WARN002 -> 159
[2025-10-12 13:01:18] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:01:18] [info] DRC_ERR9 -> 89
[2025-10-12 13:01:18] [info] DRC_ERR -> 67
[2025-10-12 13:01:18] [info] DRC_ERR5 -> 408
[2025-10-12 13:01:18] [info] DRC_ERR4 -> 241
[2025-10-12 13:01:18] [info] DRC_ERR1 -> 301
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Rule Violation Summary
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:01:18] [info] |-----|-----|-----|
[2025-10-12 13:01:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:01:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:01:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:01:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:01:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:01:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:01:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:01:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:01:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:01:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:01:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:01:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:01:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:01:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:01:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Performance Metrics
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] | Stage | Time Taken |
[2025-10-12 13:01:18] [info] |-----|-----|
[2025-10-12 13:01:18] [info] | GDS Parsing | 0 minutes 10 seconds |
[2025-10-12 13:01:18] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 13:01:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Summary
[2025-10-12 13:01:18] [info] -----
[2025-10-12 13:01:18] [info] Total Errors: 1120
[2025-10-12 13:01:18] [info] Total Warnings: 321
[2025-10-12 13:01:18] [info] DRC Status: FAILED

```

lvr_summary_148.rpt

```

[2025-10-12 13:02:15] [info] =====
[2025-10-12 13:02:15] [info] Design Rule Check Report
[2025-10-12 13:02:15] [info] =====

[2025-10-12 13:02:15] [info] Report Generated: 2025-10-12
[2025-10-12 13:02:15] [info] Run Start Time: 01:01:22 PM
[2025-10-12 13:02:15] [info] Run End Time: 01:02:15 PM
[2025-10-12 13:02:15] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 13:02:15] [info] Tool Version: LVR_2025.1
[2025-10-12 13:02:15] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:02:15] [info] User Name: ubuntu
[2025-10-12 13:02:15] [info] Cores Used: 8
[2025-10-12 13:02:15] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Top Cell Information
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Layout Top Cell:      TOP
[2025-10-12 13:02:15] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:02:15] [info] Database Format:      GDSII
[2025-10-12 13:02:15] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Type of Errors | Count
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] DB_WARN -> 1
[2025-10-12 13:02:15] [info] LRF_ERR001 -> 14
[2025-10-12 13:02:15] [info] DB_WARN002 -> 159
[2025-10-12 13:02:15] [info] DB_WARN003 -> 161
[2025-10-12 13:02:15] [info] DRC_ERR9 -> 89
[2025-10-12 13:02:15] [info] DRC_ERR -> 67
[2025-10-12 13:02:15] [info] DRC_ERR5 -> 408
[2025-10-12 13:02:15] [info] DRC_ERR4 -> 241
[2025-10-12 13:02:15] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Rule Violation Summary
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:02:15] [info] |-----|-----|-----|
[2025-10-12 13:02:15] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:02:15] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:02:15] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:02:15] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:02:15] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:02:15] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:02:15] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:02:15] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:02:15] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:02:15] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:02:15] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:02:15] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:02:15] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:02:15] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:02:15] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:02:15] [info] |
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Performance Metrics
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] | Stage          | Time Taken |
[2025-10-12 13:02:15] [info] |-----|-----|
[2025-10-12 13:02:15] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:02:15] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:02:15] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Summary
```

```
[2025-10-12 13:02:15] [info] -----
[2025-10-12 13:02:15] [info] Total Errors:      1120
[2025-10-12 13:02:15] [info] Total Warnings:    321
[2025-10-12 13:02:15] [info] DRC Status:        FAILED
```


lvr_summary_149.rpt

```
[2025-10-12 13:03:12] [info] =====
[2025-10-12 13:03:12] [info]                      Design Rule Check Report
[2025-10-12 13:03:12] [info] =====

[2025-10-12 13:03:12] [info] Report Generated:      2025-10-12
[2025-10-12 13:03:12] [info] Run Start Time:       01:02:20 PM
[2025-10-12 13:03:12] [info] Run End Time:         01:03:12 PM
[2025-10-12 13:03:12] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 13:03:12] [info] Tool Version:          LVR_2025.1
[2025-10-12 13:03:12] [info] Host Name:             ip-172-31-37-173
[2025-10-12 13:03:12] [info] User Name:             ubuntu
[2025-10-12 13:03:12] [info] Cores Used:            8
[2025-10-12 13:03:12] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info]                      Top Cell Information
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info] Layout Top Cell:      TOP
[2025-10-12 13:03:12] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 13:03:12] [info] Database Format:        GDSII
[2025-10-12 13:03:12] [info] Verification Mode:     Full Check

[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info]                      Type of Errors | Count
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info] DB_WARN -> 1
[2025-10-12 13:03:12] [info] LRF_ERR001 -> 14
[2025-10-12 13:03:12] [info] DB_WARN002 -> 159
[2025-10-12 13:03:12] [info] DB_WARN003 -> 161
[2025-10-12 13:03:12] [info] DRC_ERR9 -> 89
[2025-10-12 13:03:12] [info] DRC_ERR -> 67
[2025-10-12 13:03:12] [info] DRC_ERR5 -> 408
[2025-10-12 13:03:12] [info] DRC_ERR4 -> 241
[2025-10-12 13:03:12] [info] DRC_ERR1 -> 301
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info]                      Rule Violation Summary
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:03:12] [info] |-----|-----|-----|
[2025-10-12 13:03:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:03:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:03:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:03:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:03:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:03:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:03:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:03:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:03:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:03:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:03:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:03:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:03:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:03:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:03:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:03:12] [info]
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info]                      Performance Metrics
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:03:12] [info] |-----|-----|
[2025-10-12 13:03:12] [info] | GDS Parsing          | 0 minutes 10 seconds |
[2025-10-12 13:03:12] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 13:03:12] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info]                Summary
[2025-10-12 13:03:12] [info] -----
[2025-10-12 13:03:12] [info] Total Errors:      1120
[2025-10-12 13:03:12] [info] Total Warnings:    321
[2025-10-12 13:03:12] [info] DRC Status:        FAILED
```

lvr_summary_15.rpt

```
[2025-10-12 10:55:36] [info] =====
[2025-10-12 10:55:36] [info]                Design Rule Check Report
[2025-10-12 10:55:36] [info] =====

[2025-10-12 10:55:36] [info] Report Generated:   2025-10-12
[2025-10-12 10:55:36] [info] Run Start Time:     10:54:46 AM
[2025-10-12 10:55:36] [info] Run End Time:       10:55:36 AM
[2025-10-12 10:55:36] [info] Time Taken:         0 minutes 49 seconds

[2025-10-12 10:55:36] [info] Tool Version:       LVR_2025.1
[2025-10-12 10:55:36] [info] Host Name:          ip-172-31-37-173
[2025-10-12 10:55:36] [info] User Name:          ubuntu
[2025-10-12 10:55:36] [info] Cores Used:         8
[2025-10-12 10:55:36] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info]                Top Cell Information
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info] Layout Top Cell:    TOP
[2025-10-12 10:55:36] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 10:55:36] [info] Database Format:      GDSII
[2025-10-12 10:55:36] [info] Verification Mode:   Full Check

[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info]                Type of Errors | Count
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info] DB_WARN -> 1
[2025-10-12 10:55:36] [info] LRF_ERR001 -> 14
[2025-10-12 10:55:36] [info] DB_WARN002 -> 159
[2025-10-12 10:55:36] [info] DB_WARN003 -> 161
[2025-10-12 10:55:36] [info] DRC_ERR9 -> 89
[2025-10-12 10:55:36] [info] DRC_ERR -> 67
[2025-10-12 10:55:36] [info] DRC_ERR5 -> 408
[2025-10-12 10:55:36] [info] DRC_ERR4 -> 241
[2025-10-12 10:55:36] [info] DRC_ERR1 -> 301
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info]                Rule Violation Summary
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 10:55:36] [info] |-----|-----|-----|
[2025-10-12 10:55:36] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:55:36] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:55:36] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:55:36] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:55:36] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 10:55:36] [info] | m1.6          | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 10:55:36] [info] | m4.5          | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391          |
[2025-10-12 10:55:36] [info] | m4.5ab        | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301          |
[2025-10-12 10:55:36] [info] | psdm.1         | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 10:55:36] [info] | licon.5        | "licon.5 : min. diff enclosure of licon : 0.04um" | 131          |
[2025-10-12 10:55:36] [info] | li.1           | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 10:55:36] [info] | li.1a          | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 10:55:36] [info] | m1.1           | "m1.1 : min. m1 width : 0.14um" | 3          |
[2025-10-12 10:55:36] [info] | licon.8a       | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 10:55:36] [info] | npc.1          | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 10:55:36] [info]
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info]                      Performance Metrics
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info] | Stage              | Time Taken |
[2025-10-12 10:55:36] [info] |-----|-----|
[2025-10-12 10:55:36] [info] | GDS Parsing         | 0 minutes 9 seconds |
[2025-10-12 10:55:36] [info] | Rule Checking        | 0 minutes 38 seconds |
[2025-10-12 10:55:36] [info] | Report Generation    | 0 minutes 0 seconds |

[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info]                      Summary
[2025-10-12 10:55:36] [info] -----
[2025-10-12 10:55:36] [info] Total Errors:      1120
[2025-10-12 10:55:36] [info] Total Warnings:    321
[2025-10-12 10:55:36] [info] DRC Status:        FAILED

```

lvr_summary_150.rpt

```

[2025-10-12 13:04:09] [info] =====
[2025-10-12 13:04:09] [info]                      Design Rule Check Report
[2025-10-12 13:04:09] [info] =====

[2025-10-12 13:04:09] [info] Report Generated:   2025-10-12
[2025-10-12 13:04:09] [info] Run Start Time:     01:03:17 PM
[2025-10-12 13:04:09] [info] Run End Time:       01:04:09 PM
[2025-10-12 13:04:09] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 13:04:09] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:04:09] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:04:09] [info] User Name:          ubuntu
[2025-10-12 13:04:09] [info] Cores Used:         8
[2025-10-12 13:04:09] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info]                      Top Cell Information
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] Layout Top Cell:    TOP
[2025-10-12 13:04:09] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:04:09] [info] Database Format:      GDSII
[2025-10-12 13:04:09] [info] Verification Mode:   Full Check

[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info]                      Type of Errors | Count
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] DB_WARN -> 1
[2025-10-12 13:04:09] [info] LRF_ERR001 -> 14
[2025-10-12 13:04:09] [info] DB_WARN002 -> 159
[2025-10-12 13:04:09] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:04:09] [info] DRC_ERR9 -> 89
[2025-10-12 13:04:09] [info] DRC_ERR -> 67
[2025-10-12 13:04:09] [info] DRC_ERR5 -> 408
[2025-10-12 13:04:09] [info] DRC_ERR4 -> 241
[2025-10-12 13:04:09] [info] DRC_ERR1 -> 301
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] Rule Violation Summary
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:04:09] [info] |-----|-----|-----|
[2025-10-12 13:04:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:04:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:04:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:04:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:04:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:04:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:04:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:04:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:04:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:04:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:04:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:04:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:04:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:04:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:04:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] Performance Metrics
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] | Stage | Time Taken |
[2025-10-12 13:04:09] [info] |-----|-----|
[2025-10-12 13:04:09] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:04:09] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 13:04:09] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] Summary
[2025-10-12 13:04:09] [info] -----
[2025-10-12 13:04:09] [info] Total Errors: 1120
[2025-10-12 13:04:09] [info] Total Warnings: 321
[2025-10-12 13:04:09] [info] DRC Status: FAILED

```

lvr_summary_151.rpt

```

[2025-10-12 13:05:05] [info] =====
[2025-10-12 13:05:05] [info] Design Rule Check Report
[2025-10-12 13:05:05] [info] =====

[2025-10-12 13:05:05] [info] Report Generated: 2025-10-12
[2025-10-12 13:05:05] [info] Run Start Time: 01:04:13 PM
[2025-10-12 13:05:05] [info] Run End Time: 01:05:05 PM
[2025-10-12 13:05:05] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:05:05] [info] Tool Version: LVR_2025.1
[2025-10-12 13:05:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:05:05] [info] User Name: ubuntu
[2025-10-12 13:05:05] [info] Cores Used: 8
[2025-10-12 13:05:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Top Cell Information
[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Layout Top Cell:      TOP
[2025-10-12 13:05:05] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:05:05] [info] Database Format:      GDSII
[2025-10-12 13:05:05] [info] Verification Mode:    Full Check

```

```

[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Type of Errors | Count
[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] DB_WARN -> 1
[2025-10-12 13:05:05] [info] LRF_ERR001 -> 14
[2025-10-12 13:05:05] [info] DB_WARN002 -> 159
[2025-10-12 13:05:05] [info] DB_WARN003 -> 161
[2025-10-12 13:05:05] [info] DRC_ERR9 -> 89
[2025-10-12 13:05:05] [info] DRC_ERR -> 67
[2025-10-12 13:05:05] [info] DRC_ERR5 -> 408
[2025-10-12 13:05:05] [info] DRC_ERR4 -> 241
[2025-10-12 13:05:05] [info] DRC_ERR1 -> 301

```

```

[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Rule Violation Summary
[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:05:05] [info] |-----|-----|-----|
[2025-10-12 13:05:05] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:05:05] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:05:05] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:05:05] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:05:05] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:05:05] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:05:05] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:05:05] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:05:05] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:05:05] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:05:05] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:05:05] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:05:05] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:05:05] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:05:05] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |

```

```

[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Performance Metrics
[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] | Stage          | Time Taken |
[2025-10-12 13:05:05] [info] |-----|-----|
[2025-10-12 13:05:05] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:05:05] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 13:05:05] [info] | Report Generation | 0 minutes 0 seconds |

```

```

[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Summary
[2025-10-12 13:05:05] [info] -----
[2025-10-12 13:05:05] [info] Total Errors:      1120
[2025-10-12 13:05:05] [info] Total Warnings:    321
[2025-10-12 13:05:05] [info] DRC Status:        FAILED

```

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```
[2025-10-12 13:06:04] [info] =====
[2025-10-12 13:06:04] [info] Design Rule Check Report
[2025-10-12 13:06:04] [info] =====

[2025-10-12 13:06:04] [info] Report Generated: 2025-10-12
[2025-10-12 13:06:04] [info] Run Start Time: 01:05:12 PM
[2025-10-12 13:06:04] [info] Run End Time: 01:06:04 PM
[2025-10-12 13:06:04] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:06:04] [info] Tool Version: LVR_2025.1
[2025-10-12 13:06:04] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:06:04] [info] User Name: ubuntu
[2025-10-12 13:06:04] [info] Cores Used: 8
[2025-10-12 13:06:04] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] Top Cell Information
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] Layout Top Cell: TOP
[2025-10-12 13:06:04] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:06:04] [info] Database Format: GDSII
[2025-10-12 13:06:04] [info] Verification Mode: Full Check

[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] Type of Errors | Count
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] DB_WARN -> 1
[2025-10-12 13:06:04] [info] LRF_ERR001 -> 14
[2025-10-12 13:06:04] [info] DB_WARN002 -> 159
[2025-10-12 13:06:04] [info] DB_WARN003 -> 161
[2025-10-12 13:06:04] [info] DRC_ERR9 -> 89
[2025-10-12 13:06:04] [info] DRC_ERR -> 67
[2025-10-12 13:06:04] [info] DRC_ERR5 -> 408
[2025-10-12 13:06:04] [info] DRC_ERR4 -> 241
[2025-10-12 13:06:04] [info] DRC_ERR1 -> 301
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] Rule Violation Summary
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:06:04] [info] |-----|-----|-----|
[2025-10-12 13:06:04] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:06:04] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:06:04] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:06:04] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:06:04] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:06:04] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:06:04] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:06:04] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:06:04] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:06:04] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:06:04] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:06:04] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:06:04] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:06:04] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:06:04] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:06:04] [info]
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] Performance Metrics
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:06:04] [info] |-----|-----|
[2025-10-12 13:06:04] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 13:06:04] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 13:06:04] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info]              Summary
[2025-10-12 13:06:04] [info] -----
[2025-10-12 13:06:04] [info] Total Errors:      1120
[2025-10-12 13:06:04] [info] Total Warnings:    321
[2025-10-12 13:06:04] [info] DRC Status:        FAILED
```

lvr_summary_153.rpt

```
[2025-10-12 13:07:01] [info] =====
[2025-10-12 13:07:01] [info]              Design Rule Check Report
[2025-10-12 13:07:01] [info] =====

[2025-10-12 13:07:01] [info] Report Generated:   2025-10-12
[2025-10-12 13:07:01] [info] Run Start Time:     01:06:08 PM
[2025-10-12 13:07:01] [info] Run End Time:       01:07:01 PM
[2025-10-12 13:07:01] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 13:07:01] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:07:01] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:07:01] [info] User Name:          ubuntu
[2025-10-12 13:07:01] [info] Cores Used:         8
[2025-10-12 13:07:01] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info]              Top Cell Information
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] Layout Top Cell:    TOP
[2025-10-12 13:07:01] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:07:01] [info] Database Format:     GDSII
[2025-10-12 13:07:01] [info] Verification Mode:   Full Check

[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info]              Type of Errors | Count
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] DB_WARN -> 1
[2025-10-12 13:07:01] [info] LRF_ERR001 -> 14
[2025-10-12 13:07:01] [info] DB_WARN002 -> 159
[2025-10-12 13:07:01] [info] DB_WARN003 -> 161
[2025-10-12 13:07:01] [info] DRC_ERR9 -> 89
[2025-10-12 13:07:01] [info] DRC_ERR -> 67
[2025-10-12 13:07:01] [info] DRC_ERR5 -> 408
[2025-10-12 13:07:01] [info] DRC_ERR4 -> 241
[2025-10-12 13:07:01] [info] DRC_ERR1 -> 301
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info]              Rule Violation Summary
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:07:01] [info] |-----|-----|-----|
[2025-10-12 13:07:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:07:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:07:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:07:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:07:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 13:07:01] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:07:01] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:07:01] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:07:01] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:07:01] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:07:01] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:07:01] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:07:01] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:07:01] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:07:01] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:07:01] [info]
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] Performance Metrics
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] | Stage | Time Taken |
[2025-10-12 13:07:01] [info] |-----|-----|
[2025-10-12 13:07:01] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:07:01] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 13:07:01] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] Summary
[2025-10-12 13:07:01] [info] -----
[2025-10-12 13:07:01] [info] Total Errors: 1120
[2025-10-12 13:07:01] [info] Total Warnings: 321
[2025-10-12 13:07:01] [info] DRC Status: FAILED
```

lvr_summary_154.rpt

```
[2025-10-12 13:07:58] [info] =====
[2025-10-12 13:07:58] [info] Design Rule Check Report
[2025-10-12 13:07:58] [info] =====

[2025-10-12 13:07:58] [info] Report Generated: 2025-10-12
[2025-10-12 13:07:58] [info] Run Start Time: 01:07:06 PM
[2025-10-12 13:07:58] [info] Run End Time: 01:07:58 PM
[2025-10-12 13:07:58] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:07:58] [info] Tool Version: LVR_2025.1
[2025-10-12 13:07:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:07:58] [info] User Name: ubuntu
[2025-10-12 13:07:58] [info] Cores Used: 8
[2025-10-12 13:07:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Top Cell Information
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Layout Top Cell: TOP
[2025-10-12 13:07:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:07:58] [info] Database Format: GDSII
[2025-10-12 13:07:58] [info] Verification Mode: Full Check

[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Type of Errors | Count
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] DB_WARN -> 1
[2025-10-12 13:07:58] [info] LRF_ERR001 -> 14
[2025-10-12 13:07:58] [info] DB_WARN002 -> 159
[2025-10-12 13:07:58] [info] DB_WARN003 -> 161
```



```
[2025-10-12 13:07:58] [info] DRC_ERR9 -> 89
[2025-10-12 13:07:58] [info] DRC_ERR -> 67
[2025-10-12 13:07:58] [info] DRC_ERR5 -> 408
[2025-10-12 13:07:58] [info] DRC_ERR4 -> 241
[2025-10-12 13:07:58] [info] DRC_ERR1 -> 301
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Rule Violation Summary
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:07:58] [info] |-----|-----|-----|
[2025-10-12 13:07:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:07:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:07:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:07:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:07:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:07:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:07:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:07:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:07:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:07:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:07:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:07:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:07:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:07:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:07:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Performance Metrics
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] | Stage | Time Taken |
[2025-10-12 13:07:58] [info] |-----|-----|
[2025-10-12 13:07:58] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:07:58] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:07:58] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Summary
[2025-10-12 13:07:58] [info] -----
[2025-10-12 13:07:58] [info] Total Errors: 1120
[2025-10-12 13:07:58] [info] Total Warnings: 321
[2025-10-12 13:07:58] [info] DRC Status: FAILED
```

lvr_summary_155.rpt

```
[2025-10-12 13:08:58] [info] =====
[2025-10-12 13:08:58] [info] Design Rule Check Report
[2025-10-12 13:08:58] [info] =====

[2025-10-12 13:08:58] [info] Report Generated: 2025-10-12
[2025-10-12 13:08:58] [info] Run Start Time: 01:08:06 PM
[2025-10-12 13:08:58] [info] Run End Time: 01:08:58 PM
[2025-10-12 13:08:58] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:08:58] [info] Tool Version: LVR_2025.1
[2025-10-12 13:08:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:08:58] [info] User Name: ubuntu
[2025-10-12 13:08:58] [info] Cores Used: 8
[2025-10-12 13:08:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Top Cell Information
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Layout Top Cell:      TOP
[2025-10-12 13:08:58] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:08:58] [info] Database Format:      GDSII
[2025-10-12 13:08:58] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Type of Errors | Count
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] DB_WARN -> 1
[2025-10-12 13:08:58] [info] LRF_ERR001 -> 14
[2025-10-12 13:08:58] [info] DB_WARN002 -> 159
[2025-10-12 13:08:58] [info] DB_WARN003 -> 161
[2025-10-12 13:08:58] [info] DRC_ERR9 -> 89
[2025-10-12 13:08:58] [info] DRC_ERR -> 67
[2025-10-12 13:08:58] [info] DRC_ERR5 -> 408
[2025-10-12 13:08:58] [info] DRC_ERR4 -> 241
[2025-10-12 13:08:58] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Rule Violation Summary
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:08:58] [info] |-----|-----|-----|
[2025-10-12 13:08:58] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:08:58] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:08:58] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:08:58] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:08:58] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:08:58] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:08:58] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:08:58] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:08:58] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:08:58] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:08:58] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:08:58] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:08:58] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:08:58] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:08:58] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:08:58] [info] |
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Performance Metrics
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] | Stage          | Time Taken |
[2025-10-12 13:08:58] [info] |-----|-----|
[2025-10-12 13:08:58] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:08:58] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:08:58] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Summary
```

```
[2025-10-12 13:08:58] [info] -----
[2025-10-12 13:08:58] [info] Total Errors:      1120
[2025-10-12 13:08:58] [info] Total Warnings:    321
[2025-10-12 13:08:58] [info] DRC Status:        FAILED
```

lvr_summary_156.rpt

```
[2025-10-12 13:09:57] [info] =====
[2025-10-12 13:09:57] [info]                      Design Rule Check Report
[2025-10-12 13:09:57] [info] =====

[2025-10-12 13:09:57] [info] Report Generated:      2025-10-12
[2025-10-12 13:09:57] [info] Run Start Time:       01:09:05 PM
[2025-10-12 13:09:57] [info] Run End Time:         01:09:57 PM
[2025-10-12 13:09:57] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 13:09:57] [info] Tool Version:          LVR_2025.1
[2025-10-12 13:09:57] [info] Host Name:             ip-172-31-37-173
[2025-10-12 13:09:57] [info] User Name:             ubuntu
[2025-10-12 13:09:57] [info] Cores Used:            8
[2025-10-12 13:09:57] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info]                      Top Cell Information
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info] Layout Top Cell:      TOP
[2025-10-12 13:09:57] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 13:09:57] [info] Database Format:        GDSII
[2025-10-12 13:09:57] [info] Verification Mode:     Full Check

[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info]                      Type of Errors | Count
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info] DB_WARN -> 1
[2025-10-12 13:09:57] [info] LRF_ERR001 -> 14
[2025-10-12 13:09:57] [info] DB_WARN002 -> 159
[2025-10-12 13:09:57] [info] DB_WARN003 -> 161
[2025-10-12 13:09:57] [info] DRC_ERR9 -> 89
[2025-10-12 13:09:57] [info] DRC_ERR -> 67
[2025-10-12 13:09:57] [info] DRC_ERR5 -> 408
[2025-10-12 13:09:57] [info] DRC_ERR4 -> 241
[2025-10-12 13:09:57] [info] DRC_ERR1 -> 301
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info]                      Rule Violation Summary
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:09:57] [info] |-----|-----|-----|
[2025-10-12 13:09:57] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:09:57] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:09:57] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:09:57] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:09:57] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:09:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:09:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:09:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:09:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:09:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:09:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:09:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:09:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:09:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:09:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:09:57] [info]
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info]                      Performance Metrics
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:09:57] [info] |-----|-----|
[2025-10-12 13:09:57] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:09:57] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 13:09:57] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info] Summary
[2025-10-12 13:09:57] [info] -----
[2025-10-12 13:09:57] [info] Total Errors: 1120
[2025-10-12 13:09:57] [info] Total Warnings: 321
[2025-10-12 13:09:57] [info] DRC Status: FAILED
```

lvr_summary_157.rpt

```
[2025-10-12 13:10:54] [info] =====
[2025-10-12 13:10:54] [info] Design Rule Check Report
[2025-10-12 13:10:54] [info] =====

[2025-10-12 13:10:54] [info] Report Generated: 2025-10-12
[2025-10-12 13:10:54] [info] Run Start Time: 01:10:04 PM
[2025-10-12 13:10:54] [info] Run End Time: 01:10:54 PM
[2025-10-12 13:10:54] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:10:54] [info] Tool Version: LVR_2025.1
[2025-10-12 13:10:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:10:54] [info] User Name: ubuntu
[2025-10-12 13:10:54] [info] Cores Used: 8
[2025-10-12 13:10:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Top Cell Information
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Layout Top Cell: TOP
[2025-10-12 13:10:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:10:54] [info] Database Format: GDSII
[2025-10-12 13:10:54] [info] Verification Mode: Full Check

[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Type of Errors | Count
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] DB_WARN -> 1
[2025-10-12 13:10:54] [info] LRF_ERR001 -> 14
[2025-10-12 13:10:54] [info] DB_WARN002 -> 159
[2025-10-12 13:10:54] [info] DB_WARN003 -> 161
[2025-10-12 13:10:54] [info] DRC_ERR9 -> 89
[2025-10-12 13:10:54] [info] DRC_ERR -> 67
[2025-10-12 13:10:54] [info] DRC_ERR5 -> 408
[2025-10-12 13:10:54] [info] DRC_ERR4 -> 241
[2025-10-12 13:10:54] [info] DRC_ERR1 -> 301
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Rule Violation Summary
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:10:54] [info] |-----|-----|-----|
[2025-10-12 13:10:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:10:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:10:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:10:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:10:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 13:10:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:10:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:10:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:10:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:10:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:10:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:10:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:10:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:10:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:10:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:10:54] [info]
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Performance Metrics
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] | Stage | Time Taken |
[2025-10-12 13:10:54] [info] |-----|-----|
[2025-10-12 13:10:54] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:10:54] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:10:54] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Summary
[2025-10-12 13:10:54] [info] -----
[2025-10-12 13:10:54] [info] Total Errors: 1120
[2025-10-12 13:10:54] [info] Total Warnings: 321
[2025-10-12 13:10:54] [info] DRC Status: FAILED
```

lvr_summary_158.rpt

```
[2025-10-12 13:11:54] [info] =====
[2025-10-12 13:11:54] [info] Design Rule Check Report
[2025-10-12 13:11:54] [info] =====

[2025-10-12 13:11:54] [info] Report Generated: 2025-10-12
[2025-10-12 13:11:54] [info] Run Start Time: 01:11:03 PM
[2025-10-12 13:11:54] [info] Run End Time: 01:11:54 PM
[2025-10-12 13:11:54] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:11:54] [info] Tool Version: LVR_2025.1
[2025-10-12 13:11:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:11:54] [info] User Name: ubuntu
[2025-10-12 13:11:54] [info] Cores Used: 8
[2025-10-12 13:11:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Top Cell Information
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Layout Top Cell: TOP
[2025-10-12 13:11:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:11:54] [info] Database Format: GDSII
[2025-10-12 13:11:54] [info] Verification Mode: Full Check

[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Type of Errors | Count
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] DB_WARN -> 1
[2025-10-12 13:11:54] [info] LRF_ERR001 -> 14
[2025-10-12 13:11:54] [info] DB_WARN002 -> 159
[2025-10-12 13:11:54] [info] DB_WARN003 -> 161
```

```

[2025-10-12 13:11:54] [info] DRC_ERR9 -> 89
[2025-10-12 13:11:54] [info] DRC_ERR -> 67
[2025-10-12 13:11:54] [info] DRC_ERR5 -> 408
[2025-10-12 13:11:54] [info] DRC_ERR4 -> 241
[2025-10-12 13:11:54] [info] DRC_ERR1 -> 301
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Rule Violation Summary
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:11:54] [info] |-----|-----|-----|
[2025-10-12 13:11:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:11:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:11:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:11:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:11:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:11:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:11:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:11:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:11:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:11:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:11:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:11:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:11:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:11:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:11:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Performance Metrics
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] | Stage | Time Taken |
[2025-10-12 13:11:54] [info] |-----|-----|
[2025-10-12 13:11:54] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:11:54] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:11:54] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Summary
[2025-10-12 13:11:54] [info] -----
[2025-10-12 13:11:54] [info] Total Errors: 1120
[2025-10-12 13:11:54] [info] Total Warnings: 321
[2025-10-12 13:11:54] [info] DRC Status: FAILED

```

lvr_summary_159.rpt

```

[2025-10-12 13:12:51] [info] =====
[2025-10-12 13:12:51] [info] Design Rule Check Report
[2025-10-12 13:12:51] [info] =====

[2025-10-12 13:12:51] [info] Report Generated: 2025-10-12
[2025-10-12 13:12:51] [info] Run Start Time: 01:12:01 PM
[2025-10-12 13:12:51] [info] Run End Time: 01:12:51 PM
[2025-10-12 13:12:51] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:12:51] [info] Tool Version: LVR_2025.1
[2025-10-12 13:12:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:12:51] [info] User Name: ubuntu
[2025-10-12 13:12:51] [info] Cores Used: 8
[2025-10-12 13:12:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Top Cell Information
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Layout Top Cell:      TOP
[2025-10-12 13:12:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:12:51] [info] Database Format:      GDSII
[2025-10-12 13:12:51] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Type of Errors | Count
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] DB_WARN -> 1
[2025-10-12 13:12:51] [info] LRF_ERR001 -> 14
[2025-10-12 13:12:51] [info] DB_WARN002 -> 159
[2025-10-12 13:12:51] [info] DB_WARN003 -> 161
[2025-10-12 13:12:51] [info] DRC_ERR9 -> 89
[2025-10-12 13:12:51] [info] DRC_ERR -> 67
[2025-10-12 13:12:51] [info] DRC_ERR5 -> 408
[2025-10-12 13:12:51] [info] DRC_ERR4 -> 241
[2025-10-12 13:12:51] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Rule Violation Summary
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:12:51] [info] |-----|-----|-----|
[2025-10-12 13:12:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:12:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:12:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:12:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:12:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:12:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:12:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:12:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:12:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:12:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:12:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:12:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:12:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:12:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:12:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:12:51] [info] |
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Performance Metrics
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] | Stage          | Time Taken |
[2025-10-12 13:12:51] [info] |-----|-----|
[2025-10-12 13:12:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:12:51] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 13:12:51] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Summary
```

```
[2025-10-12 13:12:51] [info] -----
[2025-10-12 13:12:51] [info] Total Errors:      1120
[2025-10-12 13:12:51] [info] Total Warnings:    321
[2025-10-12 13:12:51] [info] DRC Status:        FAILED
```

lvr_summary_16.rpt

```
[2025-10-12 10:56:34] [info] =====
[2025-10-12 10:56:34] [info]                      Design Rule Check Report
[2025-10-12 10:56:34] [info] =====

[2025-10-12 10:56:34] [info] Report Generated:    2025-10-12
[2025-10-12 10:56:34] [info] Run Start Time:      10:55:43 AM
[2025-10-12 10:56:34] [info] Run End Time:        10:56:34 AM
[2025-10-12 10:56:34] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 10:56:34] [info] Tool Version:        LVR_2025.1
[2025-10-12 10:56:34] [info] Host Name:           ip-172-31-37-173
[2025-10-12 10:56:34] [info] User Name:           ubuntu
[2025-10-12 10:56:34] [info] Cores Used:          8
[2025-10-12 10:56:34] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info]                      Top Cell Information
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info] Layout Top Cell:     TOP
[2025-10-12 10:56:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:56:34] [info] Database Format:      GDSII
[2025-10-12 10:56:34] [info] Verification Mode:    Full Check

[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info]                      Type of Errors | Count
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info] DB_WARN -> 1
[2025-10-12 10:56:34] [info] LRF_ERR001 -> 14
[2025-10-12 10:56:34] [info] DB_WARN002 -> 159
[2025-10-12 10:56:34] [info] DB_WARN003 -> 161
[2025-10-12 10:56:34] [info] DRC_ERR9 -> 89
[2025-10-12 10:56:34] [info] DRC_ERR -> 67
[2025-10-12 10:56:34] [info] DRC_ERR5 -> 408
[2025-10-12 10:56:34] [info] DRC_ERR4 -> 241
[2025-10-12 10:56:34] [info] DRC_ERR1 -> 301
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info]                      Rule Violation Summary
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:56:34] [info] |-----|-----|-----|
[2025-10-12 10:56:34] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:56:34] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:56:34] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:56:34] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:56:34] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:56:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 10:56:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:56:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:56:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:56:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:56:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:56:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:56:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:56:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:56:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:56:34] [info]
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info]                      Performance Metrics
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info] | Stage | Time Taken |
```



```
[2025-10-12 10:56:34] [info] |-----|-----|
[2025-10-12 10:56:34] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 10:56:34] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 10:56:34] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info]                Summary
[2025-10-12 10:56:34] [info] -----
[2025-10-12 10:56:34] [info] Total Errors:    1120
[2025-10-12 10:56:34] [info] Total Warnings:    321
[2025-10-12 10:56:34] [info] DRC Status:      FAILED
```

lvr_summary_160.rpt

```
[2025-10-12 13:13:48] [info] =====
[2025-10-12 13:13:48] [info]                Design Rule Check Report
[2025-10-12 13:13:48] [info] =====

[2025-10-12 13:13:48] [info] Report Generated:    2025-10-12
[2025-10-12 13:13:48] [info] Run Start Time:      01:12:57 PM
[2025-10-12 13:13:48] [info] Run End Time:        01:13:48 PM
[2025-10-12 13:13:48] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 13:13:48] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:13:48] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:13:48] [info] User Name:           ubuntu
[2025-10-12 13:13:48] [info] Cores Used:          8
[2025-10-12 13:13:48] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info]                Top Cell Information
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] Layout Top Cell:     TOP
[2025-10-12 13:13:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:13:48] [info] Database Format:       GDSII
[2025-10-12 13:13:48] [info] Verification Mode:    Full Check

[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info]                Type of Errors | Count
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] DB_WARN -> 1
[2025-10-12 13:13:48] [info] LRF_ERR001 -> 14
[2025-10-12 13:13:48] [info] DB_WARN002 -> 159
[2025-10-12 13:13:48] [info] DB_WARN003 -> 161
[2025-10-12 13:13:48] [info] DRC_ERR9 -> 89
[2025-10-12 13:13:48] [info] DRC_ERR -> 67
[2025-10-12 13:13:48] [info] DRC_ERR5 -> 408
[2025-10-12 13:13:48] [info] DRC_ERR4 -> 241
[2025-10-12 13:13:48] [info] DRC_ERR1 -> 301
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info]                Rule Violation Summary
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:13:48] [info] |-----|-----|-----|
[2025-10-12 13:13:48] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:13:48] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:13:48] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:13:48] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:13:48] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 13:13:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 13:13:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:13:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:13:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:13:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:13:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:13:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:13:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:13:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:13:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:13:48] [info]
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] Performance Metrics
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] | Stage | Time Taken |
[2025-10-12 13:13:48] [info] |-----|-----|
[2025-10-12 13:13:48] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:13:48] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:13:48] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] Summary
[2025-10-12 13:13:48] [info] -----
[2025-10-12 13:13:48] [info] Total Errors: 1120
[2025-10-12 13:13:48] [info] Total Warnings: 321
[2025-10-12 13:13:48] [info] DRC Status: FAILED

```

lvr_summary_161.rpt

```

[2025-10-12 13:14:46] [info] =====
[2025-10-12 13:14:46] [info] Design Rule Check Report
[2025-10-12 13:14:46] [info] =====

[2025-10-12 13:14:46] [info] Report Generated: 2025-10-12
[2025-10-12 13:14:46] [info] Run Start Time: 01:13:56 PM
[2025-10-12 13:14:46] [info] Run End Time: 01:14:46 PM
[2025-10-12 13:14:46] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:14:46] [info] Tool Version: LVR_2025.1
[2025-10-12 13:14:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:14:46] [info] User Name: ubuntu
[2025-10-12 13:14:46] [info] Cores Used: 8
[2025-10-12 13:14:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Top Cell Information
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Layout Top Cell: TOP
[2025-10-12 13:14:46] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:14:46] [info] Database Format: GDSII
[2025-10-12 13:14:46] [info] Verification Mode: Full Check

[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Type of Errors | Count
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] DB_WARN -> 1
[2025-10-12 13:14:46] [info] LRF_ERR001 -> 14
[2025-10-12 13:14:46] [info] DB_WARN002 -> 159
[2025-10-12 13:14:46] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:14:46] [info] DRC_ERR9 -> 89
[2025-10-12 13:14:46] [info] DRC_ERR -> 67
[2025-10-12 13:14:46] [info] DRC_ERR5 -> 408
[2025-10-12 13:14:46] [info] DRC_ERR4 -> 241
[2025-10-12 13:14:46] [info] DRC_ERR1 -> 301
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Rule Violation Summary
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:14:46] [info] |-----|-----|-----|
[2025-10-12 13:14:46] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:14:46] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:14:46] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:14:46] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:14:46] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:14:46] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:14:46] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:14:46] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:14:46] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:14:46] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:14:46] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:14:46] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:14:46] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:14:46] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:14:46] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Performance Metrics
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] | Stage | Time Taken |
[2025-10-12 13:14:46] [info] |-----|-----|
[2025-10-12 13:14:46] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:14:46] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:14:46] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Summary
[2025-10-12 13:14:46] [info] -----
[2025-10-12 13:14:46] [info] Total Errors: 1120
[2025-10-12 13:14:46] [info] Total Warnings: 321
[2025-10-12 13:14:46] [info] DRC Status: FAILED

```

lvr_summary_162.rpt

```

[2025-10-12 13:15:45] [info] =====
[2025-10-12 13:15:45] [info] Design Rule Check Report
[2025-10-12 13:15:45] [info] =====

[2025-10-12 13:15:45] [info] Report Generated: 2025-10-12
[2025-10-12 13:15:45] [info] Run Start Time: 01:14:53 PM
[2025-10-12 13:15:45] [info] Run End Time: 01:15:45 PM
[2025-10-12 13:15:45] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 13:15:45] [info] Tool Version: LVR_2025.1
[2025-10-12 13:15:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:15:45] [info] User Name: ubuntu
[2025-10-12 13:15:45] [info] Cores Used: 8
[2025-10-12 13:15:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] Top Cell Information
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] Layout Top Cell:      TOP
[2025-10-12 13:15:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:15:45] [info] Database Format:      GDSII
[2025-10-12 13:15:45] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] Type of Errors | Count
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] DB_WARN -> 1
[2025-10-12 13:15:45] [info] LRF_ERR001 -> 14
[2025-10-12 13:15:45] [info] DB_WARN002 -> 159
[2025-10-12 13:15:45] [info] DB_WARN003 -> 161
[2025-10-12 13:15:45] [info] DRC_ERR9 -> 89
[2025-10-12 13:15:45] [info] DRC_ERR -> 67
[2025-10-12 13:15:45] [info] DRC_ERR5 -> 408
[2025-10-12 13:15:45] [info] DRC_ERR4 -> 241
[2025-10-12 13:15:45] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] Rule Violation Summary
```

```
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:15:45] [info] |-----|-----|-----|
[2025-10-12 13:15:45] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:15:45] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:15:45] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:15:45] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:15:45] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:15:45] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:15:45] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:15:45] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:15:45] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:15:45] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:15:45] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:15:45] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:15:45] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:15:45] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:15:45] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:15:45] [info] -----
```

```
[2025-10-12 13:15:45] [info] Performance Metrics
```

```
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] | Stage          | Time Taken |
[2025-10-12 13:15:45] [info] |-----|-----|
[2025-10-12 13:15:45] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:15:45] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:15:45] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:15:45] [info] -----
```

```
[2025-10-12 13:15:45] [info] Summary
```

```
[2025-10-12 13:15:45] [info] -----
[2025-10-12 13:15:45] [info] Total Errors:      1120
[2025-10-12 13:15:45] [info] Total Warnings:    321
[2025-10-12 13:15:45] [info] DRC Status:        FAILED
```

lvr_summary_163.rpt

```
[2025-10-12 13:16:43] [info] =====
[2025-10-12 13:16:43] [info]                      Design Rule Check Report
[2025-10-12 13:16:43] [info] =====

[2025-10-12 13:16:43] [info] Report Generated:    2025-10-12
[2025-10-12 13:16:43] [info] Run Start Time:      01:15:51 PM
[2025-10-12 13:16:43] [info] Run End Time:        01:16:43 PM
[2025-10-12 13:16:43] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 13:16:43] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:16:43] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:16:43] [info] User Name:           ubuntu
[2025-10-12 13:16:43] [info] Cores Used:          8
[2025-10-12 13:16:43] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info]                      Top Cell Information
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info] Layout Top Cell:     TOP
[2025-10-12 13:16:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:16:43] [info] Database Format:       GDSII
[2025-10-12 13:16:43] [info] Verification Mode:    Full Check

[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info]                      Type of Errors | Count
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info] DB_WARN -> 1
[2025-10-12 13:16:43] [info] LRF_ERR001 -> 14
[2025-10-12 13:16:43] [info] DB_WARN002 -> 159
[2025-10-12 13:16:43] [info] DB_WARN003 -> 161
[2025-10-12 13:16:43] [info] DRC_ERR9 -> 89
[2025-10-12 13:16:43] [info] DRC_ERR -> 67
[2025-10-12 13:16:43] [info] DRC_ERR5 -> 408
[2025-10-12 13:16:43] [info] DRC_ERR4 -> 241
[2025-10-12 13:16:43] [info] DRC_ERR1 -> 301
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info]                      Rule Violation Summary
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:16:43] [info] |-----|-----|-----|
[2025-10-12 13:16:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:16:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:16:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:16:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:16:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:16:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:16:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:16:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:16:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:16:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:16:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:16:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:16:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:16:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:16:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:16:43] [info]
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info]                      Performance Metrics
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:16:43] [info] |-----|-----|
[2025-10-12 13:16:43] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:16:43] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 13:16:43] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info] Summary
[2025-10-12 13:16:43] [info] -----
[2025-10-12 13:16:43] [info] Total Errors: 1120
[2025-10-12 13:16:43] [info] Total Warnings: 321
[2025-10-12 13:16:43] [info] DRC Status: FAILED
```

lvr_summary_164.rpt

```
[2025-10-12 13:17:40] [info] =====
[2025-10-12 13:17:40] [info] Design Rule Check Report
[2025-10-12 13:17:40] [info] =====

[2025-10-12 13:17:40] [info] Report Generated: 2025-10-12
[2025-10-12 13:17:40] [info] Run Start Time: 01:16:49 PM
[2025-10-12 13:17:40] [info] Run End Time: 01:17:40 PM
[2025-10-12 13:17:40] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:17:40] [info] Tool Version: LVR_2025.1
[2025-10-12 13:17:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:17:40] [info] User Name: ubuntu
[2025-10-12 13:17:40] [info] Cores Used: 8
[2025-10-12 13:17:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Top Cell Information
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Layout Top Cell: TOP
[2025-10-12 13:17:40] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:17:40] [info] Database Format: GDSII
[2025-10-12 13:17:40] [info] Verification Mode: Full Check

[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Type of Errors | Count
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] DB_WARN -> 1
[2025-10-12 13:17:40] [info] LRF_ERR001 -> 14
[2025-10-12 13:17:40] [info] DB_WARN002 -> 159
[2025-10-12 13:17:40] [info] DB_WARN003 -> 161
[2025-10-12 13:17:40] [info] DRC_ERR9 -> 89
[2025-10-12 13:17:40] [info] DRC_ERR -> 67
[2025-10-12 13:17:40] [info] DRC_ERR5 -> 408
[2025-10-12 13:17:40] [info] DRC_ERR4 -> 241
[2025-10-12 13:17:40] [info] DRC_ERR1 -> 301
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Rule Violation Summary
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:17:40] [info] |-----|-----|-----|
[2025-10-12 13:17:40] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:17:40] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:17:40] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:17:40] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:17:40] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 13:17:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:17:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:17:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:17:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:17:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:17:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:17:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:17:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:17:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:17:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:17:40] [info]
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Performance Metrics
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] | Stage | Time Taken |
[2025-10-12 13:17:40] [info] |-----|-----|
[2025-10-12 13:17:40] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:17:40] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:17:40] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Summary
[2025-10-12 13:17:40] [info] -----
[2025-10-12 13:17:40] [info] Total Errors: 1120
[2025-10-12 13:17:40] [info] Total Warnings: 321
[2025-10-12 13:17:40] [info] DRC Status: FAILED

```

lvr_summary_165.rpt

```

[2025-10-12 13:18:39] [info] =====
[2025-10-12 13:18:39] [info] Design Rule Check Report
[2025-10-12 13:18:39] [info] =====

[2025-10-12 13:18:39] [info] Report Generated: 2025-10-12
[2025-10-12 13:18:39] [info] Run Start Time: 01:17:48 PM
[2025-10-12 13:18:39] [info] Run End Time: 01:18:39 PM
[2025-10-12 13:18:39] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:18:39] [info] Tool Version: LVR_2025.1
[2025-10-12 13:18:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:18:39] [info] User Name: ubuntu
[2025-10-12 13:18:39] [info] Cores Used: 8
[2025-10-12 13:18:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Top Cell Information
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Layout Top Cell: TOP
[2025-10-12 13:18:39] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:18:39] [info] Database Format: GDSII
[2025-10-12 13:18:39] [info] Verification Mode: Full Check

[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Type of Errors | Count
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] DB_WARN -> 1
[2025-10-12 13:18:39] [info] LRF_ERR001 -> 14
[2025-10-12 13:18:39] [info] DB_WARN002 -> 159
[2025-10-12 13:18:39] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:18:39] [info] DRC_ERR9 -> 89
[2025-10-12 13:18:39] [info] DRC_ERR -> 67
[2025-10-12 13:18:39] [info] DRC_ERR5 -> 408
[2025-10-12 13:18:39] [info] DRC_ERR4 -> 241
[2025-10-12 13:18:39] [info] DRC_ERR1 -> 301
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Rule Violation Summary
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:18:39] [info] |-----|-----|-----|
[2025-10-12 13:18:39] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:18:39] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:18:39] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:18:39] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:18:39] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:18:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:18:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:18:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:18:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:18:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:18:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:18:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:18:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:18:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:18:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Performance Metrics
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] | Stage | Time Taken |
[2025-10-12 13:18:39] [info] |-----|-----|
[2025-10-12 13:18:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:18:39] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:18:39] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Summary
[2025-10-12 13:18:39] [info] -----
[2025-10-12 13:18:39] [info] Total Errors: 1120
[2025-10-12 13:18:39] [info] Total Warnings: 321
[2025-10-12 13:18:39] [info] DRC Status: FAILED

```

lvr_summary_166.rpt

```

[2025-10-12 13:19:38] [info] =====
[2025-10-12 13:19:38] [info] Design Rule Check Report
[2025-10-12 13:19:38] [info] =====

[2025-10-12 13:19:38] [info] Report Generated: 2025-10-12
[2025-10-12 13:19:38] [info] Run Start Time: 01:18:46 PM
[2025-10-12 13:19:38] [info] Run End Time: 01:19:38 PM
[2025-10-12 13:19:38] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:19:38] [info] Tool Version: LVR_2025.1
[2025-10-12 13:19:38] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:19:38] [info] User Name: ubuntu
[2025-10-12 13:19:38] [info] Cores Used: 8
[2025-10-12 13:19:38] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Top Cell Information
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Layout Top Cell:      TOP
[2025-10-12 13:19:38] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:19:38] [info] Database Format:      GDSII
[2025-10-12 13:19:38] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Type of Errors | Count
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] DB_WARN -> 1
[2025-10-12 13:19:38] [info] LRF_ERR001 -> 14
[2025-10-12 13:19:38] [info] DB_WARN002 -> 159
[2025-10-12 13:19:38] [info] DB_WARN003 -> 161
[2025-10-12 13:19:38] [info] DRC_ERR9 -> 89
[2025-10-12 13:19:38] [info] DRC_ERR -> 67
[2025-10-12 13:19:38] [info] DRC_ERR5 -> 408
[2025-10-12 13:19:38] [info] DRC_ERR4 -> 241
[2025-10-12 13:19:38] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Rule Violation Summary
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:19:38] [info] |-----|-----|-----|
[2025-10-12 13:19:38] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:19:38] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:19:38] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:19:38] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:19:38] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:19:38] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:19:38] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:19:38] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:19:38] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:19:38] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:19:38] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:19:38] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:19:38] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:19:38] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:19:38] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Performance Metrics
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] | Stage          | Time Taken |
[2025-10-12 13:19:38] [info] |-----|-----|
[2025-10-12 13:19:38] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:19:38] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:19:38] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Summary
[2025-10-12 13:19:38] [info] -----
[2025-10-12 13:19:38] [info] Total Errors:      1120
[2025-10-12 13:19:38] [info] Total Warnings:    321
[2025-10-12 13:19:38] [info] DRC Status:        FAILED
```

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```
[2025-10-12 13:20:36] [info] =====
[2025-10-12 13:20:36] [info]                      Design Rule Check Report
[2025-10-12 13:20:36] [info] =====

[2025-10-12 13:20:36] [info] Report Generated:    2025-10-12
[2025-10-12 13:20:36] [info] Run Start Time:      01:19:45 PM
[2025-10-12 13:20:36] [info] Run End Time:        01:20:36 PM
[2025-10-12 13:20:36] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 13:20:36] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:20:36] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:20:36] [info] User Name:          ubuntu
[2025-10-12 13:20:36] [info] Cores Used:         8
[2025-10-12 13:20:36] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info]                      Top Cell Information
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info] Layout Top Cell:    TOP
[2025-10-12 13:20:36] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:20:36] [info] Database Format:     GDSII
[2025-10-12 13:20:36] [info] Verification Mode:   Full Check

[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info]                      Type of Errors | Count
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info] DB_WARN -> 1
[2025-10-12 13:20:36] [info] LRF_ERR001 -> 14
[2025-10-12 13:20:36] [info] DB_WARN002 -> 159
[2025-10-12 13:20:36] [info] DB_WARN003 -> 161
[2025-10-12 13:20:36] [info] DRC_ERR9 -> 89
[2025-10-12 13:20:36] [info] DRC_ERR -> 67
[2025-10-12 13:20:36] [info] DRC_ERR5 -> 408
[2025-10-12 13:20:36] [info] DRC_ERR4 -> 241
[2025-10-12 13:20:36] [info] DRC_ERR1 -> 301
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info]                      Rule Violation Summary
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:20:36] [info] |-----|-----|-----|
[2025-10-12 13:20:36] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:20:36] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:20:36] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:20:36] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:20:36] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:20:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:20:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:20:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:20:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:20:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:20:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:20:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:20:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:20:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:20:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:20:36] [info]
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info]                      Performance Metrics
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:20:36] [info] |-----|-----|
[2025-10-12 13:20:36] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:20:36] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 13:20:36] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info] Summary
[2025-10-12 13:20:36] [info] -----
[2025-10-12 13:20:36] [info] Total Errors: 1120
[2025-10-12 13:20:36] [info] Total Warnings: 321
[2025-10-12 13:20:36] [info] DRC Status: FAILED
```

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```
[2025-10-12 13:21:35] [info] =====
[2025-10-12 13:21:35] [info] Design Rule Check Report
[2025-10-12 13:21:35] [info] =====

[2025-10-12 13:21:35] [info] Report Generated: 2025-10-12
[2025-10-12 13:21:35] [info] Run Start Time: 01:20:43 PM
[2025-10-12 13:21:35] [info] Run End Time: 01:21:35 PM
[2025-10-12 13:21:35] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:21:35] [info] Tool Version: LVR_2025.1
[2025-10-12 13:21:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:21:35] [info] User Name: ubuntu
[2025-10-12 13:21:35] [info] Cores Used: 8
[2025-10-12 13:21:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Top Cell Information
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Layout Top Cell: TOP
[2025-10-12 13:21:35] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:21:35] [info] Database Format: GDSII
[2025-10-12 13:21:35] [info] Verification Mode: Full Check

[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Type of Errors | Count
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] DB_WARN -> 1
[2025-10-12 13:21:35] [info] LRF_ERR001 -> 14
[2025-10-12 13:21:35] [info] DB_WARN002 -> 159
[2025-10-12 13:21:35] [info] DB_WARN003 -> 161
[2025-10-12 13:21:35] [info] DRC_ERR9 -> 89
[2025-10-12 13:21:35] [info] DRC_ERR -> 67
[2025-10-12 13:21:35] [info] DRC_ERR5 -> 408
[2025-10-12 13:21:35] [info] DRC_ERR4 -> 241
[2025-10-12 13:21:35] [info] DRC_ERR1 -> 301
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Rule Violation Summary
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:21:35] [info] |-----|-----|-----|
[2025-10-12 13:21:35] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:21:35] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:21:35] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:21:35] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:21:35] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 13:21:35] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 13:21:35] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:21:35] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:21:35] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:21:35] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:21:35] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:21:35] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:21:35] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:21:35] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:21:35] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:21:35] [info]
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Performance Metrics
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] | Stage | Time Taken |
[2025-10-12 13:21:35] [info] |-----|-----|
[2025-10-12 13:21:35] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:21:35] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:21:35] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Summary
[2025-10-12 13:21:35] [info] -----
[2025-10-12 13:21:35] [info] Total Errors: 1120
[2025-10-12 13:21:35] [info] Total Warnings: 321
[2025-10-12 13:21:35] [info] DRC Status: FAILED

```

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```

[2025-10-12 13:22:33] [info] =====
[2025-10-12 13:22:33] [info] Design Rule Check Report
[2025-10-12 13:22:33] [info] =====

[2025-10-12 13:22:33] [info] Report Generated: 2025-10-12
[2025-10-12 13:22:33] [info] Run Start Time: 01:21:41 PM
[2025-10-12 13:22:33] [info] Run End Time: 01:22:33 PM
[2025-10-12 13:22:33] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 13:22:33] [info] Tool Version: LVR_2025.1
[2025-10-12 13:22:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:22:33] [info] User Name: ubuntu
[2025-10-12 13:22:33] [info] Cores Used: 8
[2025-10-12 13:22:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Top Cell Information
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Layout Top Cell: TOP
[2025-10-12 13:22:33] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:22:33] [info] Database Format: GDSII
[2025-10-12 13:22:33] [info] Verification Mode: Full Check

[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Type of Errors | Count
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] DB_WARN -> 1
[2025-10-12 13:22:33] [info] LRF_ERR001 -> 14
[2025-10-12 13:22:33] [info] DB_WARN002 -> 159
[2025-10-12 13:22:33] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:22:33] [info] DRC_ERR9 -> 89
[2025-10-12 13:22:33] [info] DRC_ERR -> 67
[2025-10-12 13:22:33] [info] DRC_ERR5 -> 408
[2025-10-12 13:22:33] [info] DRC_ERR4 -> 241
[2025-10-12 13:22:33] [info] DRC_ERR1 -> 301
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Rule Violation Summary
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:22:33] [info] |-----|-----|-----|
[2025-10-12 13:22:33] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:22:33] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:22:33] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:22:33] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:22:33] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:22:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:22:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:22:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:22:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:22:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:22:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:22:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:22:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:22:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:22:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Performance Metrics
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] | Stage | Time Taken |
[2025-10-12 13:22:33] [info] |-----|-----|
[2025-10-12 13:22:33] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:22:33] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 13:22:33] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Summary
[2025-10-12 13:22:33] [info] -----
[2025-10-12 13:22:33] [info] Total Errors: 1120
[2025-10-12 13:22:33] [info] Total Warnings: 321
[2025-10-12 13:22:33] [info] DRC Status: FAILED

```

lvr_summary_17.rpt

```

[2025-10-12 10:57:29] [info] =====
[2025-10-12 10:57:29] [info] Design Rule Check Report
[2025-10-12 10:57:29] [info] =====

[2025-10-12 10:57:29] [info] Report Generated: 2025-10-12
[2025-10-12 10:57:29] [info] Run Start Time: 10:56:39 AM
[2025-10-12 10:57:29] [info] Run End Time: 10:57:29 AM
[2025-10-12 10:57:29] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 10:57:29] [info] Tool Version: LVR_2025.1
[2025-10-12 10:57:29] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:57:29] [info] User Name: ubuntu
[2025-10-12 10:57:29] [info] Cores Used: 8
[2025-10-12 10:57:29] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Top Cell Information
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Layout Top Cell:      TOP
[2025-10-12 10:57:29] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:57:29] [info] Database Format:      GDSII
[2025-10-12 10:57:29] [info] Verification Mode:    Full Check
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Type of Errors | Count
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] DB_WARN -> 1
[2025-10-12 10:57:29] [info] LRF_ERR001 -> 14
[2025-10-12 10:57:29] [info] DB_WARN002 -> 159
[2025-10-12 10:57:29] [info] DB_WARN003 -> 161
[2025-10-12 10:57:29] [info] DRC_ERR9 -> 89
[2025-10-12 10:57:29] [info] DRC_ERR -> 67
[2025-10-12 10:57:29] [info] DRC_ERR5 -> 408
[2025-10-12 10:57:29] [info] DRC_ERR4 -> 241
[2025-10-12 10:57:29] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Rule Violation Summary
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 10:57:29] [info] |-----|-----|-----|
[2025-10-12 10:57:29] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:57:29] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:57:29] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:57:29] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:57:29] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 10:57:29] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 10:57:29] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 10:57:29] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 10:57:29] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 10:57:29] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 10:57:29] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 10:57:29] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 10:57:29] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 10:57:29] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 10:57:29] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 10:57:29] [info] |
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Performance Metrics
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] | Stage          | Time Taken |
[2025-10-12 10:57:29] [info] |-----|-----|
[2025-10-12 10:57:29] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 10:57:29] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 10:57:29] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Summary
```

```
[2025-10-12 10:57:29] [info] -----
[2025-10-12 10:57:29] [info] Total Errors:      1120
[2025-10-12 10:57:29] [info] Total Warnings:    321
[2025-10-12 10:57:29] [info] DRC Status:        FAILED
```

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```
[2025-10-12 13:23:31] [info] =====
[2025-10-12 13:23:31] [info] Design Rule Check Report
[2025-10-12 13:23:31] [info] =====

[2025-10-12 13:23:31] [info] Report Generated: 2025-10-12
[2025-10-12 13:23:31] [info] Run Start Time: 01:22:40 PM
[2025-10-12 13:23:31] [info] Run End Time: 01:23:31 PM
[2025-10-12 13:23:31] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:23:31] [info] Tool Version: LVR_2025.1
[2025-10-12 13:23:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:23:31] [info] User Name: ubuntu
[2025-10-12 13:23:31] [info] Cores Used: 8
[2025-10-12 13:23:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] Top Cell Information
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] Layout Top Cell: TOP
[2025-10-12 13:23:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:23:31] [info] Database Format: GDSII
[2025-10-12 13:23:31] [info] Verification Mode: Full Check

[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] Type of Errors | Count
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] DB_WARN -> 1
[2025-10-12 13:23:31] [info] LRF_ERR001 -> 14
[2025-10-12 13:23:31] [info] DB_WARN002 -> 159
[2025-10-12 13:23:31] [info] DB_WARN003 -> 161
[2025-10-12 13:23:31] [info] DRC_ERR9 -> 89
[2025-10-12 13:23:31] [info] DRC_ERR -> 67
[2025-10-12 13:23:31] [info] DRC_ERR5 -> 408
[2025-10-12 13:23:31] [info] DRC_ERR4 -> 241
[2025-10-12 13:23:31] [info] DRC_ERR1 -> 301
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] Rule Violation Summary
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:23:31] [info] |-----|-----|-----|
[2025-10-12 13:23:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:23:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:23:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:23:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:23:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:23:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:23:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:23:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:23:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:23:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:23:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:23:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:23:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:23:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:23:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:23:31] [info]
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] Performance Metrics
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:23:31] [info] |-----|-----|
[2025-10-12 13:23:31] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 13:23:31] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 13:23:31] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info]              Summary
[2025-10-12 13:23:31] [info] -----
[2025-10-12 13:23:31] [info] Total Errors:      1120
[2025-10-12 13:23:31] [info] Total Warnings:    321
[2025-10-12 13:23:31] [info] DRC Status:        FAILED
```

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```
[2025-10-12 13:24:30] [info] =====
[2025-10-12 13:24:30] [info]              Design Rule Check Report
[2025-10-12 13:24:30] [info] =====

[2025-10-12 13:24:30] [info] Report Generated:   2025-10-12
[2025-10-12 13:24:30] [info] Run Start Time:     01:23:38 PM
[2025-10-12 13:24:30] [info] Run End Time:       01:24:30 PM
[2025-10-12 13:24:30] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 13:24:30] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:24:30] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:24:30] [info] User Name:          ubuntu
[2025-10-12 13:24:30] [info] Cores Used:         8
[2025-10-12 13:24:30] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info]              Top Cell Information
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] Layout Top Cell:    TOP
[2025-10-12 13:24:30] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:24:30] [info] Database Format:      GDSII
[2025-10-12 13:24:30] [info] Verification Mode:   Full Check

[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info]              Type of Errors | Count
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] DB_WARN -> 1
[2025-10-12 13:24:30] [info] LRF_ERR001 -> 14
[2025-10-12 13:24:30] [info] DB_WARN002 -> 159
[2025-10-12 13:24:30] [info] DB_WARN003 -> 161
[2025-10-12 13:24:30] [info] DRC_ERR9 -> 89
[2025-10-12 13:24:30] [info] DRC_ERR -> 67
[2025-10-12 13:24:30] [info] DRC_ERR5 -> 408
[2025-10-12 13:24:30] [info] DRC_ERR4 -> 241
[2025-10-12 13:24:30] [info] DRC_ERR1 -> 301
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info]              Rule Violation Summary
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:24:30] [info] |-----|-----|-----|
[2025-10-12 13:24:30] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:24:30] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:24:30] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:24:30] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:24:30] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```

[2025-10-12 13:24:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:24:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:24:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:24:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:24:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:24:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:24:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:24:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:24:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:24:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:24:30] [info]
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] Performance Metrics
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] | Stage | Time Taken |
[2025-10-12 13:24:30] [info] |-----|-----|
[2025-10-12 13:24:30] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:24:30] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 13:24:30] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] Summary
[2025-10-12 13:24:30] [info] -----
[2025-10-12 13:24:30] [info] Total Errors: 1120
[2025-10-12 13:24:30] [info] Total Warnings: 321
[2025-10-12 13:24:30] [info] DRC Status: FAILED

```

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```

[2025-10-12 13:25:28] [info] =====
[2025-10-12 13:25:28] [info] Design Rule Check Report
[2025-10-12 13:25:28] [info] =====

[2025-10-12 13:25:28] [info] Report Generated: 2025-10-12
[2025-10-12 13:25:28] [info] Run Start Time: 01:24:37 PM
[2025-10-12 13:25:28] [info] Run End Time: 01:25:28 PM
[2025-10-12 13:25:28] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:25:28] [info] Tool Version: LVR_2025.1
[2025-10-12 13:25:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:25:28] [info] User Name: ubuntu
[2025-10-12 13:25:28] [info] Cores Used: 8
[2025-10-12 13:25:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Top Cell Information
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Layout Top Cell: TOP
[2025-10-12 13:25:28] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:25:28] [info] Database Format: GDSII
[2025-10-12 13:25:28] [info] Verification Mode: Full Check

[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Type of Errors | Count
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] DB_WARN -> 1
[2025-10-12 13:25:28] [info] LRF_ERR001 -> 14
[2025-10-12 13:25:28] [info] DB_WARN002 -> 159
[2025-10-12 13:25:28] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:25:28] [info] DRC_ERR9 -> 89
[2025-10-12 13:25:28] [info] DRC_ERR -> 67
[2025-10-12 13:25:28] [info] DRC_ERR5 -> 408
[2025-10-12 13:25:28] [info] DRC_ERR4 -> 241
[2025-10-12 13:25:28] [info] DRC_ERR1 -> 301
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Rule Violation Summary
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:25:28] [info] |-----|-----|-----|
[2025-10-12 13:25:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:25:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:25:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:25:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:25:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:25:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:25:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:25:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:25:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:25:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:25:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:25:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:25:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:25:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:25:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Performance Metrics
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] | Stage | Time Taken |
[2025-10-12 13:25:28] [info] |-----|-----|
[2025-10-12 13:25:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:25:28] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:25:28] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Summary
[2025-10-12 13:25:28] [info] -----
[2025-10-12 13:25:28] [info] Total Errors: 1120
[2025-10-12 13:25:28] [info] Total Warnings: 321
[2025-10-12 13:25:28] [info] DRC Status: FAILED

```

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```

[2025-10-12 13:26:27] [info] =====
[2025-10-12 13:26:27] [info] Design Rule Check Report
[2025-10-12 13:26:27] [info] =====

[2025-10-12 13:26:27] [info] Report Generated: 2025-10-12
[2025-10-12 13:26:27] [info] Run Start Time: 01:25:35 PM
[2025-10-12 13:26:27] [info] Run End Time: 01:26:27 PM
[2025-10-12 13:26:27] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 13:26:27] [info] Tool Version: LVR_2025.1
[2025-10-12 13:26:27] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:26:27] [info] User Name: ubuntu
[2025-10-12 13:26:27] [info] Cores Used: 8
[2025-10-12 13:26:27] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Top Cell Information
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Layout Top Cell:      TOP
[2025-10-12 13:26:27] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:26:27] [info] Database Format:       GDSII
[2025-10-12 13:26:27] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Type of Errors | Count
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] DB_WARN -> 1
[2025-10-12 13:26:27] [info] LRF_ERR001 -> 14
[2025-10-12 13:26:27] [info] DB_WARN002 -> 159
[2025-10-12 13:26:27] [info] DB_WARN003 -> 161
[2025-10-12 13:26:27] [info] DRC_ERR9 -> 89
[2025-10-12 13:26:27] [info] DRC_ERR -> 67
[2025-10-12 13:26:27] [info] DRC_ERR5 -> 408
[2025-10-12 13:26:27] [info] DRC_ERR4 -> 241
[2025-10-12 13:26:27] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Rule Violation Summary
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:26:27] [info] |-----|-----|-----|
[2025-10-12 13:26:27] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:26:27] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:26:27] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:26:27] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:26:27] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:26:27] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:26:27] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:26:27] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:26:27] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:26:27] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:26:27] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:26:27] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:26:27] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:26:27] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:26:27] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:26:27] [info] |
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Performance Metrics
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] | Stage          | Time Taken |
[2025-10-12 13:26:27] [info] |-----|-----|
[2025-10-12 13:26:27] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:26:27] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:26:27] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Summary
```

```
[2025-10-12 13:26:27] [info] -----
[2025-10-12 13:26:27] [info] Total Errors:      1120
[2025-10-12 13:26:27] [info] Total Warnings:    321
[2025-10-12 13:26:27] [info] DRC Status:        FAILED
```

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```
[2025-10-12 13:27:26] [info] =====
[2025-10-12 13:27:26] [info]                      Design Rule Check Report
[2025-10-12 13:27:26] [info] =====

[2025-10-12 13:27:26] [info] Report Generated:      2025-10-12
[2025-10-12 13:27:26] [info] Run Start Time:       01:26:34 PM
[2025-10-12 13:27:26] [info] Run End Time:         01:27:26 PM
[2025-10-12 13:27:26] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 13:27:26] [info] Tool Version:         LVR_2025.1
[2025-10-12 13:27:26] [info] Host Name:            ip-172-31-37-173
[2025-10-12 13:27:26] [info] User Name:            ubuntu
[2025-10-12 13:27:26] [info] Cores Used:           8
[2025-10-12 13:27:26] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info]                      Top Cell Information
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info] Layout Top Cell:      TOP
[2025-10-12 13:27:26] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 13:27:26] [info] Database Format:        GDSII
[2025-10-12 13:27:26] [info] Verification Mode:     Full Check

[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info]                      Type of Errors | Count
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info] DB_WARN -> 1
[2025-10-12 13:27:26] [info] LRF_ERR001 -> 14
[2025-10-12 13:27:26] [info] DB_WARN002 -> 159
[2025-10-12 13:27:26] [info] DB_WARN003 -> 161
[2025-10-12 13:27:26] [info] DRC_ERR9 -> 89
[2025-10-12 13:27:26] [info] DRC_ERR -> 67
[2025-10-12 13:27:26] [info] DRC_ERR5 -> 408
[2025-10-12 13:27:26] [info] DRC_ERR4 -> 241
[2025-10-12 13:27:26] [info] DRC_ERR1 -> 301
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info]                      Rule Violation Summary
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:27:26] [info] |-----|-----|-----|
[2025-10-12 13:27:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:27:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:27:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:27:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:27:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:27:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:27:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:27:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:27:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:27:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:27:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:27:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:27:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:27:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:27:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:27:26] [info]
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info]                      Performance Metrics
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:27:26] [info] |-----|-----|
[2025-10-12 13:27:26] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 13:27:26] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 13:27:26] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info]                Summary
[2025-10-12 13:27:26] [info] -----
[2025-10-12 13:27:26] [info] Total Errors:    1120
[2025-10-12 13:27:26] [info] Total Warnings:    321
[2025-10-12 13:27:26] [info] DRC Status:      FAILED
```

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```
[2025-10-12 13:28:25] [info] =====
[2025-10-12 13:28:25] [info]                Design Rule Check Report
[2025-10-12 13:28:25] [info] =====

[2025-10-12 13:28:25] [info] Report Generated:    2025-10-12
[2025-10-12 13:28:25] [info] Run Start Time:      01:27:33 PM
[2025-10-12 13:28:25] [info] Run End Time:        01:28:25 PM
[2025-10-12 13:28:25] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 13:28:25] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:28:25] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:28:25] [info] User Name:           ubuntu
[2025-10-12 13:28:25] [info] Cores Used:          8
[2025-10-12 13:28:25] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info]                Top Cell Information
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] Layout Top Cell:     TOP
[2025-10-12 13:28:25] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:28:25] [info] Database Format:       GDSII
[2025-10-12 13:28:25] [info] Verification Mode:    Full Check

[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info]                Type of Errors | Count
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] DB_WARN -> 1
[2025-10-12 13:28:25] [info] LRF_ERR001 -> 14
[2025-10-12 13:28:25] [info] DB_WARN002 -> 159
[2025-10-12 13:28:25] [info] DB_WARN003 -> 161
[2025-10-12 13:28:25] [info] DRC_ERR9 -> 89
[2025-10-12 13:28:25] [info] DRC_ERR -> 67
[2025-10-12 13:28:25] [info] DRC_ERR5 -> 408
[2025-10-12 13:28:25] [info] DRC_ERR4 -> 241
[2025-10-12 13:28:25] [info] DRC_ERR1 -> 301
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info]                Rule Violation Summary
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:28:25] [info] |-----|-----|-----|
[2025-10-12 13:28:25] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:28:25] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:28:25] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:28:25] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:28:25] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 13:28:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:28:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:28:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:28:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:28:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:28:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:28:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:28:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:28:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:28:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:28:25] [info]
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] Performance Metrics
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] | Stage | Time Taken |
[2025-10-12 13:28:25] [info] |-----|-----|
[2025-10-12 13:28:25] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:28:25] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:28:25] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] Summary
[2025-10-12 13:28:25] [info] -----
[2025-10-12 13:28:25] [info] Total Errors: 1120
[2025-10-12 13:28:25] [info] Total Warnings: 321
[2025-10-12 13:28:25] [info] DRC Status: FAILED
```

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```
[2025-10-12 13:29:24] [info] =====
[2025-10-12 13:29:24] [info] Design Rule Check Report
[2025-10-12 13:29:24] [info] =====

[2025-10-12 13:29:24] [info] Report Generated: 2025-10-12
[2025-10-12 13:29:24] [info] Run Start Time: 01:28:32 PM
[2025-10-12 13:29:24] [info] Run End Time: 01:29:24 PM
[2025-10-12 13:29:24] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:29:24] [info] Tool Version: LVR_2025.1
[2025-10-12 13:29:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:29:24] [info] User Name: ubuntu
[2025-10-12 13:29:24] [info] Cores Used: 8
[2025-10-12 13:29:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Top Cell Information
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Layout Top Cell: TOP
[2025-10-12 13:29:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:29:24] [info] Database Format: GDSII
[2025-10-12 13:29:24] [info] Verification Mode: Full Check

[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Type of Errors | Count
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] DB_WARN -> 1
[2025-10-12 13:29:24] [info] LRF_ERR001 -> 14
[2025-10-12 13:29:24] [info] DB_WARN002 -> 159
[2025-10-12 13:29:24] [info] DB_WARN003 -> 161
```

```
[2025-10-12 13:29:24] [info] DRC_ERR9 -> 89
[2025-10-12 13:29:24] [info] DRC_ERR -> 67
[2025-10-12 13:29:24] [info] DRC_ERR5 -> 408
[2025-10-12 13:29:24] [info] DRC_ERR4 -> 241
[2025-10-12 13:29:24] [info] DRC_ERR1 -> 301
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Rule Violation Summary
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:29:24] [info] |-----|-----|-----|
[2025-10-12 13:29:24] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:29:24] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:29:24] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:29:24] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:29:24] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:29:24] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 13:29:24] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:29:24] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:29:24] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:29:24] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:29:24] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:29:24] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:29:24] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:29:24] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:29:24] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Performance Metrics
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] | Stage          | Time Taken |
[2025-10-12 13:29:24] [info] |-----|-----|
[2025-10-12 13:29:24] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:29:24] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:29:24] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Summary
[2025-10-12 13:29:24] [info] -----
[2025-10-12 13:29:24] [info] Total Errors: 1120
[2025-10-12 13:29:24] [info] Total Warnings: 321
[2025-10-12 13:29:24] [info] DRC Status: FAILED
```

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```
[2025-10-12 13:30:23] [info] =====
[2025-10-12 13:30:23] [info] Design Rule Check Report
[2025-10-12 13:30:23] [info] =====

[2025-10-12 13:30:23] [info] Report Generated: 2025-10-12
[2025-10-12 13:30:23] [info] Run Start Time: 01:29:31 PM
[2025-10-12 13:30:23] [info] Run End Time: 01:30:23 PM
[2025-10-12 13:30:23] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:30:23] [info] Tool Version: LVR_2025.1
[2025-10-12 13:30:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:30:23] [info] User Name: ubuntu
[2025-10-12 13:30:23] [info] Cores Used: 8
[2025-10-12 13:30:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```

[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Top Cell Information
[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Layout Top Cell:      TOP
[2025-10-12 13:30:23] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:30:23] [info] Database Format:       GDSII
[2025-10-12 13:30:23] [info] Verification Mode:    Full Check

```

```

[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Type of Errors | Count
[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] DB_WARN -> 1
[2025-10-12 13:30:23] [info] LRF_ERR001 -> 14
[2025-10-12 13:30:23] [info] DB_WARN002 -> 159
[2025-10-12 13:30:23] [info] DB_WARN003 -> 161
[2025-10-12 13:30:23] [info] DRC_ERR9 -> 89
[2025-10-12 13:30:23] [info] DRC_ERR -> 67
[2025-10-12 13:30:23] [info] DRC_ERR5 -> 408
[2025-10-12 13:30:23] [info] DRC_ERR4 -> 241
[2025-10-12 13:30:23] [info] DRC_ERR1 -> 301

```

```

[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Rule Violation Summary
[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:30:23] [info] |-----|-----|-----|
[2025-10-12 13:30:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:30:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:30:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:30:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:30:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:30:23] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:30:23] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:30:23] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:30:23] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:30:23] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:30:23] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:30:23] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:30:23] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:30:23] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:30:23] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |

```

```

[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Performance Metrics
[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] | Stage          | Time Taken |
[2025-10-12 13:30:23] [info] |-----|-----|
[2025-10-12 13:30:23] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:30:23] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:30:23] [info] | Report Generation | 0 minutes 0 seconds |

```

```

[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Summary
[2025-10-12 13:30:23] [info] -----
[2025-10-12 13:30:23] [info] Total Errors:      1120
[2025-10-12 13:30:23] [info] Total Warnings:    321
[2025-10-12 13:30:23] [info] DRC Status:        FAILED

```


lvr_summary_178.rpt

```
[2025-10-12 13:31:21] [info] =====
[2025-10-12 13:31:21] [info]                      Design Rule Check Report
[2025-10-12 13:31:21] [info] =====

[2025-10-12 13:31:21] [info] Report Generated:      2025-10-12
[2025-10-12 13:31:21] [info] Run Start Time:       01:30:31 PM
[2025-10-12 13:31:21] [info] Run End Time:         01:31:21 PM
[2025-10-12 13:31:21] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 13:31:21] [info] Tool Version:         LVR_2025.1
[2025-10-12 13:31:21] [info] Host Name:            ip-172-31-37-173
[2025-10-12 13:31:21] [info] User Name:            ubuntu
[2025-10-12 13:31:21] [info] Cores Used:           8
[2025-10-12 13:31:21] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info]                      Top Cell Information
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info] Layout Top Cell:      TOP
[2025-10-12 13:31:21] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 13:31:21] [info] Database Format:        GDSII
[2025-10-12 13:31:21] [info] Verification Mode:     Full Check

[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info]                      Type of Errors | Count
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info] DB_WARN -> 1
[2025-10-12 13:31:21] [info] LRF_ERR001 -> 14
[2025-10-12 13:31:21] [info] DB_WARN002 -> 159
[2025-10-12 13:31:21] [info] DB_WARN003 -> 161
[2025-10-12 13:31:21] [info] DRC_ERR9 -> 89
[2025-10-12 13:31:21] [info] DRC_ERR -> 67
[2025-10-12 13:31:21] [info] DRC_ERR5 -> 408
[2025-10-12 13:31:21] [info] DRC_ERR4 -> 241
[2025-10-12 13:31:21] [info] DRC_ERR1 -> 301
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info]                      Rule Violation Summary
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:31:21] [info] |-----|-----|-----|
[2025-10-12 13:31:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:31:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:31:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:31:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:31:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:31:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:31:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:31:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:31:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:31:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:31:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:31:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:31:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:31:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:31:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:31:21] [info]
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info]                      Performance Metrics
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:31:21] [info] |-----|-----|
[2025-10-12 13:31:21] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:31:21] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 13:31:21] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info] Summary
[2025-10-12 13:31:21] [info] -----
[2025-10-12 13:31:21] [info] Total Errors: 1120
[2025-10-12 13:31:21] [info] Total Warnings: 321
[2025-10-12 13:31:21] [info] DRC Status: FAILED
```

lvr_summary_179.rpt

```
[2025-10-12 13:32:23] [info] =====
[2025-10-12 13:32:23] [info] Design Rule Check Report
[2025-10-12 13:32:23] [info] =====

[2025-10-12 13:32:23] [info] Report Generated: 2025-10-12
[2025-10-12 13:32:23] [info] Run Start Time: 01:31:30 PM
[2025-10-12 13:32:23] [info] Run End Time: 01:32:23 PM
[2025-10-12 13:32:23] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 13:32:23] [info] Tool Version: LVR_2025.1
[2025-10-12 13:32:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:32:23] [info] User Name: ubuntu
[2025-10-12 13:32:23] [info] Cores Used: 8
[2025-10-12 13:32:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Top Cell Information
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Layout Top Cell: TOP
[2025-10-12 13:32:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:32:23] [info] Database Format: GDSII
[2025-10-12 13:32:23] [info] Verification Mode: Full Check

[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Type of Errors | Count
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] DB_WARN -> 1
[2025-10-12 13:32:23] [info] LRF_ERR001 -> 14
[2025-10-12 13:32:23] [info] DB_WARN002 -> 159
[2025-10-12 13:32:23] [info] DB_WARN003 -> 161
[2025-10-12 13:32:23] [info] DRC_ERR9 -> 89
[2025-10-12 13:32:23] [info] DRC_ERR -> 67
[2025-10-12 13:32:23] [info] DRC_ERR5 -> 408
[2025-10-12 13:32:23] [info] DRC_ERR4 -> 241
[2025-10-12 13:32:23] [info] DRC_ERR1 -> 301
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Rule Violation Summary
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:32:23] [info] |-----|-----|-----|
[2025-10-12 13:32:23] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:32:23] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:32:23] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:32:23] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:32:23] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 13:32:23] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:32:23] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:32:23] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:32:23] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:32:23] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:32:23] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:32:23] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:32:23] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:32:23] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:32:23] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:32:23] [info]
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Performance Metrics
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] | Stage | Time Taken |
[2025-10-12 13:32:23] [info] |-----|-----|
[2025-10-12 13:32:23] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:32:23] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 13:32:23] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Summary
[2025-10-12 13:32:23] [info] -----
[2025-10-12 13:32:23] [info] Total Errors: 1120
[2025-10-12 13:32:23] [info] Total Warnings: 321
[2025-10-12 13:32:23] [info] DRC Status: FAILED

```

lvr_summary_18.rpt

```

[2025-10-12 10:58:27] [info] =====
[2025-10-12 10:58:27] [info] Design Rule Check Report
[2025-10-12 10:58:27] [info] =====

[2025-10-12 10:58:27] [info] Report Generated: 2025-10-12
[2025-10-12 10:58:27] [info] Run Start Time: 10:57:36 AM
[2025-10-12 10:58:27] [info] Run End Time: 10:58:27 AM
[2025-10-12 10:58:27] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 10:58:27] [info] Tool Version: LVR_2025.1
[2025-10-12 10:58:27] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:58:27] [info] User Name: ubuntu
[2025-10-12 10:58:27] [info] Cores Used: 8
[2025-10-12 10:58:27] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Top Cell Information
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Layout Top Cell: TOP
[2025-10-12 10:58:27] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:58:27] [info] Database Format: GDSII
[2025-10-12 10:58:27] [info] Verification Mode: Full Check

[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Type of Errors | Count
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] DB_WARN -> 1
[2025-10-12 10:58:27] [info] LRF_ERR001 -> 14
[2025-10-12 10:58:27] [info] DB_WARN002 -> 159
[2025-10-12 10:58:27] [info] DB_WARN003 -> 161

```

```

[2025-10-12 10:58:27] [info] DRC_ERR9 -> 89
[2025-10-12 10:58:27] [info] DRC_ERR -> 67
[2025-10-12 10:58:27] [info] DRC_ERR5 -> 408
[2025-10-12 10:58:27] [info] DRC_ERR4 -> 241
[2025-10-12 10:58:27] [info] DRC_ERR1 -> 301
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Rule Violation Summary
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:58:27] [info] |-----|-----|-----|
[2025-10-12 10:58:27] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:58:27] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:58:27] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:58:27] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:58:27] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:58:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:58:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:58:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:58:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:58:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:58:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:58:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:58:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:58:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:58:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Performance Metrics
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] | Stage | Time Taken |
[2025-10-12 10:58:27] [info] |-----|-----|
[2025-10-12 10:58:27] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:58:27] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 10:58:27] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Summary
[2025-10-12 10:58:27] [info] -----
[2025-10-12 10:58:27] [info] Total Errors: 1120
[2025-10-12 10:58:27] [info] Total Warnings: 321
[2025-10-12 10:58:27] [info] DRC Status: FAILED

```

lvr_summary_180.rpt

```

[2025-10-12 13:33:22] [info] =====
[2025-10-12 13:33:22] [info] Design Rule Check Report
[2025-10-12 13:33:22] [info] =====

[2025-10-12 13:33:22] [info] Report Generated: 2025-10-12
[2025-10-12 13:33:22] [info] Run Start Time: 01:32:30 PM
[2025-10-12 13:33:22] [info] Run End Time: 01:33:22 PM
[2025-10-12 13:33:22] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:33:22] [info] Tool Version: LVR_2025.1
[2025-10-12 13:33:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:33:22] [info] User Name: ubuntu
[2025-10-12 13:33:22] [info] Cores Used: 8
[2025-10-12 13:33:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Top Cell Information
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Layout Top Cell:      TOP
[2025-10-12 13:33:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:33:22] [info] Database Format:      GDSII
[2025-10-12 13:33:22] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Type of Errors | Count
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] DB_WARN -> 1
[2025-10-12 13:33:22] [info] LRF_ERR001 -> 14
[2025-10-12 13:33:22] [info] DB_WARN002 -> 159
[2025-10-12 13:33:22] [info] DB_WARN003 -> 161
[2025-10-12 13:33:22] [info] DRC_ERR9 -> 89
[2025-10-12 13:33:22] [info] DRC_ERR -> 67
[2025-10-12 13:33:22] [info] DRC_ERR5 -> 408
[2025-10-12 13:33:22] [info] DRC_ERR4 -> 241
[2025-10-12 13:33:22] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Rule Violation Summary
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:33:22] [info] |-----|-----|-----|
[2025-10-12 13:33:22] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:33:22] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:33:22] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:33:22] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:33:22] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:33:22] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:33:22] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:33:22] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:33:22] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:33:22] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:33:22] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:33:22] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:33:22] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:33:22] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:33:22] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:33:22] [info] |
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Performance Metrics
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] | Stage          | Time Taken |
[2025-10-12 13:33:22] [info] |-----|-----|
[2025-10-12 13:33:22] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:33:22] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 13:33:22] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Summary
```

```
[2025-10-12 13:33:22] [info] -----
[2025-10-12 13:33:22] [info] Total Errors:      1120
[2025-10-12 13:33:22] [info] Total Warnings:    321
[2025-10-12 13:33:22] [info] DRC Status:        FAILED
```

lvr_summary_181.rpt

```
[2025-10-12 13:34:21] [info] =====
[2025-10-12 13:34:21] [info] Design Rule Check Report
[2025-10-12 13:34:21] [info] =====

[2025-10-12 13:34:21] [info] Report Generated: 2025-10-12
[2025-10-12 13:34:21] [info] Run Start Time: 01:33:29 PM
[2025-10-12 13:34:21] [info] Run End Time: 01:34:21 PM
[2025-10-12 13:34:21] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:34:21] [info] Tool Version: LVR_2025.1
[2025-10-12 13:34:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:34:21] [info] User Name: ubuntu
[2025-10-12 13:34:21] [info] Cores Used: 8
[2025-10-12 13:34:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Top Cell Information
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Layout Top Cell: TOP
[2025-10-12 13:34:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:34:21] [info] Database Format: GDSII
[2025-10-12 13:34:21] [info] Verification Mode: Full Check

[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Type of Errors | Count
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] DB_WARN -> 1
[2025-10-12 13:34:21] [info] LRF_ERR001 -> 14
[2025-10-12 13:34:21] [info] DB_WARN002 -> 159
[2025-10-12 13:34:21] [info] DB_WARN003 -> 161
[2025-10-12 13:34:21] [info] DRC_ERR9 -> 89
[2025-10-12 13:34:21] [info] DRC_ERR -> 67
[2025-10-12 13:34:21] [info] DRC_ERR5 -> 408
[2025-10-12 13:34:21] [info] DRC_ERR4 -> 241
[2025-10-12 13:34:21] [info] DRC_ERR1 -> 301
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Rule Violation Summary
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:34:21] [info] |-----|-----|-----|
[2025-10-12 13:34:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:34:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:34:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:34:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:34:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:34:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:34:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:34:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:34:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:34:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:34:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:34:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:34:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:34:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:34:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:34:21] [info]
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Performance Metrics
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:34:21] [info] |-----|-----|
[2025-10-12 13:34:21] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:34:21] [info] | Rule Checking        | 0 minutes 41 seconds |
[2025-10-12 13:34:21] [info] | Report Generation    | 0 minutes 0 seconds |

[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Summary
[2025-10-12 13:34:21] [info] -----
[2025-10-12 13:34:21] [info] Total Errors: 1120
[2025-10-12 13:34:21] [info] Total Warnings: 321
[2025-10-12 13:34:21] [info] DRC Status: FAILED
```

lvr_summary_182.rpt

```
[2025-10-12 13:35:19] [info] =====
[2025-10-12 13:35:19] [info] Design Rule Check Report
[2025-10-12 13:35:19] [info] =====

[2025-10-12 13:35:19] [info] Report Generated: 2025-10-12
[2025-10-12 13:35:19] [info] Run Start Time: 01:34:29 PM
[2025-10-12 13:35:19] [info] Run End Time: 01:35:19 PM
[2025-10-12 13:35:19] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:35:19] [info] Tool Version: LVR_2025.1
[2025-10-12 13:35:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:35:19] [info] User Name: ubuntu
[2025-10-12 13:35:19] [info] Cores Used: 8
[2025-10-12 13:35:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Top Cell Information
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Layout Top Cell: TOP
[2025-10-12 13:35:19] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:35:19] [info] Database Format: GDSII
[2025-10-12 13:35:19] [info] Verification Mode: Full Check

[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Type of Errors | Count
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] DB_WARN -> 1
[2025-10-12 13:35:19] [info] LRF_ERR001 -> 14
[2025-10-12 13:35:19] [info] DB_WARN002 -> 159
[2025-10-12 13:35:19] [info] DB_WARN003 -> 161
[2025-10-12 13:35:19] [info] DRC_ERR9 -> 89
[2025-10-12 13:35:19] [info] DRC_ERR -> 67
[2025-10-12 13:35:19] [info] DRC_ERR5 -> 408
[2025-10-12 13:35:19] [info] DRC_ERR4 -> 241
[2025-10-12 13:35:19] [info] DRC_ERR1 -> 301
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Rule Violation Summary
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:35:19] [info] |-----|-----|-----|
[2025-10-12 13:35:19] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:35:19] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:35:19] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:35:19] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:35:19] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 13:35:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:35:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:35:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:35:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:35:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:35:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:35:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:35:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:35:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:35:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:35:19] [info]
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Performance Metrics
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] | Stage | Time Taken |
[2025-10-12 13:35:19] [info] |-----|-----|
[2025-10-12 13:35:19] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:35:19] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:35:19] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Summary
[2025-10-12 13:35:19] [info] -----
[2025-10-12 13:35:19] [info] Total Errors: 1120
[2025-10-12 13:35:19] [info] Total Warnings: 321
[2025-10-12 13:35:19] [info] DRC Status: FAILED

```

lvr_summary_183.rpt

```

[2025-10-12 13:36:18] [info] =====
[2025-10-12 13:36:18] [info] Design Rule Check Report
[2025-10-12 13:36:18] [info] =====

[2025-10-12 13:36:18] [info] Report Generated: 2025-10-12
[2025-10-12 13:36:18] [info] Run Start Time: 01:35:28 PM
[2025-10-12 13:36:18] [info] Run End Time: 01:36:18 PM
[2025-10-12 13:36:18] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:36:18] [info] Tool Version: LVR_2025.1
[2025-10-12 13:36:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:36:18] [info] User Name: ubuntu
[2025-10-12 13:36:18] [info] Cores Used: 8
[2025-10-12 13:36:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Top Cell Information
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Layout Top Cell: TOP
[2025-10-12 13:36:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:36:18] [info] Database Format: GDSII
[2025-10-12 13:36:18] [info] Verification Mode: Full Check

[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Type of Errors | Count
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] DB_WARN -> 1
[2025-10-12 13:36:18] [info] LRF_ERR001 -> 14
[2025-10-12 13:36:18] [info] DB_WARN002 -> 159
[2025-10-12 13:36:18] [info] DB_WARN003 -> 161

```



```

[2025-10-12 13:36:18] [info] DRC_ERR9 -> 89
[2025-10-12 13:36:18] [info] DRC_ERR -> 67
[2025-10-12 13:36:18] [info] DRC_ERR5 -> 408
[2025-10-12 13:36:18] [info] DRC_ERR4 -> 241
[2025-10-12 13:36:18] [info] DRC_ERR1 -> 301
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Rule Violation Summary
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:36:18] [info] |-----|-----|-----|
[2025-10-12 13:36:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:36:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:36:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:36:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:36:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:36:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:36:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:36:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:36:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:36:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:36:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:36:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:36:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:36:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:36:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Performance Metrics
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] | Stage | Time Taken |
[2025-10-12 13:36:18] [info] |-----|-----|
[2025-10-12 13:36:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:36:18] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:36:18] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Summary
[2025-10-12 13:36:18] [info] -----
[2025-10-12 13:36:18] [info] Total Errors: 1120
[2025-10-12 13:36:18] [info] Total Warnings: 321
[2025-10-12 13:36:18] [info] DRC Status: FAILED

```

lvr_summary_184.rpt

```

[2025-10-12 13:37:17] [info] =====
[2025-10-12 13:37:17] [info] Design Rule Check Report
[2025-10-12 13:37:17] [info] =====

[2025-10-12 13:37:17] [info] Report Generated: 2025-10-12
[2025-10-12 13:37:17] [info] Run Start Time: 01:36:27 PM
[2025-10-12 13:37:17] [info] Run End Time: 01:37:17 PM
[2025-10-12 13:37:17] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:37:17] [info] Tool Version: LVR_2025.1
[2025-10-12 13:37:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:37:17] [info] User Name: ubuntu
[2025-10-12 13:37:17] [info] Cores Used: 8
[2025-10-12 13:37:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] Top Cell Information
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] Layout Top Cell:      TOP
[2025-10-12 13:37:17] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:37:17] [info] Database Format:      GDSII
[2025-10-12 13:37:17] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] Type of Errors | Count
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] DB_WARN -> 1
[2025-10-12 13:37:17] [info] LRF_ERR001 -> 14
[2025-10-12 13:37:17] [info] DB_WARN002 -> 159
[2025-10-12 13:37:17] [info] DB_WARN003 -> 161
[2025-10-12 13:37:17] [info] DRC_ERR9 -> 89
[2025-10-12 13:37:17] [info] DRC_ERR -> 67
[2025-10-12 13:37:17] [info] DRC_ERR5 -> 408
[2025-10-12 13:37:17] [info] DRC_ERR4 -> 241
[2025-10-12 13:37:17] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] Rule Violation Summary
```

```
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:37:17] [info] |-----|-----|-----|
[2025-10-12 13:37:17] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:37:17] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:37:17] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:37:17] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:37:17] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:37:17] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:37:17] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:37:17] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:37:17] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:37:17] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:37:17] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:37:17] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:37:17] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:37:17] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:37:17] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:37:17] [info] -----
```

```
[2025-10-12 13:37:17] [info] Performance Metrics
```

```
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] | Stage          | Time Taken |
[2025-10-12 13:37:17] [info] |-----|-----|
[2025-10-12 13:37:17] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:37:17] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 13:37:17] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:37:17] [info] -----
```

```
[2025-10-12 13:37:17] [info] Summary
```

```
[2025-10-12 13:37:17] [info] -----
[2025-10-12 13:37:17] [info] Total Errors:      1120
[2025-10-12 13:37:17] [info] Total Warnings:    321
[2025-10-12 13:37:17] [info] DRC Status:        FAILED
```

lvr_summary_185.rpt

```
[2025-10-12 13:38:17] [info] =====
[2025-10-12 13:38:17] [info]                      Design Rule Check Report
[2025-10-12 13:38:17] [info] =====

[2025-10-12 13:38:17] [info] Report Generated:    2025-10-12
[2025-10-12 13:38:17] [info] Run Start Time:      01:37:25 PM
[2025-10-12 13:38:17] [info] Run End Time:        01:38:17 PM
[2025-10-12 13:38:17] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 13:38:17] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:38:17] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:38:17] [info] User Name:          ubuntu
[2025-10-12 13:38:17] [info] Cores Used:         8
[2025-10-12 13:38:17] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info]                      Top Cell Information
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info] Layout Top Cell:    TOP
[2025-10-12 13:38:17] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:38:17] [info] Database Format:      GDSII
[2025-10-12 13:38:17] [info] Verification Mode:   Full Check

[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info]                      Type of Errors | Count
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info] DB_WARN -> 1
[2025-10-12 13:38:17] [info] LRF_ERR001 -> 14
[2025-10-12 13:38:17] [info] DB_WARN002 -> 159
[2025-10-12 13:38:17] [info] DB_WARN003 -> 161
[2025-10-12 13:38:17] [info] DRC_ERR9 -> 89
[2025-10-12 13:38:17] [info] DRC_ERR -> 67
[2025-10-12 13:38:17] [info] DRC_ERR5 -> 408
[2025-10-12 13:38:17] [info] DRC_ERR4 -> 241
[2025-10-12 13:38:17] [info] DRC_ERR1 -> 301
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info]                      Rule Violation Summary
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:38:17] [info] |-----|-----|-----|
[2025-10-12 13:38:17] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:38:17] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:38:17] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:38:17] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:38:17] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:38:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:38:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:38:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:38:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:38:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:38:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:38:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:38:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:38:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:38:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:38:17] [info]
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info]                      Performance Metrics
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:38:17] [info] |-----|-----|
[2025-10-12 13:38:17] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:38:17] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 13:38:17] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info]                Summary
[2025-10-12 13:38:17] [info] -----
[2025-10-12 13:38:17] [info] Total Errors:    1120
[2025-10-12 13:38:17] [info] Total Warnings:    321
[2025-10-12 13:38:17] [info] DRC Status:      FAILED
```

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```
[2025-10-12 13:39:14] [info] =====
[2025-10-12 13:39:14] [info]                Design Rule Check Report
[2025-10-12 13:39:14] [info] =====

[2025-10-12 13:39:14] [info] Report Generated:    2025-10-12
[2025-10-12 13:39:14] [info] Run Start Time:      01:38:24 PM
[2025-10-12 13:39:14] [info] Run End Time:        01:39:14 PM
[2025-10-12 13:39:14] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 13:39:14] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:39:14] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:39:14] [info] User Name:           ubuntu
[2025-10-12 13:39:14] [info] Cores Used:          8
[2025-10-12 13:39:14] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info]                Top Cell Information
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] Layout Top Cell:     TOP
[2025-10-12 13:39:14] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:39:14] [info] Database Format:       GDSII
[2025-10-12 13:39:14] [info] Verification Mode:    Full Check

[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info]                Type of Errors | Count
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] DB_WARN -> 1
[2025-10-12 13:39:14] [info] LRF_ERR001 -> 14
[2025-10-12 13:39:14] [info] DB_WARN002 -> 159
[2025-10-12 13:39:14] [info] DB_WARN003 -> 161
[2025-10-12 13:39:14] [info] DRC_ERR9 -> 89
[2025-10-12 13:39:14] [info] DRC_ERR -> 67
[2025-10-12 13:39:14] [info] DRC_ERR5 -> 408
[2025-10-12 13:39:14] [info] DRC_ERR4 -> 241
[2025-10-12 13:39:14] [info] DRC_ERR1 -> 301
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info]                Rule Violation Summary
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:39:14] [info] |-----|-----|-----|
[2025-10-12 13:39:14] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:39:14] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:39:14] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:39:14] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:39:14] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 13:39:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:39:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:39:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:39:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:39:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:39:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:39:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:39:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:39:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:39:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:39:14] [info]
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] Performance Metrics
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] | Stage | Time Taken |
[2025-10-12 13:39:14] [info] |-----|-----|
[2025-10-12 13:39:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:39:14] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:39:14] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] Summary
[2025-10-12 13:39:14] [info] -----
[2025-10-12 13:39:14] [info] Total Errors: 1120
[2025-10-12 13:39:14] [info] Total Warnings: 321
[2025-10-12 13:39:14] [info] DRC Status: FAILED

```

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```

[2025-10-12 13:40:13] [info] =====
[2025-10-12 13:40:13] [info] Design Rule Check Report
[2025-10-12 13:40:13] [info] =====

[2025-10-12 13:40:13] [info] Report Generated: 2025-10-12
[2025-10-12 13:40:13] [info] Run Start Time: 01:39:22 PM
[2025-10-12 13:40:13] [info] Run End Time: 01:40:13 PM
[2025-10-12 13:40:13] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:40:13] [info] Tool Version: LVR_2025.1
[2025-10-12 13:40:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:40:13] [info] User Name: ubuntu
[2025-10-12 13:40:13] [info] Cores Used: 8
[2025-10-12 13:40:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Top Cell Information
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Layout Top Cell: TOP
[2025-10-12 13:40:13] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:40:13] [info] Database Format: GDSII
[2025-10-12 13:40:13] [info] Verification Mode: Full Check

[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Type of Errors | Count
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] DB_WARN -> 1
[2025-10-12 13:40:13] [info] LRF_ERR001 -> 14
[2025-10-12 13:40:13] [info] DB_WARN002 -> 159
[2025-10-12 13:40:13] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:40:13] [info] DRC_ERR9 -> 89
[2025-10-12 13:40:13] [info] DRC_ERR -> 67
[2025-10-12 13:40:13] [info] DRC_ERR5 -> 408
[2025-10-12 13:40:13] [info] DRC_ERR4 -> 241
[2025-10-12 13:40:13] [info] DRC_ERR1 -> 301
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Rule Violation Summary
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:40:13] [info] |-----|-----|-----|
[2025-10-12 13:40:13] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:40:13] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:40:13] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:40:13] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:40:13] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:40:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:40:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:40:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:40:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:40:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:40:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:40:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:40:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:40:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:40:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Performance Metrics
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] | Stage | Time Taken |
[2025-10-12 13:40:13] [info] |-----|-----|
[2025-10-12 13:40:13] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:40:13] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:40:13] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Summary
[2025-10-12 13:40:13] [info] -----
[2025-10-12 13:40:13] [info] Total Errors: 1120
[2025-10-12 13:40:13] [info] Total Warnings: 321
[2025-10-12 13:40:13] [info] DRC Status: FAILED

```

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```

[2025-10-12 13:41:13] [info] =====
[2025-10-12 13:41:13] [info] Design Rule Check Report
[2025-10-12 13:41:13] [info] =====

[2025-10-12 13:41:13] [info] Report Generated: 2025-10-12
[2025-10-12 13:41:13] [info] Run Start Time: 01:40:21 PM
[2025-10-12 13:41:13] [info] Run End Time: 01:41:13 PM
[2025-10-12 13:41:13] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:41:13] [info] Tool Version: LVR_2025.1
[2025-10-12 13:41:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:41:13] [info] User Name: ubuntu
[2025-10-12 13:41:13] [info] Cores Used: 8
[2025-10-12 13:41:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Top Cell Information
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Layout Top Cell:      TOP
[2025-10-12 13:41:13] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:41:13] [info] Database Format:      GDSII
[2025-10-12 13:41:13] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Type of Errors | Count
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] DB_WARN -> 1
[2025-10-12 13:41:13] [info] LRF_ERR001 -> 14
[2025-10-12 13:41:13] [info] DB_WARN002 -> 159
[2025-10-12 13:41:13] [info] DB_WARN003 -> 161
[2025-10-12 13:41:13] [info] DRC_ERR9 -> 89
[2025-10-12 13:41:13] [info] DRC_ERR -> 67
[2025-10-12 13:41:13] [info] DRC_ERR5 -> 408
[2025-10-12 13:41:13] [info] DRC_ERR4 -> 241
[2025-10-12 13:41:13] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Rule Violation Summary
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:41:13] [info] |-----|-----|-----|
[2025-10-12 13:41:13] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:41:13] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:41:13] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:41:13] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:41:13] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:41:13] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:41:13] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:41:13] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:41:13] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:41:13] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:41:13] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:41:13] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:41:13] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:41:13] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:41:13] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:41:13] [info] |
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Performance Metrics
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] | Stage          | Time Taken |
[2025-10-12 13:41:13] [info] |-----|-----|
[2025-10-12 13:41:13] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:41:13] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:41:13] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Summary
```

```
[2025-10-12 13:41:13] [info] -----
[2025-10-12 13:41:13] [info] Total Errors:      1120
[2025-10-12 13:41:13] [info] Total Warnings:    321
[2025-10-12 13:41:13] [info] DRC Status:        FAILED
```

lvr_summary_189.rpt

```
[2025-10-12 13:42:11] [info] =====
[2025-10-12 13:42:11] [info]                      Design Rule Check Report
[2025-10-12 13:42:11] [info] =====

[2025-10-12 13:42:11] [info] Report Generated:    2025-10-12
[2025-10-12 13:42:11] [info] Run Start Time:      01:41:19 PM
[2025-10-12 13:42:11] [info] Run End Time:        01:42:11 PM
[2025-10-12 13:42:11] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 13:42:11] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:42:11] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:42:11] [info] User Name:          ubuntu
[2025-10-12 13:42:11] [info] Cores Used:         8
[2025-10-12 13:42:11] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info]                      Top Cell Information
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info] Layout Top Cell:    TOP
[2025-10-12 13:42:11] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:42:11] [info] Database Format:      GDSII
[2025-10-12 13:42:11] [info] Verification Mode:   Full Check

[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info]                      Type of Errors | Count
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info] DB_WARN -> 1
[2025-10-12 13:42:11] [info] LRF_ERR001 -> 14
[2025-10-12 13:42:11] [info] DB_WARN002 -> 159
[2025-10-12 13:42:11] [info] DB_WARN003 -> 161
[2025-10-12 13:42:11] [info] DRC_ERR9 -> 89
[2025-10-12 13:42:11] [info] DRC_ERR -> 67
[2025-10-12 13:42:11] [info] DRC_ERR5 -> 408
[2025-10-12 13:42:11] [info] DRC_ERR4 -> 241
[2025-10-12 13:42:11] [info] DRC_ERR1 -> 301
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info]                      Rule Violation Summary
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:42:11] [info] |-----|-----|-----|
[2025-10-12 13:42:11] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:42:11] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:42:11] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:42:11] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:42:11] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:42:11] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:42:11] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:42:11] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:42:11] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:42:11] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:42:11] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:42:11] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:42:11] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:42:11] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:42:11] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:42:11] [info]
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info]                      Performance Metrics
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info] | Stage | Time Taken |
```



```
[2025-10-12 13:42:11] [info] |-----|-----|
[2025-10-12 13:42:11] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:42:11] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 13:42:11] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info] Summary
[2025-10-12 13:42:11] [info] -----
[2025-10-12 13:42:11] [info] Total Errors: 1120
[2025-10-12 13:42:11] [info] Total Warnings: 321
[2025-10-12 13:42:11] [info] DRC Status: FAILED
```

lvr_summary_19.rpt

```
[2025-10-12 10:59:26] [info] =====
[2025-10-12 10:59:26] [info] Design Rule Check Report
[2025-10-12 10:59:26] [info] =====

[2025-10-12 10:59:26] [info] Report Generated: 2025-10-12
[2025-10-12 10:59:26] [info] Run Start Time: 10:58:33 AM
[2025-10-12 10:59:26] [info] Run End Time: 10:59:26 AM
[2025-10-12 10:59:26] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 10:59:26] [info] Tool Version: LVR_2025.1
[2025-10-12 10:59:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:59:26] [info] User Name: ubuntu
[2025-10-12 10:59:26] [info] Cores Used: 8
[2025-10-12 10:59:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Top Cell Information
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Layout Top Cell: TOP
[2025-10-12 10:59:26] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:59:26] [info] Database Format: GDSII
[2025-10-12 10:59:26] [info] Verification Mode: Full Check

[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Type of Errors | Count
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] DB_WARN -> 1
[2025-10-12 10:59:26] [info] LRF_ERR001 -> 14
[2025-10-12 10:59:26] [info] DB_WARN002 -> 159
[2025-10-12 10:59:26] [info] DB_WARN003 -> 161
[2025-10-12 10:59:26] [info] DRC_ERR9 -> 89
[2025-10-12 10:59:26] [info] DRC_ERR -> 67
[2025-10-12 10:59:26] [info] DRC_ERR5 -> 408
[2025-10-12 10:59:26] [info] DRC_ERR4 -> 241
[2025-10-12 10:59:26] [info] DRC_ERR1 -> 301
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Rule Violation Summary
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:59:26] [info] |-----|-----|-----|
[2025-10-12 10:59:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:59:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:59:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:59:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:59:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 10:59:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:59:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:59:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:59:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:59:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:59:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:59:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:59:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:59:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:59:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:59:26] [info]
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Performance Metrics
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] | Stage | Time Taken |
[2025-10-12 10:59:26] [info] |-----|-----|
[2025-10-12 10:59:26] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:59:26] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 10:59:26] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Summary
[2025-10-12 10:59:26] [info] -----
[2025-10-12 10:59:26] [info] Total Errors: 1120
[2025-10-12 10:59:26] [info] Total Warnings: 321
[2025-10-12 10:59:26] [info] DRC Status: FAILED
```

lvr_summary_190.rpt

```
[2025-10-12 13:43:10] [info] =====
[2025-10-12 13:43:10] [info] Design Rule Check Report
[2025-10-12 13:43:10] [info] =====

[2025-10-12 13:43:10] [info] Report Generated: 2025-10-12
[2025-10-12 13:43:10] [info] Run Start Time: 01:42:20 PM
[2025-10-12 13:43:10] [info] Run End Time: 01:43:10 PM
[2025-10-12 13:43:10] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:43:10] [info] Tool Version: LVR_2025.1
[2025-10-12 13:43:10] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:43:10] [info] User Name: ubuntu
[2025-10-12 13:43:10] [info] Cores Used: 8
[2025-10-12 13:43:10] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Top Cell Information
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Layout Top Cell: TOP
[2025-10-12 13:43:10] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:43:10] [info] Database Format: GDSII
[2025-10-12 13:43:10] [info] Verification Mode: Full Check

[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Type of Errors | Count
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] DB_WARN -> 1
[2025-10-12 13:43:10] [info] LRF_ERR001 -> 14
[2025-10-12 13:43:10] [info] DB_WARN002 -> 159
[2025-10-12 13:43:10] [info] DB_WARN003 -> 161
```

```
[2025-10-12 13:43:10] [info] DRC_ERR9 -> 89
[2025-10-12 13:43:10] [info] DRC_ERR -> 67
[2025-10-12 13:43:10] [info] DRC_ERR5 -> 408
[2025-10-12 13:43:10] [info] DRC_ERR4 -> 241
[2025-10-12 13:43:10] [info] DRC_ERR1 -> 301
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Rule Violation Summary
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:43:10] [info] |-----|-----|-----|
[2025-10-12 13:43:10] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:43:10] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:43:10] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:43:10] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:43:10] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:43:10] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:43:10] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:43:10] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:43:10] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:43:10] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:43:10] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:43:10] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:43:10] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:43:10] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:43:10] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Performance Metrics
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] | Stage | Time Taken |
[2025-10-12 13:43:10] [info] |-----|-----|
[2025-10-12 13:43:10] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:43:10] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:43:10] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Summary
[2025-10-12 13:43:10] [info] -----
[2025-10-12 13:43:10] [info] Total Errors: 1120
[2025-10-12 13:43:10] [info] Total Warnings: 321
[2025-10-12 13:43:10] [info] DRC Status: FAILED
```

lvr_summary_191.rpt

```
[2025-10-12 13:44:09] [info] =====
[2025-10-12 13:44:09] [info] Design Rule Check Report
[2025-10-12 13:44:09] [info] =====

[2025-10-12 13:44:09] [info] Report Generated: 2025-10-12
[2025-10-12 13:44:09] [info] Run Start Time: 01:43:18 PM
[2025-10-12 13:44:09] [info] Run End Time: 01:44:09 PM
[2025-10-12 13:44:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:44:09] [info] Tool Version: LVR_2025.1
[2025-10-12 13:44:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:44:09] [info] User Name: ubuntu
[2025-10-12 13:44:09] [info] Cores Used: 8
[2025-10-12 13:44:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Top Cell Information
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Layout Top Cell:      TOP
[2025-10-12 13:44:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:44:09] [info] Database Format:      GDSII
[2025-10-12 13:44:09] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Type of Errors | Count
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] DB_WARN -> 1
[2025-10-12 13:44:09] [info] LRF_ERR001 -> 14
[2025-10-12 13:44:09] [info] DB_WARN002 -> 159
[2025-10-12 13:44:09] [info] DB_WARN003 -> 161
[2025-10-12 13:44:09] [info] DRC_ERR9 -> 89
[2025-10-12 13:44:09] [info] DRC_ERR -> 67
[2025-10-12 13:44:09] [info] DRC_ERR5 -> 408
[2025-10-12 13:44:09] [info] DRC_ERR4 -> 241
[2025-10-12 13:44:09] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Rule Violation Summary
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:44:09] [info] |-----|-----|-----|
[2025-10-12 13:44:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:44:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:44:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:44:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:44:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:44:09] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:44:09] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:44:09] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:44:09] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:44:09] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:44:09] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:44:09] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:44:09] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:44:09] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:44:09] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:44:09] [info] |
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Performance Metrics
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] | Stage          | Time Taken |
[2025-10-12 13:44:09] [info] |-----|-----|
[2025-10-12 13:44:09] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:44:09] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 13:44:09] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Summary
```

```
[2025-10-12 13:44:09] [info] -----
[2025-10-12 13:44:09] [info] Total Errors:      1120
[2025-10-12 13:44:09] [info] Total Warnings:    321
[2025-10-12 13:44:09] [info] DRC Status:        FAILED
```

lvr_summary_192.rpt

```
[2025-10-12 13:45:11] [info] =====
[2025-10-12 13:45:11] [info] Design Rule Check Report
[2025-10-12 13:45:11] [info] =====

[2025-10-12 13:45:11] [info] Report Generated: 2025-10-12
[2025-10-12 13:45:11] [info] Run Start Time: 01:44:19 PM
[2025-10-12 13:45:11] [info] Run End Time: 01:45:11 PM
[2025-10-12 13:45:11] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:45:11] [info] Tool Version: LVR_2025.1
[2025-10-12 13:45:11] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:45:11] [info] User Name: ubuntu
[2025-10-12 13:45:11] [info] Cores Used: 8
[2025-10-12 13:45:11] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] Top Cell Information
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] Layout Top Cell: TOP
[2025-10-12 13:45:11] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:45:11] [info] Database Format: GDSII
[2025-10-12 13:45:11] [info] Verification Mode: Full Check

[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] Type of Errors | Count
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] DB_WARN -> 1
[2025-10-12 13:45:11] [info] LRF_ERR001 -> 14
[2025-10-12 13:45:11] [info] DB_WARN002 -> 159
[2025-10-12 13:45:11] [info] DB_WARN003 -> 161
[2025-10-12 13:45:11] [info] DRC_ERR9 -> 89
[2025-10-12 13:45:11] [info] DRC_ERR -> 67
[2025-10-12 13:45:11] [info] DRC_ERR5 -> 408
[2025-10-12 13:45:11] [info] DRC_ERR4 -> 241
[2025-10-12 13:45:11] [info] DRC_ERR1 -> 301
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] Rule Violation Summary
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:45:11] [info] |-----|-----|-----|
[2025-10-12 13:45:11] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:45:11] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:45:11] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:45:11] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:45:11] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:45:11] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:45:11] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:45:11] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:45:11] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:45:11] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:45:11] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:45:11] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:45:11] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:45:11] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:45:11] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:45:11] [info]
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] Performance Metrics
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:45:11] [info] |-----|-----|
[2025-10-12 13:45:11] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:45:11] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 13:45:11] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info]                Summary
[2025-10-12 13:45:11] [info] -----
[2025-10-12 13:45:11] [info] Total Errors:    1120
[2025-10-12 13:45:11] [info] Total Warnings:      321
[2025-10-12 13:45:11] [info] DRC Status:      FAILED
```

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```
[2025-10-12 13:46:09] [info] =====
[2025-10-12 13:46:09] [info]                Design Rule Check Report
[2025-10-12 13:46:09] [info] =====

[2025-10-12 13:46:09] [info] Report Generated:    2025-10-12
[2025-10-12 13:46:09] [info] Run Start Time:      01:45:18 PM
[2025-10-12 13:46:09] [info] Run End Time:        01:46:09 PM
[2025-10-12 13:46:09] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 13:46:09] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:46:09] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:46:09] [info] User Name:           ubuntu
[2025-10-12 13:46:09] [info] Cores Used:          8
[2025-10-12 13:46:09] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info]                Top Cell Information
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] Layout Top Cell:     TOP
[2025-10-12 13:46:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:46:09] [info] Database Format:       GDSII
[2025-10-12 13:46:09] [info] Verification Mode:    Full Check

[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info]                Type of Errors | Count
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] DB_WARN -> 1
[2025-10-12 13:46:09] [info] LRF_ERR001 -> 14
[2025-10-12 13:46:09] [info] DB_WARN002 -> 159
[2025-10-12 13:46:09] [info] DB_WARN003 -> 161
[2025-10-12 13:46:09] [info] DRC_ERR9 -> 89
[2025-10-12 13:46:09] [info] DRC_ERR -> 67
[2025-10-12 13:46:09] [info] DRC_ERR5 -> 408
[2025-10-12 13:46:09] [info] DRC_ERR4 -> 241
[2025-10-12 13:46:09] [info] DRC_ERR1 -> 301
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info]                Rule Violation Summary
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:46:09] [info] |-----|-----|-----|
[2025-10-12 13:46:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:46:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:46:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:46:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:46:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 13:46:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:46:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:46:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:46:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:46:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:46:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:46:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:46:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:46:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:46:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:46:09] [info]
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] Performance Metrics
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] | Stage | Time Taken |
[2025-10-12 13:46:09] [info] |-----|-----|
[2025-10-12 13:46:09] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:46:09] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:46:09] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] Summary
[2025-10-12 13:46:09] [info] -----
[2025-10-12 13:46:09] [info] Total Errors: 1120
[2025-10-12 13:46:09] [info] Total Warnings: 321
[2025-10-12 13:46:09] [info] DRC Status: FAILED

```

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```

[2025-10-12 13:47:08] [info] =====
[2025-10-12 13:47:08] [info] Design Rule Check Report
[2025-10-12 13:47:08] [info] =====

[2025-10-12 13:47:08] [info] Report Generated: 2025-10-12
[2025-10-12 13:47:08] [info] Run Start Time: 01:46:18 PM
[2025-10-12 13:47:08] [info] Run End Time: 01:47:08 PM
[2025-10-12 13:47:08] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:47:08] [info] Tool Version: LVR_2025.1
[2025-10-12 13:47:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:47:08] [info] User Name: ubuntu
[2025-10-12 13:47:08] [info] Cores Used: 8
[2025-10-12 13:47:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Top Cell Information
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Layout Top Cell: TOP
[2025-10-12 13:47:08] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:47:08] [info] Database Format: GDSII
[2025-10-12 13:47:08] [info] Verification Mode: Full Check

[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Type of Errors | Count
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] DB_WARN -> 1
[2025-10-12 13:47:08] [info] LRF_ERR001 -> 14
[2025-10-12 13:47:08] [info] DB_WARN002 -> 159
[2025-10-12 13:47:08] [info] DB_WARN003 -> 161

```

```
[2025-10-12 13:47:08] [info] DRC_ERR9 -> 89
[2025-10-12 13:47:08] [info] DRC_ERR -> 67
[2025-10-12 13:47:08] [info] DRC_ERR5 -> 408
[2025-10-12 13:47:08] [info] DRC_ERR4 -> 241
[2025-10-12 13:47:08] [info] DRC_ERR1 -> 301
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Rule Violation Summary
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:47:08] [info] |-----|-----|-----|
[2025-10-12 13:47:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:47:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:47:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:47:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:47:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:47:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:47:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:47:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:47:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:47:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:47:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:47:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:47:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:47:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:47:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Performance Metrics
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] | Stage | Time Taken |
[2025-10-12 13:47:08] [info] |-----|-----|
[2025-10-12 13:47:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:47:08] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:47:08] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Summary
[2025-10-12 13:47:08] [info] -----
[2025-10-12 13:47:08] [info] Total Errors: 1120
[2025-10-12 13:47:08] [info] Total Warnings: 321
[2025-10-12 13:47:08] [info] DRC Status: FAILED
```

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```
[2025-10-12 13:48:09] [info] =====
[2025-10-12 13:48:09] [info] Design Rule Check Report
[2025-10-12 13:48:09] [info] =====

[2025-10-12 13:48:09] [info] Report Generated: 2025-10-12
[2025-10-12 13:48:09] [info] Run Start Time: 01:47:17 PM
[2025-10-12 13:48:09] [info] Run End Time: 01:48:09 PM
[2025-10-12 13:48:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:48:09] [info] Tool Version: LVR_2025.1
[2025-10-12 13:48:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:48:09] [info] User Name: ubuntu
[2025-10-12 13:48:09] [info] Cores Used: 8
[2025-10-12 13:48:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] Top Cell Information
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] Layout Top Cell:      TOP
[2025-10-12 13:48:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:48:09] [info] Database Format:      GDSII
[2025-10-12 13:48:09] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] Type of Errors | Count
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] DB_WARN -> 1
[2025-10-12 13:48:09] [info] LRF_ERR001 -> 14
[2025-10-12 13:48:09] [info] DB_WARN002 -> 159
[2025-10-12 13:48:09] [info] DB_WARN003 -> 161
[2025-10-12 13:48:09] [info] DRC_ERR9 -> 89
[2025-10-12 13:48:09] [info] DRC_ERR -> 67
[2025-10-12 13:48:09] [info] DRC_ERR5 -> 408
[2025-10-12 13:48:09] [info] DRC_ERR4 -> 241
[2025-10-12 13:48:09] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] Rule Violation Summary
```

```
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:48:09] [info] |-----|-----|-----|
[2025-10-12 13:48:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:48:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:48:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:48:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:48:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:48:09] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:48:09] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:48:09] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:48:09] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:48:09] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:48:09] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:48:09] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:48:09] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:48:09] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:48:09] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:48:09] [info] -----
```

```
[2025-10-12 13:48:09] [info] Performance Metrics
```

```
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] | Stage          | Time Taken |
[2025-10-12 13:48:09] [info] |-----|-----|
[2025-10-12 13:48:09] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:48:09] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 13:48:09] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:48:09] [info] -----
```

```
[2025-10-12 13:48:09] [info] Summary
```

```
[2025-10-12 13:48:09] [info] -----
[2025-10-12 13:48:09] [info] Total Errors:      1120
[2025-10-12 13:48:09] [info] Total Warnings:    321
[2025-10-12 13:48:09] [info] DRC Status:        FAILED
```

lvr_summary_196.rpt

```
[2025-10-12 13:49:08] [info] =====
[2025-10-12 13:49:08] [info]                      Design Rule Check Report
[2025-10-12 13:49:08] [info] =====

[2025-10-12 13:49:08] [info] Report Generated:      2025-10-12
[2025-10-12 13:49:08] [info] Run Start Time:       01:48:16 PM
[2025-10-12 13:49:08] [info] Run End Time:         01:49:08 PM
[2025-10-12 13:49:08] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 13:49:08] [info] Tool Version:         LVR_2025.1
[2025-10-12 13:49:08] [info] Host Name:            ip-172-31-37-173
[2025-10-12 13:49:08] [info] User Name:            ubuntu
[2025-10-12 13:49:08] [info] Cores Used:           8
[2025-10-12 13:49:08] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info]                      Top Cell Information
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info] Layout Top Cell:      TOP
[2025-10-12 13:49:08] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 13:49:08] [info] Database Format:        GDSII
[2025-10-12 13:49:08] [info] Verification Mode:     Full Check

[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info]                      Type of Errors | Count
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info] DB_WARN -> 1
[2025-10-12 13:49:08] [info] LRF_ERR001 -> 14
[2025-10-12 13:49:08] [info] DB_WARN002 -> 159
[2025-10-12 13:49:08] [info] DB_WARN003 -> 161
[2025-10-12 13:49:08] [info] DRC_ERR9 -> 89
[2025-10-12 13:49:08] [info] DRC_ERR -> 67
[2025-10-12 13:49:08] [info] DRC_ERR5 -> 408
[2025-10-12 13:49:08] [info] DRC_ERR4 -> 241
[2025-10-12 13:49:08] [info] DRC_ERR1 -> 301
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info]                      Rule Violation Summary
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:49:08] [info] |-----|-----|-----|
[2025-10-12 13:49:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:49:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:49:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:49:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:49:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:49:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:49:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:49:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:49:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:49:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:49:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:49:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:49:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:49:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:49:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:49:08] [info]
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info]                      Performance Metrics
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:49:08] [info] |-----|-----|
[2025-10-12 13:49:08] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 13:49:08] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 13:49:08] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info]                Summary
[2025-10-12 13:49:08] [info] -----
[2025-10-12 13:49:08] [info] Total Errors:      1120
[2025-10-12 13:49:08] [info] Total Warnings:    321
[2025-10-12 13:49:08] [info] DRC Status:        FAILED
```

lvr_summary_197.rpt

```
[2025-10-12 13:50:04] [info] =====
[2025-10-12 13:50:04] [info]                Design Rule Check Report
[2025-10-12 13:50:04] [info] =====

[2025-10-12 13:50:04] [info] Report Generated:   2025-10-12
[2025-10-12 13:50:04] [info] Run Start Time:     01:49:14 PM
[2025-10-12 13:50:04] [info] Run End Time:       01:50:04 PM
[2025-10-12 13:50:04] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 13:50:04] [info] Tool Version:       LVR_2025.1
[2025-10-12 13:50:04] [info] Host Name:          ip-172-31-37-173
[2025-10-12 13:50:04] [info] User Name:          ubuntu
[2025-10-12 13:50:04] [info] Cores Used:         8
[2025-10-12 13:50:04] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info]                Top Cell Information
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] Layout Top Cell:    TOP
[2025-10-12 13:50:04] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 13:50:04] [info] Database Format:      GDSII
[2025-10-12 13:50:04] [info] Verification Mode:   Full Check

[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info]                Type of Errors | Count
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] DB_WARN -> 1
[2025-10-12 13:50:04] [info] LRF_ERR001 -> 14
[2025-10-12 13:50:04] [info] DB_WARN002 -> 159
[2025-10-12 13:50:04] [info] DB_WARN003 -> 161
[2025-10-12 13:50:04] [info] DRC_ERR9 -> 89
[2025-10-12 13:50:04] [info] DRC_ERR -> 67
[2025-10-12 13:50:04] [info] DRC_ERR5 -> 408
[2025-10-12 13:50:04] [info] DRC_ERR4 -> 241
[2025-10-12 13:50:04] [info] DRC_ERR1 -> 301
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info]                Rule Violation Summary
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:50:04] [info] |-----|-----|-----|
[2025-10-12 13:50:04] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:50:04] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:50:04] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:50:04] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:50:04] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 13:50:04] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:50:04] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:50:04] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:50:04] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:50:04] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:50:04] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:50:04] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:50:04] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:50:04] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:50:04] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:50:04] [info]
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] Performance Metrics
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] | Stage | Time Taken |
[2025-10-12 13:50:04] [info] |-----|-----|
[2025-10-12 13:50:04] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:50:04] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:50:04] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] Summary
[2025-10-12 13:50:04] [info] -----
[2025-10-12 13:50:04] [info] Total Errors: 1120
[2025-10-12 13:50:04] [info] Total Warnings: 321
[2025-10-12 13:50:04] [info] DRC Status: FAILED
```

lvr_summary_198.rpt

```
[2025-10-12 13:51:02] [info] =====
[2025-10-12 13:51:02] [info] Design Rule Check Report
[2025-10-12 13:51:02] [info] =====

[2025-10-12 13:51:02] [info] Report Generated: 2025-10-12
[2025-10-12 13:51:02] [info] Run Start Time: 01:50:12 PM
[2025-10-12 13:51:02] [info] Run End Time: 01:51:02 PM
[2025-10-12 13:51:02] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:51:02] [info] Tool Version: LVR_2025.1
[2025-10-12 13:51:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:51:02] [info] User Name: ubuntu
[2025-10-12 13:51:02] [info] Cores Used: 8
[2025-10-12 13:51:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Top Cell Information
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Layout Top Cell: TOP
[2025-10-12 13:51:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:51:02] [info] Database Format: GDSII
[2025-10-12 13:51:02] [info] Verification Mode: Full Check

[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Type of Errors | Count
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] DB_WARN -> 1
[2025-10-12 13:51:02] [info] LRF_ERR001 -> 14
[2025-10-12 13:51:02] [info] DB_WARN002 -> 159
[2025-10-12 13:51:02] [info] DB_WARN003 -> 161
```

```

[2025-10-12 13:51:02] [info] DRC_ERR9 -> 89
[2025-10-12 13:51:02] [info] DRC_ERR -> 67
[2025-10-12 13:51:02] [info] DRC_ERR5 -> 408
[2025-10-12 13:51:02] [info] DRC_ERR4 -> 241
[2025-10-12 13:51:02] [info] DRC_ERR1 -> 301
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Rule Violation Summary
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:51:02] [info] |-----|-----|-----|
[2025-10-12 13:51:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:51:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:51:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:51:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:51:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:51:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:51:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:51:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:51:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:51:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:51:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:51:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:51:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:51:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:51:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Performance Metrics
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] | Stage | Time Taken |
[2025-10-12 13:51:02] [info] |-----|-----|
[2025-10-12 13:51:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:51:02] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:51:02] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Summary
[2025-10-12 13:51:02] [info] -----
[2025-10-12 13:51:02] [info] Total Errors: 1120
[2025-10-12 13:51:02] [info] Total Warnings: 321
[2025-10-12 13:51:02] [info] DRC Status: FAILED

```

lvr_summary_199.rpt

```

[2025-10-12 13:52:03] [info] =====
[2025-10-12 13:52:03] [info] Design Rule Check Report
[2025-10-12 13:52:03] [info] =====

[2025-10-12 13:52:03] [info] Report Generated: 2025-10-12
[2025-10-12 13:52:03] [info] Run Start Time: 01:51:09 PM
[2025-10-12 13:52:03] [info] Run End Time: 01:52:03 PM
[2025-10-12 13:52:03] [info] Time Taken: 0 minutes 53 seconds

[2025-10-12 13:52:03] [info] Tool Version: LVR_2025.1
[2025-10-12 13:52:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:52:03] [info] User Name: ubuntu
[2025-10-12 13:52:03] [info] Cores Used: 8
[2025-10-12 13:52:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Top Cell Information
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Layout Top Cell:      TOP
[2025-10-12 13:52:03] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:52:03] [info] Database Format:      GDSII
[2025-10-12 13:52:03] [info] Verification Mode:    Full Check

[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Type of Errors | Count
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] DB_WARN -> 1
[2025-10-12 13:52:03] [info] LRF_ERR001 -> 14
[2025-10-12 13:52:03] [info] DB_WARN002 -> 159
[2025-10-12 13:52:03] [info] DB_WARN003 -> 161
[2025-10-12 13:52:03] [info] DRC_ERR9 -> 89
[2025-10-12 13:52:03] [info] DRC_ERR -> 67
[2025-10-12 13:52:03] [info] DRC_ERR5 -> 408
[2025-10-12 13:52:03] [info] DRC_ERR4 -> 241
[2025-10-12 13:52:03] [info] DRC_ERR1 -> 301
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Rule Violation Summary
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:52:03] [info] |-----|-----|-----|
[2025-10-12 13:52:03] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:52:03] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:52:03] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:52:03] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:52:03] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:52:03] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:52:03] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:52:03] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:52:03] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:52:03] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:52:03] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:52:03] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:52:03] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:52:03] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:52:03] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Performance Metrics
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] | Stage          | Time Taken |
[2025-10-12 13:52:03] [info] |-----|-----|
[2025-10-12 13:52:03] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:52:03] [info] | Rule Checking  | 0 minutes 43 seconds |
[2025-10-12 13:52:03] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Summary
[2025-10-12 13:52:03] [info] -----
[2025-10-12 13:52:03] [info] Total Errors:      1120
[2025-10-12 13:52:03] [info] Total Warnings:    321
[2025-10-12 13:52:03] [info] DRC Status:        FAILED

```

lvr_summary_2.rpt

```
[2025-10-12 10:43:23] [info] =====
[2025-10-12 10:43:23] [info] Design Rule Check Report
[2025-10-12 10:43:23] [info] =====

[2025-10-12 10:43:23] [info] Report Generated: 2025-10-12
[2025-10-12 10:43:23] [info] Run Start Time: 10:42:32 AM
[2025-10-12 10:43:23] [info] Run End Time: 10:43:23 AM
[2025-10-12 10:43:23] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:43:23] [info] Tool Version: LVR_2025.1
[2025-10-12 10:43:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:43:23] [info] User Name: ubuntu
[2025-10-12 10:43:23] [info] Cores Used: 8
[2025-10-12 10:43:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Top Cell Information
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Layout Top Cell: TOP
[2025-10-12 10:43:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:43:23] [info] Database Format: GDSII
[2025-10-12 10:43:23] [info] Verification Mode: Full Check

[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Type of Errors | Count
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] DB_WARN -> 1
[2025-10-12 10:43:23] [info] LRF_ERR001 -> 14
[2025-10-12 10:43:23] [info] DB_WARN002 -> 159
[2025-10-12 10:43:23] [info] DB_WARN003 -> 161
[2025-10-12 10:43:23] [info] DRC_ERR9 -> 89
[2025-10-12 10:43:23] [info] DRC_ERR -> 67
[2025-10-12 10:43:23] [info] DRC_ERR5 -> 408
[2025-10-12 10:43:23] [info] DRC_ERR4 -> 241
[2025-10-12 10:43:23] [info] DRC_ERR1 -> 301
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Rule Violation Summary
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:43:23] [info] |-----|-----|-----|
[2025-10-12 10:43:23] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:43:23] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:43:23] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:43:23] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:43:23] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:43:23] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 10:43:23] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:43:23] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:43:23] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:43:23] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:43:23] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:43:23] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:43:23] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:43:23] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:43:23] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:43:23] [info]
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Performance Metrics
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] | Stage | Time Taken |
```

```
[2025-10-12 10:43:23] [info] |-----|-----|
[2025-10-12 10:43:23] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 10:43:23] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 10:43:23] [info] | Report Generation   | 0 minutes 0 seconds |

[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Summary
[2025-10-12 10:43:23] [info] -----
[2025-10-12 10:43:23] [info] Total Errors: 1120
[2025-10-12 10:43:23] [info] Total Warnings: 321
[2025-10-12 10:43:23] [info] DRC Status: FAILED
```

lvr_summary_20.rpt

```
[2025-10-12 11:00:21] [info] =====
[2025-10-12 11:00:21] [info] Design Rule Check Report
[2025-10-12 11:00:21] [info] =====

[2025-10-12 11:00:21] [info] Report Generated: 2025-10-12
[2025-10-12 11:00:21] [info] Run Start Time: 10:59:30 AM
[2025-10-12 11:00:21] [info] Run End Time: 11:00:21 AM
[2025-10-12 11:00:21] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:00:21] [info] Tool Version: LVR_2025.1
[2025-10-12 11:00:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:00:21] [info] User Name: ubuntu
[2025-10-12 11:00:21] [info] Cores Used: 8
[2025-10-12 11:00:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Top Cell Information
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Layout Top Cell: TOP
[2025-10-12 11:00:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:00:21] [info] Database Format: GDSII
[2025-10-12 11:00:21] [info] Verification Mode: Full Check

[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Type of Errors | Count
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] DB_WARN -> 1
[2025-10-12 11:00:21] [info] LRF_ERR001 -> 14
[2025-10-12 11:00:21] [info] DB_WARN002 -> 159
[2025-10-12 11:00:21] [info] DB_WARN003 -> 161
[2025-10-12 11:00:21] [info] DRC_ERR9 -> 89
[2025-10-12 11:00:21] [info] DRC_ERR -> 67
[2025-10-12 11:00:21] [info] DRC_ERR5 -> 408
[2025-10-12 11:00:21] [info] DRC_ERR4 -> 241
[2025-10-12 11:00:21] [info] DRC_ERR1 -> 301
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Rule Violation Summary
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:00:21] [info] |-----|-----|-----|
[2025-10-12 11:00:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:00:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:00:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:00:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:00:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```

[2025-10-12 11:00:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:00:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:00:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:00:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:00:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:00:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:00:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:00:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:00:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:00:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:00:21] [info]
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Performance Metrics
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] | Stage | Time Taken |
[2025-10-12 11:00:21] [info] |-----|-----|
[2025-10-12 11:00:21] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:00:21] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:00:21] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Summary
[2025-10-12 11:00:21] [info] -----
[2025-10-12 11:00:21] [info] Total Errors: 1120
[2025-10-12 11:00:21] [info] Total Warnings: 321
[2025-10-12 11:00:21] [info] DRC Status: FAILED

```

lvr_summary_200.rpt

```

[2025-10-12 13:52:58] [info] =====
[2025-10-12 13:52:58] [info] Design Rule Check Report
[2025-10-12 13:52:58] [info] =====

[2025-10-12 13:52:58] [info] Report Generated: 2025-10-12
[2025-10-12 13:52:58] [info] Run Start Time: 01:52:08 PM
[2025-10-12 13:52:58] [info] Run End Time: 01:52:58 PM
[2025-10-12 13:52:58] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:52:58] [info] Tool Version: LVR_2025.1
[2025-10-12 13:52:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:52:58] [info] User Name: ubuntu
[2025-10-12 13:52:58] [info] Cores Used: 8
[2025-10-12 13:52:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Top Cell Information
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Layout Top Cell: TOP
[2025-10-12 13:52:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:52:58] [info] Database Format: GDSII
[2025-10-12 13:52:58] [info] Verification Mode: Full Check

[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Type of Errors | Count
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] DB_WARN -> 1
[2025-10-12 13:52:58] [info] LRF_ERR001 -> 14
[2025-10-12 13:52:58] [info] DB_WARN002 -> 159
[2025-10-12 13:52:58] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:52:58] [info] DRC_ERR9 -> 89
[2025-10-12 13:52:58] [info] DRC_ERR -> 67
[2025-10-12 13:52:58] [info] DRC_ERR5 -> 408
[2025-10-12 13:52:58] [info] DRC_ERR4 -> 241
[2025-10-12 13:52:58] [info] DRC_ERR1 -> 301
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Rule Violation Summary
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:52:58] [info] |-----|-----|-----|
[2025-10-12 13:52:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:52:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:52:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:52:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:52:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:52:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:52:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:52:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:52:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:52:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:52:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:52:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:52:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:52:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:52:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Performance Metrics
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] | Stage | Time Taken |
[2025-10-12 13:52:58] [info] |-----|-----|
[2025-10-12 13:52:58] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:52:58] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:52:58] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Summary
[2025-10-12 13:52:58] [info] -----
[2025-10-12 13:52:58] [info] Total Errors: 1120
[2025-10-12 13:52:58] [info] Total Warnings: 321
[2025-10-12 13:52:58] [info] DRC Status: FAILED

```

lvr_summary_201.rpt

```

[2025-10-12 13:53:57] [info] =====
[2025-10-12 13:53:57] [info] Design Rule Check Report
[2025-10-12 13:53:57] [info] =====

[2025-10-12 13:53:57] [info] Report Generated: 2025-10-12
[2025-10-12 13:53:57] [info] Run Start Time: 01:53:05 PM
[2025-10-12 13:53:57] [info] Run End Time: 01:53:57 PM
[2025-10-12 13:53:57] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:53:57] [info] Tool Version: LVR_2025.1
[2025-10-12 13:53:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:53:57] [info] User Name: ubuntu
[2025-10-12 13:53:57] [info] Cores Used: 8
[2025-10-12 13:53:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Top Cell Information
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Layout Top Cell:      TOP
[2025-10-12 13:53:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:53:57] [info] Database Format:      GDSII
[2025-10-12 13:53:57] [info] Verification Mode:    Full Check
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Type of Errors | Count
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] DB_WARN -> 1
[2025-10-12 13:53:57] [info] LRF_ERR001 -> 14
[2025-10-12 13:53:57] [info] DB_WARN002 -> 159
[2025-10-12 13:53:57] [info] DB_WARN003 -> 161
[2025-10-12 13:53:57] [info] DRC_ERR9 -> 89
[2025-10-12 13:53:57] [info] DRC_ERR -> 67
[2025-10-12 13:53:57] [info] DRC_ERR5 -> 408
[2025-10-12 13:53:57] [info] DRC_ERR4 -> 241
[2025-10-12 13:53:57] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Rule Violation Summary
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:53:57] [info] |-----|-----|-----|
[2025-10-12 13:53:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:53:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:53:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:53:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:53:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:53:57] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:53:57] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:53:57] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:53:57] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:53:57] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:53:57] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:53:57] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:53:57] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:53:57] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:53:57] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:53:57] [info] |
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Performance Metrics
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] | Stage          | Time Taken |
[2025-10-12 13:53:57] [info] |-----|-----|
[2025-10-12 13:53:57] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:53:57] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 13:53:57] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Summary
```

```
[2025-10-12 13:53:57] [info] -----
[2025-10-12 13:53:57] [info] Total Errors:      1120
[2025-10-12 13:53:57] [info] Total Warnings:    321
[2025-10-12 13:53:57] [info] DRC Status:        FAILED
```

lvr_summary_202.rpt

```
[2025-10-12 13:54:55] [info] =====
[2025-10-12 13:54:55] [info] Design Rule Check Report
[2025-10-12 13:54:55] [info] =====

[2025-10-12 13:54:55] [info] Report Generated: 2025-10-12
[2025-10-12 13:54:55] [info] Run Start Time: 01:54:03 PM
[2025-10-12 13:54:55] [info] Run End Time: 01:54:55 PM
[2025-10-12 13:54:55] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 13:54:55] [info] Tool Version: LVR_2025.1
[2025-10-12 13:54:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:54:55] [info] User Name: ubuntu
[2025-10-12 13:54:55] [info] Cores Used: 8
[2025-10-12 13:54:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] Top Cell Information
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] Layout Top Cell: TOP
[2025-10-12 13:54:55] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:54:55] [info] Database Format: GDSII
[2025-10-12 13:54:55] [info] Verification Mode: Full Check

[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] Type of Errors | Count
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] DB_WARN -> 1
[2025-10-12 13:54:55] [info] LRF_ERR001 -> 14
[2025-10-12 13:54:55] [info] DB_WARN002 -> 159
[2025-10-12 13:54:55] [info] DB_WARN003 -> 161
[2025-10-12 13:54:55] [info] DRC_ERR9 -> 89
[2025-10-12 13:54:55] [info] DRC_ERR -> 67
[2025-10-12 13:54:55] [info] DRC_ERR5 -> 408
[2025-10-12 13:54:55] [info] DRC_ERR4 -> 241
[2025-10-12 13:54:55] [info] DRC_ERR1 -> 301
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] Rule Violation Summary
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:54:55] [info] |-----|-----|-----|
[2025-10-12 13:54:55] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:54:55] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:54:55] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:54:55] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:54:55] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:54:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:54:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:54:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:54:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:54:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:54:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:54:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:54:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:54:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:54:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:54:55] [info]
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] Performance Metrics
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:54:55] [info] |-----|-----|
[2025-10-12 13:54:55] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:54:55] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 13:54:55] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info]                Summary
[2025-10-12 13:54:55] [info] -----
[2025-10-12 13:54:55] [info] Total Errors:    1120
[2025-10-12 13:54:55] [info] Total Warnings:    321
[2025-10-12 13:54:55] [info] DRC Status:      FAILED
```

lvr_summary_203.rpt

```
[2025-10-12 13:55:52] [info] =====
[2025-10-12 13:55:52] [info]                Design Rule Check Report
[2025-10-12 13:55:52] [info] =====

[2025-10-12 13:55:52] [info] Report Generated:    2025-10-12
[2025-10-12 13:55:52] [info] Run Start Time:      01:55:00 PM
[2025-10-12 13:55:52] [info] Run End Time:        01:55:52 PM
[2025-10-12 13:55:52] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 13:55:52] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:55:52] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:55:52] [info] User Name:           ubuntu
[2025-10-12 13:55:52] [info] Cores Used:          8
[2025-10-12 13:55:52] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info]                Top Cell Information
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] Layout Top Cell:     TOP
[2025-10-12 13:55:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:55:52] [info] Database Format:       GDSII
[2025-10-12 13:55:52] [info] Verification Mode:    Full Check

[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info]                Type of Errors | Count
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] DB_WARN -> 1
[2025-10-12 13:55:52] [info] LRF_ERR001 -> 14
[2025-10-12 13:55:52] [info] DB_WARN002 -> 159
[2025-10-12 13:55:52] [info] DB_WARN003 -> 161
[2025-10-12 13:55:52] [info] DRC_ERR9 -> 89
[2025-10-12 13:55:52] [info] DRC_ERR -> 67
[2025-10-12 13:55:52] [info] DRC_ERR5 -> 408
[2025-10-12 13:55:52] [info] DRC_ERR4 -> 241
[2025-10-12 13:55:52] [info] DRC_ERR1 -> 301
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info]                Rule Violation Summary
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:55:52] [info] |-----|-----|-----|
[2025-10-12 13:55:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:55:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:55:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:55:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:55:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 13:55:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 13:55:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:55:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:55:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:55:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:55:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:55:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:55:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:55:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:55:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:55:52] [info]
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] Performance Metrics
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] | Stage | Time Taken |
[2025-10-12 13:55:52] [info] |-----|-----|
[2025-10-12 13:55:52] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:55:52] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 13:55:52] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] Summary
[2025-10-12 13:55:52] [info] -----
[2025-10-12 13:55:52] [info] Total Errors: 1120
[2025-10-12 13:55:52] [info] Total Warnings: 321
[2025-10-12 13:55:52] [info] DRC Status: FAILED

```

lvr_summary_204.rpt

```

[2025-10-12 13:56:48] [info] =====
[2025-10-12 13:56:48] [info] Design Rule Check Report
[2025-10-12 13:56:48] [info] =====

[2025-10-12 13:56:48] [info] Report Generated: 2025-10-12
[2025-10-12 13:56:48] [info] Run Start Time: 01:55:57 PM
[2025-10-12 13:56:48] [info] Run End Time: 01:56:48 PM
[2025-10-12 13:56:48] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:56:48] [info] Tool Version: LVR_2025.1
[2025-10-12 13:56:48] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:56:48] [info] User Name: ubuntu
[2025-10-12 13:56:48] [info] Cores Used: 8
[2025-10-12 13:56:48] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Top Cell Information
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Layout Top Cell: TOP
[2025-10-12 13:56:48] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 13:56:48] [info] Database Format: GDSII
[2025-10-12 13:56:48] [info] Verification Mode: Full Check

[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Type of Errors | Count
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] DB_WARN -> 1
[2025-10-12 13:56:48] [info] LRF_ERR001 -> 14
[2025-10-12 13:56:48] [info] DB_WARN002 -> 159
[2025-10-12 13:56:48] [info] DB_WARN003 -> 161

```

```

[2025-10-12 13:56:48] [info] DRC_ERR9 -> 89
[2025-10-12 13:56:48] [info] DRC_ERR -> 67
[2025-10-12 13:56:48] [info] DRC_ERR5 -> 408
[2025-10-12 13:56:48] [info] DRC_ERR4 -> 241
[2025-10-12 13:56:48] [info] DRC_ERR1 -> 301
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Rule Violation Summary
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:56:48] [info] |-----|-----|-----|
[2025-10-12 13:56:48] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:56:48] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:56:48] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:56:48] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:56:48] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:56:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:56:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:56:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:56:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:56:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:56:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:56:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:56:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:56:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:56:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Performance Metrics
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] | Stage | Time Taken |
[2025-10-12 13:56:48] [info] |-----|-----|
[2025-10-12 13:56:48] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:56:48] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:56:48] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Summary
[2025-10-12 13:56:48] [info] -----
[2025-10-12 13:56:48] [info] Total Errors: 1120
[2025-10-12 13:56:48] [info] Total Warnings: 321
[2025-10-12 13:56:48] [info] DRC Status: FAILED

```

lvr_summary_205.rpt

```

[2025-10-12 13:57:44] [info] =====
[2025-10-12 13:57:44] [info] Design Rule Check Report
[2025-10-12 13:57:44] [info] =====

[2025-10-12 13:57:44] [info] Report Generated: 2025-10-12
[2025-10-12 13:57:44] [info] Run Start Time: 01:56:53 PM
[2025-10-12 13:57:44] [info] Run End Time: 01:57:44 PM
[2025-10-12 13:57:44] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 13:57:44] [info] Tool Version: LVR_2025.1
[2025-10-12 13:57:44] [info] Host Name: ip-172-31-37-173
[2025-10-12 13:57:44] [info] User Name: ubuntu
[2025-10-12 13:57:44] [info] Cores Used: 8
[2025-10-12 13:57:44] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Top Cell Information
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Layout Top Cell:      TOP
[2025-10-12 13:57:44] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:57:44] [info] Database Format:       GDSII
[2025-10-12 13:57:44] [info] Verification Mode:    Full Check

[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Type of Errors | Count
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] DB_WARN -> 1
[2025-10-12 13:57:44] [info] LRF_ERR001 -> 14
[2025-10-12 13:57:44] [info] DB_WARN002 -> 159
[2025-10-12 13:57:44] [info] DB_WARN003 -> 161
[2025-10-12 13:57:44] [info] DRC_ERR9 -> 89
[2025-10-12 13:57:44] [info] DRC_ERR -> 67
[2025-10-12 13:57:44] [info] DRC_ERR5 -> 408
[2025-10-12 13:57:44] [info] DRC_ERR4 -> 241
[2025-10-12 13:57:44] [info] DRC_ERR1 -> 301
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Rule Violation Summary
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 13:57:44] [info] |-----|-----|-----|
[2025-10-12 13:57:44] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:57:44] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:57:44] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:57:44] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:57:44] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 13:57:44] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 13:57:44] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 13:57:44] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 13:57:44] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 13:57:44] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 13:57:44] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 13:57:44] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 13:57:44] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 13:57:44] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 13:57:44] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Performance Metrics
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] | Stage          | Time Taken |
[2025-10-12 13:57:44] [info] |-----|-----|
[2025-10-12 13:57:44] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 13:57:44] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 13:57:44] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Summary
[2025-10-12 13:57:44] [info] -----
[2025-10-12 13:57:44] [info] Total Errors:      1120
[2025-10-12 13:57:44] [info] Total Warnings:    321
[2025-10-12 13:57:44] [info] DRC Status:        FAILED

```


lvr_summary_206.rpt

```
[2025-10-12 13:58:43] [info] =====
[2025-10-12 13:58:43] [info]                      Design Rule Check Report
[2025-10-12 13:58:43] [info] =====

[2025-10-12 13:58:43] [info] Report Generated:      2025-10-12
[2025-10-12 13:58:43] [info] Run Start Time:       01:57:52 PM
[2025-10-12 13:58:43] [info] Run End Time:         01:58:43 PM
[2025-10-12 13:58:43] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 13:58:43] [info] Tool Version:         LVR_2025.1
[2025-10-12 13:58:43] [info] Host Name:            ip-172-31-37-173
[2025-10-12 13:58:43] [info] User Name:            ubuntu
[2025-10-12 13:58:43] [info] Cores Used:           8
[2025-10-12 13:58:43] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info]                      Top Cell Information
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info] Layout Top Cell:      TOP
[2025-10-12 13:58:43] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 13:58:43] [info] Database Format:        GDSII
[2025-10-12 13:58:43] [info] Verification Mode:     Full Check

[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info]                      Type of Errors | Count
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info] DB_WARN -> 1
[2025-10-12 13:58:43] [info] LRF_ERR001 -> 14
[2025-10-12 13:58:43] [info] DB_WARN002 -> 159
[2025-10-12 13:58:43] [info] DB_WARN003 -> 161
[2025-10-12 13:58:43] [info] DRC_ERR9 -> 89
[2025-10-12 13:58:43] [info] DRC_ERR -> 67
[2025-10-12 13:58:43] [info] DRC_ERR5 -> 408
[2025-10-12 13:58:43] [info] DRC_ERR4 -> 241
[2025-10-12 13:58:43] [info] DRC_ERR1 -> 301
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info]                      Rule Violation Summary
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 13:58:43] [info] |-----|-----|-----|
[2025-10-12 13:58:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 13:58:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 13:58:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 13:58:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 13:58:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 13:58:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 13:58:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:58:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:58:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:58:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:58:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:58:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:58:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:58:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:58:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:58:43] [info]
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info]                      Performance Metrics
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info] | Stage | Time Taken |
```

```
[2025-10-12 13:58:43] [info] |-----|-----|
[2025-10-12 13:58:43] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 13:58:43] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 13:58:43] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info]                Summary
[2025-10-12 13:58:43] [info] -----
[2025-10-12 13:58:43] [info] Total Errors:    1120
[2025-10-12 13:58:43] [info] Total Warnings:    321
[2025-10-12 13:58:43] [info] DRC Status:      FAILED
```

lvr_summary_207.rpt

```
[2025-10-12 13:59:41] [info] =====
[2025-10-12 13:59:41] [info]                Design Rule Check Report
[2025-10-12 13:59:41] [info] =====

[2025-10-12 13:59:41] [info] Report Generated:    2025-10-12
[2025-10-12 13:59:41] [info] Run Start Time:      01:58:50 PM
[2025-10-12 13:59:41] [info] Run End Time:        01:59:41 PM
[2025-10-12 13:59:41] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 13:59:41] [info] Tool Version:        LVR_2025.1
[2025-10-12 13:59:41] [info] Host Name:           ip-172-31-37-173
[2025-10-12 13:59:41] [info] User Name:           ubuntu
[2025-10-12 13:59:41] [info] Cores Used:          8
[2025-10-12 13:59:41] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info]                Top Cell Information
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] Layout Top Cell:     TOP
[2025-10-12 13:59:41] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 13:59:41] [info] Database Format:       GDSII
[2025-10-12 13:59:41] [info] Verification Mode:    Full Check

[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info]                Type of Errors | Count
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] DB_WARN -> 1
[2025-10-12 13:59:41] [info] LRF_ERR001 -> 14
[2025-10-12 13:59:41] [info] DB_WARN002 -> 159
[2025-10-12 13:59:41] [info] DB_WARN003 -> 161
[2025-10-12 13:59:41] [info] DRC_ERR9 -> 89
[2025-10-12 13:59:41] [info] DRC_ERR -> 67
[2025-10-12 13:59:41] [info] DRC_ERR5 -> 408
[2025-10-12 13:59:41] [info] DRC_ERR4 -> 241
[2025-10-12 13:59:41] [info] DRC_ERR1 -> 301
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info]                Rule Violation Summary
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 13:59:41] [info] |-----|-----|-----|
[2025-10-12 13:59:41] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 13:59:41] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 13:59:41] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 13:59:41] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 13:59:41] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 13:59:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 13:59:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 13:59:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 13:59:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 13:59:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 13:59:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 13:59:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 13:59:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 13:59:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 13:59:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 13:59:41] [info]
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] Performance Metrics
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] | Stage | Time Taken |
[2025-10-12 13:59:41] [info] |-----|-----|
[2025-10-12 13:59:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 13:59:41] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 13:59:41] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] Summary
[2025-10-12 13:59:41] [info] -----
[2025-10-12 13:59:41] [info] Total Errors: 1120
[2025-10-12 13:59:41] [info] Total Warnings: 321
[2025-10-12 13:59:41] [info] DRC Status: FAILED
```

lvr_summary_208.rpt

```
[2025-10-12 14:00:39] [info] =====
[2025-10-12 14:00:39] [info] Design Rule Check Report
[2025-10-12 14:00:39] [info] =====

[2025-10-12 14:00:39] [info] Report Generated: 2025-10-12
[2025-10-12 14:00:39] [info] Run Start Time: 01:59:47 PM
[2025-10-12 14:00:39] [info] Run End Time: 02:00:39 PM
[2025-10-12 14:00:39] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:00:39] [info] Tool Version: LVR_2025.1
[2025-10-12 14:00:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:00:39] [info] User Name: ubuntu
[2025-10-12 14:00:39] [info] Cores Used: 8
[2025-10-12 14:00:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Top Cell Information
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Layout Top Cell: TOP
[2025-10-12 14:00:39] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:00:39] [info] Database Format: GDSII
[2025-10-12 14:00:39] [info] Verification Mode: Full Check

[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Type of Errors | Count
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] DB_WARN -> 1
[2025-10-12 14:00:39] [info] LRF_ERR001 -> 14
[2025-10-12 14:00:39] [info] DB_WARN002 -> 159
[2025-10-12 14:00:39] [info] DB_WARN003 -> 161
```

```

[2025-10-12 14:00:39] [info] DRC_ERR9 -> 89
[2025-10-12 14:00:39] [info] DRC_ERR -> 67
[2025-10-12 14:00:39] [info] DRC_ERR5 -> 408
[2025-10-12 14:00:39] [info] DRC_ERR4 -> 241
[2025-10-12 14:00:39] [info] DRC_ERR1 -> 301
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Rule Violation Summary
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:00:39] [info] |-----|-----|-----|
[2025-10-12 14:00:39] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:00:39] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:00:39] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:00:39] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:00:39] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:00:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:00:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:00:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:00:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:00:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:00:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:00:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:00:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:00:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:00:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Performance Metrics
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] | Stage | Time Taken |
[2025-10-12 14:00:39] [info] |-----|-----|
[2025-10-12 14:00:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:00:39] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:00:39] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Summary
[2025-10-12 14:00:39] [info] -----
[2025-10-12 14:00:39] [info] Total Errors: 1120
[2025-10-12 14:00:39] [info] Total Warnings: 321
[2025-10-12 14:00:39] [info] DRC Status: FAILED

```

lvr_summary_209.rpt

```

[2025-10-12 14:01:36] [info] =====
[2025-10-12 14:01:36] [info] Design Rule Check Report
[2025-10-12 14:01:36] [info] =====

[2025-10-12 14:01:36] [info] Report Generated: 2025-10-12
[2025-10-12 14:01:36] [info] Run Start Time: 02:00:44 PM
[2025-10-12 14:01:36] [info] Run End Time: 02:01:36 PM
[2025-10-12 14:01:36] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:01:36] [info] Tool Version: LVR_2025.1
[2025-10-12 14:01:36] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:01:36] [info] User Name: ubuntu
[2025-10-12 14:01:36] [info] Cores Used: 8
[2025-10-12 14:01:36] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Top Cell Information
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Layout Top Cell:      TOP
[2025-10-12 14:01:36] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:01:36] [info] Database Format:      GDSII
[2025-10-12 14:01:36] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Type of Errors | Count
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] DB_WARN -> 1
[2025-10-12 14:01:36] [info] LRF_ERR001 -> 14
[2025-10-12 14:01:36] [info] DB_WARN002 -> 159
[2025-10-12 14:01:36] [info] DB_WARN003 -> 161
[2025-10-12 14:01:36] [info] DRC_ERR9 -> 89
[2025-10-12 14:01:36] [info] DRC_ERR -> 67
[2025-10-12 14:01:36] [info] DRC_ERR5 -> 408
[2025-10-12 14:01:36] [info] DRC_ERR4 -> 241
[2025-10-12 14:01:36] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Rule Violation Summary
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:01:36] [info] |-----|-----|-----|
[2025-10-12 14:01:36] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:01:36] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:01:36] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:01:36] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:01:36] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:01:36] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:01:36] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:01:36] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:01:36] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:01:36] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:01:36] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:01:36] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:01:36] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:01:36] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:01:36] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:01:36] [info] |
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Performance Metrics
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] | Stage          | Time Taken |
[2025-10-12 14:01:36] [info] |-----|-----|
[2025-10-12 14:01:36] [info] | GDS Parsing    | 0 minutes 10 seconds |
[2025-10-12 14:01:36] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 14:01:36] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Summary
```

```
[2025-10-12 14:01:36] [info] -----
[2025-10-12 14:01:36] [info] Total Errors:      1120
[2025-10-12 14:01:36] [info] Total Warnings:    321
[2025-10-12 14:01:36] [info] DRC Status:        FAILED
```

lvr_summary_21.rpt

```
[2025-10-12 11:01:18] [info] =====
[2025-10-12 11:01:18] [info]                      Design Rule Check Report
[2025-10-12 11:01:18] [info] =====

[2025-10-12 11:01:18] [info] Report Generated:    2025-10-12
[2025-10-12 11:01:18] [info] Run Start Time:      11:00:26 AM
[2025-10-12 11:01:18] [info] Run End Time:        11:01:18 AM
[2025-10-12 11:01:18] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:01:18] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:01:18] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:01:18] [info] User Name:           ubuntu
[2025-10-12 11:01:18] [info] Cores Used:          8
[2025-10-12 11:01:18] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info]                      Top Cell Information
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info] Layout Top Cell:     TOP
[2025-10-12 11:01:18] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:01:18] [info] Database Format:       GDSII
[2025-10-12 11:01:18] [info] Verification Mode:    Full Check

[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info]                      Type of Errors | Count
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info] DB_WARN -> 1
[2025-10-12 11:01:18] [info] LRF_ERR001 -> 14
[2025-10-12 11:01:18] [info] DB_WARN002 -> 159
[2025-10-12 11:01:18] [info] DB_WARN003 -> 161
[2025-10-12 11:01:18] [info] DRC_ERR9 -> 89
[2025-10-12 11:01:18] [info] DRC_ERR -> 67
[2025-10-12 11:01:18] [info] DRC_ERR5 -> 408
[2025-10-12 11:01:18] [info] DRC_ERR4 -> 241
[2025-10-12 11:01:18] [info] DRC_ERR1 -> 301
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info]                      Rule Violation Summary
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:01:18] [info] |-----|-----|-----|
[2025-10-12 11:01:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:01:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:01:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:01:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:01:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:01:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:01:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:01:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:01:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:01:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:01:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:01:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:01:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:01:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:01:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:01:18] [info]
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info]                      Performance Metrics
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:01:18] [info] |-----|-----|
[2025-10-12 11:01:18] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:01:18] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 11:01:18] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info]                Summary
[2025-10-12 11:01:18] [info] -----
[2025-10-12 11:01:18] [info] Total Errors:    1120
[2025-10-12 11:01:18] [info] Total Warnings:    321
[2025-10-12 11:01:18] [info] DRC Status:      FAILED
```

lvr_summary_210.rpt

```
[2025-10-12 14:02:33] [info] =====
[2025-10-12 14:02:33] [info]                Design Rule Check Report
[2025-10-12 14:02:33] [info] =====

[2025-10-12 14:02:33] [info] Report Generated:    2025-10-12
[2025-10-12 14:02:33] [info] Run Start Time:      02:01:42 PM
[2025-10-12 14:02:33] [info] Run End Time:        02:02:33 PM
[2025-10-12 14:02:33] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 14:02:33] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:02:33] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:02:33] [info] User Name:           ubuntu
[2025-10-12 14:02:33] [info] Cores Used:          8
[2025-10-12 14:02:33] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info]                Top Cell Information
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] Layout Top Cell:     TOP
[2025-10-12 14:02:33] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:02:33] [info] Database Format:       GDSII
[2025-10-12 14:02:33] [info] Verification Mode:    Full Check

[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info]                Type of Errors | Count
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] DB_WARN -> 1
[2025-10-12 14:02:33] [info] LRF_ERR001 -> 14
[2025-10-12 14:02:33] [info] DB_WARN002 -> 159
[2025-10-12 14:02:33] [info] DB_WARN003 -> 161
[2025-10-12 14:02:33] [info] DRC_ERR9 -> 89
[2025-10-12 14:02:33] [info] DRC_ERR -> 67
[2025-10-12 14:02:33] [info] DRC_ERR5 -> 408
[2025-10-12 14:02:33] [info] DRC_ERR4 -> 241
[2025-10-12 14:02:33] [info] DRC_ERR1 -> 301
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info]                Rule Violation Summary
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:02:33] [info] |-----|-----|-----|
[2025-10-12 14:02:33] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:02:33] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:02:33] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:02:33] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:02:33] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:02:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:02:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:02:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:02:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:02:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:02:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:02:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:02:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:02:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:02:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:02:33] [info]
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] Performance Metrics
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] | Stage | Time Taken |
[2025-10-12 14:02:33] [info] |-----|-----|
[2025-10-12 14:02:33] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:02:33] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:02:33] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] Summary
[2025-10-12 14:02:33] [info] -----
[2025-10-12 14:02:33] [info] Total Errors: 1120
[2025-10-12 14:02:33] [info] Total Warnings: 321
[2025-10-12 14:02:33] [info] DRC Status: FAILED

```

lvr_summary_211.rpt

```

[2025-10-12 14:03:31] [info] =====
[2025-10-12 14:03:31] [info] Design Rule Check Report
[2025-10-12 14:03:31] [info] =====

[2025-10-12 14:03:31] [info] Report Generated: 2025-10-12
[2025-10-12 14:03:31] [info] Run Start Time: 02:02:39 PM
[2025-10-12 14:03:31] [info] Run End Time: 02:03:31 PM
[2025-10-12 14:03:31] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:03:31] [info] Tool Version: LVR_2025.1
[2025-10-12 14:03:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:03:31] [info] User Name: ubuntu
[2025-10-12 14:03:31] [info] Cores Used: 8
[2025-10-12 14:03:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Top Cell Information
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Layout Top Cell: TOP
[2025-10-12 14:03:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:03:31] [info] Database Format: GDSII
[2025-10-12 14:03:31] [info] Verification Mode: Full Check

[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Type of Errors | Count
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] DB_WARN -> 1
[2025-10-12 14:03:31] [info] LRF_ERR001 -> 14
[2025-10-12 14:03:31] [info] DB_WARN002 -> 159
[2025-10-12 14:03:31] [info] DB_WARN003 -> 161

```



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[2025-10-12 14:03:31] [info] DRC_ERR9 -> 89
[2025-10-12 14:03:31] [info] DRC_ERR -> 67
[2025-10-12 14:03:31] [info] DRC_ERR5 -> 408
[2025-10-12 14:03:31] [info] DRC_ERR4 -> 241
[2025-10-12 14:03:31] [info] DRC_ERR1 -> 301
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Rule Violation Summary
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:03:31] [info] |-----|-----|-----|
[2025-10-12 14:03:31] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:03:31] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:03:31] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:03:31] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:03:31] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:03:31] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 14:03:31] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:03:31] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:03:31] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:03:31] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:03:31] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:03:31] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:03:31] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:03:31] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:03:31] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Performance Metrics
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] | Stage          | Time Taken |
[2025-10-12 14:03:31] [info] |-----|-----|
[2025-10-12 14:03:31] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:03:31] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 14:03:31] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Summary
[2025-10-12 14:03:31] [info] -----
[2025-10-12 14:03:31] [info] Total Errors: 1120
[2025-10-12 14:03:31] [info] Total Warnings: 321
[2025-10-12 14:03:31] [info] DRC Status: FAILED

```

lvr_summary_212.rpt

```

[2025-10-12 14:04:28] [info] =====
[2025-10-12 14:04:28] [info] Design Rule Check Report
[2025-10-12 14:04:28] [info] =====

[2025-10-12 14:04:28] [info] Report Generated: 2025-10-12
[2025-10-12 14:04:28] [info] Run Start Time: 02:03:37 PM
[2025-10-12 14:04:28] [info] Run End Time: 02:04:28 PM
[2025-10-12 14:04:28] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:04:28] [info] Tool Version: LVR_2025.1
[2025-10-12 14:04:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:04:28] [info] User Name: ubuntu
[2025-10-12 14:04:28] [info] Cores Used: 8
[2025-10-12 14:04:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Top Cell Information
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Layout Top Cell:      TOP
[2025-10-12 14:04:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:04:28] [info] Database Format:      GDSII
[2025-10-12 14:04:28] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Type of Errors | Count
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] DB_WARN -> 1
[2025-10-12 14:04:28] [info] LRF_ERR001 -> 14
[2025-10-12 14:04:28] [info] DB_WARN002 -> 159
[2025-10-12 14:04:28] [info] DB_WARN003 -> 161
[2025-10-12 14:04:28] [info] DRC_ERR9 -> 89
[2025-10-12 14:04:28] [info] DRC_ERR -> 67
[2025-10-12 14:04:28] [info] DRC_ERR5 -> 408
[2025-10-12 14:04:28] [info] DRC_ERR4 -> 241
[2025-10-12 14:04:28] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Rule Violation Summary
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:04:28] [info] |-----|-----|-----|
[2025-10-12 14:04:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:04:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:04:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:04:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:04:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:04:28] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:04:28] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:04:28] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:04:28] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:04:28] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:04:28] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:04:28] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:04:28] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:04:28] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:04:28] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:04:28] [info] |
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Performance Metrics
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] | Stage          | Time Taken |
[2025-10-12 14:04:28] [info] |-----|-----|
[2025-10-12 14:04:28] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:04:28] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:04:28] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Summary
```

```
[2025-10-12 14:04:28] [info] -----
[2025-10-12 14:04:28] [info] Total Errors:      1120
[2025-10-12 14:04:28] [info] Total Warnings:    321
[2025-10-12 14:04:28] [info] DRC Status:        FAILED
```

lvr_summary_213.rpt

```
[2025-10-12 14:05:25] [info] =====
[2025-10-12 14:05:25] [info] Design Rule Check Report
[2025-10-12 14:05:25] [info] =====

[2025-10-12 14:05:25] [info] Report Generated: 2025-10-12
[2025-10-12 14:05:25] [info] Run Start Time: 02:04:34 PM
[2025-10-12 14:05:25] [info] Run End Time: 02:05:25 PM
[2025-10-12 14:05:25] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:05:25] [info] Tool Version: LVR_2025.1
[2025-10-12 14:05:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:05:25] [info] User Name: ubuntu
[2025-10-12 14:05:25] [info] Cores Used: 8
[2025-10-12 14:05:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] Top Cell Information
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] Layout Top Cell: TOP
[2025-10-12 14:05:25] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:05:25] [info] Database Format: GDSII
[2025-10-12 14:05:25] [info] Verification Mode: Full Check

[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] Type of Errors | Count
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] DB_WARN -> 1
[2025-10-12 14:05:25] [info] LRF_ERR001 -> 14
[2025-10-12 14:05:25] [info] DB_WARN002 -> 159
[2025-10-12 14:05:25] [info] DB_WARN003 -> 161
[2025-10-12 14:05:25] [info] DRC_ERR9 -> 89
[2025-10-12 14:05:25] [info] DRC_ERR -> 67
[2025-10-12 14:05:25] [info] DRC_ERR5 -> 408
[2025-10-12 14:05:25] [info] DRC_ERR4 -> 241
[2025-10-12 14:05:25] [info] DRC_ERR1 -> 301
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] Rule Violation Summary
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:05:25] [info] |-----|-----|-----|
[2025-10-12 14:05:25] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:05:25] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:05:25] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:05:25] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:05:25] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:05:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:05:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:05:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:05:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:05:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:05:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:05:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:05:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:05:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:05:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:05:25] [info]
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] Performance Metrics
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:05:25] [info] |-----|-----|
[2025-10-12 14:05:25] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:05:25] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 14:05:25] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info]                Summary
[2025-10-12 14:05:25] [info] -----
[2025-10-12 14:05:25] [info] Total Errors:    1120
[2025-10-12 14:05:25] [info] Total Warnings:    321
[2025-10-12 14:05:25] [info] DRC Status:      FAILED
```

lvr_summary_214.rpt

```
[2025-10-12 14:06:24] [info] =====
[2025-10-12 14:06:24] [info]                Design Rule Check Report
[2025-10-12 14:06:24] [info] =====

[2025-10-12 14:06:24] [info] Report Generated:    2025-10-12
[2025-10-12 14:06:24] [info] Run Start Time:      02:05:33 PM
[2025-10-12 14:06:24] [info] Run End Time:        02:06:24 PM
[2025-10-12 14:06:24] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:06:24] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:06:24] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:06:24] [info] User Name:           ubuntu
[2025-10-12 14:06:24] [info] Cores Used:          8
[2025-10-12 14:06:24] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info]                Top Cell Information
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] Layout Top Cell:     TOP
[2025-10-12 14:06:24] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:06:24] [info] Database Format:       GDSII
[2025-10-12 14:06:24] [info] Verification Mode:    Full Check

[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info]                Type of Errors | Count
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] DB_WARN -> 1
[2025-10-12 14:06:24] [info] LRF_ERR001 -> 14
[2025-10-12 14:06:24] [info] DB_WARN002 -> 159
[2025-10-12 14:06:24] [info] DB_WARN003 -> 161
[2025-10-12 14:06:24] [info] DRC_ERR9 -> 89
[2025-10-12 14:06:24] [info] DRC_ERR -> 67
[2025-10-12 14:06:24] [info] DRC_ERR5 -> 408
[2025-10-12 14:06:24] [info] DRC_ERR4 -> 241
[2025-10-12 14:06:24] [info] DRC_ERR1 -> 301
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info]                Rule Violation Summary
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:06:24] [info] |-----|-----|-----|
[2025-10-12 14:06:24] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:06:24] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:06:24] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:06:24] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:06:24] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:06:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:06:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:06:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:06:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:06:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:06:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:06:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:06:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:06:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:06:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:06:24] [info]
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] Performance Metrics
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] | Stage | Time Taken |
[2025-10-12 14:06:24] [info] |-----|-----|
[2025-10-12 14:06:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:06:24] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:06:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] Summary
[2025-10-12 14:06:24] [info] -----
[2025-10-12 14:06:24] [info] Total Errors: 1120
[2025-10-12 14:06:24] [info] Total Warnings: 321
[2025-10-12 14:06:24] [info] DRC Status: FAILED

```

lvr_summary_215.rpt

```

[2025-10-12 14:07:23] [info] =====
[2025-10-12 14:07:23] [info] Design Rule Check Report
[2025-10-12 14:07:23] [info] =====

[2025-10-12 14:07:23] [info] Report Generated: 2025-10-12
[2025-10-12 14:07:23] [info] Run Start Time: 02:06:33 PM
[2025-10-12 14:07:23] [info] Run End Time: 02:07:23 PM
[2025-10-12 14:07:23] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:07:23] [info] Tool Version: LVR_2025.1
[2025-10-12 14:07:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:07:23] [info] User Name: ubuntu
[2025-10-12 14:07:23] [info] Cores Used: 8
[2025-10-12 14:07:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Top Cell Information
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Layout Top Cell: TOP
[2025-10-12 14:07:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:07:23] [info] Database Format: GDSII
[2025-10-12 14:07:23] [info] Verification Mode: Full Check

[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Type of Errors | Count
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] DB_WARN -> 1
[2025-10-12 14:07:23] [info] LRF_ERR001 -> 14
[2025-10-12 14:07:23] [info] DB_WARN002 -> 159
[2025-10-12 14:07:23] [info] DB_WARN003 -> 161

```

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[2025-10-12 14:07:23] [info] DRC_ERR9 -> 89
[2025-10-12 14:07:23] [info] DRC_ERR -> 67
[2025-10-12 14:07:23] [info] DRC_ERR5 -> 408
[2025-10-12 14:07:23] [info] DRC_ERR4 -> 241
[2025-10-12 14:07:23] [info] DRC_ERR1 -> 301
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Rule Violation Summary
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:07:23] [info] |-----|-----|-----|
[2025-10-12 14:07:23] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:07:23] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:07:23] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:07:23] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:07:23] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:07:23] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:07:23] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:07:23] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:07:23] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:07:23] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:07:23] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:07:23] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:07:23] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:07:23] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:07:23] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Performance Metrics
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] | Stage | Time Taken |
[2025-10-12 14:07:23] [info] |-----|-----|
[2025-10-12 14:07:23] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:07:23] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:07:23] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Summary
[2025-10-12 14:07:23] [info] -----
[2025-10-12 14:07:23] [info] Total Errors: 1120
[2025-10-12 14:07:23] [info] Total Warnings: 321
[2025-10-12 14:07:23] [info] DRC Status: FAILED

```

lvr_summary_216.rpt

```

[2025-10-12 14:08:23] [info] =====
[2025-10-12 14:08:23] [info] Design Rule Check Report
[2025-10-12 14:08:23] [info] =====

[2025-10-12 14:08:23] [info] Report Generated: 2025-10-12
[2025-10-12 14:08:23] [info] Run Start Time: 02:07:32 PM
[2025-10-12 14:08:23] [info] Run End Time: 02:08:23 PM
[2025-10-12 14:08:23] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:08:23] [info] Tool Version: LVR_2025.1
[2025-10-12 14:08:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:08:23] [info] User Name: ubuntu
[2025-10-12 14:08:23] [info] Cores Used: 8
[2025-10-12 14:08:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Top Cell Information
[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Layout Top Cell:      TOP
[2025-10-12 14:08:23] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:08:23] [info] Database Format:      GDSII
[2025-10-12 14:08:23] [info] Verification Mode:    Full Check

```

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[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Type of Errors | Count
[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] DB_WARN -> 1
[2025-10-12 14:08:23] [info] LRF_ERR001 -> 14
[2025-10-12 14:08:23] [info] DB_WARN002 -> 159
[2025-10-12 14:08:23] [info] DB_WARN003 -> 161
[2025-10-12 14:08:23] [info] DRC_ERR9 -> 89
[2025-10-12 14:08:23] [info] DRC_ERR -> 67
[2025-10-12 14:08:23] [info] DRC_ERR5 -> 408
[2025-10-12 14:08:23] [info] DRC_ERR4 -> 241
[2025-10-12 14:08:23] [info] DRC_ERR1 -> 301

```

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[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Rule Violation Summary
[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:08:23] [info] |-----|-----|-----|
[2025-10-12 14:08:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:08:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:08:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:08:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:08:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:08:23] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:08:23] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:08:23] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:08:23] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:08:23] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:08:23] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:08:23] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:08:23] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:08:23] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:08:23] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |

```

```

[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Performance Metrics
[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] | Stage          | Time Taken |
[2025-10-12 14:08:23] [info] |-----|-----|
[2025-10-12 14:08:23] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:08:23] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 14:08:23] [info] | Report Generation | 0 minutes 0 seconds |

```

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[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Summary
[2025-10-12 14:08:23] [info] -----
[2025-10-12 14:08:23] [info] Total Errors:      1120
[2025-10-12 14:08:23] [info] Total Warnings:    321
[2025-10-12 14:08:23] [info] DRC Status:        FAILED

```

lvr_summary_217.rpt

```
[2025-10-12 14:09:22] [info] =====
[2025-10-12 14:09:22] [info]                Design Rule Check Report
[2025-10-12 14:09:22] [info] =====

[2025-10-12 14:09:22] [info] Report Generated:    2025-10-12
[2025-10-12 14:09:22] [info] Run Start Time:      02:08:30 PM
[2025-10-12 14:09:22] [info] Run End Time:        02:09:22 PM
[2025-10-12 14:09:22] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:09:22] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:09:22] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:09:22] [info] User Name:           ubuntu
[2025-10-12 14:09:22] [info] Cores Used:          8
[2025-10-12 14:09:22] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info]                Top Cell Information
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info] Layout Top Cell:     TOP
[2025-10-12 14:09:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:09:22] [info] Database Format:       GDSII
[2025-10-12 14:09:22] [info] Verification Mode:    Full Check

[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info]                Type of Errors | Count
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info] DB_WARN -> 1
[2025-10-12 14:09:22] [info] LRF_ERR001 -> 14
[2025-10-12 14:09:22] [info] DB_WARN002 -> 159
[2025-10-12 14:09:22] [info] DB_WARN003 -> 161
[2025-10-12 14:09:22] [info] DRC_ERR9 -> 89
[2025-10-12 14:09:22] [info] DRC_ERR -> 67
[2025-10-12 14:09:22] [info] DRC_ERR5 -> 408
[2025-10-12 14:09:22] [info] DRC_ERR4 -> 241
[2025-10-12 14:09:22] [info] DRC_ERR1 -> 301
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info]                Rule Violation Summary
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:09:22] [info] |-----|-----|-----|
[2025-10-12 14:09:22] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:09:22] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:09:22] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:09:22] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:09:22] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:09:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:09:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:09:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:09:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:09:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:09:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:09:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:09:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:09:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:09:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:09:22] [info]
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info]                Performance Metrics
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info] | Stage | Time Taken |
```



```
[2025-10-12 14:09:22] [info] |-----|-----|
[2025-10-12 14:09:22] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:09:22] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 14:09:22] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info]                Summary
[2025-10-12 14:09:22] [info] -----
[2025-10-12 14:09:22] [info] Total Errors:    1120
[2025-10-12 14:09:22] [info] Total Warnings:    321
[2025-10-12 14:09:22] [info] DRC Status:      FAILED
```

lvr_summary_218.rpt

```
[2025-10-12 14:10:20] [info] =====
[2025-10-12 14:10:20] [info]                Design Rule Check Report
[2025-10-12 14:10:20] [info] =====

[2025-10-12 14:10:20] [info] Report Generated:    2025-10-12
[2025-10-12 14:10:20] [info] Run Start Time:      02:09:28 PM
[2025-10-12 14:10:20] [info] Run End Time:        02:10:20 PM
[2025-10-12 14:10:20] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:10:20] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:10:20] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:10:20] [info] User Name:           ubuntu
[2025-10-12 14:10:20] [info] Cores Used:          8
[2025-10-12 14:10:20] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info]                Top Cell Information
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] Layout Top Cell:     TOP
[2025-10-12 14:10:20] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:10:20] [info] Database Format:      GDSII
[2025-10-12 14:10:20] [info] Verification Mode:    Full Check

[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info]                Type of Errors | Count
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] DB_WARN -> 1
[2025-10-12 14:10:20] [info] LRF_ERR001 -> 14
[2025-10-12 14:10:20] [info] DB_WARN002 -> 159
[2025-10-12 14:10:20] [info] DB_WARN003 -> 161
[2025-10-12 14:10:20] [info] DRC_ERR9 -> 89
[2025-10-12 14:10:20] [info] DRC_ERR -> 67
[2025-10-12 14:10:20] [info] DRC_ERR5 -> 408
[2025-10-12 14:10:20] [info] DRC_ERR4 -> 241
[2025-10-12 14:10:20] [info] DRC_ERR1 -> 301
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info]                Rule Violation Summary
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:10:20] [info] |-----|-----|-----|
[2025-10-12 14:10:20] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:10:20] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:10:20] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:10:20] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:10:20] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 14:10:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:10:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:10:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:10:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:10:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:10:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:10:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:10:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:10:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:10:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:10:20] [info]
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] Performance Metrics
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] | Stage | Time Taken |
[2025-10-12 14:10:20] [info] |-----|-----|
[2025-10-12 14:10:20] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:10:20] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:10:20] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] Summary
[2025-10-12 14:10:20] [info] -----
[2025-10-12 14:10:20] [info] Total Errors: 1120
[2025-10-12 14:10:20] [info] Total Warnings: 321
[2025-10-12 14:10:20] [info] DRC Status: FAILED
```

lvr_summary_219.rpt

```
[2025-10-12 14:11:18] [info] =====
[2025-10-12 14:11:18] [info] Design Rule Check Report
[2025-10-12 14:11:18] [info] =====

[2025-10-12 14:11:18] [info] Report Generated: 2025-10-12
[2025-10-12 14:11:18] [info] Run Start Time: 02:10:27 PM
[2025-10-12 14:11:18] [info] Run End Time: 02:11:18 PM
[2025-10-12 14:11:18] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:11:18] [info] Tool Version: LVR_2025.1
[2025-10-12 14:11:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:11:18] [info] User Name: ubuntu
[2025-10-12 14:11:18] [info] Cores Used: 8
[2025-10-12 14:11:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Top Cell Information
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Layout Top Cell: TOP
[2025-10-12 14:11:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:11:18] [info] Database Format: GDSII
[2025-10-12 14:11:18] [info] Verification Mode: Full Check

[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Type of Errors | Count
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] DB_WARN -> 1
[2025-10-12 14:11:18] [info] LRF_ERR001 -> 14
[2025-10-12 14:11:18] [info] DB_WARN002 -> 159
[2025-10-12 14:11:18] [info] DB_WARN003 -> 161
```

```

[2025-10-12 14:11:18] [info] DRC_ERR9 -> 89
[2025-10-12 14:11:18] [info] DRC_ERR -> 67
[2025-10-12 14:11:18] [info] DRC_ERR5 -> 408
[2025-10-12 14:11:18] [info] DRC_ERR4 -> 241
[2025-10-12 14:11:18] [info] DRC_ERR1 -> 301
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Rule Violation Summary
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:11:18] [info] |-----|-----|-----|
[2025-10-12 14:11:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:11:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:11:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:11:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:11:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:11:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:11:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:11:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:11:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:11:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:11:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:11:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:11:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:11:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:11:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Performance Metrics
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] | Stage | Time Taken |
[2025-10-12 14:11:18] [info] |-----|-----|
[2025-10-12 14:11:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:11:18] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:11:18] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Summary
[2025-10-12 14:11:18] [info] -----
[2025-10-12 14:11:18] [info] Total Errors: 1120
[2025-10-12 14:11:18] [info] Total Warnings: 321
[2025-10-12 14:11:18] [info] DRC Status: FAILED

```

lvr_summary_22.rpt

```

[2025-10-12 11:02:16] [info] =====
[2025-10-12 11:02:16] [info] Design Rule Check Report
[2025-10-12 11:02:16] [info] =====

[2025-10-12 11:02:16] [info] Report Generated: 2025-10-12
[2025-10-12 11:02:16] [info] Run Start Time: 11:01:24 AM
[2025-10-12 11:02:16] [info] Run End Time: 11:02:16 AM
[2025-10-12 11:02:16] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:02:16] [info] Tool Version: LVR_2025.1
[2025-10-12 11:02:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:02:16] [info] User Name: ubuntu
[2025-10-12 11:02:16] [info] Cores Used: 8
[2025-10-12 11:02:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Top Cell Information
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Layout Top Cell:      TOP
[2025-10-12 11:02:16] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:02:16] [info] Database Format:      GDSII
[2025-10-12 11:02:16] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Type of Errors | Count
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] DB_WARN -> 1
[2025-10-12 11:02:16] [info] LRF_ERR001 -> 14
[2025-10-12 11:02:16] [info] DB_WARN002 -> 159
[2025-10-12 11:02:16] [info] DB_WARN003 -> 161
[2025-10-12 11:02:16] [info] DRC_ERR9 -> 89
[2025-10-12 11:02:16] [info] DRC_ERR -> 67
[2025-10-12 11:02:16] [info] DRC_ERR5 -> 408
[2025-10-12 11:02:16] [info] DRC_ERR4 -> 241
[2025-10-12 11:02:16] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Rule Violation Summary
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:02:16] [info] |-----|-----|-----|
[2025-10-12 11:02:16] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:02:16] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:02:16] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:02:16] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:02:16] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:02:16] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:02:16] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:02:16] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:02:16] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:02:16] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:02:16] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:02:16] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:02:16] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:02:16] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:02:16] [info] | npc.1       | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Performance Metrics
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] | Stage          | Time Taken |
[2025-10-12 11:02:16] [info] |-----|-----|
[2025-10-12 11:02:16] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:02:16] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:02:16] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Summary
[2025-10-12 11:02:16] [info] -----
[2025-10-12 11:02:16] [info] Total Errors:      1120
[2025-10-12 11:02:16] [info] Total Warnings:    321
[2025-10-12 11:02:16] [info] DRC Status:        FAILED
```

lvr_summary_220.rpt

```
[2025-10-12 14:12:16] [info] =====
[2025-10-12 14:12:16] [info]                      Design Rule Check Report
[2025-10-12 14:12:16] [info] =====

[2025-10-12 14:12:16] [info] Report Generated:    2025-10-12
[2025-10-12 14:12:16] [info] Run Start Time:      02:11:24 PM
[2025-10-12 14:12:16] [info] Run End Time:        02:12:16 PM
[2025-10-12 14:12:16] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:12:16] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:12:16] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:12:16] [info] User Name:           ubuntu
[2025-10-12 14:12:16] [info] Cores Used:          8
[2025-10-12 14:12:16] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info]                      Top Cell Information
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info] Layout Top Cell:     TOP
[2025-10-12 14:12:16] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:12:16] [info] Database Format:       GDSII
[2025-10-12 14:12:16] [info] Verification Mode:    Full Check

[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info]                      Type of Errors | Count
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info] DB_WARN -> 1
[2025-10-12 14:12:16] [info] LRF_ERR001 -> 14
[2025-10-12 14:12:16] [info] DB_WARN002 -> 159
[2025-10-12 14:12:16] [info] DB_WARN003 -> 161
[2025-10-12 14:12:16] [info] DRC_ERR9 -> 89
[2025-10-12 14:12:16] [info] DRC_ERR -> 67
[2025-10-12 14:12:16] [info] DRC_ERR5 -> 408
[2025-10-12 14:12:16] [info] DRC_ERR4 -> 241
[2025-10-12 14:12:16] [info] DRC_ERR1 -> 301
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info]                      Rule Violation Summary
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:12:16] [info] |-----|-----|-----|
[2025-10-12 14:12:16] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:12:16] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:12:16] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:12:16] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:12:16] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:12:16] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:12:16] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:12:16] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:12:16] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:12:16] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:12:16] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:12:16] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:12:16] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:12:16] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:12:16] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:12:16] [info]
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info]                      Performance Metrics
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:12:16] [info] |-----|-----|
[2025-10-12 14:12:16] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 14:12:16] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 14:12:16] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info]                Summary
[2025-10-12 14:12:16] [info] -----
[2025-10-12 14:12:16] [info] Total Errors:      1120
[2025-10-12 14:12:16] [info] Total Warnings:    321
[2025-10-12 14:12:16] [info] DRC Status:        FAILED
```

lvr_summary_221.rpt

```
[2025-10-12 14:13:12] [info] =====
[2025-10-12 14:13:12] [info]                Design Rule Check Report
[2025-10-12 14:13:12] [info] =====

[2025-10-12 14:13:12] [info] Report Generated:   2025-10-12
[2025-10-12 14:13:12] [info] Run Start Time:     02:12:20 PM
[2025-10-12 14:13:12] [info] Run End Time:       02:13:12 PM
[2025-10-12 14:13:12] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 14:13:12] [info] Tool Version:       LVR_2025.1
[2025-10-12 14:13:12] [info] Host Name:          ip-172-31-37-173
[2025-10-12 14:13:12] [info] User Name:          ubuntu
[2025-10-12 14:13:12] [info] Cores Used:         8
[2025-10-12 14:13:12] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info]                Top Cell Information
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] Layout Top Cell:    TOP
[2025-10-12 14:13:12] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 14:13:12] [info] Database Format:      GDSII
[2025-10-12 14:13:12] [info] Verification Mode:   Full Check

[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info]                Type of Errors | Count
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] DB_WARN -> 1
[2025-10-12 14:13:12] [info] LRF_ERR001 -> 14
[2025-10-12 14:13:12] [info] DB_WARN002 -> 159
[2025-10-12 14:13:12] [info] DB_WARN003 -> 161
[2025-10-12 14:13:12] [info] DRC_ERR9 -> 89
[2025-10-12 14:13:12] [info] DRC_ERR -> 67
[2025-10-12 14:13:12] [info] DRC_ERR5 -> 408
[2025-10-12 14:13:12] [info] DRC_ERR4 -> 241
[2025-10-12 14:13:12] [info] DRC_ERR1 -> 301
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info]                Rule Violation Summary
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:13:12] [info] |-----|-----|-----|
[2025-10-12 14:13:12] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:13:12] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:13:12] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:13:12] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:13:12] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:13:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:13:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:13:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:13:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:13:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:13:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:13:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:13:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:13:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:13:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:13:12] [info]
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] Performance Metrics
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] | Stage | Time Taken |
[2025-10-12 14:13:12] [info] |-----|-----|
[2025-10-12 14:13:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:13:12] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:13:12] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] Summary
[2025-10-12 14:13:12] [info] -----
[2025-10-12 14:13:12] [info] Total Errors: 1120
[2025-10-12 14:13:12] [info] Total Warnings: 321
[2025-10-12 14:13:12] [info] DRC Status: FAILED

```

lvr_summary_222.rpt

```

[2025-10-12 14:14:09] [info] =====
[2025-10-12 14:14:09] [info] Design Rule Check Report
[2025-10-12 14:14:09] [info] =====

[2025-10-12 14:14:09] [info] Report Generated: 2025-10-12
[2025-10-12 14:14:09] [info] Run Start Time: 02:13:19 PM
[2025-10-12 14:14:09] [info] Run End Time: 02:14:09 PM
[2025-10-12 14:14:09] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:14:09] [info] Tool Version: LVR_2025.1
[2025-10-12 14:14:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:14:09] [info] User Name: ubuntu
[2025-10-12 14:14:09] [info] Cores Used: 8
[2025-10-12 14:14:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Top Cell Information
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Layout Top Cell: TOP
[2025-10-12 14:14:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:14:09] [info] Database Format: GDSII
[2025-10-12 14:14:09] [info] Verification Mode: Full Check

[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Type of Errors | Count
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] DB_WARN -> 1
[2025-10-12 14:14:09] [info] LRF_ERR001 -> 14
[2025-10-12 14:14:09] [info] DB_WARN002 -> 159
[2025-10-12 14:14:09] [info] DB_WARN003 -> 161

```

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[2025-10-12 14:14:09] [info] DRC_ERR9 -> 89
[2025-10-12 14:14:09] [info] DRC_ERR -> 67
[2025-10-12 14:14:09] [info] DRC_ERR5 -> 408
[2025-10-12 14:14:09] [info] DRC_ERR4 -> 241
[2025-10-12 14:14:09] [info] DRC_ERR1 -> 301
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Rule Violation Summary
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:14:09] [info] |-----|-----|-----|
[2025-10-12 14:14:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:14:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:14:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:14:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:14:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:14:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:14:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:14:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:14:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:14:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:14:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:14:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:14:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:14:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:14:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Performance Metrics
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] | Stage | Time Taken |
[2025-10-12 14:14:09] [info] |-----|-----|
[2025-10-12 14:14:09] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:14:09] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:14:09] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Summary
[2025-10-12 14:14:09] [info] -----
[2025-10-12 14:14:09] [info] Total Errors: 1120
[2025-10-12 14:14:09] [info] Total Warnings: 321
[2025-10-12 14:14:09] [info] DRC Status: FAILED

```

lvr_summary_223.rpt

```

[2025-10-12 14:15:08] [info] =====
[2025-10-12 14:15:08] [info] Design Rule Check Report
[2025-10-12 14:15:08] [info] =====

[2025-10-12 14:15:08] [info] Report Generated: 2025-10-12
[2025-10-12 14:15:08] [info] Run Start Time: 02:14:16 PM
[2025-10-12 14:15:08] [info] Run End Time: 02:15:08 PM
[2025-10-12 14:15:08] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:15:08] [info] Tool Version: LVR_2025.1
[2025-10-12 14:15:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:15:08] [info] User Name: ubuntu
[2025-10-12 14:15:08] [info] Cores Used: 8
[2025-10-12 14:15:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Top Cell Information
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Layout Top Cell:      TOP
[2025-10-12 14:15:08] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:15:08] [info] Database Format:      GDSII
[2025-10-12 14:15:08] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Type of Errors | Count
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] DB_WARN -> 1
[2025-10-12 14:15:08] [info] LRF_ERR001 -> 14
[2025-10-12 14:15:08] [info] DB_WARN002 -> 159
[2025-10-12 14:15:08] [info] DB_WARN003 -> 161
[2025-10-12 14:15:08] [info] DRC_ERR9 -> 89
[2025-10-12 14:15:08] [info] DRC_ERR -> 67
[2025-10-12 14:15:08] [info] DRC_ERR5 -> 408
[2025-10-12 14:15:08] [info] DRC_ERR4 -> 241
[2025-10-12 14:15:08] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Rule Violation Summary
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:15:08] [info] |-----|-----|-----|
[2025-10-12 14:15:08] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:15:08] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:15:08] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:15:08] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:15:08] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:15:08] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:15:08] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:15:08] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:15:08] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:15:08] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:15:08] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:15:08] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:15:08] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:15:08] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:15:08] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:15:08] [info] |
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Performance Metrics
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] | Stage          | Time Taken |
[2025-10-12 14:15:08] [info] |-----|-----|
[2025-10-12 14:15:08] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:15:08] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 14:15:08] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Summary
```

```
[2025-10-12 14:15:08] [info] -----
[2025-10-12 14:15:08] [info] Total Errors:      1120
[2025-10-12 14:15:08] [info] Total Warnings:    321
[2025-10-12 14:15:08] [info] DRC Status:        FAILED
```

lvr_summary_224.rpt

```
[2025-10-12 14:16:05] [info] =====
[2025-10-12 14:16:05] [info] Design Rule Check Report
[2025-10-12 14:16:05] [info] =====

[2025-10-12 14:16:05] [info] Report Generated: 2025-10-12
[2025-10-12 14:16:05] [info] Run Start Time: 02:15:14 PM
[2025-10-12 14:16:05] [info] Run End Time: 02:16:05 PM
[2025-10-12 14:16:05] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:16:05] [info] Tool Version: LVR_2025.1
[2025-10-12 14:16:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:16:05] [info] User Name: ubuntu
[2025-10-12 14:16:05] [info] Cores Used: 8
[2025-10-12 14:16:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] Top Cell Information
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] Layout Top Cell: TOP
[2025-10-12 14:16:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:16:05] [info] Database Format: GDSII
[2025-10-12 14:16:05] [info] Verification Mode: Full Check

[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] Type of Errors | Count
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] DB_WARN -> 1
[2025-10-12 14:16:05] [info] LRF_ERR001 -> 14
[2025-10-12 14:16:05] [info] DB_WARN002 -> 159
[2025-10-12 14:16:05] [info] DB_WARN003 -> 161
[2025-10-12 14:16:05] [info] DRC_ERR9 -> 89
[2025-10-12 14:16:05] [info] DRC_ERR -> 67
[2025-10-12 14:16:05] [info] DRC_ERR5 -> 408
[2025-10-12 14:16:05] [info] DRC_ERR4 -> 241
[2025-10-12 14:16:05] [info] DRC_ERR1 -> 301
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] Rule Violation Summary
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:16:05] [info] |-----|-----|-----|
[2025-10-12 14:16:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:16:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:16:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:16:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:16:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:16:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:16:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:16:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:16:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:16:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:16:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:16:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:16:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:16:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:16:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:16:05] [info]
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] Performance Metrics
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:16:05] [info] |-----|-----|
[2025-10-12 14:16:05] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 14:16:05] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 14:16:05] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info]                Summary
[2025-10-12 14:16:05] [info] -----
[2025-10-12 14:16:05] [info] Total Errors:      1120
[2025-10-12 14:16:05] [info] Total Warnings:    321
[2025-10-12 14:16:05] [info] DRC Status:        FAILED
```

lvr_summary_225.rpt

```
[2025-10-12 14:17:03] [info] =====
[2025-10-12 14:17:03] [info]                Design Rule Check Report
[2025-10-12 14:17:03] [info] =====

[2025-10-12 14:17:03] [info] Report Generated:   2025-10-12
[2025-10-12 14:17:03] [info] Run Start Time:     02:16:10 PM
[2025-10-12 14:17:03] [info] Run End Time:       02:17:03 PM
[2025-10-12 14:17:03] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 14:17:03] [info] Tool Version:       LVR_2025.1
[2025-10-12 14:17:03] [info] Host Name:          ip-172-31-37-173
[2025-10-12 14:17:03] [info] User Name:          ubuntu
[2025-10-12 14:17:03] [info] Cores Used:         8
[2025-10-12 14:17:03] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info]                Top Cell Information
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] Layout Top Cell:    TOP
[2025-10-12 14:17:03] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 14:17:03] [info] Database Format:      GDSII
[2025-10-12 14:17:03] [info] Verification Mode:   Full Check

[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info]                Type of Errors | Count
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] DB_WARN -> 1
[2025-10-12 14:17:03] [info] LRF_ERR001 -> 14
[2025-10-12 14:17:03] [info] DB_WARN002 -> 159
[2025-10-12 14:17:03] [info] DB_WARN003 -> 161
[2025-10-12 14:17:03] [info] DRC_ERR9 -> 89
[2025-10-12 14:17:03] [info] DRC_ERR -> 67
[2025-10-12 14:17:03] [info] DRC_ERR5 -> 408
[2025-10-12 14:17:03] [info] DRC_ERR4 -> 241
[2025-10-12 14:17:03] [info] DRC_ERR1 -> 301
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info]                Rule Violation Summary
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:17:03] [info] |-----|-----|-----|
[2025-10-12 14:17:03] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:17:03] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:17:03] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:17:03] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:17:03] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 14:17:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:17:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:17:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:17:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:17:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:17:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:17:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:17:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:17:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:17:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:17:03] [info]
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] Performance Metrics
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] | Stage | Time Taken |
[2025-10-12 14:17:03] [info] |-----|-----|
[2025-10-12 14:17:03] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:17:03] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:17:03] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] Summary
[2025-10-12 14:17:03] [info] -----
[2025-10-12 14:17:03] [info] Total Errors: 1120
[2025-10-12 14:17:03] [info] Total Warnings: 321
[2025-10-12 14:17:03] [info] DRC Status: FAILED
```

lvr_summary_226.rpt

```
[2025-10-12 14:18:00] [info] =====
[2025-10-12 14:18:00] [info] Design Rule Check Report
[2025-10-12 14:18:00] [info] =====

[2025-10-12 14:18:00] [info] Report Generated: 2025-10-12
[2025-10-12 14:18:00] [info] Run Start Time: 02:17:09 PM
[2025-10-12 14:18:00] [info] Run End Time: 02:18:00 PM
[2025-10-12 14:18:00] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:18:00] [info] Tool Version: LVR_2025.1
[2025-10-12 14:18:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:18:00] [info] User Name: ubuntu
[2025-10-12 14:18:00] [info] Cores Used: 8
[2025-10-12 14:18:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Top Cell Information
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Layout Top Cell: TOP
[2025-10-12 14:18:00] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:18:00] [info] Database Format: GDSII
[2025-10-12 14:18:00] [info] Verification Mode: Full Check

[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Type of Errors | Count
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] DB_WARN -> 1
[2025-10-12 14:18:00] [info] LRF_ERR001 -> 14
[2025-10-12 14:18:00] [info] DB_WARN002 -> 159
[2025-10-12 14:18:00] [info] DB_WARN003 -> 161
```

```

[2025-10-12 14:18:00] [info] DRC_ERR9 -> 89
[2025-10-12 14:18:00] [info] DRC_ERR -> 67
[2025-10-12 14:18:00] [info] DRC_ERR5 -> 408
[2025-10-12 14:18:00] [info] DRC_ERR4 -> 241
[2025-10-12 14:18:00] [info] DRC_ERR1 -> 301
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Rule Violation Summary
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:18:00] [info] |-----|-----|-----|
[2025-10-12 14:18:00] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:18:00] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:18:00] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:18:00] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:18:00] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:18:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:18:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:18:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:18:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:18:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:18:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:18:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:18:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:18:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:18:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Performance Metrics
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] | Stage | Time Taken |
[2025-10-12 14:18:00] [info] |-----|-----|
[2025-10-12 14:18:00] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:18:00] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:18:00] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Summary
[2025-10-12 14:18:00] [info] -----
[2025-10-12 14:18:00] [info] Total Errors: 1120
[2025-10-12 14:18:00] [info] Total Warnings: 321
[2025-10-12 14:18:00] [info] DRC Status: FAILED

```

lvr_summary_227.rpt

```

[2025-10-12 14:18:57] [info] =====
[2025-10-12 14:18:57] [info] Design Rule Check Report
[2025-10-12 14:18:57] [info] =====

[2025-10-12 14:18:57] [info] Report Generated: 2025-10-12
[2025-10-12 14:18:57] [info] Run Start Time: 02:18:05 PM
[2025-10-12 14:18:57] [info] Run End Time: 02:18:57 PM
[2025-10-12 14:18:57] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:18:57] [info] Tool Version: LVR_2025.1
[2025-10-12 14:18:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:18:57] [info] User Name: ubuntu
[2025-10-12 14:18:57] [info] Cores Used: 8
[2025-10-12 14:18:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Top Cell Information
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Layout Top Cell:      TOP
[2025-10-12 14:18:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:18:57] [info] Database Format:      GDSII
[2025-10-12 14:18:57] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Type of Errors | Count
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] DB_WARN -> 1
[2025-10-12 14:18:57] [info] LRF_ERR001 -> 14
[2025-10-12 14:18:57] [info] DB_WARN002 -> 159
[2025-10-12 14:18:57] [info] DB_WARN003 -> 161
[2025-10-12 14:18:57] [info] DRC_ERR9 -> 89
[2025-10-12 14:18:57] [info] DRC_ERR -> 67
[2025-10-12 14:18:57] [info] DRC_ERR5 -> 408
[2025-10-12 14:18:57] [info] DRC_ERR4 -> 241
[2025-10-12 14:18:57] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Rule Violation Summary
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:18:57] [info] |-----|-----|-----|
[2025-10-12 14:18:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:18:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:18:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:18:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:18:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:18:57] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:18:57] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:18:57] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:18:57] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:18:57] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:18:57] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:18:57] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:18:57] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:18:57] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:18:57] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:18:57] [info] |
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Performance Metrics
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] | Stage          | Time Taken |
[2025-10-12 14:18:57] [info] |-----|-----|
[2025-10-12 14:18:57] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:18:57] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 14:18:57] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Summary
```

```
[2025-10-12 14:18:57] [info] -----
[2025-10-12 14:18:57] [info] Total Errors:      1120
[2025-10-12 14:18:57] [info] Total Warnings:    321
[2025-10-12 14:18:57] [info] DRC Status:        FAILED
```

lvr_summary_228.rpt

```
[2025-10-12 14:19:55] [info] =====
[2025-10-12 14:19:55] [info] Design Rule Check Report
[2025-10-12 14:19:55] [info] =====

[2025-10-12 14:19:55] [info] Report Generated: 2025-10-12
[2025-10-12 14:19:55] [info] Run Start Time: 02:19:04 PM
[2025-10-12 14:19:55] [info] Run End Time: 02:19:55 PM
[2025-10-12 14:19:55] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:19:55] [info] Tool Version: LVR_2025.1
[2025-10-12 14:19:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:19:55] [info] User Name: ubuntu
[2025-10-12 14:19:55] [info] Cores Used: 8
[2025-10-12 14:19:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] Top Cell Information
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] Layout Top Cell: TOP
[2025-10-12 14:19:55] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:19:55] [info] Database Format: GDSII
[2025-10-12 14:19:55] [info] Verification Mode: Full Check

[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] Type of Errors | Count
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] DB_WARN -> 1
[2025-10-12 14:19:55] [info] LRF_ERR001 -> 14
[2025-10-12 14:19:55] [info] DB_WARN002 -> 159
[2025-10-12 14:19:55] [info] DB_WARN003 -> 161
[2025-10-12 14:19:55] [info] DRC_ERR9 -> 89
[2025-10-12 14:19:55] [info] DRC_ERR -> 67
[2025-10-12 14:19:55] [info] DRC_ERR5 -> 408
[2025-10-12 14:19:55] [info] DRC_ERR4 -> 241
[2025-10-12 14:19:55] [info] DRC_ERR1 -> 301
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] Rule Violation Summary
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:19:55] [info] |-----|-----|-----|
[2025-10-12 14:19:55] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:19:55] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:19:55] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:19:55] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:19:55] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:19:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:19:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:19:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:19:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:19:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:19:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:19:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:19:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:19:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:19:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:19:55] [info]
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] Performance Metrics
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:19:55] [info] |-----|-----|
[2025-10-12 14:19:55] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:19:55] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 14:19:55] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info]                Summary
[2025-10-12 14:19:55] [info] -----
[2025-10-12 14:19:55] [info] Total Errors:    1120
[2025-10-12 14:19:55] [info] Total Warnings:    321
[2025-10-12 14:19:55] [info] DRC Status:      FAILED
```

lvr_summary_229.rpt

```
[2025-10-12 14:20:53] [info] =====
[2025-10-12 14:20:53] [info]                Design Rule Check Report
[2025-10-12 14:20:53] [info] =====

[2025-10-12 14:20:53] [info] Report Generated:    2025-10-12
[2025-10-12 14:20:53] [info] Run Start Time:      02:20:01 PM
[2025-10-12 14:20:53] [info] Run End Time:        02:20:53 PM
[2025-10-12 14:20:53] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:20:53] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:20:53] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:20:53] [info] User Name:           ubuntu
[2025-10-12 14:20:53] [info] Cores Used:          8
[2025-10-12 14:20:53] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info]                Top Cell Information
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] Layout Top Cell:     TOP
[2025-10-12 14:20:53] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:20:53] [info] Database Format:       GDSII
[2025-10-12 14:20:53] [info] Verification Mode:    Full Check

[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info]                Type of Errors | Count
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] DB_WARN -> 1
[2025-10-12 14:20:53] [info] LRF_ERR001 -> 14
[2025-10-12 14:20:53] [info] DB_WARN002 -> 159
[2025-10-12 14:20:53] [info] DB_WARN003 -> 161
[2025-10-12 14:20:53] [info] DRC_ERR9 -> 89
[2025-10-12 14:20:53] [info] DRC_ERR -> 67
[2025-10-12 14:20:53] [info] DRC_ERR5 -> 408
[2025-10-12 14:20:53] [info] DRC_ERR4 -> 241
[2025-10-12 14:20:53] [info] DRC_ERR1 -> 301
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info]                Rule Violation Summary
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:20:53] [info] |-----|-----|-----|
[2025-10-12 14:20:53] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:20:53] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:20:53] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:20:53] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:20:53] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```

[2025-10-12 14:20:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:20:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:20:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:20:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:20:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:20:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:20:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:20:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:20:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:20:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:20:53] [info]
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] Performance Metrics
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] | Stage | Time Taken |
[2025-10-12 14:20:53] [info] |-----|-----|
[2025-10-12 14:20:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:20:53] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:20:53] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] Summary
[2025-10-12 14:20:53] [info] -----
[2025-10-12 14:20:53] [info] Total Errors: 1120
[2025-10-12 14:20:53] [info] Total Warnings: 321
[2025-10-12 14:20:53] [info] DRC Status: FAILED

```

lvr_summary_23.rpt

```

[2025-10-12 11:03:12] [info] =====
[2025-10-12 11:03:12] [info] Design Rule Check Report
[2025-10-12 11:03:12] [info] =====

[2025-10-12 11:03:12] [info] Report Generated: 2025-10-12
[2025-10-12 11:03:12] [info] Run Start Time: 11:02:20 AM
[2025-10-12 11:03:12] [info] Run End Time: 11:03:12 AM
[2025-10-12 11:03:12] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:03:12] [info] Tool Version: LVR_2025.1
[2025-10-12 11:03:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:03:12] [info] User Name: ubuntu
[2025-10-12 11:03:12] [info] Cores Used: 8
[2025-10-12 11:03:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Top Cell Information
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Layout Top Cell: TOP
[2025-10-12 11:03:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:03:12] [info] Database Format: GDSII
[2025-10-12 11:03:12] [info] Verification Mode: Full Check

[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Type of Errors | Count
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] DB_WARN -> 1
[2025-10-12 11:03:12] [info] LRF_ERR001 -> 14
[2025-10-12 11:03:12] [info] DB_WARN002 -> 159
[2025-10-12 11:03:12] [info] DB_WARN003 -> 161

```

```

[2025-10-12 11:03:12] [info] DRC_ERR9 -> 89
[2025-10-12 11:03:12] [info] DRC_ERR -> 67
[2025-10-12 11:03:12] [info] DRC_ERR5 -> 408
[2025-10-12 11:03:12] [info] DRC_ERR4 -> 241
[2025-10-12 11:03:12] [info] DRC_ERR1 -> 301
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Rule Violation Summary
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:03:12] [info] |-----|-----|-----|
[2025-10-12 11:03:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:03:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:03:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:03:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:03:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:03:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:03:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:03:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:03:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:03:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:03:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:03:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:03:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:03:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:03:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Performance Metrics
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] | Stage | Time Taken |
[2025-10-12 11:03:12] [info] |-----|-----|
[2025-10-12 11:03:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:03:12] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:03:12] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Summary
[2025-10-12 11:03:12] [info] -----
[2025-10-12 11:03:12] [info] Total Errors: 1120
[2025-10-12 11:03:12] [info] Total Warnings: 321
[2025-10-12 11:03:12] [info] DRC Status: FAILED

```

lvr_summary_230.rpt

```

[2025-10-12 14:21:50] [info] =====
[2025-10-12 14:21:50] [info] Design Rule Check Report
[2025-10-12 14:21:50] [info] =====

[2025-10-12 14:21:50] [info] Report Generated: 2025-10-12
[2025-10-12 14:21:50] [info] Run Start Time: 02:21:00 PM
[2025-10-12 14:21:50] [info] Run End Time: 02:21:50 PM
[2025-10-12 14:21:50] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:21:50] [info] Tool Version: LVR_2025.1
[2025-10-12 14:21:50] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:21:50] [info] User Name: ubuntu
[2025-10-12 14:21:50] [info] Cores Used: 8
[2025-10-12 14:21:50] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] Top Cell Information
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] Layout Top Cell:      TOP
[2025-10-12 14:21:50] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:21:50] [info] Database Format:      GDSII
[2025-10-12 14:21:50] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] Type of Errors | Count
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] DB_WARN -> 1
[2025-10-12 14:21:50] [info] LRF_ERR001 -> 14
[2025-10-12 14:21:50] [info] DB_WARN002 -> 159
[2025-10-12 14:21:50] [info] DB_WARN003 -> 161
[2025-10-12 14:21:50] [info] DRC_ERR9 -> 89
[2025-10-12 14:21:50] [info] DRC_ERR -> 67
[2025-10-12 14:21:50] [info] DRC_ERR5 -> 408
[2025-10-12 14:21:50] [info] DRC_ERR4 -> 241
[2025-10-12 14:21:50] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] Rule Violation Summary
```

```
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:21:50] [info] |-----|-----|-----|
[2025-10-12 14:21:50] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:21:50] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:21:50] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:21:50] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:21:50] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:21:50] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:21:50] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:21:50] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:21:50] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:21:50] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:21:50] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:21:50] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:21:50] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:21:50] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:21:50] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:21:50] [info] -----
```

```
[2025-10-12 14:21:50] [info] Performance Metrics
```

```
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] | Stage          | Time Taken |
[2025-10-12 14:21:50] [info] |-----|-----|
[2025-10-12 14:21:50] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:21:50] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:21:50] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:21:50] [info] -----
```

```
[2025-10-12 14:21:50] [info] Summary
```

```
[2025-10-12 14:21:50] [info] -----
[2025-10-12 14:21:50] [info] Total Errors:      1120
[2025-10-12 14:21:50] [info] Total Warnings:    321
[2025-10-12 14:21:50] [info] DRC Status:        FAILED
```

lvr_summary_231.rpt

```
[2025-10-12 14:22:49] [info] =====
[2025-10-12 14:22:49] [info]                      Design Rule Check Report
[2025-10-12 14:22:49] [info] =====

[2025-10-12 14:22:49] [info] Report Generated:      2025-10-12
[2025-10-12 14:22:49] [info] Run Start Time:       02:21:58 PM
[2025-10-12 14:22:49] [info] Run End Time:        02:22:49 PM
[2025-10-12 14:22:49] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 14:22:49] [info] Tool Version:         LVR_2025.1
[2025-10-12 14:22:49] [info] Host Name:            ip-172-31-37-173
[2025-10-12 14:22:49] [info] User Name:            ubuntu
[2025-10-12 14:22:49] [info] Cores Used:           8
[2025-10-12 14:22:49] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info]                      Top Cell Information
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info] Layout Top Cell:      TOP
[2025-10-12 14:22:49] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 14:22:49] [info] Database Format:        GDSII
[2025-10-12 14:22:49] [info] Verification Mode:     Full Check

[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info]                      Type of Errors | Count
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info] DB_WARN -> 1
[2025-10-12 14:22:49] [info] LRF_ERR001 -> 14
[2025-10-12 14:22:49] [info] DB_WARN002 -> 159
[2025-10-12 14:22:49] [info] DB_WARN003 -> 161
[2025-10-12 14:22:49] [info] DRC_ERR9 -> 89
[2025-10-12 14:22:49] [info] DRC_ERR -> 67
[2025-10-12 14:22:49] [info] DRC_ERR5 -> 408
[2025-10-12 14:22:49] [info] DRC_ERR4 -> 241
[2025-10-12 14:22:49] [info] DRC_ERR1 -> 301
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info]                      Rule Violation Summary
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:22:49] [info] |-----|-----|-----|
[2025-10-12 14:22:49] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:22:49] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:22:49] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:22:49] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:22:49] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:22:49] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:22:49] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:22:49] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:22:49] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:22:49] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:22:49] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:22:49] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:22:49] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:22:49] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:22:49] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:22:49] [info]
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info]                      Performance Metrics
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:22:49] [info] |-----|-----|
[2025-10-12 14:22:49] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:22:49] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 14:22:49] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info] Summary
[2025-10-12 14:22:49] [info] -----
[2025-10-12 14:22:49] [info] Total Errors: 1120
[2025-10-12 14:22:49] [info] Total Warnings: 321
[2025-10-12 14:22:49] [info] DRC Status: FAILED
```

lvr_summary_232.rpt

```
[2025-10-12 14:23:47] [info] =====
[2025-10-12 14:23:47] [info] Design Rule Check Report
[2025-10-12 14:23:47] [info] =====

[2025-10-12 14:23:47] [info] Report Generated: 2025-10-12
[2025-10-12 14:23:47] [info] Run Start Time: 02:22:56 PM
[2025-10-12 14:23:47] [info] Run End Time: 02:23:47 PM
[2025-10-12 14:23:47] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:23:47] [info] Tool Version: LVR_2025.1
[2025-10-12 14:23:47] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:23:47] [info] User Name: ubuntu
[2025-10-12 14:23:47] [info] Cores Used: 8
[2025-10-12 14:23:47] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Top Cell Information
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Layout Top Cell: TOP
[2025-10-12 14:23:47] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:23:47] [info] Database Format: GDSII
[2025-10-12 14:23:47] [info] Verification Mode: Full Check

[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Type of Errors | Count
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] DB_WARN -> 1
[2025-10-12 14:23:47] [info] LRF_ERR001 -> 14
[2025-10-12 14:23:47] [info] DB_WARN002 -> 159
[2025-10-12 14:23:47] [info] DB_WARN003 -> 161
[2025-10-12 14:23:47] [info] DRC_ERR9 -> 89
[2025-10-12 14:23:47] [info] DRC_ERR -> 67
[2025-10-12 14:23:47] [info] DRC_ERR5 -> 408
[2025-10-12 14:23:47] [info] DRC_ERR4 -> 241
[2025-10-12 14:23:47] [info] DRC_ERR1 -> 301
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Rule Violation Summary
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:23:47] [info] |-----|-----|-----|
[2025-10-12 14:23:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:23:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:23:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:23:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:23:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 14:23:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:23:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:23:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:23:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:23:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:23:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:23:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:23:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:23:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:23:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:23:47] [info]
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Performance Metrics
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] | Stage | Time Taken |
[2025-10-12 14:23:47] [info] |-----|-----|
[2025-10-12 14:23:47] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:23:47] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:23:47] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Summary
[2025-10-12 14:23:47] [info] -----
[2025-10-12 14:23:47] [info] Total Errors: 1120
[2025-10-12 14:23:47] [info] Total Warnings: 321
[2025-10-12 14:23:47] [info] DRC Status: FAILED
```

lvr_summary_233.rpt

```
[2025-10-12 14:24:45] [info] =====
[2025-10-12 14:24:45] [info] Design Rule Check Report
[2025-10-12 14:24:45] [info] =====

[2025-10-12 14:24:45] [info] Report Generated: 2025-10-12
[2025-10-12 14:24:45] [info] Run Start Time: 02:23:53 PM
[2025-10-12 14:24:45] [info] Run End Time: 02:24:45 PM
[2025-10-12 14:24:45] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 14:24:45] [info] Tool Version: LVR_2025.1
[2025-10-12 14:24:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:24:45] [info] User Name: ubuntu
[2025-10-12 14:24:45] [info] Cores Used: 8
[2025-10-12 14:24:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Top Cell Information
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Layout Top Cell: TOP
[2025-10-12 14:24:45] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:24:45] [info] Database Format: GDSII
[2025-10-12 14:24:45] [info] Verification Mode: Full Check

[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Type of Errors | Count
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] DB_WARN -> 1
[2025-10-12 14:24:45] [info] LRF_ERR001 -> 14
[2025-10-12 14:24:45] [info] DB_WARN002 -> 159
[2025-10-12 14:24:45] [info] DB_WARN003 -> 161
```

```

[2025-10-12 14:24:45] [info] DRC_ERR9 -> 89
[2025-10-12 14:24:45] [info] DRC_ERR -> 67
[2025-10-12 14:24:45] [info] DRC_ERR5 -> 408
[2025-10-12 14:24:45] [info] DRC_ERR4 -> 241
[2025-10-12 14:24:45] [info] DRC_ERR1 -> 301
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Rule Violation Summary
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:24:45] [info] |-----|-----|-----|
[2025-10-12 14:24:45] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:24:45] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:24:45] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:24:45] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:24:45] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:24:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:24:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:24:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:24:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:24:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:24:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:24:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:24:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:24:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:24:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Performance Metrics
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] | Stage | Time Taken |
[2025-10-12 14:24:45] [info] |-----|-----|
[2025-10-12 14:24:45] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:24:45] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:24:45] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Summary
[2025-10-12 14:24:45] [info] -----
[2025-10-12 14:24:45] [info] Total Errors: 1120
[2025-10-12 14:24:45] [info] Total Warnings: 321
[2025-10-12 14:24:45] [info] DRC Status: FAILED

```

lvr_summary_234.rpt

```

[2025-10-12 14:25:42] [info] =====
[2025-10-12 14:25:42] [info] Design Rule Check Report
[2025-10-12 14:25:42] [info] =====

[2025-10-12 14:25:42] [info] Report Generated: 2025-10-12
[2025-10-12 14:25:42] [info] Run Start Time: 02:24:50 PM
[2025-10-12 14:25:42] [info] Run End Time: 02:25:42 PM
[2025-10-12 14:25:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:25:42] [info] Tool Version: LVR_2025.1
[2025-10-12 14:25:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:25:42] [info] User Name: ubuntu
[2025-10-12 14:25:42] [info] Cores Used: 8
[2025-10-12 14:25:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Top Cell Information
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Layout Top Cell:      TOP
[2025-10-12 14:25:42] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:25:42] [info] Database Format:      GDSII
[2025-10-12 14:25:42] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Type of Errors | Count
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] DB_WARN -> 1
[2025-10-12 14:25:42] [info] LRF_ERR001 -> 14
[2025-10-12 14:25:42] [info] DB_WARN002 -> 159
[2025-10-12 14:25:42] [info] DB_WARN003 -> 161
[2025-10-12 14:25:42] [info] DRC_ERR9 -> 89
[2025-10-12 14:25:42] [info] DRC_ERR -> 67
[2025-10-12 14:25:42] [info] DRC_ERR5 -> 408
[2025-10-12 14:25:42] [info] DRC_ERR4 -> 241
[2025-10-12 14:25:42] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Rule Violation Summary
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:25:42] [info] |-----|-----|-----|
[2025-10-12 14:25:42] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:25:42] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:25:42] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:25:42] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:25:42] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:25:42] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:25:42] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:25:42] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:25:42] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:25:42] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:25:42] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:25:42] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:25:42] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:25:42] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:25:42] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:25:42] [info] |
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Performance Metrics
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] | Stage          | Time Taken |
[2025-10-12 14:25:42] [info] |-----|-----|
[2025-10-12 14:25:42] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:25:42] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 14:25:42] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Summary
```

```
[2025-10-12 14:25:42] [info] -----
[2025-10-12 14:25:42] [info] Total Errors:      1120
[2025-10-12 14:25:42] [info] Total Warnings:    321
[2025-10-12 14:25:42] [info] DRC Status:        FAILED
```


lvr_summary_235.rpt

```
[2025-10-12 14:26:38] [info] =====
[2025-10-12 14:26:38] [info]                      Design Rule Check Report
[2025-10-12 14:26:38] [info] =====

[2025-10-12 14:26:38] [info] Report Generated:      2025-10-12
[2025-10-12 14:26:38] [info] Run Start Time:       02:25:48 PM
[2025-10-12 14:26:38] [info] Run End Time:         02:26:38 PM
[2025-10-12 14:26:38] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 14:26:38] [info] Tool Version:          LVR_2025.1
[2025-10-12 14:26:38] [info] Host Name:             ip-172-31-37-173
[2025-10-12 14:26:38] [info] User Name:             ubuntu
[2025-10-12 14:26:38] [info] Cores Used:            8
[2025-10-12 14:26:38] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info]                      Top Cell Information
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info] Layout Top Cell:      TOP
[2025-10-12 14:26:38] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 14:26:38] [info] Database Format:        GDSII
[2025-10-12 14:26:38] [info] Verification Mode:     Full Check

[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info]                      Type of Errors | Count
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info] DB_WARN -> 1
[2025-10-12 14:26:38] [info] LRF_ERR001 -> 14
[2025-10-12 14:26:38] [info] DB_WARN002 -> 159
[2025-10-12 14:26:38] [info] DB_WARN003 -> 161
[2025-10-12 14:26:38] [info] DRC_ERR9 -> 89
[2025-10-12 14:26:38] [info] DRC_ERR -> 67
[2025-10-12 14:26:38] [info] DRC_ERR5 -> 408
[2025-10-12 14:26:38] [info] DRC_ERR4 -> 241
[2025-10-12 14:26:38] [info] DRC_ERR1 -> 301
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info]                      Rule Violation Summary
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:26:38] [info] |-----|-----|-----|
[2025-10-12 14:26:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:26:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:26:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:26:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:26:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:26:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:26:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:26:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:26:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:26:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:26:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:26:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:26:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:26:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:26:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:26:38] [info]
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info]                      Performance Metrics
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:26:38] [info] |-----|-----|
[2025-10-12 14:26:38] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 14:26:38] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 14:26:38] [info] | Report Generation    | 0 minutes 0 seconds  |

[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info]                Summary
[2025-10-12 14:26:38] [info] -----
[2025-10-12 14:26:38] [info] Total Errors:      1120
[2025-10-12 14:26:38] [info] Total Warnings:      321
[2025-10-12 14:26:38] [info] DRC Status:        FAILED
```

lvr_summary_236.rpt

```
[2025-10-12 14:27:38] [info] =====
[2025-10-12 14:27:38] [info]                Design Rule Check Report
[2025-10-12 14:27:38] [info] =====

[2025-10-12 14:27:38] [info] Report Generated:    2025-10-12
[2025-10-12 14:27:38] [info] Run Start Time:      02:26:47 PM
[2025-10-12 14:27:38] [info] Run End Time:        02:27:38 PM
[2025-10-12 14:27:38] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:27:38] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:27:38] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:27:38] [info] User Name:           ubuntu
[2025-10-12 14:27:38] [info] Cores Used:          8
[2025-10-12 14:27:38] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info]                Top Cell Information
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] Layout Top Cell:     TOP
[2025-10-12 14:27:38] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:27:38] [info] Database Format:       GDSII
[2025-10-12 14:27:38] [info] Verification Mode:    Full Check

[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info]                Type of Errors | Count
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] DB_WARN -> 1
[2025-10-12 14:27:38] [info] LRF_ERR001 -> 14
[2025-10-12 14:27:38] [info] DB_WARN002 -> 159
[2025-10-12 14:27:38] [info] DB_WARN003 -> 161
[2025-10-12 14:27:38] [info] DRC_ERR9 -> 89
[2025-10-12 14:27:38] [info] DRC_ERR -> 67
[2025-10-12 14:27:38] [info] DRC_ERR5 -> 408
[2025-10-12 14:27:38] [info] DRC_ERR4 -> 241
[2025-10-12 14:27:38] [info] DRC_ERR1 -> 301
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info]                Rule Violation Summary
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:27:38] [info] |-----|-----|-----|
[2025-10-12 14:27:38] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:27:38] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:27:38] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:27:38] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:27:38] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:27:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:27:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:27:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:27:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:27:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:27:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:27:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:27:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:27:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:27:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:27:38] [info]
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] Performance Metrics
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] | Stage | Time Taken |
[2025-10-12 14:27:38] [info] |-----|-----|
[2025-10-12 14:27:38] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:27:38] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:27:38] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] Summary
[2025-10-12 14:27:38] [info] -----
[2025-10-12 14:27:38] [info] Total Errors: 1120
[2025-10-12 14:27:38] [info] Total Warnings: 321
[2025-10-12 14:27:38] [info] DRC Status: FAILED

```

lvr_summary_237.rpt

```

[2025-10-12 14:28:35] [info] =====
[2025-10-12 14:28:35] [info] Design Rule Check Report
[2025-10-12 14:28:35] [info] =====

[2025-10-12 14:28:35] [info] Report Generated: 2025-10-12
[2025-10-12 14:28:35] [info] Run Start Time: 02:27:44 PM
[2025-10-12 14:28:35] [info] Run End Time: 02:28:35 PM
[2025-10-12 14:28:35] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:28:35] [info] Tool Version: LVR_2025.1
[2025-10-12 14:28:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:28:35] [info] User Name: ubuntu
[2025-10-12 14:28:35] [info] Cores Used: 8
[2025-10-12 14:28:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Top Cell Information
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Layout Top Cell: TOP
[2025-10-12 14:28:35] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:28:35] [info] Database Format: GDSII
[2025-10-12 14:28:35] [info] Verification Mode: Full Check

[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Type of Errors | Count
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] DB_WARN -> 1
[2025-10-12 14:28:35] [info] LRF_ERR001 -> 14
[2025-10-12 14:28:35] [info] DB_WARN002 -> 159
[2025-10-12 14:28:35] [info] DB_WARN003 -> 161

```

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[2025-10-12 14:28:35] [info] DRC_ERR9 -> 89
[2025-10-12 14:28:35] [info] DRC_ERR -> 67
[2025-10-12 14:28:35] [info] DRC_ERR5 -> 408
[2025-10-12 14:28:35] [info] DRC_ERR4 -> 241
[2025-10-12 14:28:35] [info] DRC_ERR1 -> 301
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Rule Violation Summary
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:28:35] [info] |-----|-----|-----|
[2025-10-12 14:28:35] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:28:35] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:28:35] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:28:35] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:28:35] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:28:35] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:28:35] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:28:35] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:28:35] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:28:35] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:28:35] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:28:35] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:28:35] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:28:35] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:28:35] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Performance Metrics
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] | Stage | Time Taken |
[2025-10-12 14:28:35] [info] |-----|-----|
[2025-10-12 14:28:35] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:28:35] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:28:35] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Summary
[2025-10-12 14:28:35] [info] -----
[2025-10-12 14:28:35] [info] Total Errors: 1120
[2025-10-12 14:28:35] [info] Total Warnings: 321
[2025-10-12 14:28:35] [info] DRC Status: FAILED

```

lvr_summary_238.rpt

```

[2025-10-12 14:29:32] [info] =====
[2025-10-12 14:29:32] [info] Design Rule Check Report
[2025-10-12 14:29:32] [info] =====

[2025-10-12 14:29:32] [info] Report Generated: 2025-10-12
[2025-10-12 14:29:32] [info] Run Start Time: 02:28:41 PM
[2025-10-12 14:29:32] [info] Run End Time: 02:29:32 PM
[2025-10-12 14:29:32] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:29:32] [info] Tool Version: LVR_2025.1
[2025-10-12 14:29:32] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:29:32] [info] User Name: ubuntu
[2025-10-12 14:29:32] [info] Cores Used: 8
[2025-10-12 14:29:32] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] Top Cell Information
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] Layout Top Cell:      TOP
[2025-10-12 14:29:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:29:32] [info] Database Format:      GDSII
[2025-10-12 14:29:32] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] Type of Errors | Count
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] DB_WARN -> 1
[2025-10-12 14:29:32] [info] LRF_ERR001 -> 14
[2025-10-12 14:29:32] [info] DB_WARN002 -> 159
[2025-10-12 14:29:32] [info] DB_WARN003 -> 161
[2025-10-12 14:29:32] [info] DRC_ERR9 -> 89
[2025-10-12 14:29:32] [info] DRC_ERR -> 67
[2025-10-12 14:29:32] [info] DRC_ERR5 -> 408
[2025-10-12 14:29:32] [info] DRC_ERR4 -> 241
[2025-10-12 14:29:32] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] Rule Violation Summary
```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:29:32] [info] |-----|-----|-----|
[2025-10-12 14:29:32] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:29:32] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:29:32] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:29:32] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:29:32] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:29:32] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:29:32] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:29:32] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:29:32] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:29:32] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:29:32] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:29:32] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:29:32] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:29:32] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:29:32] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:29:32] [info] |-----|-----|-----|
```

```
[2025-10-12 14:29:32] [info] Performance Metrics
```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] | Stage          | Time Taken |
[2025-10-12 14:29:32] [info] |-----|-----|
[2025-10-12 14:29:32] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:29:32] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 14:29:32] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] Summary
```

```
[2025-10-12 14:29:32] [info] -----
[2025-10-12 14:29:32] [info] Total Errors:      1120
[2025-10-12 14:29:32] [info] Total Warnings:    321
[2025-10-12 14:29:32] [info] DRC Status:        FAILED
```

lvr_summary_239.rpt

```
[2025-10-12 14:30:28] [info] =====
[2025-10-12 14:30:28] [info]                Design Rule Check Report
[2025-10-12 14:30:28] [info] =====

[2025-10-12 14:30:28] [info] Report Generated:    2025-10-12
[2025-10-12 14:30:28] [info] Run Start Time:      02:29:37 PM
[2025-10-12 14:30:28] [info] Run End Time:        02:30:28 PM
[2025-10-12 14:30:28] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 14:30:28] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:30:28] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:30:28] [info] User Name:           ubuntu
[2025-10-12 14:30:28] [info] Cores Used:          8
[2025-10-12 14:30:28] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info]                Top Cell Information
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info] Layout Top Cell:     TOP
[2025-10-12 14:30:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:30:28] [info] Database Format:      GDSII
[2025-10-12 14:30:28] [info] Verification Mode:    Full Check

[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info]                Type of Errors | Count
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info] DB_WARN -> 1
[2025-10-12 14:30:28] [info] LRF_ERR001 -> 14
[2025-10-12 14:30:28] [info] DB_WARN002 -> 159
[2025-10-12 14:30:28] [info] DB_WARN003 -> 161
[2025-10-12 14:30:28] [info] DRC_ERR9 -> 89
[2025-10-12 14:30:28] [info] DRC_ERR -> 67
[2025-10-12 14:30:28] [info] DRC_ERR5 -> 408
[2025-10-12 14:30:28] [info] DRC_ERR4 -> 241
[2025-10-12 14:30:28] [info] DRC_ERR1 -> 301
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info]                Rule Violation Summary
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:30:28] [info] |-----|-----|-----|
[2025-10-12 14:30:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:30:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:30:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:30:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:30:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:30:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:30:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:30:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:30:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:30:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:30:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:30:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:30:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:30:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:30:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:30:28] [info]
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info]                Performance Metrics
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:30:28] [info] |-----|-----|
[2025-10-12 14:30:28] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:30:28] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 14:30:28] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info] Summary
[2025-10-12 14:30:28] [info] -----
[2025-10-12 14:30:28] [info] Total Errors: 1120
[2025-10-12 14:30:28] [info] Total Warnings: 321
[2025-10-12 14:30:28] [info] DRC Status: FAILED
```

lvr_summary_24.rpt

```
[2025-10-12 11:04:08] [info] =====
[2025-10-12 11:04:08] [info] Design Rule Check Report
[2025-10-12 11:04:08] [info] =====

[2025-10-12 11:04:08] [info] Report Generated: 2025-10-12
[2025-10-12 11:04:08] [info] Run Start Time: 11:03:18 AM
[2025-10-12 11:04:08] [info] Run End Time: 11:04:08 AM
[2025-10-12 11:04:08] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:04:08] [info] Tool Version: LVR_2025.1
[2025-10-12 11:04:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:04:08] [info] User Name: ubuntu
[2025-10-12 11:04:08] [info] Cores Used: 8
[2025-10-12 11:04:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Top Cell Information
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Layout Top Cell: TOP
[2025-10-12 11:04:08] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:04:08] [info] Database Format: GDSII
[2025-10-12 11:04:08] [info] Verification Mode: Full Check

[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Type of Errors | Count
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] DB_WARN -> 1
[2025-10-12 11:04:08] [info] LRF_ERR001 -> 14
[2025-10-12 11:04:08] [info] DB_WARN002 -> 159
[2025-10-12 11:04:08] [info] DB_WARN003 -> 161
[2025-10-12 11:04:08] [info] DRC_ERR9 -> 89
[2025-10-12 11:04:08] [info] DRC_ERR -> 67
[2025-10-12 11:04:08] [info] DRC_ERR5 -> 408
[2025-10-12 11:04:08] [info] DRC_ERR4 -> 241
[2025-10-12 11:04:08] [info] DRC_ERR1 -> 301
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Rule Violation Summary
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:04:08] [info] |-----|-----|-----|
[2025-10-12 11:04:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:04:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:04:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:04:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:04:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 11:04:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 11:04:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:04:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:04:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:04:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:04:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:04:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:04:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:04:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:04:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:04:08] [info]
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Performance Metrics
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] | Stage | Time Taken |
[2025-10-12 11:04:08] [info] |-----|-----|
[2025-10-12 11:04:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:04:08] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 11:04:08] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Summary
[2025-10-12 11:04:08] [info] -----
[2025-10-12 11:04:08] [info] Total Errors: 1120
[2025-10-12 11:04:08] [info] Total Warnings: 321
[2025-10-12 11:04:08] [info] DRC Status: FAILED

```

lvr_summary_240.rpt

```

[2025-10-12 14:31:26] [info] =====
[2025-10-12 14:31:26] [info] Design Rule Check Report
[2025-10-12 14:31:26] [info] =====

[2025-10-12 14:31:26] [info] Report Generated: 2025-10-12
[2025-10-12 14:31:26] [info] Run Start Time: 02:30:35 PM
[2025-10-12 14:31:26] [info] Run End Time: 02:31:26 PM
[2025-10-12 14:31:26] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:31:26] [info] Tool Version: LVR_2025.1
[2025-10-12 14:31:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:31:26] [info] User Name: ubuntu
[2025-10-12 14:31:26] [info] Cores Used: 8
[2025-10-12 14:31:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Top Cell Information
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Layout Top Cell: TOP
[2025-10-12 14:31:26] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:31:26] [info] Database Format: GDSII
[2025-10-12 14:31:26] [info] Verification Mode: Full Check

[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Type of Errors | Count
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] DB_WARN -> 1
[2025-10-12 14:31:26] [info] LRF_ERR001 -> 14
[2025-10-12 14:31:26] [info] DB_WARN002 -> 159
[2025-10-12 14:31:26] [info] DB_WARN003 -> 161

```



```

[2025-10-12 14:31:26] [info] DRC_ERR9 -> 89
[2025-10-12 14:31:26] [info] DRC_ERR -> 67
[2025-10-12 14:31:26] [info] DRC_ERR5 -> 408
[2025-10-12 14:31:26] [info] DRC_ERR4 -> 241
[2025-10-12 14:31:26] [info] DRC_ERR1 -> 301
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Rule Violation Summary
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:31:26] [info] |-----|-----|-----|
[2025-10-12 14:31:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:31:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:31:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:31:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:31:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:31:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:31:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:31:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:31:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:31:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:31:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:31:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:31:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:31:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:31:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Performance Metrics
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] | Stage | Time Taken |
[2025-10-12 14:31:26] [info] |-----|-----|
[2025-10-12 14:31:26] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:31:26] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:31:26] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Summary
[2025-10-12 14:31:26] [info] -----
[2025-10-12 14:31:26] [info] Total Errors: 1120
[2025-10-12 14:31:26] [info] Total Warnings: 321
[2025-10-12 14:31:26] [info] DRC Status: FAILED

```

lvr_summary_241.rpt

```

[2025-10-12 14:32:24] [info] =====
[2025-10-12 14:32:24] [info] Design Rule Check Report
[2025-10-12 14:32:24] [info] =====

[2025-10-12 14:32:24] [info] Report Generated: 2025-10-12
[2025-10-12 14:32:24] [info] Run Start Time: 02:31:32 PM
[2025-10-12 14:32:24] [info] Run End Time: 02:32:24 PM
[2025-10-12 14:32:24] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 14:32:24] [info] Tool Version: LVR_2025.1
[2025-10-12 14:32:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:32:24] [info] User Name: ubuntu
[2025-10-12 14:32:24] [info] Cores Used: 8
[2025-10-12 14:32:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Top Cell Information
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Layout Top Cell:      TOP
[2025-10-12 14:32:24] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:32:24] [info] Database Format:      GDSII
[2025-10-12 14:32:24] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Type of Errors | Count
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] DB_WARN -> 1
[2025-10-12 14:32:24] [info] LRF_ERR001 -> 14
[2025-10-12 14:32:24] [info] DB_WARN002 -> 159
[2025-10-12 14:32:24] [info] DB_WARN003 -> 161
[2025-10-12 14:32:24] [info] DRC_ERR9 -> 89
[2025-10-12 14:32:24] [info] DRC_ERR -> 67
[2025-10-12 14:32:24] [info] DRC_ERR5 -> 408
[2025-10-12 14:32:24] [info] DRC_ERR4 -> 241
[2025-10-12 14:32:24] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Rule Violation Summary
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:32:24] [info] |-----|-----|-----|
[2025-10-12 14:32:24] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:32:24] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:32:24] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:32:24] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:32:24] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:32:24] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:32:24] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:32:24] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:32:24] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:32:24] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:32:24] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:32:24] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:32:24] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:32:24] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:32:24] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:32:24] [info] |
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Performance Metrics
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] | Stage          | Time Taken |
[2025-10-12 14:32:24] [info] |-----|-----|
[2025-10-12 14:32:24] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:32:24] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 14:32:24] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Summary
```

```
[2025-10-12 14:32:24] [info] -----
[2025-10-12 14:32:24] [info] Total Errors:      1120
[2025-10-12 14:32:24] [info] Total Warnings:    321
[2025-10-12 14:32:24] [info] DRC Status:        FAILED
```

lvr_summary_242.rpt

```
[2025-10-12 14:33:21] [info] =====
[2025-10-12 14:33:21] [info]                      Design Rule Check Report
[2025-10-12 14:33:21] [info] =====

[2025-10-12 14:33:21] [info] Report Generated:      2025-10-12
[2025-10-12 14:33:21] [info] Run Start Time:       02:32:29 PM
[2025-10-12 14:33:21] [info] Run End Time:         02:33:21 PM
[2025-10-12 14:33:21] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 14:33:21] [info] Tool Version:         LVR_2025.1
[2025-10-12 14:33:21] [info] Host Name:            ip-172-31-37-173
[2025-10-12 14:33:21] [info] User Name:            ubuntu
[2025-10-12 14:33:21] [info] Cores Used:           8
[2025-10-12 14:33:21] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info]                      Top Cell Information
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info] Layout Top Cell:      TOP
[2025-10-12 14:33:21] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 14:33:21] [info] Database Format:        GDSII
[2025-10-12 14:33:21] [info] Verification Mode:     Full Check

[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info]                      Type of Errors | Count
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info] DB_WARN -> 1
[2025-10-12 14:33:21] [info] LRF_ERR001 -> 14
[2025-10-12 14:33:21] [info] DB_WARN002 -> 159
[2025-10-12 14:33:21] [info] DB_WARN003 -> 161
[2025-10-12 14:33:21] [info] DRC_ERR9 -> 89
[2025-10-12 14:33:21] [info] DRC_ERR -> 67
[2025-10-12 14:33:21] [info] DRC_ERR5 -> 408
[2025-10-12 14:33:21] [info] DRC_ERR4 -> 241
[2025-10-12 14:33:21] [info] DRC_ERR1 -> 301
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info]                      Rule Violation Summary
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:33:21] [info] |-----|-----|-----|
[2025-10-12 14:33:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:33:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:33:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:33:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:33:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:33:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:33:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:33:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:33:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:33:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:33:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:33:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:33:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:33:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:33:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:33:21] [info]
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info]                      Performance Metrics
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:33:21] [info] |-----|-----|
[2025-10-12 14:33:21] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:33:21] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 14:33:21] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info] Summary
[2025-10-12 14:33:21] [info] -----
[2025-10-12 14:33:21] [info] Total Errors: 1120
[2025-10-12 14:33:21] [info] Total Warnings: 321
[2025-10-12 14:33:21] [info] DRC Status: FAILED
```

lvr_summary_243.rpt

```
[2025-10-12 14:34:16] [info] =====
[2025-10-12 14:34:16] [info] Design Rule Check Report
[2025-10-12 14:34:16] [info] =====

[2025-10-12 14:34:16] [info] Report Generated: 2025-10-12
[2025-10-12 14:34:16] [info] Run Start Time: 02:33:25 PM
[2025-10-12 14:34:16] [info] Run End Time: 02:34:16 PM
[2025-10-12 14:34:16] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:34:16] [info] Tool Version: LVR_2025.1
[2025-10-12 14:34:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:34:16] [info] User Name: ubuntu
[2025-10-12 14:34:16] [info] Cores Used: 8
[2025-10-12 14:34:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Top Cell Information
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Layout Top Cell: TOP
[2025-10-12 14:34:16] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:34:16] [info] Database Format: GDSII
[2025-10-12 14:34:16] [info] Verification Mode: Full Check

[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Type of Errors | Count
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] DB_WARN -> 1
[2025-10-12 14:34:16] [info] LRF_ERR001 -> 14
[2025-10-12 14:34:16] [info] DB_WARN002 -> 159
[2025-10-12 14:34:16] [info] DB_WARN003 -> 161
[2025-10-12 14:34:16] [info] DRC_ERR9 -> 89
[2025-10-12 14:34:16] [info] DRC_ERR -> 67
[2025-10-12 14:34:16] [info] DRC_ERR5 -> 408
[2025-10-12 14:34:16] [info] DRC_ERR4 -> 241
[2025-10-12 14:34:16] [info] DRC_ERR1 -> 301
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Rule Violation Summary
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:34:16] [info] |-----|-----|-----|
[2025-10-12 14:34:16] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:34:16] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:34:16] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:34:16] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:34:16] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 14:34:16] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:34:16] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:34:16] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:34:16] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:34:16] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:34:16] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:34:16] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:34:16] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:34:16] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:34:16] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:34:16] [info]
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Performance Metrics
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] | Stage | Time Taken |
[2025-10-12 14:34:16] [info] |-----|-----|
[2025-10-12 14:34:16] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:34:16] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:34:16] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Summary
[2025-10-12 14:34:16] [info] -----
[2025-10-12 14:34:16] [info] Total Errors: 1120
[2025-10-12 14:34:16] [info] Total Warnings: 321
[2025-10-12 14:34:16] [info] DRC Status: FAILED

```

lvr_summary_244.rpt

```

[2025-10-12 14:35:14] [info] =====
[2025-10-12 14:35:14] [info] Design Rule Check Report
[2025-10-12 14:35:14] [info] =====

[2025-10-12 14:35:14] [info] Report Generated: 2025-10-12
[2025-10-12 14:35:14] [info] Run Start Time: 02:34:23 PM
[2025-10-12 14:35:14] [info] Run End Time: 02:35:14 PM
[2025-10-12 14:35:14] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:35:14] [info] Tool Version: LVR_2025.1
[2025-10-12 14:35:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:35:14] [info] User Name: ubuntu
[2025-10-12 14:35:14] [info] Cores Used: 8
[2025-10-12 14:35:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Top Cell Information
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Layout Top Cell: TOP
[2025-10-12 14:35:14] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:35:14] [info] Database Format: GDSII
[2025-10-12 14:35:14] [info] Verification Mode: Full Check

[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Type of Errors | Count
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] DB_WARN -> 1
[2025-10-12 14:35:14] [info] LRF_ERR001 -> 14
[2025-10-12 14:35:14] [info] DB_WARN002 -> 159
[2025-10-12 14:35:14] [info] DB_WARN003 -> 161

```

```

[2025-10-12 14:35:14] [info] DRC_ERR9 -> 89
[2025-10-12 14:35:14] [info] DRC_ERR -> 67
[2025-10-12 14:35:14] [info] DRC_ERR5 -> 408
[2025-10-12 14:35:14] [info] DRC_ERR4 -> 241
[2025-10-12 14:35:14] [info] DRC_ERR1 -> 301
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Rule Violation Summary
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:35:14] [info] |-----|-----|-----|
[2025-10-12 14:35:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:35:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:35:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:35:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:35:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:35:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:35:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:35:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:35:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:35:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:35:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:35:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:35:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:35:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:35:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Performance Metrics
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] | Stage | Time Taken |
[2025-10-12 14:35:14] [info] |-----|-----|
[2025-10-12 14:35:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:35:14] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:35:14] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Summary
[2025-10-12 14:35:14] [info] -----
[2025-10-12 14:35:14] [info] Total Errors: 1120
[2025-10-12 14:35:14] [info] Total Warnings: 321
[2025-10-12 14:35:14] [info] DRC Status: FAILED

```

lvr_summary_245.rpt

```

[2025-10-12 14:36:12] [info] =====
[2025-10-12 14:36:12] [info] Design Rule Check Report
[2025-10-12 14:36:12] [info] =====

[2025-10-12 14:36:12] [info] Report Generated: 2025-10-12
[2025-10-12 14:36:12] [info] Run Start Time: 02:35:19 PM
[2025-10-12 14:36:12] [info] Run End Time: 02:36:12 PM
[2025-10-12 14:36:12] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 14:36:12] [info] Tool Version: LVR_2025.1
[2025-10-12 14:36:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:36:12] [info] User Name: ubuntu
[2025-10-12 14:36:12] [info] Cores Used: 8
[2025-10-12 14:36:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Top Cell Information
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Layout Top Cell:      TOP
[2025-10-12 14:36:12] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:36:12] [info] Database Format:      GDSII
[2025-10-12 14:36:12] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Type of Errors | Count
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] DB_WARN -> 1
[2025-10-12 14:36:12] [info] LRF_ERR001 -> 14
[2025-10-12 14:36:12] [info] DB_WARN002 -> 159
[2025-10-12 14:36:12] [info] DB_WARN003 -> 161
[2025-10-12 14:36:12] [info] DRC_ERR9 -> 89
[2025-10-12 14:36:12] [info] DRC_ERR -> 67
[2025-10-12 14:36:12] [info] DRC_ERR5 -> 408
[2025-10-12 14:36:12] [info] DRC_ERR4 -> 241
[2025-10-12 14:36:12] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Rule Violation Summary
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:36:12] [info] |-----|-----|-----|
[2025-10-12 14:36:12] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:36:12] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:36:12] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:36:12] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:36:12] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:36:12] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:36:12] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:36:12] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:36:12] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:36:12] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:36:12] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:36:12] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:36:12] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:36:12] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:36:12] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Performance Metrics
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] | Stage          | Time Taken |
[2025-10-12 14:36:12] [info] |-----|-----|
[2025-10-12 14:36:12] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:36:12] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 14:36:12] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Summary
[2025-10-12 14:36:12] [info] -----
[2025-10-12 14:36:12] [info] Total Errors:      1120
[2025-10-12 14:36:12] [info] Total Warnings:    321
[2025-10-12 14:36:12] [info] DRC Status:        FAILED
```

lvr_summary_246.rpt

```
[2025-10-12 14:37:07] [info] =====
[2025-10-12 14:37:07] [info]                      Design Rule Check Report
[2025-10-12 14:37:07] [info] =====

[2025-10-12 14:37:07] [info] Report Generated:    2025-10-12
[2025-10-12 14:37:07] [info] Run Start Time:      02:36:16 PM
[2025-10-12 14:37:07] [info] Run End Time:        02:37:07 PM
[2025-10-12 14:37:07] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 14:37:07] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:37:07] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:37:07] [info] User Name:           ubuntu
[2025-10-12 14:37:07] [info] Cores Used:          8
[2025-10-12 14:37:07] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info]                      Top Cell Information
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info] Layout Top Cell:     TOP
[2025-10-12 14:37:07] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:37:07] [info] Database Format:       GDSII
[2025-10-12 14:37:07] [info] Verification Mode:    Full Check

[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info]                      Type of Errors | Count
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info] DB_WARN -> 1
[2025-10-12 14:37:07] [info] LRF_ERR001 -> 14
[2025-10-12 14:37:07] [info] DB_WARN002 -> 159
[2025-10-12 14:37:07] [info] DB_WARN003 -> 161
[2025-10-12 14:37:07] [info] DRC_ERR9 -> 89
[2025-10-12 14:37:07] [info] DRC_ERR -> 67
[2025-10-12 14:37:07] [info] DRC_ERR5 -> 408
[2025-10-12 14:37:07] [info] DRC_ERR4 -> 241
[2025-10-12 14:37:07] [info] DRC_ERR1 -> 301
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info]                      Rule Violation Summary
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:37:07] [info] |-----|-----|-----|
[2025-10-12 14:37:07] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:37:07] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:37:07] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:37:07] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:37:07] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:37:07] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:37:07] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:37:07] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:37:07] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:37:07] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:37:07] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:37:07] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:37:07] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:37:07] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:37:07] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:37:07] [info]
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info]                      Performance Metrics
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info] | Stage | Time Taken |
```



```
[2025-10-12 14:37:07] [info] |-----|-----|
[2025-10-12 14:37:07] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 14:37:07] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 14:37:07] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info]              Summary
[2025-10-12 14:37:07] [info] -----
[2025-10-12 14:37:07] [info] Total Errors:    1120
[2025-10-12 14:37:07] [info] Total Warnings:      321
[2025-10-12 14:37:07] [info] DRC Status:      FAILED
```

lvr_summary_247.rpt

```
[2025-10-12 14:38:05] [info] =====
[2025-10-12 14:38:05] [info]              Design Rule Check Report
[2025-10-12 14:38:05] [info] =====

[2025-10-12 14:38:05] [info] Report Generated:    2025-10-12
[2025-10-12 14:38:05] [info] Run Start Time:      02:37:13 PM
[2025-10-12 14:38:05] [info] Run End Time:        02:38:05 PM
[2025-10-12 14:38:05] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:38:05] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:38:05] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:38:05] [info] User Name:           ubuntu
[2025-10-12 14:38:05] [info] Cores Used:          8
[2025-10-12 14:38:05] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info]              Top Cell Information
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] Layout Top Cell:     TOP
[2025-10-12 14:38:05] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:38:05] [info] Database Format:       GDSII
[2025-10-12 14:38:05] [info] Verification Mode:    Full Check

[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info]              Type of Errors | Count
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] DB_WARN -> 1
[2025-10-12 14:38:05] [info] LRF_ERR001 -> 14
[2025-10-12 14:38:05] [info] DB_WARN002 -> 159
[2025-10-12 14:38:05] [info] DB_WARN003 -> 161
[2025-10-12 14:38:05] [info] DRC_ERR9 -> 89
[2025-10-12 14:38:05] [info] DRC_ERR -> 67
[2025-10-12 14:38:05] [info] DRC_ERR5 -> 408
[2025-10-12 14:38:05] [info] DRC_ERR4 -> 241
[2025-10-12 14:38:05] [info] DRC_ERR1 -> 301
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info]              Rule Violation Summary
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:38:05] [info] |-----|-----|-----|
[2025-10-12 14:38:05] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:38:05] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:38:05] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:38:05] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:38:05] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:38:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:38:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:38:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:38:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:38:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:38:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:38:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:38:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:38:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:38:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:38:05] [info]
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] Performance Metrics
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] | Stage | Time Taken |
[2025-10-12 14:38:05] [info] |-----|-----|
[2025-10-12 14:38:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:38:05] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:38:05] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] Summary
[2025-10-12 14:38:05] [info] -----
[2025-10-12 14:38:05] [info] Total Errors: 1120
[2025-10-12 14:38:05] [info] Total Warnings: 321
[2025-10-12 14:38:05] [info] DRC Status: FAILED

```

lvr_summary_248.rpt

```

[2025-10-12 14:39:00] [info] =====
[2025-10-12 14:39:00] [info] Design Rule Check Report
[2025-10-12 14:39:00] [info] =====

[2025-10-12 14:39:00] [info] Report Generated: 2025-10-12
[2025-10-12 14:39:00] [info] Run Start Time: 02:38:10 PM
[2025-10-12 14:39:00] [info] Run End Time: 02:39:00 PM
[2025-10-12 14:39:00] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:39:00] [info] Tool Version: LVR_2025.1
[2025-10-12 14:39:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:39:00] [info] User Name: ubuntu
[2025-10-12 14:39:00] [info] Cores Used: 8
[2025-10-12 14:39:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Top Cell Information
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Layout Top Cell: TOP
[2025-10-12 14:39:00] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:39:00] [info] Database Format: GDSII
[2025-10-12 14:39:00] [info] Verification Mode: Full Check

[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Type of Errors | Count
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] DB_WARN -> 1
[2025-10-12 14:39:00] [info] LRF_ERR001 -> 14
[2025-10-12 14:39:00] [info] DB_WARN002 -> 159
[2025-10-12 14:39:00] [info] DB_WARN003 -> 161

```

```

[2025-10-12 14:39:00] [info] DRC_ERR9 -> 89
[2025-10-12 14:39:00] [info] DRC_ERR -> 67
[2025-10-12 14:39:00] [info] DRC_ERR5 -> 408
[2025-10-12 14:39:00] [info] DRC_ERR4 -> 241
[2025-10-12 14:39:00] [info] DRC_ERR1 -> 301
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Rule Violation Summary
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:39:00] [info] |-----|-----|-----|
[2025-10-12 14:39:00] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:39:00] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:39:00] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:39:00] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:39:00] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:39:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:39:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:39:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:39:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:39:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:39:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:39:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:39:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:39:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:39:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Performance Metrics
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] | Stage | Time Taken |
[2025-10-12 14:39:00] [info] |-----|-----|
[2025-10-12 14:39:00] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:39:00] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:39:00] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Summary
[2025-10-12 14:39:00] [info] -----
[2025-10-12 14:39:00] [info] Total Errors: 1120
[2025-10-12 14:39:00] [info] Total Warnings: 321
[2025-10-12 14:39:00] [info] DRC Status: FAILED

```

lvr_summary_249.rpt

```

[2025-10-12 14:39:57] [info] =====
[2025-10-12 14:39:57] [info] Design Rule Check Report
[2025-10-12 14:39:57] [info] =====

[2025-10-12 14:39:57] [info] Report Generated: 2025-10-12
[2025-10-12 14:39:57] [info] Run Start Time: 02:39:06 PM
[2025-10-12 14:39:57] [info] Run End Time: 02:39:57 PM
[2025-10-12 14:39:57] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:39:57] [info] Tool Version: LVR_2025.1
[2025-10-12 14:39:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:39:57] [info] User Name: ubuntu
[2025-10-12 14:39:57] [info] Cores Used: 8
[2025-10-12 14:39:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Top Cell Information
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Layout Top Cell:      TOP
[2025-10-12 14:39:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:39:57] [info] Database Format:      GDSII
[2025-10-12 14:39:57] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Type of Errors | Count
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] DB_WARN -> 1
[2025-10-12 14:39:57] [info] LRF_ERR001 -> 14
[2025-10-12 14:39:57] [info] DB_WARN002 -> 159
[2025-10-12 14:39:57] [info] DB_WARN003 -> 161
[2025-10-12 14:39:57] [info] DRC_ERR9 -> 89
[2025-10-12 14:39:57] [info] DRC_ERR -> 67
[2025-10-12 14:39:57] [info] DRC_ERR5 -> 408
[2025-10-12 14:39:57] [info] DRC_ERR4 -> 241
[2025-10-12 14:39:57] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Rule Violation Summary
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:39:57] [info] |-----|-----|-----|
[2025-10-12 14:39:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:39:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:39:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:39:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:39:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:39:57] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:39:57] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:39:57] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:39:57] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:39:57] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:39:57] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:39:57] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:39:57] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:39:57] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:39:57] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Performance Metrics
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] | Stage          | Time Taken |
[2025-10-12 14:39:57] [info] |-----|-----|
[2025-10-12 14:39:57] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:39:57] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 14:39:57] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Summary
[2025-10-12 14:39:57] [info] -----
[2025-10-12 14:39:57] [info] Total Errors:      1120
[2025-10-12 14:39:57] [info] Total Warnings:    321
[2025-10-12 14:39:57] [info] DRC Status:        FAILED
```

lvr_summary_25.rpt

```
[2025-10-12 11:05:05] [info] =====
[2025-10-12 11:05:05] [info]                      Design Rule Check Report
[2025-10-12 11:05:05] [info] =====

[2025-10-12 11:05:05] [info] Report Generated:      2025-10-12
[2025-10-12 11:05:05] [info] Run Start Time:       11:04:14 AM
[2025-10-12 11:05:05] [info] Run End Time:         11:05:05 AM
[2025-10-12 11:05:05] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 11:05:05] [info] Tool Version:         LVR_2025.1
[2025-10-12 11:05:05] [info] Host Name:            ip-172-31-37-173
[2025-10-12 11:05:05] [info] User Name:            ubuntu
[2025-10-12 11:05:05] [info] Cores Used:           8
[2025-10-12 11:05:05] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info]                      Top Cell Information
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info] Layout Top Cell:      TOP
[2025-10-12 11:05:05] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 11:05:05] [info] Database Format:        GDSII
[2025-10-12 11:05:05] [info] Verification Mode:     Full Check

[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info]                      Type of Errors | Count
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info] DB_WARN -> 1
[2025-10-12 11:05:05] [info] LRF_ERR001 -> 14
[2025-10-12 11:05:05] [info] DB_WARN002 -> 159
[2025-10-12 11:05:05] [info] DB_WARN003 -> 161
[2025-10-12 11:05:05] [info] DRC_ERR9 -> 89
[2025-10-12 11:05:05] [info] DRC_ERR -> 67
[2025-10-12 11:05:05] [info] DRC_ERR5 -> 408
[2025-10-12 11:05:05] [info] DRC_ERR4 -> 241
[2025-10-12 11:05:05] [info] DRC_ERR1 -> 301
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info]                      Rule Violation Summary
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:05:05] [info] |-----|-----|-----|
[2025-10-12 11:05:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:05:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:05:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:05:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:05:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:05:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:05:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:05:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:05:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:05:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:05:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:05:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:05:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:05:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:05:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:05:05] [info]
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info]                      Performance Metrics
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:05:05] [info] |-----|-----|
[2025-10-12 11:05:05] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:05:05] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:05:05] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info] Summary
[2025-10-12 11:05:05] [info] -----
[2025-10-12 11:05:05] [info] Total Errors: 1120
[2025-10-12 11:05:05] [info] Total Warnings: 321
[2025-10-12 11:05:05] [info] DRC Status: FAILED
```

lvr_summary_250.rpt

```
[2025-10-12 14:40:54] [info] =====
[2025-10-12 14:40:54] [info] Design Rule Check Report
[2025-10-12 14:40:54] [info] =====

[2025-10-12 14:40:54] [info] Report Generated: 2025-10-12
[2025-10-12 14:40:54] [info] Run Start Time: 02:40:03 PM
[2025-10-12 14:40:54] [info] Run End Time: 02:40:54 PM
[2025-10-12 14:40:54] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:40:54] [info] Tool Version: LVR_2025.1
[2025-10-12 14:40:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:40:54] [info] User Name: ubuntu
[2025-10-12 14:40:54] [info] Cores Used: 8
[2025-10-12 14:40:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Top Cell Information
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Layout Top Cell: TOP
[2025-10-12 14:40:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:40:54] [info] Database Format: GDSII
[2025-10-12 14:40:54] [info] Verification Mode: Full Check

[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Type of Errors | Count
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] DB_WARN -> 1
[2025-10-12 14:40:54] [info] LRF_ERR001 -> 14
[2025-10-12 14:40:54] [info] DB_WARN002 -> 159
[2025-10-12 14:40:54] [info] DB_WARN003 -> 161
[2025-10-12 14:40:54] [info] DRC_ERR9 -> 89
[2025-10-12 14:40:54] [info] DRC_ERR -> 67
[2025-10-12 14:40:54] [info] DRC_ERR5 -> 408
[2025-10-12 14:40:54] [info] DRC_ERR4 -> 241
[2025-10-12 14:40:54] [info] DRC_ERR1 -> 301
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Rule Violation Summary
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:40:54] [info] |-----|-----|-----|
[2025-10-12 14:40:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:40:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:40:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:40:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:40:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 14:40:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:40:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:40:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:40:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:40:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:40:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:40:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:40:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:40:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:40:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:40:54] [info]
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Performance Metrics
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] | Stage | Time Taken |
[2025-10-12 14:40:54] [info] |-----|-----|
[2025-10-12 14:40:54] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:40:54] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:40:54] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Summary
[2025-10-12 14:40:54] [info] -----
[2025-10-12 14:40:54] [info] Total Errors: 1120
[2025-10-12 14:40:54] [info] Total Warnings: 321
[2025-10-12 14:40:54] [info] DRC Status: FAILED

```

lvr_summary_251.rpt

```

[2025-10-12 14:41:51] [info] =====
[2025-10-12 14:41:51] [info] Design Rule Check Report
[2025-10-12 14:41:51] [info] =====

[2025-10-12 14:41:51] [info] Report Generated: 2025-10-12
[2025-10-12 14:41:51] [info] Run Start Time: 02:41:01 PM
[2025-10-12 14:41:51] [info] Run End Time: 02:41:51 PM
[2025-10-12 14:41:51] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:41:51] [info] Tool Version: LVR_2025.1
[2025-10-12 14:41:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:41:51] [info] User Name: ubuntu
[2025-10-12 14:41:51] [info] Cores Used: 8
[2025-10-12 14:41:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Top Cell Information
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Layout Top Cell: TOP
[2025-10-12 14:41:51] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:41:51] [info] Database Format: GDSII
[2025-10-12 14:41:51] [info] Verification Mode: Full Check

[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Type of Errors | Count
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] DB_WARN -> 1
[2025-10-12 14:41:51] [info] LRF_ERR001 -> 14
[2025-10-12 14:41:51] [info] DB_WARN002 -> 159
[2025-10-12 14:41:51] [info] DB_WARN003 -> 161

```

```

[2025-10-12 14:41:51] [info] DRC_ERR9 -> 89
[2025-10-12 14:41:51] [info] DRC_ERR -> 67
[2025-10-12 14:41:51] [info] DRC_ERR5 -> 408
[2025-10-12 14:41:51] [info] DRC_ERR4 -> 241
[2025-10-12 14:41:51] [info] DRC_ERR1 -> 301
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Rule Violation Summary
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:41:51] [info] |-----|-----|-----|
[2025-10-12 14:41:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:41:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:41:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:41:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:41:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:41:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 14:41:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:41:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:41:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:41:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:41:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:41:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:41:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:41:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:41:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Performance Metrics
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] | Stage          | Time Taken |
[2025-10-12 14:41:51] [info] |-----|-----|
[2025-10-12 14:41:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:41:51] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:41:51] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Summary
[2025-10-12 14:41:51] [info] -----
[2025-10-12 14:41:51] [info] Total Errors: 1120
[2025-10-12 14:41:51] [info] Total Warnings: 321
[2025-10-12 14:41:51] [info] DRC Status: FAILED

```

lvr_summary_252.rpt

```

[2025-10-12 14:42:48] [info] =====
[2025-10-12 14:42:48] [info] Design Rule Check Report
[2025-10-12 14:42:48] [info] =====

[2025-10-12 14:42:48] [info] Report Generated: 2025-10-12
[2025-10-12 14:42:48] [info] Run Start Time: 02:41:57 PM
[2025-10-12 14:42:48] [info] Run End Time: 02:42:48 PM
[2025-10-12 14:42:48] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:42:48] [info] Tool Version: LVR_2025.1
[2025-10-12 14:42:48] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:42:48] [info] User Name: ubuntu
[2025-10-12 14:42:48] [info] Cores Used: 8
[2025-10-12 14:42:48] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Top Cell Information
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Layout Top Cell:      TOP
[2025-10-12 14:42:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:42:48] [info] Database Format:      GDSII
[2025-10-12 14:42:48] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Type of Errors | Count
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] DB_WARN -> 1
[2025-10-12 14:42:48] [info] LRF_ERR001 -> 14
[2025-10-12 14:42:48] [info] DB_WARN002 -> 159
[2025-10-12 14:42:48] [info] DB_WARN003 -> 161
[2025-10-12 14:42:48] [info] DRC_ERR9 -> 89
[2025-10-12 14:42:48] [info] DRC_ERR -> 67
[2025-10-12 14:42:48] [info] DRC_ERR5 -> 408
[2025-10-12 14:42:48] [info] DRC_ERR4 -> 241
[2025-10-12 14:42:48] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Rule Violation Summary
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:42:48] [info] |-----|-----|-----|
[2025-10-12 14:42:48] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:42:48] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:42:48] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:42:48] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:42:48] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:42:48] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:42:48] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:42:48] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:42:48] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:42:48] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:42:48] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:42:48] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:42:48] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:42:48] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:42:48] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:42:48] [info] |
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Performance Metrics
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] | Stage          | Time Taken |
[2025-10-12 14:42:48] [info] |-----|-----|
[2025-10-12 14:42:48] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:42:48] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 14:42:48] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Summary
```

```
[2025-10-12 14:42:48] [info] -----
[2025-10-12 14:42:48] [info] Total Errors:      1120
[2025-10-12 14:42:48] [info] Total Warnings:    321
[2025-10-12 14:42:48] [info] DRC Status:        FAILED
```

lvr_summary_253.rpt

```
[2025-10-12 14:43:45] [info] =====
[2025-10-12 14:43:45] [info] Design Rule Check Report
[2025-10-12 14:43:45] [info] =====

[2025-10-12 14:43:45] [info] Report Generated: 2025-10-12
[2025-10-12 14:43:45] [info] Run Start Time: 02:42:54 PM
[2025-10-12 14:43:45] [info] Run End Time: 02:43:45 PM
[2025-10-12 14:43:45] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:43:45] [info] Tool Version: LVR_2025.1
[2025-10-12 14:43:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:43:45] [info] User Name: ubuntu
[2025-10-12 14:43:45] [info] Cores Used: 8
[2025-10-12 14:43:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Top Cell Information
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Layout Top Cell: TOP
[2025-10-12 14:43:45] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:43:45] [info] Database Format: GDSII
[2025-10-12 14:43:45] [info] Verification Mode: Full Check

[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Type of Errors | Count
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] DB_WARN -> 1
[2025-10-12 14:43:45] [info] LRF_ERR001 -> 14
[2025-10-12 14:43:45] [info] DB_WARN002 -> 159
[2025-10-12 14:43:45] [info] DB_WARN003 -> 161
[2025-10-12 14:43:45] [info] DRC_ERR9 -> 89
[2025-10-12 14:43:45] [info] DRC_ERR -> 67
[2025-10-12 14:43:45] [info] DRC_ERR5 -> 408
[2025-10-12 14:43:45] [info] DRC_ERR4 -> 241
[2025-10-12 14:43:45] [info] DRC_ERR1 -> 301
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Rule Violation Summary
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:43:45] [info] |-----|-----|-----|
[2025-10-12 14:43:45] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:43:45] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:43:45] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:43:45] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:43:45] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:43:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:43:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:43:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:43:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:43:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:43:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:43:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:43:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:43:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:43:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:43:45] [info]
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Performance Metrics
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:43:45] [info] |-----|-----|
[2025-10-12 14:43:45] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:43:45] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 14:43:45] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Summary
[2025-10-12 14:43:45] [info] -----
[2025-10-12 14:43:45] [info] Total Errors: 1120
[2025-10-12 14:43:45] [info] Total Warnings: 321
[2025-10-12 14:43:45] [info] DRC Status: FAILED
```

lvr_summary_254.rpt

```
[2025-10-12 14:44:40] [info] =====
[2025-10-12 14:44:40] [info] Design Rule Check Report
[2025-10-12 14:44:40] [info] =====

[2025-10-12 14:44:40] [info] Report Generated: 2025-10-12
[2025-10-12 14:44:40] [info] Run Start Time: 02:43:50 PM
[2025-10-12 14:44:40] [info] Run End Time: 02:44:40 PM
[2025-10-12 14:44:40] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:44:40] [info] Tool Version: LVR_2025.1
[2025-10-12 14:44:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:44:40] [info] User Name: ubuntu
[2025-10-12 14:44:40] [info] Cores Used: 8
[2025-10-12 14:44:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Top Cell Information
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Layout Top Cell: TOP
[2025-10-12 14:44:40] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:44:40] [info] Database Format: GDSII
[2025-10-12 14:44:40] [info] Verification Mode: Full Check

[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Type of Errors | Count
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] DB_WARN -> 1
[2025-10-12 14:44:40] [info] LRF_ERR001 -> 14
[2025-10-12 14:44:40] [info] DB_WARN002 -> 159
[2025-10-12 14:44:40] [info] DB_WARN003 -> 161
[2025-10-12 14:44:40] [info] DRC_ERR9 -> 89
[2025-10-12 14:44:40] [info] DRC_ERR -> 67
[2025-10-12 14:44:40] [info] DRC_ERR5 -> 408
[2025-10-12 14:44:40] [info] DRC_ERR4 -> 241
[2025-10-12 14:44:40] [info] DRC_ERR1 -> 301
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Rule Violation Summary
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:44:40] [info] |-----|-----|-----|
[2025-10-12 14:44:40] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:44:40] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:44:40] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:44:40] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:44:40] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 14:44:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:44:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:44:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:44:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:44:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:44:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:44:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:44:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:44:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:44:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:44:40] [info]
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Performance Metrics
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] | Stage | Time Taken |
[2025-10-12 14:44:40] [info] |-----|-----|
[2025-10-12 14:44:40] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:44:40] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:44:40] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Summary
[2025-10-12 14:44:40] [info] -----
[2025-10-12 14:44:40] [info] Total Errors: 1120
[2025-10-12 14:44:40] [info] Total Warnings: 321
[2025-10-12 14:44:40] [info] DRC Status: FAILED
```

lvr_summary_255.rpt

```
[2025-10-12 14:45:38] [info] =====
[2025-10-12 14:45:38] [info] Design Rule Check Report
[2025-10-12 14:45:38] [info] =====

[2025-10-12 14:45:38] [info] Report Generated: 2025-10-12
[2025-10-12 14:45:38] [info] Run Start Time: 02:44:47 PM
[2025-10-12 14:45:38] [info] Run End Time: 02:45:38 PM
[2025-10-12 14:45:38] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:45:38] [info] Tool Version: LVR_2025.1
[2025-10-12 14:45:38] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:45:38] [info] User Name: ubuntu
[2025-10-12 14:45:38] [info] Cores Used: 8
[2025-10-12 14:45:38] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Top Cell Information
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Layout Top Cell: TOP
[2025-10-12 14:45:38] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:45:38] [info] Database Format: GDSII
[2025-10-12 14:45:38] [info] Verification Mode: Full Check

[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Type of Errors | Count
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] DB_WARN -> 1
[2025-10-12 14:45:38] [info] LRF_ERR001 -> 14
[2025-10-12 14:45:38] [info] DB_WARN002 -> 159
[2025-10-12 14:45:38] [info] DB_WARN003 -> 161
```

```

[2025-10-12 14:45:38] [info] DRC_ERR9 -> 89
[2025-10-12 14:45:38] [info] DRC_ERR -> 67
[2025-10-12 14:45:38] [info] DRC_ERR5 -> 408
[2025-10-12 14:45:38] [info] DRC_ERR4 -> 241
[2025-10-12 14:45:38] [info] DRC_ERR1 -> 301
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Rule Violation Summary
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:45:38] [info] |-----|-----|-----|
[2025-10-12 14:45:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:45:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:45:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:45:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:45:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:45:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:45:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:45:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:45:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:45:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:45:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:45:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:45:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:45:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:45:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Performance Metrics
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] | Stage | Time Taken |
[2025-10-12 14:45:38] [info] |-----|-----|
[2025-10-12 14:45:38] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:45:38] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:45:38] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Summary
[2025-10-12 14:45:38] [info] -----
[2025-10-12 14:45:38] [info] Total Errors: 1120
[2025-10-12 14:45:38] [info] Total Warnings: 321
[2025-10-12 14:45:38] [info] DRC Status: FAILED

```

lvr_summary_256.rpt

```

[2025-10-12 14:46:34] [info] =====
[2025-10-12 14:46:34] [info] Design Rule Check Report
[2025-10-12 14:46:34] [info] =====

[2025-10-12 14:46:34] [info] Report Generated: 2025-10-12
[2025-10-12 14:46:34] [info] Run Start Time: 02:45:43 PM
[2025-10-12 14:46:34] [info] Run End Time: 02:46:34 PM
[2025-10-12 14:46:34] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:46:34] [info] Tool Version: LVR_2025.1
[2025-10-12 14:46:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:46:34] [info] User Name: ubuntu
[2025-10-12 14:46:34] [info] Cores Used: 8
[2025-10-12 14:46:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Top Cell Information
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Layout Top Cell:      TOP
[2025-10-12 14:46:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:46:34] [info] Database Format:       GDSII
[2025-10-12 14:46:34] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Type of Errors | Count
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] DB_WARN -> 1
[2025-10-12 14:46:34] [info] LRF_ERR001 -> 14
[2025-10-12 14:46:34] [info] DB_WARN002 -> 159
[2025-10-12 14:46:34] [info] DB_WARN003 -> 161
[2025-10-12 14:46:34] [info] DRC_ERR9 -> 89
[2025-10-12 14:46:34] [info] DRC_ERR -> 67
[2025-10-12 14:46:34] [info] DRC_ERR5 -> 408
[2025-10-12 14:46:34] [info] DRC_ERR4 -> 241
[2025-10-12 14:46:34] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Rule Violation Summary
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:46:34] [info] |-----|-----|-----|
[2025-10-12 14:46:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:46:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:46:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:46:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:46:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:46:34] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:46:34] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:46:34] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:46:34] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:46:34] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:46:34] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:46:34] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:46:34] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:46:34] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:46:34] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:46:34] [info] |
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Performance Metrics
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] | Stage          | Time Taken |
[2025-10-12 14:46:34] [info] |-----|-----|
[2025-10-12 14:46:34] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:46:34] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:46:34] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Summary
```

```
[2025-10-12 14:46:34] [info] -----
[2025-10-12 14:46:34] [info] Total Errors:      1120
[2025-10-12 14:46:34] [info] Total Warnings:    321
[2025-10-12 14:46:34] [info] DRC Status:        FAILED
```

lvr_summary_257.rpt

```
[2025-10-12 14:47:31] [info] =====
[2025-10-12 14:47:31] [info] Design Rule Check Report
[2025-10-12 14:47:31] [info] =====

[2025-10-12 14:47:31] [info] Report Generated: 2025-10-12
[2025-10-12 14:47:31] [info] Run Start Time: 02:46:40 PM
[2025-10-12 14:47:31] [info] Run End Time: 02:47:31 PM
[2025-10-12 14:47:31] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:47:31] [info] Tool Version: LVR_2025.1
[2025-10-12 14:47:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:47:31] [info] User Name: ubuntu
[2025-10-12 14:47:31] [info] Cores Used: 8
[2025-10-12 14:47:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] Top Cell Information
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] Layout Top Cell: TOP
[2025-10-12 14:47:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:47:31] [info] Database Format: GDSII
[2025-10-12 14:47:31] [info] Verification Mode: Full Check

[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] Type of Errors | Count
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] DB_WARN -> 1
[2025-10-12 14:47:31] [info] LRF_ERR001 -> 14
[2025-10-12 14:47:31] [info] DB_WARN002 -> 159
[2025-10-12 14:47:31] [info] DB_WARN003 -> 161
[2025-10-12 14:47:31] [info] DRC_ERR9 -> 89
[2025-10-12 14:47:31] [info] DRC_ERR -> 67
[2025-10-12 14:47:31] [info] DRC_ERR5 -> 408
[2025-10-12 14:47:31] [info] DRC_ERR4 -> 241
[2025-10-12 14:47:31] [info] DRC_ERR1 -> 301
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] Rule Violation Summary
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:47:31] [info] |-----|-----|-----|
[2025-10-12 14:47:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:47:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:47:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:47:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:47:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:47:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:47:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:47:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:47:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:47:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:47:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:47:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:47:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:47:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:47:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:47:31] [info]
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] Performance Metrics
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:47:31] [info] |-----|-----|
[2025-10-12 14:47:31] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:47:31] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 14:47:31] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info]                Summary
[2025-10-12 14:47:31] [info] -----
[2025-10-12 14:47:31] [info] Total Errors:    1120
[2025-10-12 14:47:31] [info] Total Warnings:    321
[2025-10-12 14:47:31] [info] DRC Status:      FAILED
```

lvr_summary_258.rpt

```
[2025-10-12 14:48:28] [info] =====
[2025-10-12 14:48:28] [info]                Design Rule Check Report
[2025-10-12 14:48:28] [info] =====

[2025-10-12 14:48:28] [info] Report Generated:    2025-10-12
[2025-10-12 14:48:28] [info] Run Start Time:      02:47:37 PM
[2025-10-12 14:48:28] [info] Run End Time:        02:48:28 PM
[2025-10-12 14:48:28] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:48:28] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:48:28] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:48:28] [info] User Name:           ubuntu
[2025-10-12 14:48:28] [info] Cores Used:          8
[2025-10-12 14:48:28] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info]                Top Cell Information
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] Layout Top Cell:     TOP
[2025-10-12 14:48:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:48:28] [info] Database Format:       GDSII
[2025-10-12 14:48:28] [info] Verification Mode:    Full Check

[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info]                Type of Errors | Count
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] DB_WARN -> 1
[2025-10-12 14:48:28] [info] LRF_ERR001 -> 14
[2025-10-12 14:48:28] [info] DB_WARN002 -> 159
[2025-10-12 14:48:28] [info] DB_WARN003 -> 161
[2025-10-12 14:48:28] [info] DRC_ERR9 -> 89
[2025-10-12 14:48:28] [info] DRC_ERR -> 67
[2025-10-12 14:48:28] [info] DRC_ERR5 -> 408
[2025-10-12 14:48:28] [info] DRC_ERR4 -> 241
[2025-10-12 14:48:28] [info] DRC_ERR1 -> 301
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info]                Rule Violation Summary
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:48:28] [info] |-----|-----|-----|
[2025-10-12 14:48:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:48:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:48:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:48:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:48:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```

[2025-10-12 14:48:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:48:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:48:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:48:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:48:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:48:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:48:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:48:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:48:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:48:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:48:28] [info]
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] Performance Metrics
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] | Stage | Time Taken |
[2025-10-12 14:48:28] [info] |-----|-----|
[2025-10-12 14:48:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:48:28] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:48:28] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] Summary
[2025-10-12 14:48:28] [info] -----
[2025-10-12 14:48:28] [info] Total Errors: 1120
[2025-10-12 14:48:28] [info] Total Warnings: 321
[2025-10-12 14:48:28] [info] DRC Status: FAILED

```

lvr_summary_259.rpt

```

[2025-10-12 14:49:24] [info] =====
[2025-10-12 14:49:24] [info] Design Rule Check Report
[2025-10-12 14:49:24] [info] =====

[2025-10-12 14:49:24] [info] Report Generated: 2025-10-12
[2025-10-12 14:49:24] [info] Run Start Time: 02:48:34 PM
[2025-10-12 14:49:24] [info] Run End Time: 02:49:24 PM
[2025-10-12 14:49:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:49:24] [info] Tool Version: LVR_2025.1
[2025-10-12 14:49:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:49:24] [info] User Name: ubuntu
[2025-10-12 14:49:24] [info] Cores Used: 8
[2025-10-12 14:49:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Top Cell Information
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Layout Top Cell: TOP
[2025-10-12 14:49:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:49:24] [info] Database Format: GDSII
[2025-10-12 14:49:24] [info] Verification Mode: Full Check

[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Type of Errors | Count
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] DB_WARN -> 1
[2025-10-12 14:49:24] [info] LRF_ERR001 -> 14
[2025-10-12 14:49:24] [info] DB_WARN002 -> 159
[2025-10-12 14:49:24] [info] DB_WARN003 -> 161

```

```

[2025-10-12 14:49:24] [info] DRC_ERR9 -> 89
[2025-10-12 14:49:24] [info] DRC_ERR -> 67
[2025-10-12 14:49:24] [info] DRC_ERR5 -> 408
[2025-10-12 14:49:24] [info] DRC_ERR4 -> 241
[2025-10-12 14:49:24] [info] DRC_ERR1 -> 301
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Rule Violation Summary
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:49:24] [info] |-----|-----|-----|
[2025-10-12 14:49:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:49:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:49:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:49:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:49:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:49:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:49:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:49:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:49:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:49:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:49:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:49:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:49:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:49:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:49:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Performance Metrics
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] | Stage | Time Taken |
[2025-10-12 14:49:24] [info] |-----|-----|
[2025-10-12 14:49:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:49:24] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:49:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Summary
[2025-10-12 14:49:24] [info] -----
[2025-10-12 14:49:24] [info] Total Errors: 1120
[2025-10-12 14:49:24] [info] Total Warnings: 321
[2025-10-12 14:49:24] [info] DRC Status: FAILED

```

lvr_summary_26.rpt

```

[2025-10-12 11:06:03] [info] =====
[2025-10-12 11:06:03] [info] Design Rule Check Report
[2025-10-12 11:06:03] [info] =====

[2025-10-12 11:06:03] [info] Report Generated: 2025-10-12
[2025-10-12 11:06:03] [info] Run Start Time: 11:05:11 AM
[2025-10-12 11:06:03] [info] Run End Time: 11:06:03 AM
[2025-10-12 11:06:03] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:06:03] [info] Tool Version: LVR_2025.1
[2025-10-12 11:06:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:06:03] [info] User Name: ubuntu
[2025-10-12 11:06:03] [info] Cores Used: 8
[2025-10-12 11:06:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```

[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Top Cell Information
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Layout Top Cell:      TOP
[2025-10-12 11:06:03] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:06:03] [info] Database Format:       GDSII
[2025-10-12 11:06:03] [info] Verification Mode:    Full Check

[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Type of Errors | Count
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] DB_WARN -> 1
[2025-10-12 11:06:03] [info] LRF_ERR001 -> 14
[2025-10-12 11:06:03] [info] DB_WARN002 -> 159
[2025-10-12 11:06:03] [info] DB_WARN003 -> 161
[2025-10-12 11:06:03] [info] DRC_ERR9 -> 89
[2025-10-12 11:06:03] [info] DRC_ERR -> 67
[2025-10-12 11:06:03] [info] DRC_ERR5 -> 408
[2025-10-12 11:06:03] [info] DRC_ERR4 -> 241
[2025-10-12 11:06:03] [info] DRC_ERR1 -> 301
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Rule Violation Summary
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:06:03] [info] |-----|-----|-----|
[2025-10-12 11:06:03] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:06:03] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:06:03] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:06:03] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:06:03] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:06:03] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:06:03] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:06:03] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:06:03] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:06:03] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:06:03] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:06:03] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:06:03] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:06:03] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:06:03] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Performance Metrics
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] | Stage          | Time Taken |
[2025-10-12 11:06:03] [info] |-----|-----|
[2025-10-12 11:06:03] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:06:03] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:06:03] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Summary
[2025-10-12 11:06:03] [info] -----
[2025-10-12 11:06:03] [info] Total Errors:      1120
[2025-10-12 11:06:03] [info] Total Warnings:    321
[2025-10-12 11:06:03] [info] DRC Status:        FAILED

```

lvr_summary_260.rpt

```
[2025-10-12 14:50:22] [info] =====
[2025-10-12 14:50:22] [info]                      Design Rule Check Report
[2025-10-12 14:50:22] [info] =====

[2025-10-12 14:50:22] [info] Report Generated:      2025-10-12
[2025-10-12 14:50:22] [info] Run Start Time:       02:49:31 PM
[2025-10-12 14:50:22] [info] Run End Time:         02:50:22 PM
[2025-10-12 14:50:22] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 14:50:22] [info] Tool Version:         LVR_2025.1
[2025-10-12 14:50:22] [info] Host Name:            ip-172-31-37-173
[2025-10-12 14:50:22] [info] User Name:            ubuntu
[2025-10-12 14:50:22] [info] Cores Used:           8
[2025-10-12 14:50:22] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info]                      Top Cell Information
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info] Layout Top Cell:      TOP
[2025-10-12 14:50:22] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 14:50:22] [info] Database Format:        GDSII
[2025-10-12 14:50:22] [info] Verification Mode:     Full Check

[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info]                      Type of Errors | Count
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info] DB_WARN -> 1
[2025-10-12 14:50:22] [info] LRF_ERR001 -> 14
[2025-10-12 14:50:22] [info] DB_WARN002 -> 159
[2025-10-12 14:50:22] [info] DB_WARN003 -> 161
[2025-10-12 14:50:22] [info] DRC_ERR9 -> 89
[2025-10-12 14:50:22] [info] DRC_ERR -> 67
[2025-10-12 14:50:22] [info] DRC_ERR5 -> 408
[2025-10-12 14:50:22] [info] DRC_ERR4 -> 241
[2025-10-12 14:50:22] [info] DRC_ERR1 -> 301
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info]                      Rule Violation Summary
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:50:22] [info] |-----|-----|-----|
[2025-10-12 14:50:22] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:50:22] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:50:22] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:50:22] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:50:22] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:50:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:50:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:50:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:50:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:50:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:50:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:50:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:50:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:50:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:50:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:50:22] [info]
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info]                      Performance Metrics
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:50:22] [info] |-----|-----|
[2025-10-12 14:50:22] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:50:22] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 14:50:22] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info]                Summary
[2025-10-12 14:50:22] [info] -----
[2025-10-12 14:50:22] [info] Total Errors:    1120
[2025-10-12 14:50:22] [info] Total Warnings:    321
[2025-10-12 14:50:22] [info] DRC Status:      FAILED
```

lvr_summary_261.rpt

```
[2025-10-12 14:51:19] [info] =====
[2025-10-12 14:51:19] [info]                Design Rule Check Report
[2025-10-12 14:51:19] [info] =====

[2025-10-12 14:51:19] [info] Report Generated:    2025-10-12
[2025-10-12 14:51:19] [info] Run Start Time:      02:50:28 PM
[2025-10-12 14:51:19] [info] Run End Time:        02:51:19 PM
[2025-10-12 14:51:19] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:51:19] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:51:19] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:51:19] [info] User Name:           ubuntu
[2025-10-12 14:51:19] [info] Cores Used:          8
[2025-10-12 14:51:19] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info]                Top Cell Information
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] Layout Top Cell:     TOP
[2025-10-12 14:51:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:51:19] [info] Database Format:       GDSII
[2025-10-12 14:51:19] [info] Verification Mode:    Full Check

[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info]                Type of Errors | Count
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] DB_WARN -> 1
[2025-10-12 14:51:19] [info] LRF_ERR001 -> 14
[2025-10-12 14:51:19] [info] DB_WARN002 -> 159
[2025-10-12 14:51:19] [info] DB_WARN003 -> 161
[2025-10-12 14:51:19] [info] DRC_ERR9 -> 89
[2025-10-12 14:51:19] [info] DRC_ERR -> 67
[2025-10-12 14:51:19] [info] DRC_ERR5 -> 408
[2025-10-12 14:51:19] [info] DRC_ERR4 -> 241
[2025-10-12 14:51:19] [info] DRC_ERR1 -> 301
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info]                Rule Violation Summary
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:51:19] [info] |-----|-----|-----|
[2025-10-12 14:51:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:51:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:51:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:51:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:51:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:51:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 14:51:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:51:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:51:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:51:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:51:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:51:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:51:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:51:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:51:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:51:19] [info]
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] Performance Metrics
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] | Stage | Time Taken |
[2025-10-12 14:51:19] [info] |-----|-----|
[2025-10-12 14:51:19] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:51:19] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:51:19] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] Summary
[2025-10-12 14:51:19] [info] -----
[2025-10-12 14:51:19] [info] Total Errors: 1120
[2025-10-12 14:51:19] [info] Total Warnings: 321
[2025-10-12 14:51:19] [info] DRC Status: FAILED

```

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```

[2025-10-12 14:52:15] [info] =====
[2025-10-12 14:52:15] [info] Design Rule Check Report
[2025-10-12 14:52:15] [info] =====

[2025-10-12 14:52:15] [info] Report Generated: 2025-10-12
[2025-10-12 14:52:15] [info] Run Start Time: 02:51:25 PM
[2025-10-12 14:52:15] [info] Run End Time: 02:52:15 PM
[2025-10-12 14:52:15] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:52:15] [info] Tool Version: LVR_2025.1
[2025-10-12 14:52:15] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:52:15] [info] User Name: ubuntu
[2025-10-12 14:52:15] [info] Cores Used: 8
[2025-10-12 14:52:15] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Top Cell Information
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Layout Top Cell: TOP
[2025-10-12 14:52:15] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:52:15] [info] Database Format: GDSII
[2025-10-12 14:52:15] [info] Verification Mode: Full Check

[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Type of Errors | Count
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] DB_WARN -> 1
[2025-10-12 14:52:15] [info] LRF_ERR001 -> 14
[2025-10-12 14:52:15] [info] DB_WARN002 -> 159
[2025-10-12 14:52:15] [info] DB_WARN003 -> 161

```

```

[2025-10-12 14:52:15] [info] DRC_ERR9 -> 89
[2025-10-12 14:52:15] [info] DRC_ERR -> 67
[2025-10-12 14:52:15] [info] DRC_ERR5 -> 408
[2025-10-12 14:52:15] [info] DRC_ERR4 -> 241
[2025-10-12 14:52:15] [info] DRC_ERR1 -> 301
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Rule Violation Summary
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:52:15] [info] |-----|-----|-----|
[2025-10-12 14:52:15] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:52:15] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:52:15] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:52:15] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:52:15] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:52:15] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 14:52:15] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:52:15] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:52:15] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:52:15] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:52:15] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:52:15] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:52:15] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:52:15] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:52:15] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Performance Metrics
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] | Stage          | Time Taken |
[2025-10-12 14:52:15] [info] |-----|-----|
[2025-10-12 14:52:15] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:52:15] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:52:15] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Summary
[2025-10-12 14:52:15] [info] -----
[2025-10-12 14:52:15] [info] Total Errors: 1120
[2025-10-12 14:52:15] [info] Total Warnings: 321
[2025-10-12 14:52:15] [info] DRC Status: FAILED

```

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```

[2025-10-12 14:53:14] [info] =====
[2025-10-12 14:53:14] [info] Design Rule Check Report
[2025-10-12 14:53:14] [info] =====

[2025-10-12 14:53:14] [info] Report Generated: 2025-10-12
[2025-10-12 14:53:14] [info] Run Start Time: 02:52:22 PM
[2025-10-12 14:53:14] [info] Run End Time: 02:53:14 PM
[2025-10-12 14:53:14] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 14:53:14] [info] Tool Version: LVR_2025.1
[2025-10-12 14:53:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:53:14] [info] User Name: ubuntu
[2025-10-12 14:53:14] [info] Cores Used: 8
[2025-10-12 14:53:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:53:14] [info] -----
[2025-10-12 14:53:14] [info] Top Cell Information
[2025-10-12 14:53:14] [info] -----
[2025-10-12 14:53:14] [info] Layout Top Cell: TOP
[2025-10-12 14:53:14] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:53:14] [info] Database Format: GDSII
[2025-10-12 14:53:14] [info] Verification Mode: Full Check
```

```
[2025-10-12 14:53:14] [info] -----
[2025-10-12 14:53:14] [info] Type of Errors | Count
[2025-10-12 14:53:14] [info] -----
[2025-10-12 14:53:14] [info] DB_WARN -> 1
[2025-10-12 14:53:14] [info] LRF_ERR001 -> 14
[2025-10-12 14:53:14] [info] DB_WARN002 -> 159
[2025-10-12 14:53:14] [info] DB_WARN003 -> 161
[2025-10-12 14:53:14] [info] DRC_ERR9 -> 89
[2025-10-12 14:53:14] [info] DRC_ERR -> 67
[2025-10-12 14:53:14] [info] DRC_ERR5 -> 408
[2025-10-12 14:53:14] [info] DRC_ERR4 -> 241
[2025-10-12 14:53:14] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:53:14] [info] -----
[2025-10-12 14:53:14] [info] Rule Violation Summary
```

```
[2025-10-12 14:53:14] [info] -----
[2025-10-12 14:53:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:53:14] [info] |-----|-----|-----|
[2025-10-12 14:53:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:53:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:53:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:53:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:53:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:53:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:53:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:53:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:53:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:53:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:53:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:53:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:53:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:53:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:53:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 14:53:14] [info] -----
```

```
[2025-10-12 14:53:14] [info] Performance Metrics
```

```
[2025-10-12 14:53:14] [info] -----
```

```
[2025-10-12 14:53:14] [info] | Stage | Time Taken |
[2025-10-12 14:53:14] [info] |-----|-----|
[2025-10-12 14:53:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:53:14] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 14:53:14] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:53:14] [info] -----
```

```
[2025-10-12 14:53:14] [info] Summary
```

```
[2025-10-12 14:53:14] [info] -----
```

```
[2025-10-12 14:53:14] [info] Total Errors: 1120
[2025-10-12 14:53:14] [info] Total Warnings: 321
[2025-10-12 14:53:14] [info] DRC Status: FAILED
```


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```
[2025-10-12 14:54:09] [info] =====
[2025-10-12 14:54:09] [info] Design Rule Check Report
[2025-10-12 14:54:09] [info] =====

[2025-10-12 14:54:09] [info] Report Generated: 2025-10-12
[2025-10-12 14:54:09] [info] Run Start Time: 02:53:19 PM
[2025-10-12 14:54:09] [info] Run End Time: 02:54:09 PM
[2025-10-12 14:54:09] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:54:09] [info] Tool Version: LVR_2025.1
[2025-10-12 14:54:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:54:09] [info] User Name: ubuntu
[2025-10-12 14:54:09] [info] Cores Used: 8
[2025-10-12 14:54:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] Top Cell Information
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] Layout Top Cell: TOP
[2025-10-12 14:54:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:54:09] [info] Database Format: GDSII
[2025-10-12 14:54:09] [info] Verification Mode: Full Check

[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] Type of Errors | Count
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] DB_WARN -> 1
[2025-10-12 14:54:09] [info] LRF_ERR001 -> 14
[2025-10-12 14:54:09] [info] DB_WARN002 -> 159
[2025-10-12 14:54:09] [info] DB_WARN003 -> 161
[2025-10-12 14:54:09] [info] DRC_ERR9 -> 89
[2025-10-12 14:54:09] [info] DRC_ERR -> 67
[2025-10-12 14:54:09] [info] DRC_ERR5 -> 408
[2025-10-12 14:54:09] [info] DRC_ERR4 -> 241
[2025-10-12 14:54:09] [info] DRC_ERR1 -> 301
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] Rule Violation Summary
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:54:09] [info] |-----|-----|-----|
[2025-10-12 14:54:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:54:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:54:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:54:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:54:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:54:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:54:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:54:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:54:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:54:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:54:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:54:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:54:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:54:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:54:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:54:09] [info]
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] Performance Metrics
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:54:09] [info] |-----|-----|
[2025-10-12 14:54:09] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 14:54:09] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 14:54:09] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info]                Summary
[2025-10-12 14:54:09] [info] -----
[2025-10-12 14:54:09] [info] Total Errors:    1120
[2025-10-12 14:54:09] [info] Total Warnings:    321
[2025-10-12 14:54:09] [info] DRC Status:      FAILED
```

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```
[2025-10-12 14:55:07] [info] =====
[2025-10-12 14:55:07] [info]                Design Rule Check Report
[2025-10-12 14:55:07] [info] =====

[2025-10-12 14:55:07] [info] Report Generated:    2025-10-12
[2025-10-12 14:55:07] [info] Run Start Time:      02:54:16 PM
[2025-10-12 14:55:07] [info] Run End Time:        02:55:07 PM
[2025-10-12 14:55:07] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 14:55:07] [info] Tool Version:        LVR_2025.1
[2025-10-12 14:55:07] [info] Host Name:           ip-172-31-37-173
[2025-10-12 14:55:07] [info] User Name:           ubuntu
[2025-10-12 14:55:07] [info] Cores Used:          8
[2025-10-12 14:55:07] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info]                Top Cell Information
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] Layout Top Cell:     TOP
[2025-10-12 14:55:07] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:55:07] [info] Database Format:       GDSII
[2025-10-12 14:55:07] [info] Verification Mode:    Full Check

[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info]                Type of Errors | Count
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] DB_WARN -> 1
[2025-10-12 14:55:07] [info] LRF_ERR001 -> 14
[2025-10-12 14:55:07] [info] DB_WARN002 -> 159
[2025-10-12 14:55:07] [info] DB_WARN003 -> 161
[2025-10-12 14:55:07] [info] DRC_ERR9 -> 89
[2025-10-12 14:55:07] [info] DRC_ERR -> 67
[2025-10-12 14:55:07] [info] DRC_ERR5 -> 408
[2025-10-12 14:55:07] [info] DRC_ERR4 -> 241
[2025-10-12 14:55:07] [info] DRC_ERR1 -> 301
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info]                Rule Violation Summary
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:55:07] [info] |-----|-----|-----|
[2025-10-12 14:55:07] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:55:07] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:55:07] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:55:07] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:55:07] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 14:55:07] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:55:07] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:55:07] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:55:07] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:55:07] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:55:07] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:55:07] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:55:07] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:55:07] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:55:07] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:55:07] [info]
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] Performance Metrics
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] | Stage | Time Taken |
[2025-10-12 14:55:07] [info] |-----|-----|
[2025-10-12 14:55:07] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:55:07] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:55:07] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] Summary
[2025-10-12 14:55:07] [info] -----
[2025-10-12 14:55:07] [info] Total Errors: 1120
[2025-10-12 14:55:07] [info] Total Warnings: 321
[2025-10-12 14:55:07] [info] DRC Status: FAILED
```

lvr_summary_266.rpt

```
[2025-10-12 14:56:03] [info] =====
[2025-10-12 14:56:03] [info] Design Rule Check Report
[2025-10-12 14:56:03] [info] =====

[2025-10-12 14:56:03] [info] Report Generated: 2025-10-12
[2025-10-12 14:56:03] [info] Run Start Time: 02:55:12 PM
[2025-10-12 14:56:03] [info] Run End Time: 02:56:03 PM
[2025-10-12 14:56:03] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:56:03] [info] Tool Version: LVR_2025.1
[2025-10-12 14:56:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:56:03] [info] User Name: ubuntu
[2025-10-12 14:56:03] [info] Cores Used: 8
[2025-10-12 14:56:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Top Cell Information
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Layout Top Cell: TOP
[2025-10-12 14:56:03] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:56:03] [info] Database Format: GDSII
[2025-10-12 14:56:03] [info] Verification Mode: Full Check

[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Type of Errors | Count
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] DB_WARN -> 1
[2025-10-12 14:56:03] [info] LRF_ERR001 -> 14
[2025-10-12 14:56:03] [info] DB_WARN002 -> 159
[2025-10-12 14:56:03] [info] DB_WARN003 -> 161
```

```

[2025-10-12 14:56:03] [info] DRC_ERR9 -> 89
[2025-10-12 14:56:03] [info] DRC_ERR -> 67
[2025-10-12 14:56:03] [info] DRC_ERR5 -> 408
[2025-10-12 14:56:03] [info] DRC_ERR4 -> 241
[2025-10-12 14:56:03] [info] DRC_ERR1 -> 301
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Rule Violation Summary
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:56:03] [info] |-----|-----|-----|
[2025-10-12 14:56:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:56:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:56:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:56:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:56:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:56:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:56:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:56:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:56:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:56:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:56:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:56:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:56:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:56:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:56:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Performance Metrics
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] | Stage | Time Taken |
[2025-10-12 14:56:03] [info] |-----|-----|
[2025-10-12 14:56:03] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:56:03] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 14:56:03] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Summary
[2025-10-12 14:56:03] [info] -----
[2025-10-12 14:56:03] [info] Total Errors: 1120
[2025-10-12 14:56:03] [info] Total Warnings: 321
[2025-10-12 14:56:03] [info] DRC Status: FAILED

```

lvr_summary_267.rpt

```

[2025-10-12 14:57:00] [info] =====
[2025-10-12 14:57:00] [info] Design Rule Check Report
[2025-10-12 14:57:00] [info] =====

[2025-10-12 14:57:00] [info] Report Generated: 2025-10-12
[2025-10-12 14:57:00] [info] Run Start Time: 02:56:09 PM
[2025-10-12 14:57:00] [info] Run End Time: 02:57:00 PM
[2025-10-12 14:57:00] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:57:00] [info] Tool Version: LVR_2025.1
[2025-10-12 14:57:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:57:00] [info] User Name: ubuntu
[2025-10-12 14:57:00] [info] Cores Used: 8
[2025-10-12 14:57:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Top Cell Information
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Layout Top Cell:      TOP
[2025-10-12 14:57:00] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:57:00] [info] Database Format:      GDSII
[2025-10-12 14:57:00] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Type of Errors | Count
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] DB_WARN -> 1
[2025-10-12 14:57:00] [info] LRF_ERR001 -> 14
[2025-10-12 14:57:00] [info] DB_WARN002 -> 159
[2025-10-12 14:57:00] [info] DB_WARN003 -> 161
[2025-10-12 14:57:00] [info] DRC_ERR9 -> 89
[2025-10-12 14:57:00] [info] DRC_ERR -> 67
[2025-10-12 14:57:00] [info] DRC_ERR5 -> 408
[2025-10-12 14:57:00] [info] DRC_ERR4 -> 241
[2025-10-12 14:57:00] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Rule Violation Summary
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:57:00] [info] |-----|-----|-----|
[2025-10-12 14:57:00] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:57:00] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:57:00] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:57:00] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:57:00] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:57:00] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:57:00] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:57:00] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:57:00] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:57:00] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:57:00] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:57:00] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:57:00] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:57:00] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:57:00] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:57:00] [info] |
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Performance Metrics
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] | Stage          | Time Taken |
[2025-10-12 14:57:00] [info] |-----|-----|
[2025-10-12 14:57:00] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:57:00] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:57:00] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Summary
```

```
[2025-10-12 14:57:00] [info] -----
[2025-10-12 14:57:00] [info] Total Errors:      1120
[2025-10-12 14:57:00] [info] Total Warnings:    321
[2025-10-12 14:57:00] [info] DRC Status:        FAILED
```

lvr_summary_268.rpt

```
[2025-10-12 14:57:58] [info] =====
[2025-10-12 14:57:58] [info] Design Rule Check Report
[2025-10-12 14:57:58] [info] =====

[2025-10-12 14:57:58] [info] Report Generated: 2025-10-12
[2025-10-12 14:57:58] [info] Run Start Time: 02:57:07 PM
[2025-10-12 14:57:58] [info] Run End Time: 02:57:58 PM
[2025-10-12 14:57:58] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 14:57:58] [info] Tool Version: LVR_2025.1
[2025-10-12 14:57:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:57:58] [info] User Name: ubuntu
[2025-10-12 14:57:58] [info] Cores Used: 8
[2025-10-12 14:57:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] Top Cell Information
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] Layout Top Cell: TOP
[2025-10-12 14:57:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 14:57:58] [info] Database Format: GDSII
[2025-10-12 14:57:58] [info] Verification Mode: Full Check

[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] Type of Errors | Count
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] DB_WARN -> 1
[2025-10-12 14:57:58] [info] LRF_ERR001 -> 14
[2025-10-12 14:57:58] [info] DB_WARN002 -> 159
[2025-10-12 14:57:58] [info] DB_WARN003 -> 161
[2025-10-12 14:57:58] [info] DRC_ERR9 -> 89
[2025-10-12 14:57:58] [info] DRC_ERR -> 67
[2025-10-12 14:57:58] [info] DRC_ERR5 -> 408
[2025-10-12 14:57:58] [info] DRC_ERR4 -> 241
[2025-10-12 14:57:58] [info] DRC_ERR1 -> 301
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] Rule Violation Summary
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 14:57:58] [info] |-----|-----|-----|
[2025-10-12 14:57:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 14:57:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 14:57:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 14:57:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 14:57:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 14:57:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 14:57:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:57:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:57:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:57:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:57:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:57:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:57:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:57:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:57:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:57:58] [info]
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] Performance Metrics
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] | Stage | Time Taken |
```

```
[2025-10-12 14:57:58] [info] |-----|-----|
[2025-10-12 14:57:58] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 14:57:58] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 14:57:58] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info]                Summary
[2025-10-12 14:57:58] [info] -----
[2025-10-12 14:57:58] [info] Total Errors:      1120
[2025-10-12 14:57:58] [info] Total Warnings:    321
[2025-10-12 14:57:58] [info] DRC Status:        FAILED
```

lvr_summary_269.rpt

```
[2025-10-12 14:58:54] [info] =====
[2025-10-12 14:58:54] [info]                Design Rule Check Report
[2025-10-12 14:58:54] [info] =====

[2025-10-12 14:58:54] [info] Report Generated:   2025-10-12
[2025-10-12 14:58:54] [info] Run Start Time:     02:58:03 PM
[2025-10-12 14:58:54] [info] Run End Time:       02:58:54 PM
[2025-10-12 14:58:54] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 14:58:54] [info] Tool Version:       LVR_2025.1
[2025-10-12 14:58:54] [info] Host Name:          ip-172-31-37-173
[2025-10-12 14:58:54] [info] User Name:          ubuntu
[2025-10-12 14:58:54] [info] Cores Used:         8
[2025-10-12 14:58:54] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info]                Top Cell Information
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] Layout Top Cell:    TOP
[2025-10-12 14:58:54] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 14:58:54] [info] Database Format:      GDSII
[2025-10-12 14:58:54] [info] Verification Mode:   Full Check

[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info]                Type of Errors | Count
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] DB_WARN -> 1
[2025-10-12 14:58:54] [info] LRF_ERR001 -> 14
[2025-10-12 14:58:54] [info] DB_WARN002 -> 159
[2025-10-12 14:58:54] [info] DB_WARN003 -> 161
[2025-10-12 14:58:54] [info] DRC_ERR9 -> 89
[2025-10-12 14:58:54] [info] DRC_ERR -> 67
[2025-10-12 14:58:54] [info] DRC_ERR5 -> 408
[2025-10-12 14:58:54] [info] DRC_ERR4 -> 241
[2025-10-12 14:58:54] [info] DRC_ERR1 -> 301
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info]                Rule Violation Summary
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 14:58:54] [info] |-----|-----|-----|
[2025-10-12 14:58:54] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:58:54] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:58:54] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:58:54] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:58:54] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 14:58:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 14:58:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 14:58:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 14:58:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 14:58:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 14:58:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 14:58:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 14:58:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 14:58:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 14:58:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 14:58:54] [info]
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] Performance Metrics
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] | Stage | Time Taken |
[2025-10-12 14:58:54] [info] |-----|-----|
[2025-10-12 14:58:54] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 14:58:54] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 14:58:54] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] Summary
[2025-10-12 14:58:54] [info] -----
[2025-10-12 14:58:54] [info] Total Errors: 1120
[2025-10-12 14:58:54] [info] Total Warnings: 321
[2025-10-12 14:58:54] [info] DRC Status: FAILED

```

lvr_summary_27.rpt

```

[2025-10-12 11:06:59] [info] =====
[2025-10-12 11:06:59] [info] Design Rule Check Report
[2025-10-12 11:06:59] [info] =====

[2025-10-12 11:06:59] [info] Report Generated: 2025-10-12
[2025-10-12 11:06:59] [info] Run Start Time: 11:06:07 AM
[2025-10-12 11:06:59] [info] Run End Time: 11:06:59 AM
[2025-10-12 11:06:59] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:06:59] [info] Tool Version: LVR_2025.1
[2025-10-12 11:06:59] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:06:59] [info] User Name: ubuntu
[2025-10-12 11:06:59] [info] Cores Used: 8
[2025-10-12 11:06:59] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Top Cell Information
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Layout Top Cell: TOP
[2025-10-12 11:06:59] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:06:59] [info] Database Format: GDSII
[2025-10-12 11:06:59] [info] Verification Mode: Full Check

[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Type of Errors | Count
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] DB_WARN -> 1
[2025-10-12 11:06:59] [info] LRF_ERR001 -> 14
[2025-10-12 11:06:59] [info] DB_WARN002 -> 159
[2025-10-12 11:06:59] [info] DB_WARN003 -> 161

```



```

[2025-10-12 11:06:59] [info] DRC_ERR9 -> 89
[2025-10-12 11:06:59] [info] DRC_ERR -> 67
[2025-10-12 11:06:59] [info] DRC_ERR5 -> 408
[2025-10-12 11:06:59] [info] DRC_ERR4 -> 241
[2025-10-12 11:06:59] [info] DRC_ERR1 -> 301
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Rule Violation Summary
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:06:59] [info] |-----|-----|-----|
[2025-10-12 11:06:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:06:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:06:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:06:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:06:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:06:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:06:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:06:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:06:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:06:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:06:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:06:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:06:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:06:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:06:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Performance Metrics
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] | Stage | Time Taken |
[2025-10-12 11:06:59] [info] |-----|-----|
[2025-10-12 11:06:59] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:06:59] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:06:59] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Summary
[2025-10-12 11:06:59] [info] -----
[2025-10-12 11:06:59] [info] Total Errors: 1120
[2025-10-12 11:06:59] [info] Total Warnings: 321
[2025-10-12 11:06:59] [info] DRC Status: FAILED

```

lvr_summary_270.rpt

```

[2025-10-12 14:59:51] [info] =====
[2025-10-12 14:59:51] [info] Design Rule Check Report
[2025-10-12 14:59:51] [info] =====

[2025-10-12 14:59:51] [info] Report Generated: 2025-10-12
[2025-10-12 14:59:51] [info] Run Start Time: 02:59:00 PM
[2025-10-12 14:59:51] [info] Run End Time: 02:59:51 PM
[2025-10-12 14:59:51] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 14:59:51] [info] Tool Version: LVR_2025.1
[2025-10-12 14:59:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 14:59:51] [info] User Name: ubuntu
[2025-10-12 14:59:51] [info] Cores Used: 8
[2025-10-12 14:59:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Top Cell Information
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Layout Top Cell:      TOP
[2025-10-12 14:59:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 14:59:51] [info] Database Format:      GDSII
[2025-10-12 14:59:51] [info] Verification Mode:    Full Check
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Type of Errors | Count
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] DB_WARN -> 1
[2025-10-12 14:59:51] [info] LRF_ERR001 -> 14
[2025-10-12 14:59:51] [info] DB_WARN002 -> 159
[2025-10-12 14:59:51] [info] DB_WARN003 -> 161
[2025-10-12 14:59:51] [info] DRC_ERR9 -> 89
[2025-10-12 14:59:51] [info] DRC_ERR -> 67
[2025-10-12 14:59:51] [info] DRC_ERR5 -> 408
[2025-10-12 14:59:51] [info] DRC_ERR4 -> 241
[2025-10-12 14:59:51] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Rule Violation Summary
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 14:59:51] [info] |-----|-----|-----|
[2025-10-12 14:59:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 14:59:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 14:59:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 14:59:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 14:59:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 14:59:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 14:59:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 14:59:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 14:59:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 14:59:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 14:59:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 14:59:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 14:59:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 14:59:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 14:59:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 14:59:51] [info] |
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Performance Metrics
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] | Stage          | Time Taken |
[2025-10-12 14:59:51] [info] |-----|-----|
[2025-10-12 14:59:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 14:59:51] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 14:59:51] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Summary
```

```
[2025-10-12 14:59:51] [info] -----
[2025-10-12 14:59:51] [info] Total Errors:      1120
[2025-10-12 14:59:51] [info] Total Warnings:    321
[2025-10-12 14:59:51] [info] DRC Status:        FAILED
```

lvr_summary_271.rpt

```
[2025-10-12 15:00:49] [info] =====
[2025-10-12 15:00:49] [info]                      Design Rule Check Report
[2025-10-12 15:00:49] [info] =====

[2025-10-12 15:00:49] [info] Report Generated:      2025-10-12
[2025-10-12 15:00:49] [info] Run Start Time:       02:59:58 PM
[2025-10-12 15:00:49] [info] Run End Time:         03:00:49 PM
[2025-10-12 15:00:49] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 15:00:49] [info] Tool Version:          LVR_2025.1
[2025-10-12 15:00:49] [info] Host Name:             ip-172-31-37-173
[2025-10-12 15:00:49] [info] User Name:             ubuntu
[2025-10-12 15:00:49] [info] Cores Used:            8
[2025-10-12 15:00:49] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info]                      Top Cell Information
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info] Layout Top Cell:      TOP
[2025-10-12 15:00:49] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 15:00:49] [info] Database Format:       GDSII
[2025-10-12 15:00:49] [info] Verification Mode:     Full Check

[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info]                      Type of Errors | Count
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info] DB_WARN -> 1
[2025-10-12 15:00:49] [info] LRF_ERR001 -> 14
[2025-10-12 15:00:49] [info] DB_WARN002 -> 159
[2025-10-12 15:00:49] [info] DB_WARN003 -> 161
[2025-10-12 15:00:49] [info] DRC_ERR9 -> 89
[2025-10-12 15:00:49] [info] DRC_ERR -> 67
[2025-10-12 15:00:49] [info] DRC_ERR5 -> 408
[2025-10-12 15:00:49] [info] DRC_ERR4 -> 241
[2025-10-12 15:00:49] [info] DRC_ERR1 -> 301
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info]                      Rule Violation Summary
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:00:49] [info] |-----|-----|-----|
[2025-10-12 15:00:49] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:00:49] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:00:49] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:00:49] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:00:49] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:00:49] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:00:49] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:00:49] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:00:49] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:00:49] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:00:49] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:00:49] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:00:49] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:00:49] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:00:49] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:00:49] [info]
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info]                      Performance Metrics
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:00:49] [info] |-----|-----|
[2025-10-12 15:00:49] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:00:49] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 15:00:49] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info]                Summary
[2025-10-12 15:00:49] [info] -----
[2025-10-12 15:00:49] [info] Total Errors:    1120
[2025-10-12 15:00:49] [info] Total Warnings:    321
[2025-10-12 15:00:49] [info] DRC Status:      FAILED
```

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```
[2025-10-12 15:01:47] [info] =====
[2025-10-12 15:01:47] [info]                Design Rule Check Report
[2025-10-12 15:01:47] [info] =====

[2025-10-12 15:01:47] [info] Report Generated:    2025-10-12
[2025-10-12 15:01:47] [info] Run Start Time:      03:00:55 PM
[2025-10-12 15:01:47] [info] Run End Time:        03:01:47 PM
[2025-10-12 15:01:47] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 15:01:47] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:01:47] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:01:47] [info] User Name:           ubuntu
[2025-10-12 15:01:47] [info] Cores Used:          8
[2025-10-12 15:01:47] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info]                Top Cell Information
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] Layout Top Cell:     TOP
[2025-10-12 15:01:47] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:01:47] [info] Database Format:       GDSII
[2025-10-12 15:01:47] [info] Verification Mode:    Full Check

[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info]                Type of Errors | Count
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] DB_WARN -> 1
[2025-10-12 15:01:47] [info] LRF_ERR001 -> 14
[2025-10-12 15:01:47] [info] DB_WARN002 -> 159
[2025-10-12 15:01:47] [info] DB_WARN003 -> 161
[2025-10-12 15:01:47] [info] DRC_ERR9 -> 89
[2025-10-12 15:01:47] [info] DRC_ERR -> 67
[2025-10-12 15:01:47] [info] DRC_ERR5 -> 408
[2025-10-12 15:01:47] [info] DRC_ERR4 -> 241
[2025-10-12 15:01:47] [info] DRC_ERR1 -> 301
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info]                Rule Violation Summary
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:01:47] [info] |-----|-----|-----|
[2025-10-12 15:01:47] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:01:47] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:01:47] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:01:47] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:01:47] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 15:01:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:01:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:01:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:01:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:01:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:01:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:01:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:01:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:01:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:01:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:01:47] [info]
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] Performance Metrics
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] | Stage | Time Taken |
[2025-10-12 15:01:47] [info] |-----|-----|
[2025-10-12 15:01:47] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:01:47] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:01:47] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] Summary
[2025-10-12 15:01:47] [info] -----
[2025-10-12 15:01:47] [info] Total Errors: 1120
[2025-10-12 15:01:47] [info] Total Warnings: 321
[2025-10-12 15:01:47] [info] DRC Status: FAILED

```

lvr_summary_273.rpt

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[2025-10-12 15:02:42] [info] =====
[2025-10-12 15:02:42] [info] Design Rule Check Report
[2025-10-12 15:02:42] [info] =====

[2025-10-12 15:02:42] [info] Report Generated: 2025-10-12
[2025-10-12 15:02:42] [info] Run Start Time: 03:01:51 PM
[2025-10-12 15:02:42] [info] Run End Time: 03:02:42 PM
[2025-10-12 15:02:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:02:42] [info] Tool Version: LVR_2025.1
[2025-10-12 15:02:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:02:42] [info] User Name: ubuntu
[2025-10-12 15:02:42] [info] Cores Used: 8
[2025-10-12 15:02:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Top Cell Information
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Layout Top Cell: TOP
[2025-10-12 15:02:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:02:42] [info] Database Format: GDSII
[2025-10-12 15:02:42] [info] Verification Mode: Full Check

[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Type of Errors | Count
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] DB_WARN -> 1
[2025-10-12 15:02:42] [info] LRF_ERR001 -> 14
[2025-10-12 15:02:42] [info] DB_WARN002 -> 159
[2025-10-12 15:02:42] [info] DB_WARN003 -> 161

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[2025-10-12 15:02:42] [info] DRC_ERR9 -> 89
[2025-10-12 15:02:42] [info] DRC_ERR -> 67
[2025-10-12 15:02:42] [info] DRC_ERR5 -> 408
[2025-10-12 15:02:42] [info] DRC_ERR4 -> 241
[2025-10-12 15:02:42] [info] DRC_ERR1 -> 301
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Rule Violation Summary
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:02:42] [info] |-----|-----|-----|
[2025-10-12 15:02:42] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:02:42] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:02:42] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:02:42] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:02:42] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:02:42] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 15:02:42] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:02:42] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:02:42] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:02:42] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:02:42] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:02:42] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:02:42] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:02:42] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:02:42] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Performance Metrics
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] | Stage          | Time Taken |
[2025-10-12 15:02:42] [info] |-----|-----|
[2025-10-12 15:02:42] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:02:42] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 15:02:42] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Summary
[2025-10-12 15:02:42] [info] -----
[2025-10-12 15:02:42] [info] Total Errors: 1120
[2025-10-12 15:02:42] [info] Total Warnings: 321
[2025-10-12 15:02:42] [info] DRC Status: FAILED

```

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```

[2025-10-12 15:03:39] [info] =====
[2025-10-12 15:03:39] [info] Design Rule Check Report
[2025-10-12 15:03:39] [info] =====

[2025-10-12 15:03:39] [info] Report Generated: 2025-10-12
[2025-10-12 15:03:39] [info] Run Start Time: 03:02:49 PM
[2025-10-12 15:03:39] [info] Run End Time: 03:03:39 PM
[2025-10-12 15:03:39] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:03:39] [info] Tool Version: LVR_2025.1
[2025-10-12 15:03:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:03:39] [info] User Name: ubuntu
[2025-10-12 15:03:39] [info] Cores Used: 8
[2025-10-12 15:03:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] Top Cell Information
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] Layout Top Cell:      TOP
[2025-10-12 15:03:39] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:03:39] [info] Database Format:      GDSII
[2025-10-12 15:03:39] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] Type of Errors | Count
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] DB_WARN -> 1
[2025-10-12 15:03:39] [info] LRF_ERR001 -> 14
[2025-10-12 15:03:39] [info] DB_WARN002 -> 159
[2025-10-12 15:03:39] [info] DB_WARN003 -> 161
[2025-10-12 15:03:39] [info] DRC_ERR9 -> 89
[2025-10-12 15:03:39] [info] DRC_ERR -> 67
[2025-10-12 15:03:39] [info] DRC_ERR5 -> 408
[2025-10-12 15:03:39] [info] DRC_ERR4 -> 241
[2025-10-12 15:03:39] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] Rule Violation Summary
```

```
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:03:39] [info] |-----|-----|-----|
[2025-10-12 15:03:39] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:03:39] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:03:39] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:03:39] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:03:39] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:03:39] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:03:39] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:03:39] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:03:39] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:03:39] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:03:39] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:03:39] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:03:39] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:03:39] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:03:39] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:03:39] [info] -----
```

```
[2025-10-12 15:03:39] [info] Performance Metrics
```

```
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] | Stage          | Time Taken |
[2025-10-12 15:03:39] [info] |-----|-----|
[2025-10-12 15:03:39] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:03:39] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 15:03:39] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:03:39] [info] -----
```

```
[2025-10-12 15:03:39] [info] Summary
```

```
[2025-10-12 15:03:39] [info] -----
[2025-10-12 15:03:39] [info] Total Errors:      1120
[2025-10-12 15:03:39] [info] Total Warnings:    321
[2025-10-12 15:03:39] [info] DRC Status:        FAILED
```

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```
[2025-10-12 15:04:35] [info] =====
[2025-10-12 15:04:35] [info] Design Rule Check Report
[2025-10-12 15:04:35] [info] =====

[2025-10-12 15:04:35] [info] Report Generated: 2025-10-12
[2025-10-12 15:04:35] [info] Run Start Time: 03:03:45 PM
[2025-10-12 15:04:35] [info] Run End Time: 03:04:35 PM
[2025-10-12 15:04:35] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:04:35] [info] Tool Version: LVR_2025.1
[2025-10-12 15:04:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:04:35] [info] User Name: ubuntu
[2025-10-12 15:04:35] [info] Cores Used: 8
[2025-10-12 15:04:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Top Cell Information
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Layout Top Cell: TOP
[2025-10-12 15:04:35] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:04:35] [info] Database Format: GDSII
[2025-10-12 15:04:35] [info] Verification Mode: Full Check

[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Type of Errors | Count
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] DB_WARN -> 1
[2025-10-12 15:04:35] [info] LRF_ERR001 -> 14
[2025-10-12 15:04:35] [info] DB_WARN002 -> 159
[2025-10-12 15:04:35] [info] DB_WARN003 -> 161
[2025-10-12 15:04:35] [info] DRC_ERR9 -> 89
[2025-10-12 15:04:35] [info] DRC_ERR -> 67
[2025-10-12 15:04:35] [info] DRC_ERR5 -> 408
[2025-10-12 15:04:35] [info] DRC_ERR4 -> 241
[2025-10-12 15:04:35] [info] DRC_ERR1 -> 301
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Rule Violation Summary
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:04:35] [info] |-----|-----|-----|
[2025-10-12 15:04:35] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:04:35] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:04:35] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:04:35] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:04:35] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:04:35] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:04:35] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:04:35] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:04:35] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:04:35] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:04:35] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:04:35] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:04:35] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:04:35] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:04:35] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:04:35] [info]
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Performance Metrics
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] | Stage | Time Taken |
```



```
[2025-10-12 15:04:35] [info] |-----|-----|
[2025-10-12 15:04:35] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:04:35] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 15:04:35] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Summary
[2025-10-12 15:04:35] [info] -----
[2025-10-12 15:04:35] [info] Total Errors: 1120
[2025-10-12 15:04:35] [info] Total Warnings: 321
[2025-10-12 15:04:35] [info] DRC Status: FAILED
```

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```
[2025-10-12 15:05:33] [info] =====
[2025-10-12 15:05:33] [info] Design Rule Check Report
[2025-10-12 15:05:33] [info] =====

[2025-10-12 15:05:33] [info] Report Generated: 2025-10-12
[2025-10-12 15:05:33] [info] Run Start Time: 03:04:42 PM
[2025-10-12 15:05:33] [info] Run End Time: 03:05:33 PM
[2025-10-12 15:05:33] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:05:33] [info] Tool Version: LVR_2025.1
[2025-10-12 15:05:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:05:33] [info] User Name: ubuntu
[2025-10-12 15:05:33] [info] Cores Used: 8
[2025-10-12 15:05:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Top Cell Information
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Layout Top Cell: TOP
[2025-10-12 15:05:33] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:05:33] [info] Database Format: GDSII
[2025-10-12 15:05:33] [info] Verification Mode: Full Check

[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Type of Errors | Count
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] DB_WARN -> 1
[2025-10-12 15:05:33] [info] LRF_ERR001 -> 14
[2025-10-12 15:05:33] [info] DB_WARN002 -> 159
[2025-10-12 15:05:33] [info] DB_WARN003 -> 161
[2025-10-12 15:05:33] [info] DRC_ERR9 -> 89
[2025-10-12 15:05:33] [info] DRC_ERR -> 67
[2025-10-12 15:05:33] [info] DRC_ERR5 -> 408
[2025-10-12 15:05:33] [info] DRC_ERR4 -> 241
[2025-10-12 15:05:33] [info] DRC_ERR1 -> 301
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Rule Violation Summary
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:05:33] [info] |-----|-----|-----|
[2025-10-12 15:05:33] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:05:33] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:05:33] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:05:33] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:05:33] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 15:05:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:05:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:05:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:05:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:05:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:05:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:05:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:05:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:05:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:05:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:05:33] [info]
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Performance Metrics
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] | Stage | Time Taken |
[2025-10-12 15:05:33] [info] |-----|-----|
[2025-10-12 15:05:33] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:05:33] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:05:33] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Summary
[2025-10-12 15:05:33] [info] -----
[2025-10-12 15:05:33] [info] Total Errors: 1120
[2025-10-12 15:05:33] [info] Total Warnings: 321
[2025-10-12 15:05:33] [info] DRC Status: FAILED
```

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```
[2025-10-12 15:06:30] [info] =====
[2025-10-12 15:06:30] [info] Design Rule Check Report
[2025-10-12 15:06:30] [info] =====

[2025-10-12 15:06:30] [info] Report Generated: 2025-10-12
[2025-10-12 15:06:30] [info] Run Start Time: 03:05:39 PM
[2025-10-12 15:06:30] [info] Run End Time: 03:06:30 PM
[2025-10-12 15:06:30] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:06:30] [info] Tool Version: LVR_2025.1
[2025-10-12 15:06:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:06:30] [info] User Name: ubuntu
[2025-10-12 15:06:30] [info] Cores Used: 8
[2025-10-12 15:06:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Top Cell Information
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Layout Top Cell: TOP
[2025-10-12 15:06:30] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:06:30] [info] Database Format: GDSII
[2025-10-12 15:06:30] [info] Verification Mode: Full Check

[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Type of Errors | Count
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] DB_WARN -> 1
[2025-10-12 15:06:30] [info] LRF_ERR001 -> 14
[2025-10-12 15:06:30] [info] DB_WARN002 -> 159
[2025-10-12 15:06:30] [info] DB_WARN003 -> 161
```

```

[2025-10-12 15:06:30] [info] DRC_ERR9 -> 89
[2025-10-12 15:06:30] [info] DRC_ERR -> 67
[2025-10-12 15:06:30] [info] DRC_ERR5 -> 408
[2025-10-12 15:06:30] [info] DRC_ERR4 -> 241
[2025-10-12 15:06:30] [info] DRC_ERR1 -> 301
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Rule Violation Summary
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:06:30] [info] |-----|-----|-----|
[2025-10-12 15:06:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:06:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:06:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:06:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:06:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:06:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:06:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:06:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:06:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:06:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:06:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:06:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:06:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:06:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:06:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Performance Metrics
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] | Stage | Time Taken |
[2025-10-12 15:06:30] [info] |-----|-----|
[2025-10-12 15:06:30] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:06:30] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:06:30] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Summary
[2025-10-12 15:06:30] [info] -----
[2025-10-12 15:06:30] [info] Total Errors: 1120
[2025-10-12 15:06:30] [info] Total Warnings: 321
[2025-10-12 15:06:30] [info] DRC Status: FAILED

```

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```

[2025-10-12 15:07:29] [info] =====
[2025-10-12 15:07:29] [info] Design Rule Check Report
[2025-10-12 15:07:29] [info] =====

[2025-10-12 15:07:29] [info] Report Generated: 2025-10-12
[2025-10-12 15:07:29] [info] Run Start Time: 03:06:37 PM
[2025-10-12 15:07:29] [info] Run End Time: 03:07:29 PM
[2025-10-12 15:07:29] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:07:29] [info] Tool Version: LVR_2025.1
[2025-10-12 15:07:29] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:07:29] [info] User Name: ubuntu
[2025-10-12 15:07:29] [info] Cores Used: 8
[2025-10-12 15:07:29] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Top Cell Information
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Layout Top Cell:      TOP
[2025-10-12 15:07:29] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:07:29] [info] Database Format:       GDSII
[2025-10-12 15:07:29] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Type of Errors | Count
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] DB_WARN -> 1
[2025-10-12 15:07:29] [info] LRF_ERR001 -> 14
[2025-10-12 15:07:29] [info] DB_WARN002 -> 159
[2025-10-12 15:07:29] [info] DB_WARN003 -> 161
[2025-10-12 15:07:29] [info] DRC_ERR9 -> 89
[2025-10-12 15:07:29] [info] DRC_ERR -> 67
[2025-10-12 15:07:29] [info] DRC_ERR5 -> 408
[2025-10-12 15:07:29] [info] DRC_ERR4 -> 241
[2025-10-12 15:07:29] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Rule Violation Summary
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:07:29] [info] |-----|-----|-----|
[2025-10-12 15:07:29] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:07:29] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:07:29] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:07:29] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:07:29] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:07:29] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:07:29] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:07:29] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:07:29] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:07:29] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:07:29] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:07:29] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:07:29] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:07:29] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:07:29] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:07:29] [info] |
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Performance Metrics
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] | Stage          | Time Taken |
[2025-10-12 15:07:29] [info] |-----|-----|
[2025-10-12 15:07:29] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:07:29] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 15:07:29] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Summary
```

```
[2025-10-12 15:07:29] [info] -----
[2025-10-12 15:07:29] [info] Total Errors:      1120
[2025-10-12 15:07:29] [info] Total Warnings:    321
[2025-10-12 15:07:29] [info] DRC Status:        FAILED
```

lvr_summary_279.rpt

```
[2025-10-12 15:08:25] [info] =====
[2025-10-12 15:08:25] [info]                Design Rule Check Report
[2025-10-12 15:08:25] [info] =====

[2025-10-12 15:08:25] [info] Report Generated:    2025-10-12
[2025-10-12 15:08:25] [info] Run Start Time:     03:07:34 PM
[2025-10-12 15:08:25] [info] Run End Time:       03:08:25 PM
[2025-10-12 15:08:25] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 15:08:25] [info] Tool Version:       LVR_2025.1
[2025-10-12 15:08:25] [info] Host Name:          ip-172-31-37-173
[2025-10-12 15:08:25] [info] User Name:          ubuntu
[2025-10-12 15:08:25] [info] Cores Used:         8
[2025-10-12 15:08:25] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info]                Top Cell Information
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info] Layout Top Cell:    TOP
[2025-10-12 15:08:25] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 15:08:25] [info] Database Format:      GDSII
[2025-10-12 15:08:25] [info] Verification Mode:   Full Check

[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info]                Type of Errors | Count
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info] DB_WARN -> 1
[2025-10-12 15:08:25] [info] LRF_ERR001 -> 14
[2025-10-12 15:08:25] [info] DB_WARN002 -> 159
[2025-10-12 15:08:25] [info] DB_WARN003 -> 161
[2025-10-12 15:08:25] [info] DRC_ERR9 -> 89
[2025-10-12 15:08:25] [info] DRC_ERR -> 67
[2025-10-12 15:08:25] [info] DRC_ERR5 -> 408
[2025-10-12 15:08:25] [info] DRC_ERR4 -> 241
[2025-10-12 15:08:25] [info] DRC_ERR1 -> 301
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info]                Rule Violation Summary
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:08:25] [info] |-----|-----|-----|
[2025-10-12 15:08:25] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:08:25] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:08:25] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:08:25] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:08:25] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:08:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:08:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:08:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:08:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:08:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:08:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:08:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:08:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:08:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:08:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:08:25] [info]
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info]                Performance Metrics
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:08:25] [info] |-----|-----|
[2025-10-12 15:08:25] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 15:08:25] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 15:08:25] [info] | Report Generation   | 0 minutes 0 seconds |

[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info]                Summary
[2025-10-12 15:08:25] [info] -----
[2025-10-12 15:08:25] [info] Total Errors:      1120
[2025-10-12 15:08:25] [info] Total Warnings:    321
[2025-10-12 15:08:25] [info] DRC Status:        FAILED
```

lvr_summary_28.rpt

```
[2025-10-12 11:07:56] [info] =====
[2025-10-12 11:07:56] [info]                Design Rule Check Report
[2025-10-12 11:07:56] [info] =====

[2025-10-12 11:07:56] [info] Report Generated:   2025-10-12
[2025-10-12 11:07:56] [info] Run Start Time:     11:07:04 AM
[2025-10-12 11:07:56] [info] Run End Time:       11:07:56 AM
[2025-10-12 11:07:56] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 11:07:56] [info] Tool Version:       LVR_2025.1
[2025-10-12 11:07:56] [info] Host Name:          ip-172-31-37-173
[2025-10-12 11:07:56] [info] User Name:          ubuntu
[2025-10-12 11:07:56] [info] Cores Used:         8
[2025-10-12 11:07:56] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info]                Top Cell Information
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] Layout Top Cell:    TOP
[2025-10-12 11:07:56] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 11:07:56] [info] Database Format:      GDSII
[2025-10-12 11:07:56] [info] Verification Mode:   Full Check

[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info]                Type of Errors | Count
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] DB_WARN -> 1
[2025-10-12 11:07:56] [info] LRF_ERR001 -> 14
[2025-10-12 11:07:56] [info] DB_WARN002 -> 159
[2025-10-12 11:07:56] [info] DB_WARN003 -> 161
[2025-10-12 11:07:56] [info] DRC_ERR9 -> 89
[2025-10-12 11:07:56] [info] DRC_ERR -> 67
[2025-10-12 11:07:56] [info] DRC_ERR5 -> 408
[2025-10-12 11:07:56] [info] DRC_ERR4 -> 241
[2025-10-12 11:07:56] [info] DRC_ERR1 -> 301
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info]                Rule Violation Summary
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:07:56] [info] |-----|-----|-----|
[2025-10-12 11:07:56] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:07:56] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:07:56] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:07:56] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:07:56] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 11:07:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:07:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:07:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:07:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:07:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:07:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:07:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:07:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:07:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:07:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:07:56] [info]
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] Performance Metrics
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] | Stage | Time Taken |
[2025-10-12 11:07:56] [info] |-----|-----|
[2025-10-12 11:07:56] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:07:56] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:07:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] Summary
[2025-10-12 11:07:56] [info] -----
[2025-10-12 11:07:56] [info] Total Errors: 1120
[2025-10-12 11:07:56] [info] Total Warnings: 321
[2025-10-12 11:07:56] [info] DRC Status: FAILED

```

lvr_summary_280.rpt

```

[2025-10-12 15:09:22] [info] =====
[2025-10-12 15:09:22] [info] Design Rule Check Report
[2025-10-12 15:09:22] [info] =====

[2025-10-12 15:09:22] [info] Report Generated: 2025-10-12
[2025-10-12 15:09:22] [info] Run Start Time: 03:08:31 PM
[2025-10-12 15:09:22] [info] Run End Time: 03:09:22 PM
[2025-10-12 15:09:22] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:09:22] [info] Tool Version: LVR_2025.1
[2025-10-12 15:09:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:09:22] [info] User Name: ubuntu
[2025-10-12 15:09:22] [info] Cores Used: 8
[2025-10-12 15:09:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Top Cell Information
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Layout Top Cell: TOP
[2025-10-12 15:09:22] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:09:22] [info] Database Format: GDSII
[2025-10-12 15:09:22] [info] Verification Mode: Full Check

[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Type of Errors | Count
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] DB_WARN -> 1
[2025-10-12 15:09:22] [info] LRF_ERR001 -> 14
[2025-10-12 15:09:22] [info] DB_WARN002 -> 159
[2025-10-12 15:09:22] [info] DB_WARN003 -> 161

```

```

[2025-10-12 15:09:22] [info] DRC_ERR9 -> 89
[2025-10-12 15:09:22] [info] DRC_ERR -> 67
[2025-10-12 15:09:22] [info] DRC_ERR5 -> 408
[2025-10-12 15:09:22] [info] DRC_ERR4 -> 241
[2025-10-12 15:09:22] [info] DRC_ERR1 -> 301
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Rule Violation Summary
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:09:22] [info] |-----|-----|-----|
[2025-10-12 15:09:22] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:09:22] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:09:22] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:09:22] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:09:22] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:09:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:09:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:09:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:09:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:09:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:09:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:09:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:09:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:09:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:09:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Performance Metrics
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] | Stage | Time Taken |
[2025-10-12 15:09:22] [info] |-----|-----|
[2025-10-12 15:09:22] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:09:22] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:09:22] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Summary
[2025-10-12 15:09:22] [info] -----
[2025-10-12 15:09:22] [info] Total Errors: 1120
[2025-10-12 15:09:22] [info] Total Warnings: 321
[2025-10-12 15:09:22] [info] DRC Status: FAILED

```

lvr_summary_281.rpt

```

[2025-10-12 15:10:19] [info] =====
[2025-10-12 15:10:19] [info] Design Rule Check Report
[2025-10-12 15:10:19] [info] =====

[2025-10-12 15:10:19] [info] Report Generated: 2025-10-12
[2025-10-12 15:10:19] [info] Run Start Time: 03:09:28 PM
[2025-10-12 15:10:19] [info] Run End Time: 03:10:19 PM
[2025-10-12 15:10:19] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:10:19] [info] Tool Version: LVR_2025.1
[2025-10-12 15:10:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:10:19] [info] User Name: ubuntu
[2025-10-12 15:10:19] [info] Cores Used: 8
[2025-10-12 15:10:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] Top Cell Information
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] Layout Top Cell:      TOP
[2025-10-12 15:10:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:10:19] [info] Database Format:      GDSII
[2025-10-12 15:10:19] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] Type of Errors | Count
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] DB_WARN -> 1
[2025-10-12 15:10:19] [info] LRF_ERR001 -> 14
[2025-10-12 15:10:19] [info] DB_WARN002 -> 159
[2025-10-12 15:10:19] [info] DB_WARN003 -> 161
[2025-10-12 15:10:19] [info] DRC_ERR9 -> 89
[2025-10-12 15:10:19] [info] DRC_ERR -> 67
[2025-10-12 15:10:19] [info] DRC_ERR5 -> 408
[2025-10-12 15:10:19] [info] DRC_ERR4 -> 241
[2025-10-12 15:10:19] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] Rule Violation Summary
```

```
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:10:19] [info] |-----|-----|-----|
[2025-10-12 15:10:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:10:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:10:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:10:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:10:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:10:19] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:10:19] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:10:19] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:10:19] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:10:19] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:10:19] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:10:19] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:10:19] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:10:19] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:10:19] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:10:19] [info] -----
```

```
[2025-10-12 15:10:19] [info] Performance Metrics
```

```
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] | Stage          | Time Taken |
[2025-10-12 15:10:19] [info] |-----|-----|
[2025-10-12 15:10:19] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:10:19] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 15:10:19] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:10:19] [info] -----
```

```
[2025-10-12 15:10:19] [info] Summary
```

```
[2025-10-12 15:10:19] [info] -----
[2025-10-12 15:10:19] [info] Total Errors:      1120
[2025-10-12 15:10:19] [info] Total Warnings:    321
[2025-10-12 15:10:19] [info] DRC Status:        FAILED
```

lvr_summary_282.rpt

```
[2025-10-12 15:11:15] [info] =====
[2025-10-12 15:11:15] [info]                      Design Rule Check Report
[2025-10-12 15:11:15] [info] =====

[2025-10-12 15:11:15] [info] Report Generated:    2025-10-12
[2025-10-12 15:11:15] [info] Run Start Time:      03:10:24 PM
[2025-10-12 15:11:15] [info] Run End Time:        03:11:15 PM
[2025-10-12 15:11:15] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 15:11:15] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:11:15] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:11:15] [info] User Name:           ubuntu
[2025-10-12 15:11:15] [info] Cores Used:          8
[2025-10-12 15:11:15] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info]                      Top Cell Information
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info] Layout Top Cell:     TOP
[2025-10-12 15:11:15] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:11:15] [info] Database Format:       GDSII
[2025-10-12 15:11:15] [info] Verification Mode:    Full Check

[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info]                      Type of Errors | Count
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info] DB_WARN -> 1
[2025-10-12 15:11:15] [info] LRF_ERR001 -> 14
[2025-10-12 15:11:15] [info] DB_WARN002 -> 159
[2025-10-12 15:11:15] [info] DB_WARN003 -> 161
[2025-10-12 15:11:15] [info] DRC_ERR9 -> 89
[2025-10-12 15:11:15] [info] DRC_ERR -> 67
[2025-10-12 15:11:15] [info] DRC_ERR5 -> 408
[2025-10-12 15:11:15] [info] DRC_ERR4 -> 241
[2025-10-12 15:11:15] [info] DRC_ERR1 -> 301
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info]                      Rule Violation Summary
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:11:15] [info] |-----|-----|-----|
[2025-10-12 15:11:15] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:11:15] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:11:15] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:11:15] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:11:15] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:11:15] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:11:15] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:11:15] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:11:15] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:11:15] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:11:15] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:11:15] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:11:15] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:11:15] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:11:15] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:11:15] [info]
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info]                      Performance Metrics
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:11:15] [info] |-----|-----|
[2025-10-12 15:11:15] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:11:15] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 15:11:15] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info] Summary
[2025-10-12 15:11:15] [info] -----
[2025-10-12 15:11:15] [info] Total Errors: 1120
[2025-10-12 15:11:15] [info] Total Warnings: 321
[2025-10-12 15:11:15] [info] DRC Status: FAILED
```

lvr_summary_283.rpt

```
[2025-10-12 15:12:14] [info] =====
[2025-10-12 15:12:14] [info] Design Rule Check Report
[2025-10-12 15:12:14] [info] =====

[2025-10-12 15:12:14] [info] Report Generated: 2025-10-12
[2025-10-12 15:12:14] [info] Run Start Time: 03:11:22 PM
[2025-10-12 15:12:14] [info] Run End Time: 03:12:14 PM
[2025-10-12 15:12:14] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:12:14] [info] Tool Version: LVR_2025.1
[2025-10-12 15:12:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:12:14] [info] User Name: ubuntu
[2025-10-12 15:12:14] [info] Cores Used: 8
[2025-10-12 15:12:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Top Cell Information
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Layout Top Cell: TOP
[2025-10-12 15:12:14] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:12:14] [info] Database Format: GDSII
[2025-10-12 15:12:14] [info] Verification Mode: Full Check

[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Type of Errors | Count
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] DB_WARN -> 1
[2025-10-12 15:12:14] [info] LRF_ERR001 -> 14
[2025-10-12 15:12:14] [info] DB_WARN002 -> 159
[2025-10-12 15:12:14] [info] DB_WARN003 -> 161
[2025-10-12 15:12:14] [info] DRC_ERR9 -> 89
[2025-10-12 15:12:14] [info] DRC_ERR -> 67
[2025-10-12 15:12:14] [info] DRC_ERR5 -> 408
[2025-10-12 15:12:14] [info] DRC_ERR4 -> 241
[2025-10-12 15:12:14] [info] DRC_ERR1 -> 301
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Rule Violation Summary
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:12:14] [info] |-----|-----|-----|
[2025-10-12 15:12:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:12:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:12:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:12:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:12:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 15:12:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:12:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:12:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:12:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:12:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:12:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:12:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:12:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:12:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:12:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:12:14] [info]
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Performance Metrics
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] | Stage | Time Taken |
[2025-10-12 15:12:14] [info] |-----|-----|
[2025-10-12 15:12:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:12:14] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:12:14] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Summary
[2025-10-12 15:12:14] [info] -----
[2025-10-12 15:12:14] [info] Total Errors: 1120
[2025-10-12 15:12:14] [info] Total Warnings: 321
[2025-10-12 15:12:14] [info] DRC Status: FAILED
```

lvr_summary_284.rpt

```
[2025-10-12 15:13:10] [info] =====
[2025-10-12 15:13:10] [info] Design Rule Check Report
[2025-10-12 15:13:10] [info] =====

[2025-10-12 15:13:10] [info] Report Generated: 2025-10-12
[2025-10-12 15:13:10] [info] Run Start Time: 03:12:19 PM
[2025-10-12 15:13:10] [info] Run End Time: 03:13:10 PM
[2025-10-12 15:13:10] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:13:10] [info] Tool Version: LVR_2025.1
[2025-10-12 15:13:10] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:13:10] [info] User Name: ubuntu
[2025-10-12 15:13:10] [info] Cores Used: 8
[2025-10-12 15:13:10] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Top Cell Information
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Layout Top Cell: TOP
[2025-10-12 15:13:10] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:13:10] [info] Database Format: GDSII
[2025-10-12 15:13:10] [info] Verification Mode: Full Check

[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Type of Errors | Count
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] DB_WARN -> 1
[2025-10-12 15:13:10] [info] LRF_ERR001 -> 14
[2025-10-12 15:13:10] [info] DB_WARN002 -> 159
[2025-10-12 15:13:10] [info] DB_WARN003 -> 161
```

```
[2025-10-12 15:13:10] [info] DRC_ERR9 -> 89
[2025-10-12 15:13:10] [info] DRC_ERR -> 67
[2025-10-12 15:13:10] [info] DRC_ERR5 -> 408
[2025-10-12 15:13:10] [info] DRC_ERR4 -> 241
[2025-10-12 15:13:10] [info] DRC_ERR1 -> 301
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Rule Violation Summary
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] | Rule ID          | Description                                     | Error Count |
[2025-10-12 15:13:10] [info] |-----|-----|-----|
[2025-10-12 15:13:10] [info] | m5.1             | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:13:10] [info] | li.5             | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:13:10] [info] | poly.1a          | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:13:10] [info] | poly.7           | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:13:10] [info] | licon.15         | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:13:10] [info] | m1.6             | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 15:13:10] [info] | m4.5             | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:13:10] [info] | m4.5ab           | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:13:10] [info] | psdm.1           | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:13:10] [info] | licon.5          | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:13:10] [info] | li.1             | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:13:10] [info] | li.1a            | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:13:10] [info] | m1.1             | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:13:10] [info] | licon.8a         | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:13:10] [info] | npc.1            | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Performance Metrics
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] | Stage              | Time Taken |
[2025-10-12 15:13:10] [info] |-----|-----|
[2025-10-12 15:13:10] [info] | GDS Parsing        | 0 minutes 9 seconds |
[2025-10-12 15:13:10] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 15:13:10] [info] | Report Generation   | 0 minutes 0 seconds |
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Summary
[2025-10-12 15:13:10] [info] -----
[2025-10-12 15:13:10] [info] Total Errors:      1120
[2025-10-12 15:13:10] [info] Total Warnings:    321
[2025-10-12 15:13:10] [info] DRC Status:        FAILED
```

lvr_summary_285.rpt

```
[2025-10-12 15:14:06] [info] =====
[2025-10-12 15:14:06] [info] Design Rule Check Report
[2025-10-12 15:14:06] [info] =====

[2025-10-12 15:14:06] [info] Report Generated:    2025-10-12
[2025-10-12 15:14:06] [info] Run Start Time:      03:13:16 PM
[2025-10-12 15:14:06] [info] Run End Time:        03:14:06 PM
[2025-10-12 15:14:06] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 15:14:06] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:14:06] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:14:06] [info] User Name:           ubuntu
[2025-10-12 15:14:06] [info] Cores Used:          8
[2025-10-12 15:14:06] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Top Cell Information
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Layout Top Cell:      TOP
[2025-10-12 15:14:06] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:14:06] [info] Database Format:      GDSII
[2025-10-12 15:14:06] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Type of Errors | Count
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] DB_WARN -> 1
[2025-10-12 15:14:06] [info] LRF_ERR001 -> 14
[2025-10-12 15:14:06] [info] DB_WARN002 -> 159
[2025-10-12 15:14:06] [info] DB_WARN003 -> 161
[2025-10-12 15:14:06] [info] DRC_ERR9 -> 89
[2025-10-12 15:14:06] [info] DRC_ERR -> 67
[2025-10-12 15:14:06] [info] DRC_ERR5 -> 408
[2025-10-12 15:14:06] [info] DRC_ERR4 -> 241
[2025-10-12 15:14:06] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Rule Violation Summary
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:14:06] [info] |-----|-----|-----|
[2025-10-12 15:14:06] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:14:06] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:14:06] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:14:06] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:14:06] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:14:06] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:14:06] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:14:06] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:14:06] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:14:06] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:14:06] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:14:06] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:14:06] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:14:06] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:14:06] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:14:06] [info] |
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Performance Metrics
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] | Stage          | Time Taken |
[2025-10-12 15:14:06] [info] |-----|-----|
[2025-10-12 15:14:06] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:14:06] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 15:14:06] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Summary
```

```
[2025-10-12 15:14:06] [info] -----
[2025-10-12 15:14:06] [info] Total Errors:      1120
[2025-10-12 15:14:06] [info] Total Warnings:    321
[2025-10-12 15:14:06] [info] DRC Status:        FAILED
```

lvr_summary_286.rpt

```
[2025-10-12 15:15:04] [info] =====
[2025-10-12 15:15:04] [info]                Design Rule Check Report
[2025-10-12 15:15:04] [info] =====

[2025-10-12 15:15:04] [info] Report Generated:    2025-10-12
[2025-10-12 15:15:04] [info] Run Start Time:      03:14:12 PM
[2025-10-12 15:15:04] [info] Run End Time:        03:15:04 PM
[2025-10-12 15:15:04] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 15:15:04] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:15:04] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:15:04] [info] User Name:           ubuntu
[2025-10-12 15:15:04] [info] Cores Used:          8
[2025-10-12 15:15:04] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info]                Top Cell Information
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info] Layout Top Cell:     TOP
[2025-10-12 15:15:04] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:15:04] [info] Database Format:      GDSII
[2025-10-12 15:15:04] [info] Verification Mode:    Full Check

[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info]                Type of Errors | Count
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info] DB_WARN -> 1
[2025-10-12 15:15:04] [info] LRF_ERR001 -> 14
[2025-10-12 15:15:04] [info] DB_WARN002 -> 159
[2025-10-12 15:15:04] [info] DB_WARN003 -> 161
[2025-10-12 15:15:04] [info] DRC_ERR9 -> 89
[2025-10-12 15:15:04] [info] DRC_ERR -> 67
[2025-10-12 15:15:04] [info] DRC_ERR5 -> 408
[2025-10-12 15:15:04] [info] DRC_ERR4 -> 241
[2025-10-12 15:15:04] [info] DRC_ERR1 -> 301
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info]                Rule Violation Summary
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:15:04] [info] |-----|-----|-----|
[2025-10-12 15:15:04] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:15:04] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:15:04] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:15:04] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:15:04] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:15:04] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:15:04] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:15:04] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:15:04] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:15:04] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:15:04] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:15:04] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:15:04] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:15:04] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:15:04] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:15:04] [info]
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info]                Performance Metrics
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:15:04] [info] |-----|-----|
[2025-10-12 15:15:04] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 15:15:04] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 15:15:04] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info]                Summary
[2025-10-12 15:15:04] [info] -----
[2025-10-12 15:15:04] [info] Total Errors:      1120
[2025-10-12 15:15:04] [info] Total Warnings:    321
[2025-10-12 15:15:04] [info] DRC Status:        FAILED
```

lvr_summary_287.rpt

```
[2025-10-12 15:16:00] [info] =====
[2025-10-12 15:16:00] [info]                Design Rule Check Report
[2025-10-12 15:16:00] [info] =====

[2025-10-12 15:16:00] [info] Report Generated:   2025-10-12
[2025-10-12 15:16:00] [info] Run Start Time:     03:15:09 PM
[2025-10-12 15:16:00] [info] Run End Time:       03:16:00 PM
[2025-10-12 15:16:00] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 15:16:00] [info] Tool Version:       LVR_2025.1
[2025-10-12 15:16:00] [info] Host Name:          ip-172-31-37-173
[2025-10-12 15:16:00] [info] User Name:          ubuntu
[2025-10-12 15:16:00] [info] Cores Used:         8
[2025-10-12 15:16:00] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info]                Top Cell Information
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] Layout Top Cell:    TOP
[2025-10-12 15:16:00] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 15:16:00] [info] Database Format:      GDSII
[2025-10-12 15:16:00] [info] Verification Mode:   Full Check

[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info]                Type of Errors | Count
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] DB_WARN -> 1
[2025-10-12 15:16:00] [info] LRF_ERR001 -> 14
[2025-10-12 15:16:00] [info] DB_WARN002 -> 159
[2025-10-12 15:16:00] [info] DB_WARN003 -> 161
[2025-10-12 15:16:00] [info] DRC_ERR9 -> 89
[2025-10-12 15:16:00] [info] DRC_ERR -> 67
[2025-10-12 15:16:00] [info] DRC_ERR5 -> 408
[2025-10-12 15:16:00] [info] DRC_ERR4 -> 241
[2025-10-12 15:16:00] [info] DRC_ERR1 -> 301
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info]                Rule Violation Summary
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:16:00] [info] |-----|-----|-----|
[2025-10-12 15:16:00] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:16:00] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:16:00] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:16:00] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:16:00] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



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[2025-10-12 15:16:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:16:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:16:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:16:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:16:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:16:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:16:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:16:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:16:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:16:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:16:00] [info]
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] Performance Metrics
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] | Stage | Time Taken |
[2025-10-12 15:16:00] [info] |-----|-----|
[2025-10-12 15:16:00] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:16:00] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:16:00] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] Summary
[2025-10-12 15:16:00] [info] -----
[2025-10-12 15:16:00] [info] Total Errors: 1120
[2025-10-12 15:16:00] [info] Total Warnings: 321
[2025-10-12 15:16:00] [info] DRC Status: FAILED

```

lvr_summary_288.rpt

```

[2025-10-12 15:16:58] [info] =====
[2025-10-12 15:16:58] [info] Design Rule Check Report
[2025-10-12 15:16:58] [info] =====

[2025-10-12 15:16:58] [info] Report Generated: 2025-10-12
[2025-10-12 15:16:58] [info] Run Start Time: 03:16:06 PM
[2025-10-12 15:16:58] [info] Run End Time: 03:16:58 PM
[2025-10-12 15:16:58] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 15:16:58] [info] Tool Version: LVR_2025.1
[2025-10-12 15:16:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:16:58] [info] User Name: ubuntu
[2025-10-12 15:16:58] [info] Cores Used: 8
[2025-10-12 15:16:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Top Cell Information
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Layout Top Cell: TOP
[2025-10-12 15:16:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:16:58] [info] Database Format: GDSII
[2025-10-12 15:16:58] [info] Verification Mode: Full Check

[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Type of Errors | Count
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] DB_WARN -> 1
[2025-10-12 15:16:58] [info] LRF_ERR001 -> 14
[2025-10-12 15:16:58] [info] DB_WARN002 -> 159
[2025-10-12 15:16:58] [info] DB_WARN003 -> 161

```

```

[2025-10-12 15:16:58] [info] DRC_ERR9 -> 89
[2025-10-12 15:16:58] [info] DRC_ERR -> 67
[2025-10-12 15:16:58] [info] DRC_ERR5 -> 408
[2025-10-12 15:16:58] [info] DRC_ERR4 -> 241
[2025-10-12 15:16:58] [info] DRC_ERR1 -> 301
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Rule Violation Summary
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:16:58] [info] |-----|-----|-----|
[2025-10-12 15:16:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:16:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:16:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:16:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:16:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:16:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:16:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:16:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:16:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:16:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:16:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:16:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:16:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:16:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:16:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Performance Metrics
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] | Stage | Time Taken |
[2025-10-12 15:16:58] [info] |-----|-----|
[2025-10-12 15:16:58] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:16:58] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:16:58] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Summary
[2025-10-12 15:16:58] [info] -----
[2025-10-12 15:16:58] [info] Total Errors: 1120
[2025-10-12 15:16:58] [info] Total Warnings: 321
[2025-10-12 15:16:58] [info] DRC Status: FAILED

```

lvr_summary_289.rpt

```

[2025-10-12 15:17:54] [info] =====
[2025-10-12 15:17:54] [info] Design Rule Check Report
[2025-10-12 15:17:54] [info] =====

[2025-10-12 15:17:54] [info] Report Generated: 2025-10-12
[2025-10-12 15:17:54] [info] Run Start Time: 03:17:03 PM
[2025-10-12 15:17:54] [info] Run End Time: 03:17:54 PM
[2025-10-12 15:17:54] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:17:54] [info] Tool Version: LVR_2025.1
[2025-10-12 15:17:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:17:54] [info] User Name: ubuntu
[2025-10-12 15:17:54] [info] Cores Used: 8
[2025-10-12 15:17:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Top Cell Information
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Layout Top Cell:      TOP
[2025-10-12 15:17:54] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:17:54] [info] Database Format:      GDSII
[2025-10-12 15:17:54] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Type of Errors | Count
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] DB_WARN -> 1
[2025-10-12 15:17:54] [info] LRF_ERR001 -> 14
[2025-10-12 15:17:54] [info] DB_WARN002 -> 159
[2025-10-12 15:17:54] [info] DB_WARN003 -> 161
[2025-10-12 15:17:54] [info] DRC_ERR9 -> 89
[2025-10-12 15:17:54] [info] DRC_ERR -> 67
[2025-10-12 15:17:54] [info] DRC_ERR5 -> 408
[2025-10-12 15:17:54] [info] DRC_ERR4 -> 241
[2025-10-12 15:17:54] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Rule Violation Summary
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:17:54] [info] |-----|-----|-----|
[2025-10-12 15:17:54] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:17:54] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:17:54] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:17:54] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:17:54] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:17:54] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:17:54] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:17:54] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:17:54] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:17:54] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:17:54] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:17:54] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:17:54] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:17:54] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:17:54] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:17:54] [info] |
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Performance Metrics
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] | Stage          | Time Taken |
[2025-10-12 15:17:54] [info] |-----|-----|
[2025-10-12 15:17:54] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:17:54] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 15:17:54] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Summary
```

```
[2025-10-12 15:17:54] [info] -----
[2025-10-12 15:17:54] [info] Total Errors:      1120
[2025-10-12 15:17:54] [info] Total Warnings:    321
[2025-10-12 15:17:54] [info] DRC Status:        FAILED
```

lvr_summary_29.rpt

```
[2025-10-12 11:08:52] [info] =====
[2025-10-12 11:08:52] [info]                      Design Rule Check Report
[2025-10-12 11:08:52] [info] =====

[2025-10-12 11:08:52] [info] Report Generated:    2025-10-12
[2025-10-12 11:08:52] [info] Run Start Time:      11:08:00 AM
[2025-10-12 11:08:52] [info] Run End Time:        11:08:52 AM
[2025-10-12 11:08:52] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:08:52] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:08:52] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:08:52] [info] User Name:           ubuntu
[2025-10-12 11:08:52] [info] Cores Used:          8
[2025-10-12 11:08:52] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info]                      Top Cell Information
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info] Layout Top Cell:     TOP
[2025-10-12 11:08:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:08:52] [info] Database Format:      GDSII
[2025-10-12 11:08:52] [info] Verification Mode:    Full Check

[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info]                      Type of Errors | Count
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info] DB_WARN -> 1
[2025-10-12 11:08:52] [info] LRF_ERR001 -> 14
[2025-10-12 11:08:52] [info] DB_WARN002 -> 159
[2025-10-12 11:08:52] [info] DB_WARN003 -> 161
[2025-10-12 11:08:52] [info] DRC_ERR9 -> 89
[2025-10-12 11:08:52] [info] DRC_ERR -> 67
[2025-10-12 11:08:52] [info] DRC_ERR5 -> 408
[2025-10-12 11:08:52] [info] DRC_ERR4 -> 241
[2025-10-12 11:08:52] [info] DRC_ERR1 -> 301
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info]                      Rule Violation Summary
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:08:52] [info] |-----|-----|-----|
[2025-10-12 11:08:52] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:08:52] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:08:52] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:08:52] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:08:52] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:08:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:08:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:08:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:08:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:08:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:08:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:08:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:08:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:08:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:08:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:08:52] [info]
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info]                      Performance Metrics
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:08:52] [info] |-----|-----|
[2025-10-12 11:08:52] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:08:52] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 11:08:52] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info]                Summary
[2025-10-12 11:08:52] [info] -----
[2025-10-12 11:08:52] [info] Total Errors:    1120
[2025-10-12 11:08:52] [info] Total Warnings:    321
[2025-10-12 11:08:52] [info] DRC Status:      FAILED
```

lvr_summary_290.rpt

```
[2025-10-12 15:18:52] [info] =====
[2025-10-12 15:18:52] [info]                Design Rule Check Report
[2025-10-12 15:18:52] [info] =====

[2025-10-12 15:18:52] [info] Report Generated:    2025-10-12
[2025-10-12 15:18:52] [info] Run Start Time:      03:18:00 PM
[2025-10-12 15:18:52] [info] Run End Time:        03:18:52 PM
[2025-10-12 15:18:52] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 15:18:52] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:18:52] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:18:52] [info] User Name:           ubuntu
[2025-10-12 15:18:52] [info] Cores Used:          8
[2025-10-12 15:18:52] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info]                Top Cell Information
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] Layout Top Cell:     TOP
[2025-10-12 15:18:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:18:52] [info] Database Format:       GDSII
[2025-10-12 15:18:52] [info] Verification Mode:    Full Check

[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info]                Type of Errors | Count
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] DB_WARN -> 1
[2025-10-12 15:18:52] [info] LRF_ERR001 -> 14
[2025-10-12 15:18:52] [info] DB_WARN002 -> 159
[2025-10-12 15:18:52] [info] DB_WARN003 -> 161
[2025-10-12 15:18:52] [info] DRC_ERR9 -> 89
[2025-10-12 15:18:52] [info] DRC_ERR -> 67
[2025-10-12 15:18:52] [info] DRC_ERR5 -> 408
[2025-10-12 15:18:52] [info] DRC_ERR4 -> 241
[2025-10-12 15:18:52] [info] DRC_ERR1 -> 301
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info]                Rule Violation Summary
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:18:52] [info] |-----|-----|-----|
[2025-10-12 15:18:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:18:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:18:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:18:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:18:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 15:18:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:18:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:18:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:18:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:18:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:18:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:18:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:18:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:18:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:18:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:18:52] [info]
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] Performance Metrics
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] | Stage | Time Taken |
[2025-10-12 15:18:52] [info] |-----|-----|
[2025-10-12 15:18:52] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:18:52] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:18:52] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] Summary
[2025-10-12 15:18:52] [info] -----
[2025-10-12 15:18:52] [info] Total Errors: 1120
[2025-10-12 15:18:52] [info] Total Warnings: 321
[2025-10-12 15:18:52] [info] DRC Status: FAILED
```

lvr_summary_291.rpt

```
[2025-10-12 15:19:48] [info] =====
[2025-10-12 15:19:48] [info] Design Rule Check Report
[2025-10-12 15:19:48] [info] =====

[2025-10-12 15:19:48] [info] Report Generated: 2025-10-12
[2025-10-12 15:19:48] [info] Run Start Time: 03:18:57 PM
[2025-10-12 15:19:48] [info] Run End Time: 03:19:48 PM
[2025-10-12 15:19:48] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:19:48] [info] Tool Version: LVR_2025.1
[2025-10-12 15:19:48] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:19:48] [info] User Name: ubuntu
[2025-10-12 15:19:48] [info] Cores Used: 8
[2025-10-12 15:19:48] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Top Cell Information
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Layout Top Cell: TOP
[2025-10-12 15:19:48] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:19:48] [info] Database Format: GDSII
[2025-10-12 15:19:48] [info] Verification Mode: Full Check

[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Type of Errors | Count
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] DB_WARN -> 1
[2025-10-12 15:19:48] [info] LRF_ERR001 -> 14
[2025-10-12 15:19:48] [info] DB_WARN002 -> 159
[2025-10-12 15:19:48] [info] DB_WARN003 -> 161
```

```

[2025-10-12 15:19:48] [info] DRC_ERR9 -> 89
[2025-10-12 15:19:48] [info] DRC_ERR -> 67
[2025-10-12 15:19:48] [info] DRC_ERR5 -> 408
[2025-10-12 15:19:48] [info] DRC_ERR4 -> 241
[2025-10-12 15:19:48] [info] DRC_ERR1 -> 301
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Rule Violation Summary
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:19:48] [info] |-----|-----|-----|
[2025-10-12 15:19:48] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:19:48] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:19:48] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:19:48] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:19:48] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:19:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:19:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:19:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:19:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:19:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:19:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:19:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:19:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:19:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:19:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Performance Metrics
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] | Stage | Time Taken |
[2025-10-12 15:19:48] [info] |-----|-----|
[2025-10-12 15:19:48] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:19:48] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:19:48] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Summary
[2025-10-12 15:19:48] [info] -----
[2025-10-12 15:19:48] [info] Total Errors: 1120
[2025-10-12 15:19:48] [info] Total Warnings: 321
[2025-10-12 15:19:48] [info] DRC Status: FAILED

```

lvr_summary_292.rpt

```

[2025-10-12 15:20:46] [info] =====
[2025-10-12 15:20:46] [info] Design Rule Check Report
[2025-10-12 15:20:46] [info] =====

[2025-10-12 15:20:46] [info] Report Generated: 2025-10-12
[2025-10-12 15:20:46] [info] Run Start Time: 03:19:54 PM
[2025-10-12 15:20:46] [info] Run End Time: 03:20:46 PM
[2025-10-12 15:20:46] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:20:46] [info] Tool Version: LVR_2025.1
[2025-10-12 15:20:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:20:46] [info] User Name: ubuntu
[2025-10-12 15:20:46] [info] Cores Used: 8
[2025-10-12 15:20:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] Top Cell Information
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] Layout Top Cell:      TOP
[2025-10-12 15:20:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:20:46] [info] Database Format:      GDSII
[2025-10-12 15:20:46] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] Type of Errors | Count
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] DB_WARN -> 1
[2025-10-12 15:20:46] [info] LRF_ERR001 -> 14
[2025-10-12 15:20:46] [info] DB_WARN002 -> 159
[2025-10-12 15:20:46] [info] DB_WARN003 -> 161
[2025-10-12 15:20:46] [info] DRC_ERR9 -> 89
[2025-10-12 15:20:46] [info] DRC_ERR -> 67
[2025-10-12 15:20:46] [info] DRC_ERR5 -> 408
[2025-10-12 15:20:46] [info] DRC_ERR4 -> 241
[2025-10-12 15:20:46] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] Rule Violation Summary
[2025-10-12 15:20:46] [info] -----
```

[2025-10-12 15:20:46] [info]	Rule ID	Description	Error Count
[2025-10-12 15:20:46] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 15:20:46] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 15:20:46] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 15:20:46] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 15:20:46] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 15:20:46] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 15:20:46] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 15:20:46] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 15:20:46] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 15:20:46] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 15:20:46] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 15:20:46] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" 15	
[2025-10-12 15:20:46] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 15:20:46] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 15:20:46] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

```
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] Performance Metrics
[2025-10-12 15:20:46] [info] -----
```

[2025-10-12 15:20:46] [info]	Stage	Time Taken
[2025-10-12 15:20:46] [info]	GDS Parsing	0 minutes 9 seconds
[2025-10-12 15:20:46] [info]	Rule Checking	0 minutes 41 seconds
[2025-10-12 15:20:46] [info]	Report Generation	0 minutes 0 seconds

```
[2025-10-12 15:20:46] [info] -----
[2025-10-12 15:20:46] [info] Summary
[2025-10-12 15:20:46] [info] -----
```

```
[2025-10-12 15:20:46] [info] Total Errors:      1120
[2025-10-12 15:20:46] [info] Total Warnings:    321
[2025-10-12 15:20:46] [info] DRC Status:        FAILED
```


lvr_summary_293.rpt

```
[2025-10-12 15:21:42] [info] =====
[2025-10-12 15:21:42] [info] Design Rule Check Report
[2025-10-12 15:21:42] [info] =====

[2025-10-12 15:21:42] [info] Report Generated: 2025-10-12
[2025-10-12 15:21:42] [info] Run Start Time: 03:20:51 PM
[2025-10-12 15:21:42] [info] Run End Time: 03:21:42 PM
[2025-10-12 15:21:42] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:21:42] [info] Tool Version: LVR_2025.1
[2025-10-12 15:21:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:21:42] [info] User Name: ubuntu
[2025-10-12 15:21:42] [info] Cores Used: 8
[2025-10-12 15:21:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] Top Cell Information
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] Layout Top Cell: TOP
[2025-10-12 15:21:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:21:42] [info] Database Format: GDSII
[2025-10-12 15:21:42] [info] Verification Mode: Full Check

[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] Type of Errors | Count
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] DB_WARN -> 1
[2025-10-12 15:21:42] [info] LRF_ERR001 -> 14
[2025-10-12 15:21:42] [info] DB_WARN002 -> 159
[2025-10-12 15:21:42] [info] DB_WARN003 -> 161
[2025-10-12 15:21:42] [info] DRC_ERR9 -> 89
[2025-10-12 15:21:42] [info] DRC_ERR -> 67
[2025-10-12 15:21:42] [info] DRC_ERR5 -> 408
[2025-10-12 15:21:42] [info] DRC_ERR4 -> 241
[2025-10-12 15:21:42] [info] DRC_ERR1 -> 301
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] Rule Violation Summary
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:21:42] [info] |-----|-----|-----|
[2025-10-12 15:21:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:21:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:21:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:21:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:21:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:21:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:21:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:21:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:21:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:21:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:21:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:21:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:21:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:21:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:21:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:21:42] [info]
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] Performance Metrics
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:21:42] [info] |-----|-----|
[2025-10-12 15:21:42] [info] | GDS Parsing          | 0 minutes 10 seconds |
[2025-10-12 15:21:42] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 15:21:42] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info]                Summary
[2025-10-12 15:21:42] [info] -----
[2025-10-12 15:21:42] [info] Total Errors:      1120
[2025-10-12 15:21:42] [info] Total Warnings:    321
[2025-10-12 15:21:42] [info] DRC Status:        FAILED
```

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```
[2025-10-12 15:22:40] [info] =====
[2025-10-12 15:22:40] [info]                Design Rule Check Report
[2025-10-12 15:22:40] [info] =====

[2025-10-12 15:22:40] [info] Report Generated:   2025-10-12
[2025-10-12 15:22:40] [info] Run Start Time:     03:21:50 PM
[2025-10-12 15:22:40] [info] Run End Time:       03:22:40 PM
[2025-10-12 15:22:40] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 15:22:40] [info] Tool Version:       LVR_2025.1
[2025-10-12 15:22:40] [info] Host Name:          ip-172-31-37-173
[2025-10-12 15:22:40] [info] User Name:          ubuntu
[2025-10-12 15:22:40] [info] Cores Used:         8
[2025-10-12 15:22:40] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info]                Top Cell Information
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] Layout Top Cell:    TOP
[2025-10-12 15:22:40] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 15:22:40] [info] Database Format:      GDSII
[2025-10-12 15:22:40] [info] Verification Mode:   Full Check

[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info]                Type of Errors | Count
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] DB_WARN -> 1
[2025-10-12 15:22:40] [info] LRF_ERR001 -> 14
[2025-10-12 15:22:40] [info] DB_WARN002 -> 159
[2025-10-12 15:22:40] [info] DB_WARN003 -> 161
[2025-10-12 15:22:40] [info] DRC_ERR9 -> 89
[2025-10-12 15:22:40] [info] DRC_ERR -> 67
[2025-10-12 15:22:40] [info] DRC_ERR5 -> 408
[2025-10-12 15:22:40] [info] DRC_ERR4 -> 241
[2025-10-12 15:22:40] [info] DRC_ERR1 -> 301
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info]                Rule Violation Summary
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:22:40] [info] |-----|-----|-----|
[2025-10-12 15:22:40] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:22:40] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:22:40] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:22:40] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:22:40] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 15:22:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:22:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:22:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:22:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:22:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:22:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:22:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:22:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:22:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:22:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:22:40] [info]
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] Performance Metrics
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] | Stage | Time Taken |
[2025-10-12 15:22:40] [info] |-----|-----|
[2025-10-12 15:22:40] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:22:40] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:22:40] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] Summary
[2025-10-12 15:22:40] [info] -----
[2025-10-12 15:22:40] [info] Total Errors: 1120
[2025-10-12 15:22:40] [info] Total Warnings: 321
[2025-10-12 15:22:40] [info] DRC Status: FAILED

```

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```

[2025-10-12 15:23:37] [info] =====
[2025-10-12 15:23:37] [info] Design Rule Check Report
[2025-10-12 15:23:37] [info] =====

[2025-10-12 15:23:37] [info] Report Generated: 2025-10-12
[2025-10-12 15:23:37] [info] Run Start Time: 03:22:46 PM
[2025-10-12 15:23:37] [info] Run End Time: 03:23:37 PM
[2025-10-12 15:23:37] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:23:37] [info] Tool Version: LVR_2025.1
[2025-10-12 15:23:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:23:37] [info] User Name: ubuntu
[2025-10-12 15:23:37] [info] Cores Used: 8
[2025-10-12 15:23:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Top Cell Information
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Layout Top Cell: TOP
[2025-10-12 15:23:37] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:23:37] [info] Database Format: GDSII
[2025-10-12 15:23:37] [info] Verification Mode: Full Check

[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Type of Errors | Count
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] DB_WARN -> 1
[2025-10-12 15:23:37] [info] LRF_ERR001 -> 14
[2025-10-12 15:23:37] [info] DB_WARN002 -> 159
[2025-10-12 15:23:37] [info] DB_WARN003 -> 161

```

```

[2025-10-12 15:23:37] [info] DRC_ERR9 -> 89
[2025-10-12 15:23:37] [info] DRC_ERR -> 67
[2025-10-12 15:23:37] [info] DRC_ERR5 -> 408
[2025-10-12 15:23:37] [info] DRC_ERR4 -> 241
[2025-10-12 15:23:37] [info] DRC_ERR1 -> 301
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Rule Violation Summary
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:23:37] [info] |-----|-----|-----|
[2025-10-12 15:23:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:23:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:23:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:23:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:23:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:23:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 15:23:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:23:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:23:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:23:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:23:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:23:37] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:23:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:23:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:23:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Performance Metrics
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] | Stage          | Time Taken |
[2025-10-12 15:23:37] [info] |-----|-----|
[2025-10-12 15:23:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:23:37] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 15:23:37] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Summary
[2025-10-12 15:23:37] [info] -----
[2025-10-12 15:23:37] [info] Total Errors: 1120
[2025-10-12 15:23:37] [info] Total Warnings: 321
[2025-10-12 15:23:37] [info] DRC Status: FAILED

```

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```

[2025-10-12 15:24:37] [info] =====
[2025-10-12 15:24:37] [info] Design Rule Check Report
[2025-10-12 15:24:37] [info] =====

[2025-10-12 15:24:37] [info] Report Generated: 2025-10-12
[2025-10-12 15:24:37] [info] Run Start Time: 03:23:46 PM
[2025-10-12 15:24:37] [info] Run End Time: 03:24:37 PM
[2025-10-12 15:24:37] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:24:37] [info] Tool Version: LVR_2025.1
[2025-10-12 15:24:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:24:37] [info] User Name: ubuntu
[2025-10-12 15:24:37] [info] Cores Used: 8
[2025-10-12 15:24:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Top Cell Information
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Layout Top Cell:      TOP
[2025-10-12 15:24:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:24:37] [info] Database Format:      GDSII
[2025-10-12 15:24:37] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Type of Errors | Count
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] DB_WARN -> 1
[2025-10-12 15:24:37] [info] LRF_ERR001 -> 14
[2025-10-12 15:24:37] [info] DB_WARN002 -> 159
[2025-10-12 15:24:37] [info] DB_WARN003 -> 161
[2025-10-12 15:24:37] [info] DRC_ERR9 -> 89
[2025-10-12 15:24:37] [info] DRC_ERR -> 67
[2025-10-12 15:24:37] [info] DRC_ERR5 -> 408
[2025-10-12 15:24:37] [info] DRC_ERR4 -> 241
[2025-10-12 15:24:37] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Rule Violation Summary
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:24:37] [info] |-----|-----|-----|
[2025-10-12 15:24:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:24:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:24:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:24:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:24:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:24:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:24:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:24:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:24:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:24:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:24:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:24:37] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:24:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:24:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:24:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:24:37] [info] |
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Performance Metrics
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] | Stage          | Time Taken |
[2025-10-12 15:24:37] [info] |-----|-----|
[2025-10-12 15:24:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:24:37] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 15:24:37] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Summary
```

```
[2025-10-12 15:24:37] [info] -----
[2025-10-12 15:24:37] [info] Total Errors:      1120
[2025-10-12 15:24:37] [info] Total Warnings:    321
[2025-10-12 15:24:37] [info] DRC Status:        FAILED
```

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```
[2025-10-12 15:25:36] [info] =====
[2025-10-12 15:25:36] [info]                      Design Rule Check Report
[2025-10-12 15:25:36] [info] =====

[2025-10-12 15:25:36] [info] Report Generated:      2025-10-12
[2025-10-12 15:25:36] [info] Run Start Time:       03:24:44 PM
[2025-10-12 15:25:36] [info] Run End Time:         03:25:36 PM
[2025-10-12 15:25:36] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 15:25:36] [info] Tool Version:          LVR_2025.1
[2025-10-12 15:25:36] [info] Host Name:             ip-172-31-37-173
[2025-10-12 15:25:36] [info] User Name:             ubuntu
[2025-10-12 15:25:36] [info] Cores Used:            8
[2025-10-12 15:25:36] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info]                      Top Cell Information
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info] Layout Top Cell:      TOP
[2025-10-12 15:25:36] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 15:25:36] [info] Database Format:        GDSII
[2025-10-12 15:25:36] [info] Verification Mode:     Full Check

[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info]                      Type of Errors | Count
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info] DB_WARN -> 1
[2025-10-12 15:25:36] [info] LRF_ERR001 -> 14
[2025-10-12 15:25:36] [info] DB_WARN002 -> 159
[2025-10-12 15:25:36] [info] DB_WARN003 -> 161
[2025-10-12 15:25:36] [info] DRC_ERR9 -> 89
[2025-10-12 15:25:36] [info] DRC_ERR -> 67
[2025-10-12 15:25:36] [info] DRC_ERR5 -> 408
[2025-10-12 15:25:36] [info] DRC_ERR4 -> 241
[2025-10-12 15:25:36] [info] DRC_ERR1 -> 301
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info]                      Rule Violation Summary
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:25:36] [info] |-----|-----|-----|
[2025-10-12 15:25:36] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:25:36] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:25:36] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:25:36] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:25:36] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:25:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:25:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:25:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:25:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:25:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:25:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:25:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:25:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:25:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:25:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:25:36] [info]
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info]                      Performance Metrics
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:25:36] [info] |-----|-----|
[2025-10-12 15:25:36] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:25:36] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 15:25:36] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info]                Summary
[2025-10-12 15:25:36] [info] -----
[2025-10-12 15:25:36] [info] Total Errors:    1120
[2025-10-12 15:25:36] [info] Total Warnings:    321
[2025-10-12 15:25:36] [info] DRC Status:      FAILED
```

lvr_summary_298.rpt

```
[2025-10-12 15:26:34] [info] =====
[2025-10-12 15:26:34] [info]                Design Rule Check Report
[2025-10-12 15:26:34] [info] =====

[2025-10-12 15:26:34] [info] Report Generated:    2025-10-12
[2025-10-12 15:26:34] [info] Run Start Time:      03:25:43 PM
[2025-10-12 15:26:34] [info] Run End Time:        03:26:34 PM
[2025-10-12 15:26:34] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 15:26:34] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:26:34] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:26:34] [info] User Name:           ubuntu
[2025-10-12 15:26:34] [info] Cores Used:          8
[2025-10-12 15:26:34] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info]                Top Cell Information
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] Layout Top Cell:     TOP
[2025-10-12 15:26:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:26:34] [info] Database Format:       GDSII
[2025-10-12 15:26:34] [info] Verification Mode:    Full Check

[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info]                Type of Errors | Count
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] DB_WARN -> 1
[2025-10-12 15:26:34] [info] LRF_ERR001 -> 14
[2025-10-12 15:26:34] [info] DB_WARN002 -> 159
[2025-10-12 15:26:34] [info] DB_WARN003 -> 161
[2025-10-12 15:26:34] [info] DRC_ERR9 -> 89
[2025-10-12 15:26:34] [info] DRC_ERR -> 67
[2025-10-12 15:26:34] [info] DRC_ERR5 -> 408
[2025-10-12 15:26:34] [info] DRC_ERR4 -> 241
[2025-10-12 15:26:34] [info] DRC_ERR1 -> 301
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info]                Rule Violation Summary
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:26:34] [info] |-----|-----|-----|
[2025-10-12 15:26:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:26:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:26:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:26:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:26:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 15:26:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:26:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:26:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:26:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:26:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:26:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:26:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:26:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:26:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:26:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:26:34] [info]
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] Performance Metrics
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] | Stage | Time Taken |
[2025-10-12 15:26:34] [info] |-----|-----|
[2025-10-12 15:26:34] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:26:34] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:26:34] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] Summary
[2025-10-12 15:26:34] [info] -----
[2025-10-12 15:26:34] [info] Total Errors: 1120
[2025-10-12 15:26:34] [info] Total Warnings: 321
[2025-10-12 15:26:34] [info] DRC Status: FAILED
```

lvr_summary_299.rpt

```
[2025-10-12 15:27:30] [info] =====
[2025-10-12 15:27:30] [info] Design Rule Check Report
[2025-10-12 15:27:30] [info] =====

[2025-10-12 15:27:30] [info] Report Generated: 2025-10-12
[2025-10-12 15:27:30] [info] Run Start Time: 03:26:39 PM
[2025-10-12 15:27:30] [info] Run End Time: 03:27:30 PM
[2025-10-12 15:27:30] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:27:30] [info] Tool Version: LVR_2025.1
[2025-10-12 15:27:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:27:30] [info] User Name: ubuntu
[2025-10-12 15:27:30] [info] Cores Used: 8
[2025-10-12 15:27:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Top Cell Information
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Layout Top Cell: TOP
[2025-10-12 15:27:30] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:27:30] [info] Database Format: GDSII
[2025-10-12 15:27:30] [info] Verification Mode: Full Check

[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Type of Errors | Count
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] DB_WARN -> 1
[2025-10-12 15:27:30] [info] LRF_ERR001 -> 14
[2025-10-12 15:27:30] [info] DB_WARN002 -> 159
[2025-10-12 15:27:30] [info] DB_WARN003 -> 161
```



```

[2025-10-12 15:27:30] [info] DRC_ERR9 -> 89
[2025-10-12 15:27:30] [info] DRC_ERR -> 67
[2025-10-12 15:27:30] [info] DRC_ERR5 -> 408
[2025-10-12 15:27:30] [info] DRC_ERR4 -> 241
[2025-10-12 15:27:30] [info] DRC_ERR1 -> 301
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Rule Violation Summary
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:27:30] [info] |-----|-----|-----|
[2025-10-12 15:27:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:27:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:27:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:27:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:27:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:27:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:27:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:27:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:27:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:27:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:27:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:27:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:27:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:27:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:27:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Performance Metrics
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] | Stage | Time Taken |
[2025-10-12 15:27:30] [info] |-----|-----|
[2025-10-12 15:27:30] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:27:30] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:27:30] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Summary
[2025-10-12 15:27:30] [info] -----
[2025-10-12 15:27:30] [info] Total Errors: 1120
[2025-10-12 15:27:30] [info] Total Warnings: 321
[2025-10-12 15:27:30] [info] DRC Status: FAILED

```

lvr_summary_3.rpt

```

[2025-10-12 10:44:19] [info] =====
[2025-10-12 10:44:19] [info] Design Rule Check Report
[2025-10-12 10:44:19] [info] =====

[2025-10-12 10:44:19] [info] Report Generated: 2025-10-12
[2025-10-12 10:44:19] [info] Run Start Time: 10:43:28 AM
[2025-10-12 10:44:19] [info] Run End Time: 10:44:19 AM
[2025-10-12 10:44:19] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:44:19] [info] Tool Version: LVR_2025.1
[2025-10-12 10:44:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:44:19] [info] User Name: ubuntu
[2025-10-12 10:44:19] [info] Cores Used: 8
[2025-10-12 10:44:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Top Cell Information
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Layout Top Cell:      TOP
[2025-10-12 10:44:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:44:19] [info] Database Format:      GDSII
[2025-10-12 10:44:19] [info] Verification Mode:    Full Check
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Type of Errors | Count
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] DB_WARN -> 1
[2025-10-12 10:44:19] [info] LRF_ERR001 -> 14
[2025-10-12 10:44:19] [info] DB_WARN002 -> 159
[2025-10-12 10:44:19] [info] DB_WARN003 -> 161
[2025-10-12 10:44:19] [info] DRC_ERR9 -> 89
[2025-10-12 10:44:19] [info] DRC_ERR -> 67
[2025-10-12 10:44:19] [info] DRC_ERR5 -> 408
[2025-10-12 10:44:19] [info] DRC_ERR4 -> 241
[2025-10-12 10:44:19] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Rule Violation Summary
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 10:44:19] [info] |-----|-----|-----|
[2025-10-12 10:44:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:44:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:44:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:44:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:44:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 10:44:19] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 10:44:19] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 10:44:19] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 10:44:19] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 10:44:19] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 10:44:19] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 10:44:19] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 10:44:19] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 10:44:19] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 10:44:19] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 10:44:19] [info] |
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Performance Metrics
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] | Stage          | Time Taken |
[2025-10-12 10:44:19] [info] |-----|-----|
[2025-10-12 10:44:19] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 10:44:19] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 10:44:19] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Summary
```

```
[2025-10-12 10:44:19] [info] -----
[2025-10-12 10:44:19] [info] Total Errors:      1120
[2025-10-12 10:44:19] [info] Total Warnings:    321
[2025-10-12 10:44:19] [info] DRC Status:        FAILED
```

lvr_summary_30.rpt

```
[2025-10-12 11:09:47] [info] =====
[2025-10-12 11:09:47] [info]                      Design Rule Check Report
[2025-10-12 11:09:47] [info] =====

[2025-10-12 11:09:47] [info] Report Generated:      2025-10-12
[2025-10-12 11:09:47] [info] Run Start Time:       11:08:57 AM
[2025-10-12 11:09:47] [info] Run End Time:         11:09:47 AM
[2025-10-12 11:09:47] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 11:09:47] [info] Tool Version:          LVR_2025.1
[2025-10-12 11:09:47] [info] Host Name:             ip-172-31-37-173
[2025-10-12 11:09:47] [info] User Name:             ubuntu
[2025-10-12 11:09:47] [info] Cores Used:            8
[2025-10-12 11:09:47] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info]                      Top Cell Information
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info] Layout Top Cell:      TOP
[2025-10-12 11:09:47] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 11:09:47] [info] Database Format:        GDSII
[2025-10-12 11:09:47] [info] Verification Mode:     Full Check

[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info]                      Type of Errors | Count
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info] DB_WARN -> 1
[2025-10-12 11:09:47] [info] LRF_ERR001 -> 14
[2025-10-12 11:09:47] [info] DB_WARN002 -> 159
[2025-10-12 11:09:47] [info] DB_WARN003 -> 161
[2025-10-12 11:09:47] [info] DRC_ERR9 -> 89
[2025-10-12 11:09:47] [info] DRC_ERR -> 67
[2025-10-12 11:09:47] [info] DRC_ERR5 -> 408
[2025-10-12 11:09:47] [info] DRC_ERR4 -> 241
[2025-10-12 11:09:47] [info] DRC_ERR1 -> 301
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info]                      Rule Violation Summary
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:09:47] [info] |-----|-----|-----|
[2025-10-12 11:09:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:09:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:09:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:09:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:09:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:09:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:09:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:09:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:09:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:09:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:09:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:09:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:09:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:09:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:09:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:09:47] [info]
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info]                      Performance Metrics
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:09:47] [info] |-----|-----|
[2025-10-12 11:09:47] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:09:47] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:09:47] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info]                Summary
[2025-10-12 11:09:47] [info] -----
[2025-10-12 11:09:47] [info] Total Errors:    1120
[2025-10-12 11:09:47] [info] Total Warnings:    321
[2025-10-12 11:09:47] [info] DRC Status:      FAILED
```

lvr_summary_300.rpt

```
[2025-10-12 15:28:28] [info] =====
[2025-10-12 15:28:28] [info]                Design Rule Check Report
[2025-10-12 15:28:28] [info] =====

[2025-10-12 15:28:28] [info] Report Generated:    2025-10-12
[2025-10-12 15:28:28] [info] Run Start Time:      03:27:36 PM
[2025-10-12 15:28:28] [info] Run End Time:        03:28:28 PM
[2025-10-12 15:28:28] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 15:28:28] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:28:28] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:28:28] [info] User Name:           ubuntu
[2025-10-12 15:28:28] [info] Cores Used:          8
[2025-10-12 15:28:28] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info]                Top Cell Information
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] Layout Top Cell:     TOP
[2025-10-12 15:28:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:28:28] [info] Database Format:       GDSII
[2025-10-12 15:28:28] [info] Verification Mode:    Full Check

[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info]                Type of Errors | Count
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] DB_WARN -> 1
[2025-10-12 15:28:28] [info] LRF_ERR001 -> 14
[2025-10-12 15:28:28] [info] DB_WARN002 -> 159
[2025-10-12 15:28:28] [info] DB_WARN003 -> 161
[2025-10-12 15:28:28] [info] DRC_ERR9 -> 89
[2025-10-12 15:28:28] [info] DRC_ERR -> 67
[2025-10-12 15:28:28] [info] DRC_ERR5 -> 408
[2025-10-12 15:28:28] [info] DRC_ERR4 -> 241
[2025-10-12 15:28:28] [info] DRC_ERR1 -> 301
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info]                Rule Violation Summary
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:28:28] [info] |-----|-----|-----|
[2025-10-12 15:28:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:28:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:28:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:28:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:28:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 15:28:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:28:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:28:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:28:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:28:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:28:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:28:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:28:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:28:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:28:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:28:28] [info]
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] Performance Metrics
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] | Stage | Time Taken |
[2025-10-12 15:28:28] [info] |-----|-----|
[2025-10-12 15:28:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:28:28] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:28:28] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] Summary
[2025-10-12 15:28:28] [info] -----
[2025-10-12 15:28:28] [info] Total Errors: 1120
[2025-10-12 15:28:28] [info] Total Warnings: 321
[2025-10-12 15:28:28] [info] DRC Status: FAILED

```

lvr_summary_301.rpt

```

[2025-10-12 15:29:24] [info] =====
[2025-10-12 15:29:24] [info] Design Rule Check Report
[2025-10-12 15:29:24] [info] =====

[2025-10-12 15:29:24] [info] Report Generated: 2025-10-12
[2025-10-12 15:29:24] [info] Run Start Time: 03:28:33 PM
[2025-10-12 15:29:24] [info] Run End Time: 03:29:24 PM
[2025-10-12 15:29:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:29:24] [info] Tool Version: LVR_2025.1
[2025-10-12 15:29:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:29:24] [info] User Name: ubuntu
[2025-10-12 15:29:24] [info] Cores Used: 8
[2025-10-12 15:29:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Top Cell Information
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Layout Top Cell: TOP
[2025-10-12 15:29:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:29:24] [info] Database Format: GDSII
[2025-10-12 15:29:24] [info] Verification Mode: Full Check

[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Type of Errors | Count
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] DB_WARN -> 1
[2025-10-12 15:29:24] [info] LRF_ERR001 -> 14
[2025-10-12 15:29:24] [info] DB_WARN002 -> 159
[2025-10-12 15:29:24] [info] DB_WARN003 -> 161

```

```

[2025-10-12 15:29:24] [info] DRC_ERR9 -> 89
[2025-10-12 15:29:24] [info] DRC_ERR -> 67
[2025-10-12 15:29:24] [info] DRC_ERR5 -> 408
[2025-10-12 15:29:24] [info] DRC_ERR4 -> 241
[2025-10-12 15:29:24] [info] DRC_ERR1 -> 301
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Rule Violation Summary
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:29:24] [info] |-----|-----|-----|
[2025-10-12 15:29:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:29:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:29:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:29:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:29:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:29:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:29:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:29:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:29:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:29:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:29:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:29:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:29:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:29:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:29:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Performance Metrics
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] | Stage | Time Taken |
[2025-10-12 15:29:24] [info] |-----|-----|
[2025-10-12 15:29:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:29:24] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:29:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Summary
[2025-10-12 15:29:24] [info] -----
[2025-10-12 15:29:24] [info] Total Errors: 1120
[2025-10-12 15:29:24] [info] Total Warnings: 321
[2025-10-12 15:29:24] [info] DRC Status: FAILED

```

lvr_summary_302.rpt

```

[2025-10-12 15:30:22] [info] =====
[2025-10-12 15:30:22] [info] Design Rule Check Report
[2025-10-12 15:30:22] [info] =====

[2025-10-12 15:30:22] [info] Report Generated: 2025-10-12
[2025-10-12 15:30:22] [info] Run Start Time: 03:29:30 PM
[2025-10-12 15:30:22] [info] Run End Time: 03:30:22 PM
[2025-10-12 15:30:22] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:30:22] [info] Tool Version: LVR_2025.1
[2025-10-12 15:30:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:30:22] [info] User Name: ubuntu
[2025-10-12 15:30:22] [info] Cores Used: 8
[2025-10-12 15:30:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Top Cell Information
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Layout Top Cell:      TOP
[2025-10-12 15:30:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:30:22] [info] Database Format:      GDSII
[2025-10-12 15:30:22] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Type of Errors | Count
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] DB_WARN -> 1
[2025-10-12 15:30:22] [info] LRF_ERR001 -> 14
[2025-10-12 15:30:22] [info] DB_WARN002 -> 159
[2025-10-12 15:30:22] [info] DB_WARN003 -> 161
[2025-10-12 15:30:22] [info] DRC_ERR9 -> 89
[2025-10-12 15:30:22] [info] DRC_ERR -> 67
[2025-10-12 15:30:22] [info] DRC_ERR5 -> 408
[2025-10-12 15:30:22] [info] DRC_ERR4 -> 241
[2025-10-12 15:30:22] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Rule Violation Summary
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:30:22] [info] |-----|-----|-----|
[2025-10-12 15:30:22] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:30:22] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:30:22] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:30:22] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:30:22] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:30:22] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:30:22] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:30:22] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:30:22] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:30:22] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:30:22] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:30:22] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:30:22] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:30:22] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:30:22] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:30:22] [info] |
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Performance Metrics
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] | Stage          | Time Taken |
[2025-10-12 15:30:22] [info] |-----|-----|
[2025-10-12 15:30:22] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:30:22] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 15:30:22] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Summary
```

```
[2025-10-12 15:30:22] [info] -----
[2025-10-12 15:30:22] [info] Total Errors:      1120
[2025-10-12 15:30:22] [info] Total Warnings:    321
[2025-10-12 15:30:22] [info] DRC Status:        FAILED
```

lvr_summary_303.rpt

```
[2025-10-12 15:31:17] [info] =====
[2025-10-12 15:31:17] [info]                      Design Rule Check Report
[2025-10-12 15:31:17] [info] =====

[2025-10-12 15:31:17] [info] Report Generated:    2025-10-12
[2025-10-12 15:31:17] [info] Run Start Time:      03:30:26 PM
[2025-10-12 15:31:17] [info] Run End Time:        03:31:17 PM
[2025-10-12 15:31:17] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 15:31:17] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:31:17] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:31:17] [info] User Name:           ubuntu
[2025-10-12 15:31:17] [info] Cores Used:          8
[2025-10-12 15:31:17] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info]                      Top Cell Information
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info] Layout Top Cell:     TOP
[2025-10-12 15:31:17] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:31:17] [info] Database Format:       GDSII
[2025-10-12 15:31:17] [info] Verification Mode:    Full Check

[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info]                      Type of Errors | Count
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info] DB_WARN -> 1
[2025-10-12 15:31:17] [info] LRF_ERR001 -> 14
[2025-10-12 15:31:17] [info] DB_WARN002 -> 159
[2025-10-12 15:31:17] [info] DB_WARN003 -> 161
[2025-10-12 15:31:17] [info] DRC_ERR9 -> 89
[2025-10-12 15:31:17] [info] DRC_ERR -> 67
[2025-10-12 15:31:17] [info] DRC_ERR5 -> 408
[2025-10-12 15:31:17] [info] DRC_ERR4 -> 241
[2025-10-12 15:31:17] [info] DRC_ERR1 -> 301
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info]                      Rule Violation Summary
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:31:17] [info] |-----|-----|-----|
[2025-10-12 15:31:17] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:31:17] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:31:17] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:31:17] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:31:17] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:31:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:31:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:31:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:31:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:31:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:31:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:31:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:31:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:31:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:31:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:31:17] [info]
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info]                      Performance Metrics
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info] | Stage | Time Taken |
```



```
[2025-10-12 15:31:17] [info] |-----|-----|
[2025-10-12 15:31:17] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:31:17] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 15:31:17] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info] Summary
[2025-10-12 15:31:17] [info] -----
[2025-10-12 15:31:17] [info] Total Errors: 1120
[2025-10-12 15:31:17] [info] Total Warnings: 321
[2025-10-12 15:31:17] [info] DRC Status: FAILED
```

lvr_summary_304.rpt

```
[2025-10-12 15:32:14] [info] =====
[2025-10-12 15:32:14] [info] Design Rule Check Report
[2025-10-12 15:32:14] [info] =====

[2025-10-12 15:32:14] [info] Report Generated: 2025-10-12
[2025-10-12 15:32:14] [info] Run Start Time: 03:31:24 PM
[2025-10-12 15:32:14] [info] Run End Time: 03:32:14 PM
[2025-10-12 15:32:14] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:32:14] [info] Tool Version: LVR_2025.1
[2025-10-12 15:32:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:32:14] [info] User Name: ubuntu
[2025-10-12 15:32:14] [info] Cores Used: 8
[2025-10-12 15:32:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Top Cell Information
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Layout Top Cell: TOP
[2025-10-12 15:32:14] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:32:14] [info] Database Format: GDSII
[2025-10-12 15:32:14] [info] Verification Mode: Full Check

[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Type of Errors | Count
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] DB_WARN -> 1
[2025-10-12 15:32:14] [info] LRF_ERR001 -> 14
[2025-10-12 15:32:14] [info] DB_WARN002 -> 159
[2025-10-12 15:32:14] [info] DB_WARN003 -> 161
[2025-10-12 15:32:14] [info] DRC_ERR9 -> 89
[2025-10-12 15:32:14] [info] DRC_ERR -> 67
[2025-10-12 15:32:14] [info] DRC_ERR5 -> 408
[2025-10-12 15:32:14] [info] DRC_ERR4 -> 241
[2025-10-12 15:32:14] [info] DRC_ERR1 -> 301
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Rule Violation Summary
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:32:14] [info] |-----|-----|-----|
[2025-10-12 15:32:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:32:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:32:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:32:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:32:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 15:32:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:32:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:32:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:32:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:32:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:32:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:32:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:32:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:32:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:32:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:32:14] [info]
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Performance Metrics
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] | Stage | Time Taken |
[2025-10-12 15:32:14] [info] |-----|-----|
[2025-10-12 15:32:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:32:14] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:32:14] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Summary
[2025-10-12 15:32:14] [info] -----
[2025-10-12 15:32:14] [info] Total Errors: 1120
[2025-10-12 15:32:14] [info] Total Warnings: 321
[2025-10-12 15:32:14] [info] DRC Status: FAILED

```

lvr_summary_305.rpt

```

[2025-10-12 15:33:12] [info] =====
[2025-10-12 15:33:12] [info] Design Rule Check Report
[2025-10-12 15:33:12] [info] =====

[2025-10-12 15:33:12] [info] Report Generated: 2025-10-12
[2025-10-12 15:33:12] [info] Run Start Time: 03:32:21 PM
[2025-10-12 15:33:12] [info] Run End Time: 03:33:12 PM
[2025-10-12 15:33:12] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:33:12] [info] Tool Version: LVR_2025.1
[2025-10-12 15:33:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:33:12] [info] User Name: ubuntu
[2025-10-12 15:33:12] [info] Cores Used: 8
[2025-10-12 15:33:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Top Cell Information
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Layout Top Cell: TOP
[2025-10-12 15:33:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:33:12] [info] Database Format: GDSII
[2025-10-12 15:33:12] [info] Verification Mode: Full Check

[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Type of Errors | Count
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] DB_WARN -> 1
[2025-10-12 15:33:12] [info] LRF_ERR001 -> 14
[2025-10-12 15:33:12] [info] DB_WARN002 -> 159
[2025-10-12 15:33:12] [info] DB_WARN003 -> 161

```

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[2025-10-12 15:33:12] [info] DRC_ERR9 -> 89
[2025-10-12 15:33:12] [info] DRC_ERR -> 67
[2025-10-12 15:33:12] [info] DRC_ERR5 -> 408
[2025-10-12 15:33:12] [info] DRC_ERR4 -> 241
[2025-10-12 15:33:12] [info] DRC_ERR1 -> 301
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Rule Violation Summary
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:33:12] [info] |-----|-----|-----|
[2025-10-12 15:33:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:33:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:33:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:33:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:33:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:33:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:33:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:33:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:33:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:33:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:33:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:33:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:33:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:33:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:33:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Performance Metrics
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] | Stage | Time Taken |
[2025-10-12 15:33:12] [info] |-----|-----|
[2025-10-12 15:33:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:33:12] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:33:12] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Summary
[2025-10-12 15:33:12] [info] -----
[2025-10-12 15:33:12] [info] Total Errors: 1120
[2025-10-12 15:33:12] [info] Total Warnings: 321
[2025-10-12 15:33:12] [info] DRC Status: FAILED

```

lvr_summary_306.rpt

```

[2025-10-12 15:34:09] [info] =====
[2025-10-12 15:34:09] [info] Design Rule Check Report
[2025-10-12 15:34:09] [info] =====

[2025-10-12 15:34:09] [info] Report Generated: 2025-10-12
[2025-10-12 15:34:09] [info] Run Start Time: 03:33:18 PM
[2025-10-12 15:34:09] [info] Run End Time: 03:34:09 PM
[2025-10-12 15:34:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:34:09] [info] Tool Version: LVR_2025.1
[2025-10-12 15:34:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:34:09] [info] User Name: ubuntu
[2025-10-12 15:34:09] [info] Cores Used: 8
[2025-10-12 15:34:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Top Cell Information
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Layout Top Cell:      TOP
[2025-10-12 15:34:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:34:09] [info] Database Format:      GDSII
[2025-10-12 15:34:09] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Type of Errors | Count
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] DB_WARN -> 1
[2025-10-12 15:34:09] [info] LRF_ERR001 -> 14
[2025-10-12 15:34:09] [info] DB_WARN002 -> 159
[2025-10-12 15:34:09] [info] DB_WARN003 -> 161
[2025-10-12 15:34:09] [info] DRC_ERR9 -> 89
[2025-10-12 15:34:09] [info] DRC_ERR -> 67
[2025-10-12 15:34:09] [info] DRC_ERR5 -> 408
[2025-10-12 15:34:09] [info] DRC_ERR4 -> 241
[2025-10-12 15:34:09] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Rule Violation Summary
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:34:09] [info] |-----|-----|-----|
[2025-10-12 15:34:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:34:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:34:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:34:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:34:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:34:09] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:34:09] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:34:09] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:34:09] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:34:09] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:34:09] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:34:09] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:34:09] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:34:09] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:34:09] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:34:09] [info] |
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Performance Metrics
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] | Stage          | Time Taken |
[2025-10-12 15:34:09] [info] |-----|-----|
[2025-10-12 15:34:09] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:34:09] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 15:34:09] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Summary
```

```
[2025-10-12 15:34:09] [info] -----
[2025-10-12 15:34:09] [info] Total Errors:      1120
[2025-10-12 15:34:09] [info] Total Warnings:    321
[2025-10-12 15:34:09] [info] DRC Status:        FAILED
```

lvr_summary_307.rpt

```
[2025-10-12 15:35:06] [info] =====
[2025-10-12 15:35:06] [info] Design Rule Check Report
[2025-10-12 15:35:06] [info] =====

[2025-10-12 15:35:06] [info] Report Generated: 2025-10-12
[2025-10-12 15:35:06] [info] Run Start Time: 03:34:15 PM
[2025-10-12 15:35:06] [info] Run End Time: 03:35:06 PM
[2025-10-12 15:35:06] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:35:06] [info] Tool Version: LVR_2025.1
[2025-10-12 15:35:06] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:35:06] [info] User Name: ubuntu
[2025-10-12 15:35:06] [info] Cores Used: 8
[2025-10-12 15:35:06] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] Top Cell Information
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] Layout Top Cell: TOP
[2025-10-12 15:35:06] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:35:06] [info] Database Format: GDSII
[2025-10-12 15:35:06] [info] Verification Mode: Full Check

[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] Type of Errors | Count
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] DB_WARN -> 1
[2025-10-12 15:35:06] [info] LRF_ERR001 -> 14
[2025-10-12 15:35:06] [info] DB_WARN002 -> 159
[2025-10-12 15:35:06] [info] DB_WARN003 -> 161
[2025-10-12 15:35:06] [info] DRC_ERR9 -> 89
[2025-10-12 15:35:06] [info] DRC_ERR -> 67
[2025-10-12 15:35:06] [info] DRC_ERR5 -> 408
[2025-10-12 15:35:06] [info] DRC_ERR4 -> 241
[2025-10-12 15:35:06] [info] DRC_ERR1 -> 301
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] Rule Violation Summary
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:35:06] [info] |-----|-----|-----|
[2025-10-12 15:35:06] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:35:06] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:35:06] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:35:06] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:35:06] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:35:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:35:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:35:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:35:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:35:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:35:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:35:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:35:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:35:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:35:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:35:06] [info]
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] Performance Metrics
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:35:06] [info] |-----|-----|
[2025-10-12 15:35:06] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:35:06] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 15:35:06] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info]                Summary
[2025-10-12 15:35:06] [info] -----
[2025-10-12 15:35:06] [info] Total Errors:    1120
[2025-10-12 15:35:06] [info] Total Warnings:    321
[2025-10-12 15:35:06] [info] DRC Status:      FAILED
```

lvr_summary_308.rpt

```
[2025-10-12 15:36:02] [info] =====
[2025-10-12 15:36:02] [info]                Design Rule Check Report
[2025-10-12 15:36:02] [info] =====

[2025-10-12 15:36:02] [info] Report Generated:    2025-10-12
[2025-10-12 15:36:02] [info] Run Start Time:      03:35:12 PM
[2025-10-12 15:36:02] [info] Run End Time:        03:36:02 PM
[2025-10-12 15:36:02] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 15:36:02] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:36:02] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:36:02] [info] User Name:           ubuntu
[2025-10-12 15:36:02] [info] Cores Used:          8
[2025-10-12 15:36:02] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info]                Top Cell Information
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] Layout Top Cell:     TOP
[2025-10-12 15:36:02] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:36:02] [info] Database Format:       GDSII
[2025-10-12 15:36:02] [info] Verification Mode:    Full Check

[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info]                Type of Errors | Count
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] DB_WARN -> 1
[2025-10-12 15:36:02] [info] LRF_ERR001 -> 14
[2025-10-12 15:36:02] [info] DB_WARN002 -> 159
[2025-10-12 15:36:02] [info] DB_WARN003 -> 161
[2025-10-12 15:36:02] [info] DRC_ERR9 -> 89
[2025-10-12 15:36:02] [info] DRC_ERR -> 67
[2025-10-12 15:36:02] [info] DRC_ERR5 -> 408
[2025-10-12 15:36:02] [info] DRC_ERR4 -> 241
[2025-10-12 15:36:02] [info] DRC_ERR1 -> 301
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info]                Rule Violation Summary
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:36:02] [info] |-----|-----|-----|
[2025-10-12 15:36:02] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:36:02] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:36:02] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:36:02] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:36:02] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 15:36:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:36:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:36:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:36:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:36:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:36:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:36:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:36:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:36:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:36:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:36:02] [info]
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] Performance Metrics
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] | Stage | Time Taken |
[2025-10-12 15:36:02] [info] |-----|-----|
[2025-10-12 15:36:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:36:02] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:36:02] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] Summary
[2025-10-12 15:36:02] [info] -----
[2025-10-12 15:36:02] [info] Total Errors: 1120
[2025-10-12 15:36:02] [info] Total Warnings: 321
[2025-10-12 15:36:02] [info] DRC Status: FAILED

```

lvr_summary_309.rpt

```

[2025-10-12 15:36:59] [info] =====
[2025-10-12 15:36:59] [info] Design Rule Check Report
[2025-10-12 15:36:59] [info] =====

[2025-10-12 15:36:59] [info] Report Generated: 2025-10-12
[2025-10-12 15:36:59] [info] Run Start Time: 03:36:08 PM
[2025-10-12 15:36:59] [info] Run End Time: 03:36:59 PM
[2025-10-12 15:36:59] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:36:59] [info] Tool Version: LVR_2025.1
[2025-10-12 15:36:59] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:36:59] [info] User Name: ubuntu
[2025-10-12 15:36:59] [info] Cores Used: 8
[2025-10-12 15:36:59] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Top Cell Information
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Layout Top Cell: TOP
[2025-10-12 15:36:59] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:36:59] [info] Database Format: GDSII
[2025-10-12 15:36:59] [info] Verification Mode: Full Check

[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Type of Errors | Count
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] DB_WARN -> 1
[2025-10-12 15:36:59] [info] LRF_ERR001 -> 14
[2025-10-12 15:36:59] [info] DB_WARN002 -> 159
[2025-10-12 15:36:59] [info] DB_WARN003 -> 161

```

```

[2025-10-12 15:36:59] [info] DRC_ERR9 -> 89
[2025-10-12 15:36:59] [info] DRC_ERR -> 67
[2025-10-12 15:36:59] [info] DRC_ERR5 -> 408
[2025-10-12 15:36:59] [info] DRC_ERR4 -> 241
[2025-10-12 15:36:59] [info] DRC_ERR1 -> 301
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Rule Violation Summary
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:36:59] [info] |-----|-----|-----|
[2025-10-12 15:36:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:36:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:36:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:36:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:36:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:36:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:36:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:36:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:36:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:36:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:36:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:36:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:36:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:36:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:36:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Performance Metrics
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] | Stage | Time Taken |
[2025-10-12 15:36:59] [info] |-----|-----|
[2025-10-12 15:36:59] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:36:59] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 15:36:59] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Summary
[2025-10-12 15:36:59] [info] -----
[2025-10-12 15:36:59] [info] Total Errors: 1120
[2025-10-12 15:36:59] [info] Total Warnings: 321
[2025-10-12 15:36:59] [info] DRC Status: FAILED

```

lvr_summary_31.rpt

```

[2025-10-12 11:10:46] [info] =====
[2025-10-12 11:10:46] [info] Design Rule Check Report
[2025-10-12 11:10:46] [info] =====

[2025-10-12 11:10:46] [info] Report Generated: 2025-10-12
[2025-10-12 11:10:46] [info] Run Start Time: 11:09:54 AM
[2025-10-12 11:10:46] [info] Run End Time: 11:10:46 AM
[2025-10-12 11:10:46] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:10:46] [info] Tool Version: LVR_2025.1
[2025-10-12 11:10:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:10:46] [info] User Name: ubuntu
[2025-10-12 11:10:46] [info] Cores Used: 8
[2025-10-12 11:10:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Top Cell Information
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Layout Top Cell:      TOP
[2025-10-12 11:10:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:10:46] [info] Database Format:       GDSII
[2025-10-12 11:10:46] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Type of Errors | Count
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] DB_WARN -> 1
[2025-10-12 11:10:46] [info] LRF_ERR001 -> 14
[2025-10-12 11:10:46] [info] DB_WARN002 -> 159
[2025-10-12 11:10:46] [info] DB_WARN003 -> 161
[2025-10-12 11:10:46] [info] DRC_ERR9 -> 89
[2025-10-12 11:10:46] [info] DRC_ERR -> 67
[2025-10-12 11:10:46] [info] DRC_ERR5 -> 408
[2025-10-12 11:10:46] [info] DRC_ERR4 -> 241
[2025-10-12 11:10:46] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Rule Violation Summary
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:10:46] [info] |-----|-----|-----|
[2025-10-12 11:10:46] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:10:46] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:10:46] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:10:46] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:10:46] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:10:46] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:10:46] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:10:46] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:10:46] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:10:46] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:10:46] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:10:46] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:10:46] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:10:46] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:10:46] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:10:46] [info] |
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Performance Metrics
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] | Stage          | Time Taken |
[2025-10-12 11:10:46] [info] |-----|-----|
[2025-10-12 11:10:46] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:10:46] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:10:46] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Summary
```

```
[2025-10-12 11:10:46] [info] -----
[2025-10-12 11:10:46] [info] Total Errors:      1120
[2025-10-12 11:10:46] [info] Total Warnings:    321
[2025-10-12 11:10:46] [info] DRC Status:        FAILED
```

lvr_summary_310.rpt

```
[2025-10-12 15:37:58] [info] =====
[2025-10-12 15:37:58] [info] Design Rule Check Report
[2025-10-12 15:37:58] [info] =====

[2025-10-12 15:37:58] [info] Report Generated: 2025-10-12
[2025-10-12 15:37:58] [info] Run Start Time: 03:37:06 PM
[2025-10-12 15:37:58] [info] Run End Time: 03:37:58 PM
[2025-10-12 15:37:58] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 15:37:58] [info] Tool Version: LVR_2025.1
[2025-10-12 15:37:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:37:58] [info] User Name: ubuntu
[2025-10-12 15:37:58] [info] Cores Used: 8
[2025-10-12 15:37:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] Top Cell Information
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] Layout Top Cell: TOP
[2025-10-12 15:37:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:37:58] [info] Database Format: GDSII
[2025-10-12 15:37:58] [info] Verification Mode: Full Check

[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] Type of Errors | Count
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] DB_WARN -> 1
[2025-10-12 15:37:58] [info] LRF_ERR001 -> 14
[2025-10-12 15:37:58] [info] DB_WARN002 -> 159
[2025-10-12 15:37:58] [info] DB_WARN003 -> 161
[2025-10-12 15:37:58] [info] DRC_ERR9 -> 89
[2025-10-12 15:37:58] [info] DRC_ERR -> 67
[2025-10-12 15:37:58] [info] DRC_ERR5 -> 408
[2025-10-12 15:37:58] [info] DRC_ERR4 -> 241
[2025-10-12 15:37:58] [info] DRC_ERR1 -> 301
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] Rule Violation Summary
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:37:58] [info] |-----|-----|-----|
[2025-10-12 15:37:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:37:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:37:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:37:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:37:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:37:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:37:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:37:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:37:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:37:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:37:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:37:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:37:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:37:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:37:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:37:58] [info]
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] Performance Metrics
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:37:58] [info] |-----|-----|
[2025-10-12 15:37:58] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:37:58] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 15:37:58] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info]                Summary
[2025-10-12 15:37:58] [info] -----
[2025-10-12 15:37:58] [info] Total Errors:    1120
[2025-10-12 15:37:58] [info] Total Warnings:    321
[2025-10-12 15:37:58] [info] DRC Status:      FAILED
```

lvr_summary_311.rpt

```
[2025-10-12 15:38:55] [info] =====
[2025-10-12 15:38:55] [info]                Design Rule Check Report
[2025-10-12 15:38:55] [info] =====

[2025-10-12 15:38:55] [info] Report Generated:    2025-10-12
[2025-10-12 15:38:55] [info] Run Start Time:      03:38:03 PM
[2025-10-12 15:38:55] [info] Run End Time:        03:38:55 PM
[2025-10-12 15:38:55] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 15:38:55] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:38:55] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:38:55] [info] User Name:           ubuntu
[2025-10-12 15:38:55] [info] Cores Used:          8
[2025-10-12 15:38:55] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info]                Top Cell Information
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] Layout Top Cell:     TOP
[2025-10-12 15:38:55] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:38:55] [info] Database Format:       GDSII
[2025-10-12 15:38:55] [info] Verification Mode:    Full Check

[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info]                Type of Errors | Count
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] DB_WARN -> 1
[2025-10-12 15:38:55] [info] LRF_ERR001 -> 14
[2025-10-12 15:38:55] [info] DB_WARN002 -> 159
[2025-10-12 15:38:55] [info] DB_WARN003 -> 161
[2025-10-12 15:38:55] [info] DRC_ERR9 -> 89
[2025-10-12 15:38:55] [info] DRC_ERR -> 67
[2025-10-12 15:38:55] [info] DRC_ERR5 -> 408
[2025-10-12 15:38:55] [info] DRC_ERR4 -> 241
[2025-10-12 15:38:55] [info] DRC_ERR1 -> 301
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info]                Rule Violation Summary
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:38:55] [info] |-----|-----|-----|
[2025-10-12 15:38:55] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:38:55] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:38:55] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:38:55] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:38:55] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 15:38:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:38:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:38:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:38:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:38:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:38:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:38:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:38:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:38:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:38:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:38:55] [info]
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] Performance Metrics
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] | Stage | Time Taken |
[2025-10-12 15:38:55] [info] |-----|-----|
[2025-10-12 15:38:55] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:38:55] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:38:55] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] Summary
[2025-10-12 15:38:55] [info] -----
[2025-10-12 15:38:55] [info] Total Errors: 1120
[2025-10-12 15:38:55] [info] Total Warnings: 321
[2025-10-12 15:38:55] [info] DRC Status: FAILED
```

lvr_summary_312.rpt

```
[2025-10-12 15:39:52] [info] =====
[2025-10-12 15:39:52] [info] Design Rule Check Report
[2025-10-12 15:39:52] [info] =====

[2025-10-12 15:39:52] [info] Report Generated: 2025-10-12
[2025-10-12 15:39:52] [info] Run Start Time: 03:39:00 PM
[2025-10-12 15:39:52] [info] Run End Time: 03:39:52 PM
[2025-10-12 15:39:52] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:39:52] [info] Tool Version: LVR_2025.1
[2025-10-12 15:39:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:39:52] [info] User Name: ubuntu
[2025-10-12 15:39:52] [info] Cores Used: 8
[2025-10-12 15:39:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Top Cell Information
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Layout Top Cell: TOP
[2025-10-12 15:39:52] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:39:52] [info] Database Format: GDSII
[2025-10-12 15:39:52] [info] Verification Mode: Full Check

[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Type of Errors | Count
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] DB_WARN -> 1
[2025-10-12 15:39:52] [info] LRF_ERR001 -> 14
[2025-10-12 15:39:52] [info] DB_WARN002 -> 159
[2025-10-12 15:39:52] [info] DB_WARN003 -> 161
```

```

[2025-10-12 15:39:52] [info] DRC_ERR9 -> 89
[2025-10-12 15:39:52] [info] DRC_ERR -> 67
[2025-10-12 15:39:52] [info] DRC_ERR5 -> 408
[2025-10-12 15:39:52] [info] DRC_ERR4 -> 241
[2025-10-12 15:39:52] [info] DRC_ERR1 -> 301
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Rule Violation Summary
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:39:52] [info] |-----|-----|-----|
[2025-10-12 15:39:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:39:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:39:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:39:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:39:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:39:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 15:39:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:39:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:39:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:39:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:39:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:39:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:39:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:39:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:39:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Performance Metrics
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] | Stage          | Time Taken |
[2025-10-12 15:39:52] [info] |-----|-----|
[2025-10-12 15:39:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:39:52] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 15:39:52] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Summary
[2025-10-12 15:39:52] [info] -----
[2025-10-12 15:39:52] [info] Total Errors: 1120
[2025-10-12 15:39:52] [info] Total Warnings: 321
[2025-10-12 15:39:52] [info] DRC Status: FAILED

```

lvr_summary_313.rpt

```

[2025-10-12 15:40:47] [info] =====
[2025-10-12 15:40:47] [info] Design Rule Check Report
[2025-10-12 15:40:47] [info] =====

[2025-10-12 15:40:47] [info] Report Generated: 2025-10-12
[2025-10-12 15:40:47] [info] Run Start Time: 03:39:57 PM
[2025-10-12 15:40:47] [info] Run End Time: 03:40:47 PM
[2025-10-12 15:40:47] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:40:47] [info] Tool Version: LVR_2025.1
[2025-10-12 15:40:47] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:40:47] [info] User Name: ubuntu
[2025-10-12 15:40:47] [info] Cores Used: 8
[2025-10-12 15:40:47] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] Top Cell Information
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] Layout Top Cell:      TOP
[2025-10-12 15:40:47] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:40:47] [info] Database Format:      GDSII
[2025-10-12 15:40:47] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] Type of Errors | Count
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] DB_WARN -> 1
[2025-10-12 15:40:47] [info] LRF_ERROR01 -> 14
[2025-10-12 15:40:47] [info] DB_WARN002 -> 159
[2025-10-12 15:40:47] [info] DB_WARN003 -> 161
[2025-10-12 15:40:47] [info] DRC_ERR9 -> 89
[2025-10-12 15:40:47] [info] DRC_ERR -> 67
[2025-10-12 15:40:47] [info] DRC_ERR5 -> 408
[2025-10-12 15:40:47] [info] DRC_ERR4 -> 241
[2025-10-12 15:40:47] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] Rule Violation Summary
```

```
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:40:47] [info] |-----|-----|-----|
[2025-10-12 15:40:47] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:40:47] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:40:47] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:40:47] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:40:47] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:40:47] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:40:47] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:40:47] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:40:47] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:40:47] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:40:47] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:40:47] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:40:47] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:40:47] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:40:47] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:40:47] [info] -----
```

```
[2025-10-12 15:40:47] [info] Performance Metrics
```

```
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] | Stage          | Time Taken |
[2025-10-12 15:40:47] [info] |-----|-----|
[2025-10-12 15:40:47] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:40:47] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 15:40:47] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:40:47] [info] -----
```

```
[2025-10-12 15:40:47] [info] Summary
```

```
[2025-10-12 15:40:47] [info] -----
[2025-10-12 15:40:47] [info] Total Errors:      1120
[2025-10-12 15:40:47] [info] Total Warnings:    321
[2025-10-12 15:40:47] [info] DRC Status:        FAILED
```

lvr_summary_314.rpt

```
[2025-10-12 15:41:46] [info] =====
[2025-10-12 15:41:46] [info]                      Design Rule Check Report
[2025-10-12 15:41:46] [info] =====

[2025-10-12 15:41:46] [info] Report Generated:    2025-10-12
[2025-10-12 15:41:46] [info] Run Start Time:      03:40:54 PM
[2025-10-12 15:41:46] [info] Run End Time:        03:41:46 PM
[2025-10-12 15:41:46] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 15:41:46] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:41:46] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:41:46] [info] User Name:           ubuntu
[2025-10-12 15:41:46] [info] Cores Used:          8
[2025-10-12 15:41:46] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info]                      Top Cell Information
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info] Layout Top Cell:     TOP
[2025-10-12 15:41:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:41:46] [info] Database Format:      GDSII
[2025-10-12 15:41:46] [info] Verification Mode:    Full Check

[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info]                      Type of Errors | Count
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info] DB_WARN -> 1
[2025-10-12 15:41:46] [info] LRF_ERR001 -> 14
[2025-10-12 15:41:46] [info] DB_WARN002 -> 159
[2025-10-12 15:41:46] [info] DB_WARN003 -> 161
[2025-10-12 15:41:46] [info] DRC_ERR9 -> 89
[2025-10-12 15:41:46] [info] DRC_ERR -> 67
[2025-10-12 15:41:46] [info] DRC_ERR5 -> 408
[2025-10-12 15:41:46] [info] DRC_ERR4 -> 241
[2025-10-12 15:41:46] [info] DRC_ERR1 -> 301
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info]                      Rule Violation Summary
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:41:46] [info] |-----|-----|-----|
[2025-10-12 15:41:46] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:41:46] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:41:46] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:41:46] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:41:46] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:41:46] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:41:46] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:41:46] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:41:46] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:41:46] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:41:46] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:41:46] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:41:46] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:41:46] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:41:46] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:41:46] [info]
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info]                      Performance Metrics
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:41:46] [info] |-----|-----|
[2025-10-12 15:41:46] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:41:46] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 15:41:46] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info] Summary
[2025-10-12 15:41:46] [info] -----
[2025-10-12 15:41:46] [info] Total Errors: 1120
[2025-10-12 15:41:46] [info] Total Warnings: 321
[2025-10-12 15:41:46] [info] DRC Status: FAILED
```

lvr_summary_315.rpt

```
[2025-10-12 15:42:43] [info] =====
[2025-10-12 15:42:43] [info] Design Rule Check Report
[2025-10-12 15:42:43] [info] =====

[2025-10-12 15:42:43] [info] Report Generated: 2025-10-12
[2025-10-12 15:42:43] [info] Run Start Time: 03:41:51 PM
[2025-10-12 15:42:43] [info] Run End Time: 03:42:43 PM
[2025-10-12 15:42:43] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 15:42:43] [info] Tool Version: LVR_2025.1
[2025-10-12 15:42:43] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:42:43] [info] User Name: ubuntu
[2025-10-12 15:42:43] [info] Cores Used: 8
[2025-10-12 15:42:43] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Top Cell Information
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Layout Top Cell: TOP
[2025-10-12 15:42:43] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:42:43] [info] Database Format: GDSII
[2025-10-12 15:42:43] [info] Verification Mode: Full Check

[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Type of Errors | Count
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] DB_WARN -> 1
[2025-10-12 15:42:43] [info] LRF_ERR001 -> 14
[2025-10-12 15:42:43] [info] DB_WARN002 -> 159
[2025-10-12 15:42:43] [info] DB_WARN003 -> 161
[2025-10-12 15:42:43] [info] DRC_ERR9 -> 89
[2025-10-12 15:42:43] [info] DRC_ERR -> 67
[2025-10-12 15:42:43] [info] DRC_ERR5 -> 408
[2025-10-12 15:42:43] [info] DRC_ERR4 -> 241
[2025-10-12 15:42:43] [info] DRC_ERR1 -> 301
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Rule Violation Summary
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:42:43] [info] |-----|-----|-----|
[2025-10-12 15:42:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:42:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:42:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:42:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:42:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```
[2025-10-12 15:42:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:42:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:42:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:42:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:42:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:42:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:42:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:42:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:42:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:42:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:42:43] [info]
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Performance Metrics
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] | Stage | Time Taken |
[2025-10-12 15:42:43] [info] |-----|-----|
[2025-10-12 15:42:43] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:42:43] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:42:43] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Summary
[2025-10-12 15:42:43] [info] -----
[2025-10-12 15:42:43] [info] Total Errors: 1120
[2025-10-12 15:42:43] [info] Total Warnings: 321
[2025-10-12 15:42:43] [info] DRC Status: FAILED
```

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```
[2025-10-12 15:43:39] [info] =====
[2025-10-12 15:43:39] [info] Design Rule Check Report
[2025-10-12 15:43:39] [info] =====

[2025-10-12 15:43:39] [info] Report Generated: 2025-10-12
[2025-10-12 15:43:39] [info] Run Start Time: 03:42:49 PM
[2025-10-12 15:43:39] [info] Run End Time: 03:43:39 PM
[2025-10-12 15:43:39] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:43:39] [info] Tool Version: LVR_2025.1
[2025-10-12 15:43:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:43:39] [info] User Name: ubuntu
[2025-10-12 15:43:39] [info] Cores Used: 8
[2025-10-12 15:43:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Top Cell Information
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Layout Top Cell: TOP
[2025-10-12 15:43:39] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:43:39] [info] Database Format: GDSII
[2025-10-12 15:43:39] [info] Verification Mode: Full Check

[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Type of Errors | Count
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] DB_WARN -> 1
[2025-10-12 15:43:39] [info] LRF_ERR001 -> 14
[2025-10-12 15:43:39] [info] DB_WARN002 -> 159
[2025-10-12 15:43:39] [info] DB_WARN003 -> 161
```

```

[2025-10-12 15:43:39] [info] DRC_ERR9 -> 89
[2025-10-12 15:43:39] [info] DRC_ERR -> 67
[2025-10-12 15:43:39] [info] DRC_ERR5 -> 408
[2025-10-12 15:43:39] [info] DRC_ERR4 -> 241
[2025-10-12 15:43:39] [info] DRC_ERR1 -> 301
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Rule Violation Summary
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:43:39] [info] |-----|-----|-----|
[2025-10-12 15:43:39] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:43:39] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:43:39] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:43:39] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:43:39] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:43:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:43:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:43:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:43:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:43:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:43:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:43:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:43:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:43:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:43:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Performance Metrics
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] | Stage | Time Taken |
[2025-10-12 15:43:39] [info] |-----|-----|
[2025-10-12 15:43:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:43:39] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:43:39] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Summary
[2025-10-12 15:43:39] [info] -----
[2025-10-12 15:43:39] [info] Total Errors: 1120
[2025-10-12 15:43:39] [info] Total Warnings: 321
[2025-10-12 15:43:39] [info] DRC Status: FAILED

```

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```

[2025-10-12 15:44:35] [info] =====
[2025-10-12 15:44:35] [info] Design Rule Check Report
[2025-10-12 15:44:35] [info] =====

[2025-10-12 15:44:35] [info] Report Generated: 2025-10-12
[2025-10-12 15:44:35] [info] Run Start Time: 03:43:44 PM
[2025-10-12 15:44:35] [info] Run End Time: 03:44:35 PM
[2025-10-12 15:44:35] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:44:35] [info] Tool Version: LVR_2025.1
[2025-10-12 15:44:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:44:35] [info] User Name: ubuntu
[2025-10-12 15:44:35] [info] Cores Used: 8
[2025-10-12 15:44:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] Top Cell Information
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] Layout Top Cell:      TOP
[2025-10-12 15:44:35] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:44:35] [info] Database Format:      GDSII
[2025-10-12 15:44:35] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] Type of Errors | Count
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] DB_WARN -> 1
[2025-10-12 15:44:35] [info] LRF_ERR001 -> 14
[2025-10-12 15:44:35] [info] DB_WARN002 -> 159
[2025-10-12 15:44:35] [info] DB_WARN003 -> 161
[2025-10-12 15:44:35] [info] DRC_ERR9 -> 89
[2025-10-12 15:44:35] [info] DRC_ERR -> 67
[2025-10-12 15:44:35] [info] DRC_ERR5 -> 408
[2025-10-12 15:44:35] [info] DRC_ERR4 -> 241
[2025-10-12 15:44:35] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] Rule Violation Summary
```

```
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:44:35] [info] |-----|-----|-----|
[2025-10-12 15:44:35] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:44:35] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:44:35] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:44:35] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:44:35] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:44:35] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:44:35] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:44:35] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:44:35] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:44:35] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:44:35] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:44:35] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:44:35] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:44:35] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:44:35] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:44:35] [info] -----
```

```
[2025-10-12 15:44:35] [info] Performance Metrics
```

```
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] | Stage          | Time Taken |
[2025-10-12 15:44:35] [info] |-----|-----|
[2025-10-12 15:44:35] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:44:35] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 15:44:35] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:44:35] [info] -----
```

```
[2025-10-12 15:44:35] [info] Summary
```

```
[2025-10-12 15:44:35] [info] -----
[2025-10-12 15:44:35] [info] Total Errors:      1120
[2025-10-12 15:44:35] [info] Total Warnings:    321
[2025-10-12 15:44:35] [info] DRC Status:        FAILED
```

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```
[2025-10-12 15:45:32] [info] =====
[2025-10-12 15:45:32] [info]                Design Rule Check Report
[2025-10-12 15:45:32] [info] =====

[2025-10-12 15:45:32] [info] Report Generated:    2025-10-12
[2025-10-12 15:45:32] [info] Run Start Time:      03:44:42 PM
[2025-10-12 15:45:32] [info] Run End Time:        03:45:32 PM
[2025-10-12 15:45:32] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 15:45:32] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:45:32] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:45:32] [info] User Name:           ubuntu
[2025-10-12 15:45:32] [info] Cores Used:          8
[2025-10-12 15:45:32] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info]                Top Cell Information
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info] Layout Top Cell:     TOP
[2025-10-12 15:45:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:45:32] [info] Database Format:      GDSII
[2025-10-12 15:45:32] [info] Verification Mode:    Full Check

[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info]                Type of Errors | Count
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info] DB_WARN -> 1
[2025-10-12 15:45:32] [info] LRF_ERR001 -> 14
[2025-10-12 15:45:32] [info] DB_WARN002 -> 159
[2025-10-12 15:45:32] [info] DB_WARN003 -> 161
[2025-10-12 15:45:32] [info] DRC_ERR9 -> 89
[2025-10-12 15:45:32] [info] DRC_ERR -> 67
[2025-10-12 15:45:32] [info] DRC_ERR5 -> 408
[2025-10-12 15:45:32] [info] DRC_ERR4 -> 241
[2025-10-12 15:45:32] [info] DRC_ERR1 -> 301
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info]                Rule Violation Summary
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:45:32] [info] |-----|-----|-----|
[2025-10-12 15:45:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:45:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:45:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:45:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:45:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:45:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:45:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:45:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:45:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:45:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:45:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:45:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:45:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:45:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:45:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:45:32] [info]
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info]                Performance Metrics
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:45:32] [info] |-----|-----|
[2025-10-12 15:45:32] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:45:32] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 15:45:32] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info] Summary
[2025-10-12 15:45:32] [info] -----
[2025-10-12 15:45:32] [info] Total Errors: 1120
[2025-10-12 15:45:32] [info] Total Warnings: 321
[2025-10-12 15:45:32] [info] DRC Status: FAILED
```

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```
[2025-10-12 15:46:29] [info] =====
[2025-10-12 15:46:29] [info] Design Rule Check Report
[2025-10-12 15:46:29] [info] =====

[2025-10-12 15:46:29] [info] Report Generated: 2025-10-12
[2025-10-12 15:46:29] [info] Run Start Time: 03:45:39 PM
[2025-10-12 15:46:29] [info] Run End Time: 03:46:29 PM
[2025-10-12 15:46:29] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:46:29] [info] Tool Version: LVR_2025.1
[2025-10-12 15:46:29] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:46:29] [info] User Name: ubuntu
[2025-10-12 15:46:29] [info] Cores Used: 8
[2025-10-12 15:46:29] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Top Cell Information
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Layout Top Cell: TOP
[2025-10-12 15:46:29] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:46:29] [info] Database Format: GDSII
[2025-10-12 15:46:29] [info] Verification Mode: Full Check

[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Type of Errors | Count
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] DB_WARN -> 1
[2025-10-12 15:46:29] [info] LRF_ERR001 -> 14
[2025-10-12 15:46:29] [info] DB_WARN002 -> 159
[2025-10-12 15:46:29] [info] DB_WARN003 -> 161
[2025-10-12 15:46:29] [info] DRC_ERR9 -> 89
[2025-10-12 15:46:29] [info] DRC_ERR -> 67
[2025-10-12 15:46:29] [info] DRC_ERR5 -> 408
[2025-10-12 15:46:29] [info] DRC_ERR4 -> 241
[2025-10-12 15:46:29] [info] DRC_ERR1 -> 301
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Rule Violation Summary
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:46:29] [info] |-----|-----|-----|
[2025-10-12 15:46:29] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:46:29] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:46:29] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:46:29] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:46:29] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 15:46:29] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:46:29] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:46:29] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:46:29] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:46:29] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:46:29] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:46:29] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:46:29] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:46:29] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:46:29] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:46:29] [info]
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Performance Metrics
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] | Stage | Time Taken |
[2025-10-12 15:46:29] [info] |-----|-----|
[2025-10-12 15:46:29] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:46:29] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:46:29] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Summary
[2025-10-12 15:46:29] [info] -----
[2025-10-12 15:46:29] [info] Total Errors: 1120
[2025-10-12 15:46:29] [info] Total Warnings: 321
[2025-10-12 15:46:29] [info] DRC Status: FAILED
```

lvr_summary_32.rpt

```
[2025-10-12 11:11:42] [info] =====
[2025-10-12 11:11:42] [info] Design Rule Check Report
[2025-10-12 11:11:42] [info] =====

[2025-10-12 11:11:42] [info] Report Generated: 2025-10-12
[2025-10-12 11:11:42] [info] Run Start Time: 11:10:51 AM
[2025-10-12 11:11:42] [info] Run End Time: 11:11:42 AM
[2025-10-12 11:11:42] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:11:42] [info] Tool Version: LVR_2025.1
[2025-10-12 11:11:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:11:42] [info] User Name: ubuntu
[2025-10-12 11:11:42] [info] Cores Used: 8
[2025-10-12 11:11:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Top Cell Information
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Layout Top Cell: TOP
[2025-10-12 11:11:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:11:42] [info] Database Format: GDSII
[2025-10-12 11:11:42] [info] Verification Mode: Full Check

[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Type of Errors | Count
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] DB_WARN -> 1
[2025-10-12 11:11:42] [info] LRF_ERR001 -> 14
[2025-10-12 11:11:42] [info] DB_WARN002 -> 159
[2025-10-12 11:11:42] [info] DB_WARN003 -> 161
```

```

[2025-10-12 11:11:42] [info] DRC_ERR9 -> 89
[2025-10-12 11:11:42] [info] DRC_ERR -> 67
[2025-10-12 11:11:42] [info] DRC_ERR5 -> 408
[2025-10-12 11:11:42] [info] DRC_ERR4 -> 241
[2025-10-12 11:11:42] [info] DRC_ERR1 -> 301
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Rule Violation Summary
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:11:42] [info] |-----|-----|-----|
[2025-10-12 11:11:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:11:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:11:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:11:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:11:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:11:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:11:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:11:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:11:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:11:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:11:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:11:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:11:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:11:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:11:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Performance Metrics
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] | Stage | Time Taken |
[2025-10-12 11:11:42] [info] |-----|-----|
[2025-10-12 11:11:42] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:11:42] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 11:11:42] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Summary
[2025-10-12 11:11:42] [info] -----
[2025-10-12 11:11:42] [info] Total Errors: 1120
[2025-10-12 11:11:42] [info] Total Warnings: 321
[2025-10-12 11:11:42] [info] DRC Status: FAILED

```

lvr_summary_320.rpt

```

[2025-10-12 15:47:26] [info] =====
[2025-10-12 15:47:26] [info] Design Rule Check Report
[2025-10-12 15:47:26] [info] =====

[2025-10-12 15:47:26] [info] Report Generated: 2025-10-12
[2025-10-12 15:47:26] [info] Run Start Time: 03:46:36 PM
[2025-10-12 15:47:26] [info] Run End Time: 03:47:26 PM
[2025-10-12 15:47:26] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:47:26] [info] Tool Version: LVR_2025.1
[2025-10-12 15:47:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:47:26] [info] User Name: ubuntu
[2025-10-12 15:47:26] [info] Cores Used: 8
[2025-10-12 15:47:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Top Cell Information
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Layout Top Cell:      TOP
[2025-10-12 15:47:26] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:47:26] [info] Database Format:       GDSII
[2025-10-12 15:47:26] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Type of Errors | Count
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] DB_WARN -> 1
[2025-10-12 15:47:26] [info] LRF_ERR001 -> 14
[2025-10-12 15:47:26] [info] DB_WARN002 -> 159
[2025-10-12 15:47:26] [info] DB_WARN003 -> 161
[2025-10-12 15:47:26] [info] DRC_ERR9 -> 89
[2025-10-12 15:47:26] [info] DRC_ERR -> 67
[2025-10-12 15:47:26] [info] DRC_ERR5 -> 408
[2025-10-12 15:47:26] [info] DRC_ERR4 -> 241
[2025-10-12 15:47:26] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Rule Violation Summary
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:47:26] [info] |-----|-----|-----|
[2025-10-12 15:47:26] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:47:26] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:47:26] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:47:26] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:47:26] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:47:26] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:47:26] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:47:26] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:47:26] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:47:26] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:47:26] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:47:26] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:47:26] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:47:26] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:47:26] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:47:26] [info] |
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Performance Metrics
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] | Stage          | Time Taken |
[2025-10-12 15:47:26] [info] |-----|-----|
[2025-10-12 15:47:26] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:47:26] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 15:47:26] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Summary
```

```
[2025-10-12 15:47:26] [info] -----
[2025-10-12 15:47:26] [info] Total Errors:      1120
[2025-10-12 15:47:26] [info] Total Warnings:    321
[2025-10-12 15:47:26] [info] DRC Status:        FAILED
```


lvr_summary_321.rpt

```
[2025-10-12 15:48:25] [info] =====
[2025-10-12 15:48:25] [info]                      Design Rule Check Report
[2025-10-12 15:48:25] [info] =====

[2025-10-12 15:48:25] [info] Report Generated:      2025-10-12
[2025-10-12 15:48:25] [info] Run Start Time:       03:47:34 PM
[2025-10-12 15:48:25] [info] Run End Time:         03:48:25 PM
[2025-10-12 15:48:25] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 15:48:25] [info] Tool Version:         LVR_2025.1
[2025-10-12 15:48:25] [info] Host Name:            ip-172-31-37-173
[2025-10-12 15:48:25] [info] User Name:            ubuntu
[2025-10-12 15:48:25] [info] Cores Used:           8
[2025-10-12 15:48:25] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info]                      Top Cell Information
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info] Layout Top Cell:      TOP
[2025-10-12 15:48:25] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 15:48:25] [info] Database Format:       GDSII
[2025-10-12 15:48:25] [info] Verification Mode:     Full Check

[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info]                      Type of Errors | Count
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info] DB_WARN -> 1
[2025-10-12 15:48:25] [info] LRF_ERR001 -> 14
[2025-10-12 15:48:25] [info] DB_WARN002 -> 159
[2025-10-12 15:48:25] [info] DB_WARN003 -> 161
[2025-10-12 15:48:25] [info] DRC_ERR9 -> 89
[2025-10-12 15:48:25] [info] DRC_ERR -> 67
[2025-10-12 15:48:25] [info] DRC_ERR5 -> 408
[2025-10-12 15:48:25] [info] DRC_ERR4 -> 241
[2025-10-12 15:48:25] [info] DRC_ERR1 -> 301
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info]                      Rule Violation Summary
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:48:25] [info] |-----|-----|-----|
[2025-10-12 15:48:25] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:48:25] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:48:25] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:48:25] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:48:25] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:48:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:48:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:48:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:48:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:48:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:48:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:48:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:48:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:48:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:48:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:48:25] [info]
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info]                      Performance Metrics
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:48:25] [info] |-----|-----|
[2025-10-12 15:48:25] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 15:48:25] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 15:48:25] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info]                Summary
[2025-10-12 15:48:25] [info] -----
[2025-10-12 15:48:25] [info] Total Errors:      1120
[2025-10-12 15:48:25] [info] Total Warnings:    321
[2025-10-12 15:48:25] [info] DRC Status:        FAILED
```

lvr_summary_322.rpt

```
[2025-10-12 15:49:23] [info] =====
[2025-10-12 15:49:23] [info]                Design Rule Check Report
[2025-10-12 15:49:23] [info] =====

[2025-10-12 15:49:23] [info] Report Generated:   2025-10-12
[2025-10-12 15:49:23] [info] Run Start Time:     03:48:31 PM
[2025-10-12 15:49:23] [info] Run End Time:       03:49:23 PM
[2025-10-12 15:49:23] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 15:49:23] [info] Tool Version:       LVR_2025.1
[2025-10-12 15:49:23] [info] Host Name:          ip-172-31-37-173
[2025-10-12 15:49:23] [info] User Name:          ubuntu
[2025-10-12 15:49:23] [info] Cores Used:         8
[2025-10-12 15:49:23] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info]                Top Cell Information
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] Layout Top Cell:    TOP
[2025-10-12 15:49:23] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 15:49:23] [info] Database Format:      GDSII
[2025-10-12 15:49:23] [info] Verification Mode:   Full Check

[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info]                Type of Errors | Count
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] DB_WARN -> 1
[2025-10-12 15:49:23] [info] LRF_ERR001 -> 14
[2025-10-12 15:49:23] [info] DB_WARN002 -> 159
[2025-10-12 15:49:23] [info] DB_WARN003 -> 161
[2025-10-12 15:49:23] [info] DRC_ERR9 -> 89
[2025-10-12 15:49:23] [info] DRC_ERR -> 67
[2025-10-12 15:49:23] [info] DRC_ERR5 -> 408
[2025-10-12 15:49:23] [info] DRC_ERR4 -> 241
[2025-10-12 15:49:23] [info] DRC_ERR1 -> 301
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info]                Rule Violation Summary
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:49:23] [info] |-----|-----|-----|
[2025-10-12 15:49:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:49:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:49:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:49:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:49:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 15:49:23] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:49:23] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:49:23] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:49:23] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:49:23] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:49:23] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:49:23] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:49:23] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:49:23] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:49:23] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:49:23] [info]
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] Performance Metrics
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] | Stage | Time Taken |
[2025-10-12 15:49:23] [info] |-----|-----|
[2025-10-12 15:49:23] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:49:23] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:49:23] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] Summary
[2025-10-12 15:49:23] [info] -----
[2025-10-12 15:49:23] [info] Total Errors: 1120
[2025-10-12 15:49:23] [info] Total Warnings: 321
[2025-10-12 15:49:23] [info] DRC Status: FAILED
```

lvr_summary_323.rpt

```
[2025-10-12 15:50:20] [info] =====
[2025-10-12 15:50:20] [info] Design Rule Check Report
[2025-10-12 15:50:20] [info] =====

[2025-10-12 15:50:20] [info] Report Generated: 2025-10-12
[2025-10-12 15:50:20] [info] Run Start Time: 03:49:29 PM
[2025-10-12 15:50:20] [info] Run End Time: 03:50:20 PM
[2025-10-12 15:50:20] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:50:20] [info] Tool Version: LVR_2025.1
[2025-10-12 15:50:20] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:50:20] [info] User Name: ubuntu
[2025-10-12 15:50:20] [info] Cores Used: 8
[2025-10-12 15:50:20] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Top Cell Information
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Layout Top Cell: TOP
[2025-10-12 15:50:20] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:50:20] [info] Database Format: GDSII
[2025-10-12 15:50:20] [info] Verification Mode: Full Check

[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Type of Errors | Count
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] DB_WARN -> 1
[2025-10-12 15:50:20] [info] LRF_ERR001 -> 14
[2025-10-12 15:50:20] [info] DB_WARN002 -> 159
[2025-10-12 15:50:20] [info] DB_WARN003 -> 161
```

```

[2025-10-12 15:50:20] [info] DRC_ERR9 -> 89
[2025-10-12 15:50:20] [info] DRC_ERR -> 67
[2025-10-12 15:50:20] [info] DRC_ERR5 -> 408
[2025-10-12 15:50:20] [info] DRC_ERR4 -> 241
[2025-10-12 15:50:20] [info] DRC_ERR1 -> 301
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Rule Violation Summary
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:50:20] [info] |-----|-----|-----|
[2025-10-12 15:50:20] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:50:20] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:50:20] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:50:20] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:50:20] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:50:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:50:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:50:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:50:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:50:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:50:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:50:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:50:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:50:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:50:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Performance Metrics
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] | Stage | Time Taken |
[2025-10-12 15:50:20] [info] |-----|-----|
[2025-10-12 15:50:20] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:50:20] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:50:20] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Summary
[2025-10-12 15:50:20] [info] -----
[2025-10-12 15:50:20] [info] Total Errors: 1120
[2025-10-12 15:50:20] [info] Total Warnings: 321
[2025-10-12 15:50:20] [info] DRC Status: FAILED

```

lvr_summary_324.rpt

```

[2025-10-12 15:51:20] [info] =====
[2025-10-12 15:51:20] [info] Design Rule Check Report
[2025-10-12 15:51:20] [info] =====

[2025-10-12 15:51:20] [info] Report Generated: 2025-10-12
[2025-10-12 15:51:20] [info] Run Start Time: 03:50:28 PM
[2025-10-12 15:51:20] [info] Run End Time: 03:51:20 PM
[2025-10-12 15:51:20] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:51:20] [info] Tool Version: LVR_2025.1
[2025-10-12 15:51:20] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:51:20] [info] User Name: ubuntu
[2025-10-12 15:51:20] [info] Cores Used: 8
[2025-10-12 15:51:20] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] Top Cell Information
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] Layout Top Cell:      TOP
[2025-10-12 15:51:20] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:51:20] [info] Database Format:      GDSII
[2025-10-12 15:51:20] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] Type of Errors | Count
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] DB_WARN -> 1
[2025-10-12 15:51:20] [info] LRF_ERR001 -> 14
[2025-10-12 15:51:20] [info] DB_WARN002 -> 159
[2025-10-12 15:51:20] [info] DB_WARN003 -> 161
[2025-10-12 15:51:20] [info] DRC_ERR9 -> 89
[2025-10-12 15:51:20] [info] DRC_ERR -> 67
[2025-10-12 15:51:20] [info] DRC_ERR5 -> 408
[2025-10-12 15:51:20] [info] DRC_ERR4 -> 241
[2025-10-12 15:51:20] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] Rule Violation Summary
```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:51:20] [info] |-----|-----|-----|
[2025-10-12 15:51:20] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:51:20] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:51:20] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:51:20] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:51:20] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:51:20] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:51:20] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:51:20] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:51:20] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:51:20] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:51:20] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:51:20] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:51:20] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:51:20] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:51:20] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:51:20] [info] -----
```

```
[2025-10-12 15:51:20] [info] Performance Metrics
```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] | Stage          | Time Taken |
[2025-10-12 15:51:20] [info] |-----|-----|
[2025-10-12 15:51:20] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:51:20] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 15:51:20] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] Summary
```

```
[2025-10-12 15:51:20] [info] -----
[2025-10-12 15:51:20] [info] Total Errors:      1120
[2025-10-12 15:51:20] [info] Total Warnings:    321
[2025-10-12 15:51:20] [info] DRC Status:        FAILED
```

lvr_summary_325.rpt

```
[2025-10-12 15:52:16] [info] =====
[2025-10-12 15:52:16] [info]                      Design Rule Check Report
[2025-10-12 15:52:16] [info] =====

[2025-10-12 15:52:16] [info] Report Generated:      2025-10-12
[2025-10-12 15:52:16] [info] Run Start Time:       03:51:24 PM
[2025-10-12 15:52:16] [info] Run End Time:         03:52:16 PM
[2025-10-12 15:52:16] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 15:52:16] [info] Tool Version:         LVR_2025.1
[2025-10-12 15:52:16] [info] Host Name:            ip-172-31-37-173
[2025-10-12 15:52:16] [info] User Name:            ubuntu
[2025-10-12 15:52:16] [info] Cores Used:           8
[2025-10-12 15:52:16] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info]                      Top Cell Information
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info] Layout Top Cell:      TOP
[2025-10-12 15:52:16] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 15:52:16] [info] Database Format:        GDSII
[2025-10-12 15:52:16] [info] Verification Mode:     Full Check

[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info]                      Type of Errors | Count
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info] DB_WARN -> 1
[2025-10-12 15:52:16] [info] LRF_ERR001 -> 14
[2025-10-12 15:52:16] [info] DB_WARN002 -> 159
[2025-10-12 15:52:16] [info] DB_WARN003 -> 161
[2025-10-12 15:52:16] [info] DRC_ERR9 -> 89
[2025-10-12 15:52:16] [info] DRC_ERR -> 67
[2025-10-12 15:52:16] [info] DRC_ERR5 -> 408
[2025-10-12 15:52:16] [info] DRC_ERR4 -> 241
[2025-10-12 15:52:16] [info] DRC_ERR1 -> 301
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info]                      Rule Violation Summary
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:52:16] [info] |-----|-----|-----|
[2025-10-12 15:52:16] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:52:16] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:52:16] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:52:16] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:52:16] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:52:16] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:52:16] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:52:16] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:52:16] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:52:16] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:52:16] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:52:16] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:52:16] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:52:16] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:52:16] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:52:16] [info]
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info]                      Performance Metrics
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:52:16] [info] |-----|-----|
[2025-10-12 15:52:16] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:52:16] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 15:52:16] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info]                Summary
[2025-10-12 15:52:16] [info] -----
[2025-10-12 15:52:16] [info] Total Errors:    1120
[2025-10-12 15:52:16] [info] Total Warnings:    321
[2025-10-12 15:52:16] [info] DRC Status:      FAILED
```

lvr_summary_326.rpt

```
[2025-10-12 15:53:13] [info] =====
[2025-10-12 15:53:13] [info]                Design Rule Check Report
[2025-10-12 15:53:13] [info] =====

[2025-10-12 15:53:13] [info] Report Generated:    2025-10-12
[2025-10-12 15:53:13] [info] Run Start Time:      03:52:23 PM
[2025-10-12 15:53:13] [info] Run End Time:        03:53:13 PM
[2025-10-12 15:53:13] [info] Time Taken:          0 minutes 49 seconds

[2025-10-12 15:53:13] [info] Tool Version:        LVR_2025.1
[2025-10-12 15:53:13] [info] Host Name:           ip-172-31-37-173
[2025-10-12 15:53:13] [info] User Name:           ubuntu
[2025-10-12 15:53:13] [info] Cores Used:          8
[2025-10-12 15:53:13] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info]                Top Cell Information
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] Layout Top Cell:     TOP
[2025-10-12 15:53:13] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:53:13] [info] Database Format:       GDSII
[2025-10-12 15:53:13] [info] Verification Mode:    Full Check

[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info]                Type of Errors | Count
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] DB_WARN -> 1
[2025-10-12 15:53:13] [info] LRF_ERR001 -> 14
[2025-10-12 15:53:13] [info] DB_WARN002 -> 159
[2025-10-12 15:53:13] [info] DB_WARN003 -> 161
[2025-10-12 15:53:13] [info] DRC_ERR9 -> 89
[2025-10-12 15:53:13] [info] DRC_ERR -> 67
[2025-10-12 15:53:13] [info] DRC_ERR5 -> 408
[2025-10-12 15:53:13] [info] DRC_ERR4 -> 241
[2025-10-12 15:53:13] [info] DRC_ERR1 -> 301
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info]                Rule Violation Summary
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 15:53:13] [info] |-----|-----|-----|
[2025-10-12 15:53:13] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:53:13] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:53:13] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:53:13] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:53:13] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 15:53:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 15:53:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:53:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:53:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:53:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:53:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:53:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:53:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:53:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:53:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:53:13] [info]
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] Performance Metrics
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] | Stage | Time Taken |
[2025-10-12 15:53:13] [info] |-----|-----|
[2025-10-12 15:53:13] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:53:13] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:53:13] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] Summary
[2025-10-12 15:53:13] [info] -----
[2025-10-12 15:53:13] [info] Total Errors: 1120
[2025-10-12 15:53:13] [info] Total Warnings: 321
[2025-10-12 15:53:13] [info] DRC Status: FAILED

```

lvr_summary_327.rpt

```

[2025-10-12 15:54:11] [info] =====
[2025-10-12 15:54:11] [info] Design Rule Check Report
[2025-10-12 15:54:11] [info] =====

[2025-10-12 15:54:11] [info] Report Generated: 2025-10-12
[2025-10-12 15:54:11] [info] Run Start Time: 03:53:21 PM
[2025-10-12 15:54:11] [info] Run End Time: 03:54:11 PM
[2025-10-12 15:54:11] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:54:11] [info] Tool Version: LVR_2025.1
[2025-10-12 15:54:11] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:54:11] [info] User Name: ubuntu
[2025-10-12 15:54:11] [info] Cores Used: 8
[2025-10-12 15:54:11] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Top Cell Information
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Layout Top Cell: TOP
[2025-10-12 15:54:11] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:54:11] [info] Database Format: GDSII
[2025-10-12 15:54:11] [info] Verification Mode: Full Check

[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Type of Errors | Count
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] DB_WARN -> 1
[2025-10-12 15:54:11] [info] LRF_ERR001 -> 14
[2025-10-12 15:54:11] [info] DB_WARN002 -> 159
[2025-10-12 15:54:11] [info] DB_WARN003 -> 161

```



```

[2025-10-12 15:54:11] [info] DRC_ERR9 -> 89
[2025-10-12 15:54:11] [info] DRC_ERR -> 67
[2025-10-12 15:54:11] [info] DRC_ERR5 -> 408
[2025-10-12 15:54:11] [info] DRC_ERR4 -> 241
[2025-10-12 15:54:11] [info] DRC_ERR1 -> 301
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Rule Violation Summary
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:54:11] [info] |-----|-----|-----|
[2025-10-12 15:54:11] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:54:11] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:54:11] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:54:11] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:54:11] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:54:11] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:54:11] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:54:11] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:54:11] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:54:11] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:54:11] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:54:11] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:54:11] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:54:11] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:54:11] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Performance Metrics
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] | Stage | Time Taken |
[2025-10-12 15:54:11] [info] |-----|-----|
[2025-10-12 15:54:11] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:54:11] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 15:54:11] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Summary
[2025-10-12 15:54:11] [info] -----
[2025-10-12 15:54:11] [info] Total Errors: 1120
[2025-10-12 15:54:11] [info] Total Warnings: 321
[2025-10-12 15:54:11] [info] DRC Status: FAILED

```

lvr_summary_328.rpt

```

[2025-10-12 15:55:10] [info] =====
[2025-10-12 15:55:10] [info] Design Rule Check Report
[2025-10-12 15:55:10] [info] =====

[2025-10-12 15:55:10] [info] Report Generated: 2025-10-12
[2025-10-12 15:55:10] [info] Run Start Time: 03:54:19 PM
[2025-10-12 15:55:10] [info] Run End Time: 03:55:10 PM
[2025-10-12 15:55:10] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 15:55:10] [info] Tool Version: LVR_2025.1
[2025-10-12 15:55:10] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:55:10] [info] User Name: ubuntu
[2025-10-12 15:55:10] [info] Cores Used: 8
[2025-10-12 15:55:10] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] Top Cell Information
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] Layout Top Cell:      TOP
[2025-10-12 15:55:10] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:55:10] [info] Database Format:      GDSII
[2025-10-12 15:55:10] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] Type of Errors | Count
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] DB_WARN -> 1
[2025-10-12 15:55:10] [info] LRF_ERR001 -> 14
[2025-10-12 15:55:10] [info] DB_WARN002 -> 159
[2025-10-12 15:55:10] [info] DB_WARN003 -> 161
[2025-10-12 15:55:10] [info] DRC_ERR9 -> 89
[2025-10-12 15:55:10] [info] DRC_ERR -> 67
[2025-10-12 15:55:10] [info] DRC_ERR5 -> 408
[2025-10-12 15:55:10] [info] DRC_ERR4 -> 241
[2025-10-12 15:55:10] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] Rule Violation Summary
```

```
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 15:55:10] [info] |-----|-----|-----|
[2025-10-12 15:55:10] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 15:55:10] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 15:55:10] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 15:55:10] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 15:55:10] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 15:55:10] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 15:55:10] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 15:55:10] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 15:55:10] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 15:55:10] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 15:55:10] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 15:55:10] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 15:55:10] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 15:55:10] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 15:55:10] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 15:55:10] [info] -----
```

```
[2025-10-12 15:55:10] [info] Performance Metrics
```

```
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] | Stage          | Time Taken |
[2025-10-12 15:55:10] [info] |-----|-----|
[2025-10-12 15:55:10] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 15:55:10] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 15:55:10] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 15:55:10] [info] -----
```

```
[2025-10-12 15:55:10] [info] Summary
```

```
[2025-10-12 15:55:10] [info] -----
[2025-10-12 15:55:10] [info] Total Errors:      1120
[2025-10-12 15:55:10] [info] Total Warnings:    321
[2025-10-12 15:55:10] [info] DRC Status:        FAILED
```

lvr_summary_329.rpt

```
[2025-10-12 15:56:08] [info] =====
[2025-10-12 15:56:08] [info] Design Rule Check Report
[2025-10-12 15:56:08] [info] =====

[2025-10-12 15:56:08] [info] Report Generated: 2025-10-12
[2025-10-12 15:56:08] [info] Run Start Time: 03:55:16 PM
[2025-10-12 15:56:08] [info] Run End Time: 03:56:08 PM
[2025-10-12 15:56:08] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:56:08] [info] Tool Version: LVR_2025.1
[2025-10-12 15:56:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:56:08] [info] User Name: ubuntu
[2025-10-12 15:56:08] [info] Cores Used: 8
[2025-10-12 15:56:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] Top Cell Information
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] Layout Top Cell: TOP
[2025-10-12 15:56:08] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:56:08] [info] Database Format: GDSII
[2025-10-12 15:56:08] [info] Verification Mode: Full Check

[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] Type of Errors | Count
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] DB_WARN -> 1
[2025-10-12 15:56:08] [info] LRF_ERR001 -> 14
[2025-10-12 15:56:08] [info] DB_WARN002 -> 159
[2025-10-12 15:56:08] [info] DB_WARN003 -> 161
[2025-10-12 15:56:08] [info] DRC_ERR9 -> 89
[2025-10-12 15:56:08] [info] DRC_ERR -> 67
[2025-10-12 15:56:08] [info] DRC_ERR5 -> 408
[2025-10-12 15:56:08] [info] DRC_ERR4 -> 241
[2025-10-12 15:56:08] [info] DRC_ERR1 -> 301
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] Rule Violation Summary
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:56:08] [info] |-----|-----|-----|
[2025-10-12 15:56:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:56:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:56:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:56:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:56:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:56:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:56:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:56:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:56:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:56:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:56:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:56:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:56:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:56:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:56:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:56:08] [info]
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] Performance Metrics
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] | Stage | Time Taken |
```

```
[2025-10-12 15:56:08] [info] |-----|-----|
[2025-10-12 15:56:08] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:56:08] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 15:56:08] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info]                Summary
[2025-10-12 15:56:08] [info] -----
[2025-10-12 15:56:08] [info] Total Errors:    1120
[2025-10-12 15:56:08] [info] Total Warnings:    321
[2025-10-12 15:56:08] [info] DRC Status:      FAILED
```

lvr_summary_33.rpt

```
[2025-10-12 11:12:39] [info] =====
[2025-10-12 11:12:39] [info]                Design Rule Check Report
[2025-10-12 11:12:39] [info] =====

[2025-10-12 11:12:39] [info] Report Generated:    2025-10-12
[2025-10-12 11:12:39] [info] Run Start Time:      11:11:48 AM
[2025-10-12 11:12:39] [info] Run End Time:        11:12:39 AM
[2025-10-12 11:12:39] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:12:39] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:12:39] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:12:39] [info] User Name:           ubuntu
[2025-10-12 11:12:39] [info] Cores Used:          8
[2025-10-12 11:12:39] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info]                Top Cell Information
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] Layout Top Cell:     TOP
[2025-10-12 11:12:39] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:12:39] [info] Database Format:      GDSII
[2025-10-12 11:12:39] [info] Verification Mode:    Full Check

[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info]                Type of Errors | Count
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] DB_WARN -> 1
[2025-10-12 11:12:39] [info] LRF_ERR001 -> 14
[2025-10-12 11:12:39] [info] DB_WARN002 -> 159
[2025-10-12 11:12:39] [info] DB_WARN003 -> 161
[2025-10-12 11:12:39] [info] DRC_ERR9 -> 89
[2025-10-12 11:12:39] [info] DRC_ERR -> 67
[2025-10-12 11:12:39] [info] DRC_ERR5 -> 408
[2025-10-12 11:12:39] [info] DRC_ERR4 -> 241
[2025-10-12 11:12:39] [info] DRC_ERR1 -> 301
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info]                Rule Violation Summary
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:12:39] [info] |-----|-----|-----|
[2025-10-12 11:12:39] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:12:39] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:12:39] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:12:39] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:12:39] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:12:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 11:12:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:12:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:12:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:12:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:12:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:12:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:12:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:12:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:12:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:12:39] [info]
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] Performance Metrics
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] | Stage | Time Taken |
[2025-10-12 11:12:39] [info] |-----|-----|
[2025-10-12 11:12:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:12:39] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:12:39] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] Summary
[2025-10-12 11:12:39] [info] -----
[2025-10-12 11:12:39] [info] Total Errors: 1120
[2025-10-12 11:12:39] [info] Total Warnings: 321
[2025-10-12 11:12:39] [info] DRC Status: FAILED
```

lvr_summary_330.rpt

```
[2025-10-12 15:57:06] [info] =====
[2025-10-12 15:57:06] [info] Design Rule Check Report
[2025-10-12 15:57:06] [info] =====

[2025-10-12 15:57:06] [info] Report Generated: 2025-10-12
[2025-10-12 15:57:06] [info] Run Start Time: 03:56:14 PM
[2025-10-12 15:57:06] [info] Run End Time: 03:57:06 PM
[2025-10-12 15:57:06] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:57:06] [info] Tool Version: LVR_2025.1
[2025-10-12 15:57:06] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:57:06] [info] User Name: ubuntu
[2025-10-12 15:57:06] [info] Cores Used: 8
[2025-10-12 15:57:06] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Top Cell Information
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Layout Top Cell: TOP
[2025-10-12 15:57:06] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 15:57:06] [info] Database Format: GDSII
[2025-10-12 15:57:06] [info] Verification Mode: Full Check

[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Type of Errors | Count
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] DB_WARN -> 1
[2025-10-12 15:57:06] [info] LRF_ERR001 -> 14
[2025-10-12 15:57:06] [info] DB_WARN002 -> 159
[2025-10-12 15:57:06] [info] DB_WARN003 -> 161
```

```

[2025-10-12 15:57:06] [info] DRC_ERR9 -> 89
[2025-10-12 15:57:06] [info] DRC_ERR -> 67
[2025-10-12 15:57:06] [info] DRC_ERR5 -> 408
[2025-10-12 15:57:06] [info] DRC_ERR4 -> 241
[2025-10-12 15:57:06] [info] DRC_ERR1 -> 301
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Rule Violation Summary
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:57:06] [info] |-----|-----|-----|
[2025-10-12 15:57:06] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:57:06] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:57:06] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:57:06] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:57:06] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:57:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 15:57:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:57:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:57:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:57:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:57:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:57:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:57:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:57:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:57:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Performance Metrics
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] | Stage | Time Taken |
[2025-10-12 15:57:06] [info] |-----|-----|
[2025-10-12 15:57:06] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 15:57:06] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 15:57:06] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Summary
[2025-10-12 15:57:06] [info] -----
[2025-10-12 15:57:06] [info] Total Errors: 1120
[2025-10-12 15:57:06] [info] Total Warnings: 321
[2025-10-12 15:57:06] [info] DRC Status: FAILED

```

lvr_summary_331.rpt

```

[2025-10-12 15:58:07] [info] =====
[2025-10-12 15:58:07] [info] Design Rule Check Report
[2025-10-12 15:58:07] [info] =====

[2025-10-12 15:58:07] [info] Report Generated: 2025-10-12
[2025-10-12 15:58:07] [info] Run Start Time: 03:57:15 PM
[2025-10-12 15:58:07] [info] Run End Time: 03:58:07 PM
[2025-10-12 15:58:07] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 15:58:07] [info] Tool Version: LVR_2025.1
[2025-10-12 15:58:07] [info] Host Name: ip-172-31-37-173
[2025-10-12 15:58:07] [info] User Name: ubuntu
[2025-10-12 15:58:07] [info] Cores Used: 8
[2025-10-12 15:58:07] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] Top Cell Information
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] Layout Top Cell:      TOP
[2025-10-12 15:58:07] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 15:58:07] [info] Database Format:      GDSII
[2025-10-12 15:58:07] [info] Verification Mode:    Full Check
```

```
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] Type of Errors | Count
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] DB_WARN -> 1
[2025-10-12 15:58:07] [info] LRF_ERR001 -> 14
[2025-10-12 15:58:07] [info] DB_WARN002 -> 159
[2025-10-12 15:58:07] [info] DB_WARN003 -> 161
[2025-10-12 15:58:07] [info] DRC_ERR9 -> 89
[2025-10-12 15:58:07] [info] DRC_ERR -> 67
[2025-10-12 15:58:07] [info] DRC_ERR5 -> 408
[2025-10-12 15:58:07] [info] DRC_ERR4 -> 241
[2025-10-12 15:58:07] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] Rule Violation Summary
[2025-10-12 15:58:07] [info] -----
```

[2025-10-12 15:58:07] [info]	Rule ID	Description	Error Count
[2025-10-12 15:58:07] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 15:58:07] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 15:58:07] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 15:58:07] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 15:58:07] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 15:58:07] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 15:58:07] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 15:58:07] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 15:58:07] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 15:58:07] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 15:58:07] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 15:58:07] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" 15	
[2025-10-12 15:58:07] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 15:58:07] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 15:58:07] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

```
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] Performance Metrics
[2025-10-12 15:58:07] [info] -----
```

[2025-10-12 15:58:07] [info]	Stage	Time Taken
[2025-10-12 15:58:07] [info]	GDS Parsing	0 minutes 9 seconds
[2025-10-12 15:58:07] [info]	Rule Checking	0 minutes 41 seconds
[2025-10-12 15:58:07] [info]	Report Generation	0 minutes 0 seconds

```
[2025-10-12 15:58:07] [info] -----
[2025-10-12 15:58:07] [info] Summary
[2025-10-12 15:58:07] [info] -----
```

```
[2025-10-12 15:58:07] [info] Total Errors:      1120
[2025-10-12 15:58:07] [info] Total Warnings:    321
[2025-10-12 15:58:07] [info] DRC Status:        FAILED
```

lvr_summary_332.rpt

```
[2025-10-12 15:59:03] [info] =====
[2025-10-12 15:59:03] [info]                      Design Rule Check Report
[2025-10-12 15:59:03] [info] =====

[2025-10-12 15:59:03] [info] Report Generated:      2025-10-12
[2025-10-12 15:59:03] [info] Run Start Time:       03:58:13 PM
[2025-10-12 15:59:03] [info] Run End Time:         03:59:03 PM
[2025-10-12 15:59:03] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 15:59:03] [info] Tool Version:          LVR_2025.1
[2025-10-12 15:59:03] [info] Host Name:             ip-172-31-37-173
[2025-10-12 15:59:03] [info] User Name:             ubuntu
[2025-10-12 15:59:03] [info] Cores Used:            8
[2025-10-12 15:59:03] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info]                      Top Cell Information
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info] Layout Top Cell:      TOP
[2025-10-12 15:59:03] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 15:59:03] [info] Database Format:       GDSII
[2025-10-12 15:59:03] [info] Verification Mode:     Full Check

[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info]                      Type of Errors | Count
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info] DB_WARN -> 1
[2025-10-12 15:59:03] [info] LRF_ERR001 -> 14
[2025-10-12 15:59:03] [info] DB_WARN002 -> 159
[2025-10-12 15:59:03] [info] DB_WARN003 -> 161
[2025-10-12 15:59:03] [info] DRC_ERR9 -> 89
[2025-10-12 15:59:03] [info] DRC_ERR -> 67
[2025-10-12 15:59:03] [info] DRC_ERR5 -> 408
[2025-10-12 15:59:03] [info] DRC_ERR4 -> 241
[2025-10-12 15:59:03] [info] DRC_ERR1 -> 301
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info]                      Rule Violation Summary
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 15:59:03] [info] |-----|-----|-----|
[2025-10-12 15:59:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 15:59:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 15:59:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 15:59:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 15:59:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 15:59:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 15:59:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 15:59:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 15:59:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 15:59:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 15:59:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 15:59:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 15:59:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 15:59:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 15:59:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 15:59:03] [info]
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info]                      Performance Metrics
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info] | Stage | Time Taken |
```



```
[2025-10-12 15:59:03] [info] |-----|-----|
[2025-10-12 15:59:03] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 15:59:03] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 15:59:03] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info] Summary
[2025-10-12 15:59:03] [info] -----
[2025-10-12 15:59:03] [info] Total Errors: 1120
[2025-10-12 15:59:03] [info] Total Warnings: 321
[2025-10-12 15:59:03] [info] DRC Status: FAILED
```

lvr_summary_333.rpt

```
[2025-10-12 16:00:02] [info] =====
[2025-10-12 16:00:02] [info] Design Rule Check Report
[2025-10-12 16:00:02] [info] =====

[2025-10-12 16:00:02] [info] Report Generated: 2025-10-12
[2025-10-12 16:00:02] [info] Run Start Time: 03:59:11 PM
[2025-10-12 16:00:02] [info] Run End Time: 04:00:02 PM
[2025-10-12 16:00:02] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:00:02] [info] Tool Version: LVR_2025.1
[2025-10-12 16:00:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:00:02] [info] User Name: ubuntu
[2025-10-12 16:00:02] [info] Cores Used: 8
[2025-10-12 16:00:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Top Cell Information
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Layout Top Cell: TOP
[2025-10-12 16:00:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:00:02] [info] Database Format: GDSII
[2025-10-12 16:00:02] [info] Verification Mode: Full Check

[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Type of Errors | Count
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] DB_WARN -> 1
[2025-10-12 16:00:02] [info] LRF_ERR001 -> 14
[2025-10-12 16:00:02] [info] DB_WARN002 -> 159
[2025-10-12 16:00:02] [info] DB_WARN003 -> 161
[2025-10-12 16:00:02] [info] DRC_ERR9 -> 89
[2025-10-12 16:00:02] [info] DRC_ERR -> 67
[2025-10-12 16:00:02] [info] DRC_ERR5 -> 408
[2025-10-12 16:00:02] [info] DRC_ERR4 -> 241
[2025-10-12 16:00:02] [info] DRC_ERR1 -> 301
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Rule Violation Summary
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:00:02] [info] |-----|-----|-----|
[2025-10-12 16:00:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:00:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:00:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:00:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:00:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

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[2025-10-12 16:00:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 16:00:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:00:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:00:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:00:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:00:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:00:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:00:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:00:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:00:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:00:02] [info]
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Performance Metrics
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] | Stage | Time Taken |
[2025-10-12 16:00:02] [info] |-----|-----|
[2025-10-12 16:00:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:00:02] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:00:02] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Summary
[2025-10-12 16:00:02] [info] -----
[2025-10-12 16:00:02] [info] Total Errors: 1120
[2025-10-12 16:00:02] [info] Total Warnings: 321
[2025-10-12 16:00:02] [info] DRC Status: FAILED

```

lvr_summary_334.rpt

```

[2025-10-12 16:01:02] [info] =====
[2025-10-12 16:01:02] [info] Design Rule Check Report
[2025-10-12 16:01:02] [info] =====

[2025-10-12 16:01:02] [info] Report Generated: 2025-10-12
[2025-10-12 16:01:02] [info] Run Start Time: 04:00:10 PM
[2025-10-12 16:01:02] [info] Run End Time: 04:01:02 PM
[2025-10-12 16:01:02] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:01:02] [info] Tool Version: LVR_2025.1
[2025-10-12 16:01:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:01:02] [info] User Name: ubuntu
[2025-10-12 16:01:02] [info] Cores Used: 8
[2025-10-12 16:01:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Top Cell Information
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Layout Top Cell: TOP
[2025-10-12 16:01:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:01:02] [info] Database Format: GDSII
[2025-10-12 16:01:02] [info] Verification Mode: Full Check

[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Type of Errors | Count
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] DB_WARN -> 1
[2025-10-12 16:01:02] [info] LRF_ERR001 -> 14
[2025-10-12 16:01:02] [info] DB_WARN002 -> 159
[2025-10-12 16:01:02] [info] DB_WARN003 -> 161

```

```

[2025-10-12 16:01:02] [info] DRC_ERR9 -> 89
[2025-10-12 16:01:02] [info] DRC_ERR -> 67
[2025-10-12 16:01:02] [info] DRC_ERR5 -> 408
[2025-10-12 16:01:02] [info] DRC_ERR4 -> 241
[2025-10-12 16:01:02] [info] DRC_ERR1 -> 301
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Rule Violation Summary
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:01:02] [info] |-----|-----|-----|
[2025-10-12 16:01:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:01:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:01:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:01:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:01:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:01:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:01:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:01:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:01:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:01:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:01:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:01:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:01:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:01:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:01:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Performance Metrics
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] | Stage | Time Taken |
[2025-10-12 16:01:02] [info] |-----|-----|
[2025-10-12 16:01:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:01:02] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:01:02] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Summary
[2025-10-12 16:01:02] [info] -----
[2025-10-12 16:01:02] [info] Total Errors: 1120
[2025-10-12 16:01:02] [info] Total Warnings: 321
[2025-10-12 16:01:02] [info] DRC Status: FAILED

```

lvr_summary_335.rpt

```

[2025-10-12 16:02:01] [info] =====
[2025-10-12 16:02:01] [info] Design Rule Check Report
[2025-10-12 16:02:01] [info] =====

[2025-10-12 16:02:01] [info] Report Generated: 2025-10-12
[2025-10-12 16:02:01] [info] Run Start Time: 04:01:08 PM
[2025-10-12 16:02:01] [info] Run End Time: 04:02:01 PM
[2025-10-12 16:02:01] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 16:02:01] [info] Tool Version: LVR_2025.1
[2025-10-12 16:02:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:02:01] [info] User Name: ubuntu
[2025-10-12 16:02:01] [info] Cores Used: 8
[2025-10-12 16:02:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Top Cell Information
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Layout Top Cell:      TOP
[2025-10-12 16:02:01] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:02:01] [info] Database Format:      GDSII
[2025-10-12 16:02:01] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Type of Errors | Count
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] DB_WARN -> 1
[2025-10-12 16:02:01] [info] LRF_ERR001 -> 14
[2025-10-12 16:02:01] [info] DB_WARN002 -> 159
[2025-10-12 16:02:01] [info] DB_WARN003 -> 161
[2025-10-12 16:02:01] [info] DRC_ERR9 -> 89
[2025-10-12 16:02:01] [info] DRC_ERR -> 67
[2025-10-12 16:02:01] [info] DRC_ERR5 -> 408
[2025-10-12 16:02:01] [info] DRC_ERR4 -> 241
[2025-10-12 16:02:01] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Rule Violation Summary
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:02:01] [info] |-----|-----|-----|
[2025-10-12 16:02:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:02:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:02:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:02:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:02:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:02:01] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:02:01] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:02:01] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:02:01] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:02:01] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:02:01] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:02:01] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:02:01] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:02:01] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:02:01] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:02:01] [info] |
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Performance Metrics
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] | Stage          | Time Taken |
[2025-10-12 16:02:01] [info] |-----|-----|
[2025-10-12 16:02:01] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:02:01] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 16:02:01] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Summary
```

```
[2025-10-12 16:02:01] [info] -----
[2025-10-12 16:02:01] [info] Total Errors:      1120
[2025-10-12 16:02:01] [info] Total Warnings:    321
[2025-10-12 16:02:01] [info] DRC Status:        FAILED
```

lvr_summary_336.rpt

```
[2025-10-12 16:03:00] [info] =====
[2025-10-12 16:03:00] [info]                      Design Rule Check Report
[2025-10-12 16:03:00] [info] =====

[2025-10-12 16:03:00] [info] Report Generated:      2025-10-12
[2025-10-12 16:03:00] [info] Run Start Time:       04:02:08 PM
[2025-10-12 16:03:00] [info] Run End Time:         04:03:00 PM
[2025-10-12 16:03:00] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 16:03:00] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:03:00] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:03:00] [info] User Name:            ubuntu
[2025-10-12 16:03:00] [info] Cores Used:            8
[2025-10-12 16:03:00] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info]                      Top Cell Information
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info] Layout Top Cell:      TOP
[2025-10-12 16:03:00] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:03:00] [info] Database Format:        GDSII
[2025-10-12 16:03:00] [info] Verification Mode:     Full Check

[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info]                      Type of Errors | Count
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info] DB_WARN -> 1
[2025-10-12 16:03:00] [info] LRF_ERR001 -> 14
[2025-10-12 16:03:00] [info] DB_WARN002 -> 159
[2025-10-12 16:03:00] [info] DB_WARN003 -> 161
[2025-10-12 16:03:00] [info] DRC_ERR9 -> 89
[2025-10-12 16:03:00] [info] DRC_ERR -> 67
[2025-10-12 16:03:00] [info] DRC_ERR5 -> 408
[2025-10-12 16:03:00] [info] DRC_ERR4 -> 241
[2025-10-12 16:03:00] [info] DRC_ERR1 -> 301
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info]                      Rule Violation Summary
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:03:00] [info] |-----|-----|-----|
[2025-10-12 16:03:00] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:03:00] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:03:00] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:03:00] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:03:00] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:03:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:03:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:03:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:03:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:03:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:03:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:03:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:03:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:03:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:03:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:03:00] [info]
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info]                      Performance Metrics
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:03:00] [info] |-----|-----|
[2025-10-12 16:03:00] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:03:00] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 16:03:00] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info]                Summary
[2025-10-12 16:03:00] [info] -----
[2025-10-12 16:03:00] [info] Total Errors:    1120
[2025-10-12 16:03:00] [info] Total Warnings:    321
[2025-10-12 16:03:00] [info] DRC Status:      FAILED
```

lvr_summary_337.rpt

```
[2025-10-12 16:03:58] [info] =====
[2025-10-12 16:03:58] [info]                Design Rule Check Report
[2025-10-12 16:03:58] [info] =====

[2025-10-12 16:03:58] [info] Report Generated:    2025-10-12
[2025-10-12 16:03:58] [info] Run Start Time:      04:03:07 PM
[2025-10-12 16:03:58] [info] Run End Time:        04:03:58 PM
[2025-10-12 16:03:58] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:03:58] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:03:58] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:03:58] [info] User Name:           ubuntu
[2025-10-12 16:03:58] [info] Cores Used:          8
[2025-10-12 16:03:58] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info]                Top Cell Information
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] Layout Top Cell:     TOP
[2025-10-12 16:03:58] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:03:58] [info] Database Format:       GDSII
[2025-10-12 16:03:58] [info] Verification Mode:    Full Check

[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info]                Type of Errors | Count
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] DB_WARN -> 1
[2025-10-12 16:03:58] [info] LRF_ERR001 -> 14
[2025-10-12 16:03:58] [info] DB_WARN002 -> 159
[2025-10-12 16:03:58] [info] DB_WARN003 -> 161
[2025-10-12 16:03:58] [info] DRC_ERR9 -> 89
[2025-10-12 16:03:58] [info] DRC_ERR -> 67
[2025-10-12 16:03:58] [info] DRC_ERR5 -> 408
[2025-10-12 16:03:58] [info] DRC_ERR4 -> 241
[2025-10-12 16:03:58] [info] DRC_ERR1 -> 301
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info]                Rule Violation Summary
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:03:58] [info] |-----|-----|-----|
[2025-10-12 16:03:58] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:03:58] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:03:58] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:03:58] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:03:58] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 16:03:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:03:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:03:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:03:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:03:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:03:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:03:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:03:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:03:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:03:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:03:58] [info]
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] Performance Metrics
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] | Stage | Time Taken |
[2025-10-12 16:03:58] [info] |-----|-----|
[2025-10-12 16:03:58] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:03:58] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:03:58] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] Summary
[2025-10-12 16:03:58] [info] -----
[2025-10-12 16:03:58] [info] Total Errors: 1120
[2025-10-12 16:03:58] [info] Total Warnings: 321
[2025-10-12 16:03:58] [info] DRC Status: FAILED

```

lvr_summary_338.rpt

```

[2025-10-12 16:04:58] [info] =====
[2025-10-12 16:04:58] [info] Design Rule Check Report
[2025-10-12 16:04:58] [info] =====

[2025-10-12 16:04:58] [info] Report Generated: 2025-10-12
[2025-10-12 16:04:58] [info] Run Start Time: 04:04:06 PM
[2025-10-12 16:04:58] [info] Run End Time: 04:04:58 PM
[2025-10-12 16:04:58] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:04:58] [info] Tool Version: LVR_2025.1
[2025-10-12 16:04:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:04:58] [info] User Name: ubuntu
[2025-10-12 16:04:58] [info] Cores Used: 8
[2025-10-12 16:04:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Top Cell Information
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Layout Top Cell: TOP
[2025-10-12 16:04:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:04:58] [info] Database Format: GDSII
[2025-10-12 16:04:58] [info] Verification Mode: Full Check

[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Type of Errors | Count
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] DB_WARN -> 1
[2025-10-12 16:04:58] [info] LRF_ERR001 -> 14
[2025-10-12 16:04:58] [info] DB_WARN002 -> 159
[2025-10-12 16:04:58] [info] DB_WARN003 -> 161

```

```
[2025-10-12 16:04:58] [info] DRC_ERR9 -> 89
[2025-10-12 16:04:58] [info] DRC_ERR -> 67
[2025-10-12 16:04:58] [info] DRC_ERR5 -> 408
[2025-10-12 16:04:58] [info] DRC_ERR4 -> 241
[2025-10-12 16:04:58] [info] DRC_ERR1 -> 301
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Rule Violation Summary
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:04:58] [info] |-----|-----|-----|
[2025-10-12 16:04:58] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:04:58] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:04:58] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:04:58] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:04:58] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:04:58] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 16:04:58] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:04:58] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:04:58] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:04:58] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:04:58] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:04:58] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:04:58] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:04:58] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:04:58] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Performance Metrics
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] | Stage          | Time Taken |
[2025-10-12 16:04:58] [info] |-----|-----|
[2025-10-12 16:04:58] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:04:58] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 16:04:58] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Summary
[2025-10-12 16:04:58] [info] -----
[2025-10-12 16:04:58] [info] Total Errors: 1120
[2025-10-12 16:04:58] [info] Total Warnings: 321
[2025-10-12 16:04:58] [info] DRC Status: FAILED
```

lvr_summary_339.rpt

```
[2025-10-12 16:05:56] [info] =====
[2025-10-12 16:05:56] [info] Design Rule Check Report
[2025-10-12 16:05:56] [info] =====

[2025-10-12 16:05:56] [info] Report Generated: 2025-10-12
[2025-10-12 16:05:56] [info] Run Start Time: 04:05:04 PM
[2025-10-12 16:05:56] [info] Run End Time: 04:05:56 PM
[2025-10-12 16:05:56] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:05:56] [info] Tool Version: LVR_2025.1
[2025-10-12 16:05:56] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:05:56] [info] User Name: ubuntu
[2025-10-12 16:05:56] [info] Cores Used: 8
[2025-10-12 16:05:56] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 16:05:56] [info] -----
[2025-10-12 16:05:56] [info] Top Cell Information
[2025-10-12 16:05:56] [info] -----
[2025-10-12 16:05:56] [info] Layout Top Cell: TOP
[2025-10-12 16:05:56] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:05:56] [info] Database Format: GDSII
[2025-10-12 16:05:56] [info] Verification Mode: Full Check
```

```
[2025-10-12 16:05:56] [info] -----
[2025-10-12 16:05:56] [info] Type of Errors | Count
[2025-10-12 16:05:56] [info] -----
[2025-10-12 16:05:56] [info] DB_WARN -> 1
[2025-10-12 16:05:56] [info] LRF_ERR001 -> 14
[2025-10-12 16:05:56] [info] DB_WARN002 -> 159
[2025-10-12 16:05:56] [info] DB_WARN003 -> 161
[2025-10-12 16:05:56] [info] DRC_ERR9 -> 89
[2025-10-12 16:05:56] [info] DRC_ERR -> 67
[2025-10-12 16:05:56] [info] DRC_ERR5 -> 408
[2025-10-12 16:05:56] [info] DRC_ERR4 -> 241
[2025-10-12 16:05:56] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:05:56] [info] -----
[2025-10-12 16:05:56] [info] Rule Violation Summary
```

```
[2025-10-12 16:05:56] [info] -----
[2025-10-12 16:05:56] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:05:56] [info] |-----|-----|-----|
[2025-10-12 16:05:56] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:05:56] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:05:56] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:05:56] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:05:56] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:05:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:05:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:05:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:05:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:05:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:05:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:05:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:05:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:05:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:05:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 16:05:56] [info] -----
```

```
[2025-10-12 16:05:56] [info] Performance Metrics
```

```
[2025-10-12 16:05:56] [info] -----
```

```
[2025-10-12 16:05:56] [info] | Stage | Time Taken |
[2025-10-12 16:05:56] [info] |-----|-----|
[2025-10-12 16:05:56] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:05:56] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:05:56] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:05:56] [info] -----
```

```
[2025-10-12 16:05:56] [info] Summary
```

```
[2025-10-12 16:05:56] [info] -----
```

```
[2025-10-12 16:05:56] [info] Total Errors: 1120
[2025-10-12 16:05:56] [info] Total Warnings: 321
[2025-10-12 16:05:56] [info] DRC Status: FAILED
```

lvr_summary_34.rpt

```
[2025-10-12 11:13:37] [info] =====
[2025-10-12 11:13:37] [info] Design Rule Check Report
[2025-10-12 11:13:37] [info] =====

[2025-10-12 11:13:37] [info] Report Generated: 2025-10-12
[2025-10-12 11:13:37] [info] Run Start Time: 11:12:45 AM
[2025-10-12 11:13:37] [info] Run End Time: 11:13:37 AM
[2025-10-12 11:13:37] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:13:37] [info] Tool Version: LVR_2025.1
[2025-10-12 11:13:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:13:37] [info] User Name: ubuntu
[2025-10-12 11:13:37] [info] Cores Used: 8
[2025-10-12 11:13:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] Top Cell Information
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] Layout Top Cell: TOP
[2025-10-12 11:13:37] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:13:37] [info] Database Format: GDSII
[2025-10-12 11:13:37] [info] Verification Mode: Full Check

[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] Type of Errors | Count
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] DB_WARN -> 1
[2025-10-12 11:13:37] [info] LRF_ERR001 -> 14
[2025-10-12 11:13:37] [info] DB_WARN002 -> 159
[2025-10-12 11:13:37] [info] DB_WARN003 -> 161
[2025-10-12 11:13:37] [info] DRC_ERR9 -> 89
[2025-10-12 11:13:37] [info] DRC_ERR -> 67
[2025-10-12 11:13:37] [info] DRC_ERR5 -> 408
[2025-10-12 11:13:37] [info] DRC_ERR4 -> 241
[2025-10-12 11:13:37] [info] DRC_ERR1 -> 301
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] Rule Violation Summary
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:13:37] [info] |-----|-----|-----|
[2025-10-12 11:13:37] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:13:37] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:13:37] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:13:37] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:13:37] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:13:37] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:13:37] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:13:37] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:13:37] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:13:37] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:13:37] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:13:37] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:13:37] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:13:37] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:13:37] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:13:37] [info]
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] Performance Metrics
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:13:37] [info] |-----|-----|
[2025-10-12 11:13:37] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:13:37] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 11:13:37] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info]                Summary
[2025-10-12 11:13:37] [info] -----
[2025-10-12 11:13:37] [info] Total Errors:    1120
[2025-10-12 11:13:37] [info] Total Warnings:    321
[2025-10-12 11:13:37] [info] DRC Status:      FAILED
```

lvr_summary_340.rpt

```
[2025-10-12 16:06:53] [info] =====
[2025-10-12 16:06:53] [info]                Design Rule Check Report
[2025-10-12 16:06:53] [info] =====

[2025-10-12 16:06:53] [info] Report Generated:    2025-10-12
[2025-10-12 16:06:53] [info] Run Start Time:      04:06:02 PM
[2025-10-12 16:06:53] [info] Run End Time:        04:06:53 PM
[2025-10-12 16:06:53] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 16:06:53] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:06:53] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:06:53] [info] User Name:           ubuntu
[2025-10-12 16:06:53] [info] Cores Used:          8
[2025-10-12 16:06:53] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info]                Top Cell Information
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] Layout Top Cell:     TOP
[2025-10-12 16:06:53] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:06:53] [info] Database Format:       GDSII
[2025-10-12 16:06:53] [info] Verification Mode:    Full Check

[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info]                Type of Errors | Count
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] DB_WARN -> 1
[2025-10-12 16:06:53] [info] LRF_ERR001 -> 14
[2025-10-12 16:06:53] [info] DB_WARN002 -> 159
[2025-10-12 16:06:53] [info] DB_WARN003 -> 161
[2025-10-12 16:06:53] [info] DRC_ERR9 -> 89
[2025-10-12 16:06:53] [info] DRC_ERR -> 67
[2025-10-12 16:06:53] [info] DRC_ERR5 -> 408
[2025-10-12 16:06:53] [info] DRC_ERR4 -> 241
[2025-10-12 16:06:53] [info] DRC_ERR1 -> 301
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info]                Rule Violation Summary
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:06:53] [info] |-----|-----|-----|
[2025-10-12 16:06:53] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:06:53] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:06:53] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:06:53] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:06:53] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:06:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:06:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:06:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:06:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:06:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:06:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:06:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:06:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:06:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:06:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:06:53] [info]
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] Performance Metrics
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] | Stage | Time Taken |
[2025-10-12 16:06:53] [info] |-----|-----|
[2025-10-12 16:06:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:06:53] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:06:53] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] Summary
[2025-10-12 16:06:53] [info] -----
[2025-10-12 16:06:53] [info] Total Errors: 1120
[2025-10-12 16:06:53] [info] Total Warnings: 321
[2025-10-12 16:06:53] [info] DRC Status: FAILED
```

lvr_summary_341.rpt

```
[2025-10-12 16:07:52] [info] =====
[2025-10-12 16:07:52] [info] Design Rule Check Report
[2025-10-12 16:07:52] [info] =====

[2025-10-12 16:07:52] [info] Report Generated: 2025-10-12
[2025-10-12 16:07:52] [info] Run Start Time: 04:07:00 PM
[2025-10-12 16:07:52] [info] Run End Time: 04:07:52 PM
[2025-10-12 16:07:52] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:07:52] [info] Tool Version: LVR_2025.1
[2025-10-12 16:07:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:07:52] [info] User Name: ubuntu
[2025-10-12 16:07:52] [info] Cores Used: 8
[2025-10-12 16:07:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Top Cell Information
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Layout Top Cell: TOP
[2025-10-12 16:07:52] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:07:52] [info] Database Format: GDSII
[2025-10-12 16:07:52] [info] Verification Mode: Full Check

[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Type of Errors | Count
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] DB_WARN -> 1
[2025-10-12 16:07:52] [info] LRF_ERR001 -> 14
[2025-10-12 16:07:52] [info] DB_WARN002 -> 159
[2025-10-12 16:07:52] [info] DB_WARN003 -> 161
```

```

[2025-10-12 16:07:52] [info] DRC_ERR9 -> 89
[2025-10-12 16:07:52] [info] DRC_ERR -> 67
[2025-10-12 16:07:52] [info] DRC_ERR5 -> 408
[2025-10-12 16:07:52] [info] DRC_ERR4 -> 241
[2025-10-12 16:07:52] [info] DRC_ERR1 -> 301
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Rule Violation Summary
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:07:52] [info] |-----|-----|-----|
[2025-10-12 16:07:52] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:07:52] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:07:52] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:07:52] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:07:52] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:07:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:07:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:07:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:07:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:07:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:07:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:07:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:07:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:07:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:07:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Performance Metrics
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] | Stage | Time Taken |
[2025-10-12 16:07:52] [info] |-----|-----|
[2025-10-12 16:07:52] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:07:52] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:07:52] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Summary
[2025-10-12 16:07:52] [info] -----
[2025-10-12 16:07:52] [info] Total Errors: 1120
[2025-10-12 16:07:52] [info] Total Warnings: 321
[2025-10-12 16:07:52] [info] DRC Status: FAILED

```

lvr_summary_342.rpt

```

[2025-10-12 16:08:48] [info] =====
[2025-10-12 16:08:48] [info] Design Rule Check Report
[2025-10-12 16:08:48] [info] =====

[2025-10-12 16:08:48] [info] Report Generated: 2025-10-12
[2025-10-12 16:08:48] [info] Run Start Time: 04:07:57 PM
[2025-10-12 16:08:48] [info] Run End Time: 04:08:48 PM
[2025-10-12 16:08:48] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:08:48] [info] Tool Version: LVR_2025.1
[2025-10-12 16:08:48] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:08:48] [info] User Name: ubuntu
[2025-10-12 16:08:48] [info] Cores Used: 8
[2025-10-12 16:08:48] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Top Cell Information
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Layout Top Cell:      TOP
[2025-10-12 16:08:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:08:48] [info] Database Format:      GDSII
[2025-10-12 16:08:48] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Type of Errors | Count
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] DB_WARN -> 1
[2025-10-12 16:08:48] [info] LRF_ERR001 -> 14
[2025-10-12 16:08:48] [info] DB_WARN002 -> 159
[2025-10-12 16:08:48] [info] DB_WARN003 -> 161
[2025-10-12 16:08:48] [info] DRC_ERR9 -> 89
[2025-10-12 16:08:48] [info] DRC_ERR -> 67
[2025-10-12 16:08:48] [info] DRC_ERR5 -> 408
[2025-10-12 16:08:48] [info] DRC_ERR4 -> 241
[2025-10-12 16:08:48] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Rule Violation Summary
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:08:48] [info] |-----|-----|-----|
[2025-10-12 16:08:48] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:08:48] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:08:48] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:08:48] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:08:48] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:08:48] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:08:48] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:08:48] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:08:48] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:08:48] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:08:48] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:08:48] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:08:48] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:08:48] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:08:48] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:08:48] [info] |
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Performance Metrics
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] | Stage          | Time Taken |
[2025-10-12 16:08:48] [info] |-----|-----|
[2025-10-12 16:08:48] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:08:48] [info] | Rule Checking   | 0 minutes 40 seconds |
[2025-10-12 16:08:48] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Summary
```

```
[2025-10-12 16:08:48] [info] -----
[2025-10-12 16:08:48] [info] Total Errors:      1120
[2025-10-12 16:08:48] [info] Total Warnings:    321
[2025-10-12 16:08:48] [info] DRC Status:        FAILED
```

lvr_summary_343.rpt

```
[2025-10-12 16:09:47] [info] =====
[2025-10-12 16:09:47] [info]                      Design Rule Check Report
[2025-10-12 16:09:47] [info] =====

[2025-10-12 16:09:47] [info] Report Generated:      2025-10-12
[2025-10-12 16:09:47] [info] Run Start Time:       04:08:55 PM
[2025-10-12 16:09:47] [info] Run End Time:         04:09:47 PM
[2025-10-12 16:09:47] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 16:09:47] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:09:47] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:09:47] [info] User Name:            ubuntu
[2025-10-12 16:09:47] [info] Cores Used:           8
[2025-10-12 16:09:47] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info]                      Top Cell Information
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info] Layout Top Cell:      TOP
[2025-10-12 16:09:47] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:09:47] [info] Database Format:       GDSII
[2025-10-12 16:09:47] [info] Verification Mode:     Full Check

[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info]                      Type of Errors | Count
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info] DB_WARN -> 1
[2025-10-12 16:09:47] [info] LRF_ERR001 -> 14
[2025-10-12 16:09:47] [info] DB_WARN002 -> 159
[2025-10-12 16:09:47] [info] DB_WARN003 -> 161
[2025-10-12 16:09:47] [info] DRC_ERR9 -> 89
[2025-10-12 16:09:47] [info] DRC_ERR -> 67
[2025-10-12 16:09:47] [info] DRC_ERR5 -> 408
[2025-10-12 16:09:47] [info] DRC_ERR4 -> 241
[2025-10-12 16:09:47] [info] DRC_ERR1 -> 301
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info]                      Rule Violation Summary
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:09:47] [info] |-----|-----|-----|
[2025-10-12 16:09:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:09:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:09:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:09:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:09:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:09:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:09:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:09:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:09:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:09:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:09:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:09:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:09:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:09:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:09:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:09:47] [info]
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info]                      Performance Metrics
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:09:47] [info] |-----|-----|
[2025-10-12 16:09:47] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:09:47] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 16:09:47] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info] Summary
[2025-10-12 16:09:47] [info] -----
[2025-10-12 16:09:47] [info] Total Errors: 1120
[2025-10-12 16:09:47] [info] Total Warnings: 321
[2025-10-12 16:09:47] [info] DRC Status: FAILED
```

lvr_summary_344.rpt

```
[2025-10-12 16:10:43] [info] =====
[2025-10-12 16:10:43] [info] Design Rule Check Report
[2025-10-12 16:10:43] [info] =====

[2025-10-12 16:10:43] [info] Report Generated: 2025-10-12
[2025-10-12 16:10:43] [info] Run Start Time: 04:09:53 PM
[2025-10-12 16:10:43] [info] Run End Time: 04:10:43 PM
[2025-10-12 16:10:43] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:10:43] [info] Tool Version: LVR_2025.1
[2025-10-12 16:10:43] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:10:43] [info] User Name: ubuntu
[2025-10-12 16:10:43] [info] Cores Used: 8
[2025-10-12 16:10:43] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Top Cell Information
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Layout Top Cell: TOP
[2025-10-12 16:10:43] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:10:43] [info] Database Format: GDSII
[2025-10-12 16:10:43] [info] Verification Mode: Full Check

[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Type of Errors | Count
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] DB_WARN -> 1
[2025-10-12 16:10:43] [info] LRF_ERR001 -> 14
[2025-10-12 16:10:43] [info] DB_WARN002 -> 159
[2025-10-12 16:10:43] [info] DB_WARN003 -> 161
[2025-10-12 16:10:43] [info] DRC_ERR9 -> 89
[2025-10-12 16:10:43] [info] DRC_ERR -> 67
[2025-10-12 16:10:43] [info] DRC_ERR5 -> 408
[2025-10-12 16:10:43] [info] DRC_ERR4 -> 241
[2025-10-12 16:10:43] [info] DRC_ERR1 -> 301
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Rule Violation Summary
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:10:43] [info] |-----|-----|-----|
[2025-10-12 16:10:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:10:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:10:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:10:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:10:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```
[2025-10-12 16:10:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 16:10:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:10:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:10:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:10:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:10:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:10:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:10:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:10:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:10:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:10:43] [info]
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Performance Metrics
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] | Stage | Time Taken |
[2025-10-12 16:10:43] [info] |-----|-----|
[2025-10-12 16:10:43] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:10:43] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:10:43] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Summary
[2025-10-12 16:10:43] [info] -----
[2025-10-12 16:10:43] [info] Total Errors: 1120
[2025-10-12 16:10:43] [info] Total Warnings: 321
[2025-10-12 16:10:43] [info] DRC Status: FAILED
```

lvr_summary_345.rpt

```
[2025-10-12 16:11:44] [info] =====
[2025-10-12 16:11:44] [info] Design Rule Check Report
[2025-10-12 16:11:44] [info] =====

[2025-10-12 16:11:44] [info] Report Generated: 2025-10-12
[2025-10-12 16:11:44] [info] Run Start Time: 04:10:54 PM
[2025-10-12 16:11:44] [info] Run End Time: 04:11:44 PM
[2025-10-12 16:11:44] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:11:44] [info] Tool Version: LVR_2025.1
[2025-10-12 16:11:44] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:11:44] [info] User Name: ubuntu
[2025-10-12 16:11:44] [info] Cores Used: 8
[2025-10-12 16:11:44] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Top Cell Information
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Layout Top Cell: TOP
[2025-10-12 16:11:44] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:11:44] [info] Database Format: GDSII
[2025-10-12 16:11:44] [info] Verification Mode: Full Check

[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Type of Errors | Count
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] DB_WARN -> 1
[2025-10-12 16:11:44] [info] LRF_ERR001 -> 14
[2025-10-12 16:11:44] [info] DB_WARN002 -> 159
[2025-10-12 16:11:44] [info] DB_WARN003 -> 161
```

```

[2025-10-12 16:11:44] [info] DRC_ERR9 -> 89
[2025-10-12 16:11:44] [info] DRC_ERR -> 67
[2025-10-12 16:11:44] [info] DRC_ERR5 -> 408
[2025-10-12 16:11:44] [info] DRC_ERR4 -> 241
[2025-10-12 16:11:44] [info] DRC_ERR1 -> 301
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Rule Violation Summary
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:11:44] [info] |-----|-----|-----|
[2025-10-12 16:11:44] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:11:44] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:11:44] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:11:44] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:11:44] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:11:44] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:11:44] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:11:44] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:11:44] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:11:44] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:11:44] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:11:44] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:11:44] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:11:44] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:11:44] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Performance Metrics
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] | Stage | Time Taken |
[2025-10-12 16:11:44] [info] |-----|-----|
[2025-10-12 16:11:44] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:11:44] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:11:44] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Summary
[2025-10-12 16:11:44] [info] -----
[2025-10-12 16:11:44] [info] Total Errors: 1120
[2025-10-12 16:11:44] [info] Total Warnings: 321
[2025-10-12 16:11:44] [info] DRC Status: FAILED

```

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```

[2025-10-12 16:12:43] [info] =====
[2025-10-12 16:12:43] [info] Design Rule Check Report
[2025-10-12 16:12:43] [info] =====

[2025-10-12 16:12:43] [info] Report Generated: 2025-10-12
[2025-10-12 16:12:43] [info] Run Start Time: 04:11:52 PM
[2025-10-12 16:12:43] [info] Run End Time: 04:12:43 PM
[2025-10-12 16:12:43] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:12:43] [info] Tool Version: LVR_2025.1
[2025-10-12 16:12:43] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:12:43] [info] User Name: ubuntu
[2025-10-12 16:12:43] [info] Cores Used: 8
[2025-10-12 16:12:43] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] Top Cell Information
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] Layout Top Cell:      TOP
[2025-10-12 16:12:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:12:43] [info] Database Format:      GDSII
[2025-10-12 16:12:43] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] Type of Errors | Count
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] DB_WARN -> 1
[2025-10-12 16:12:43] [info] LRF_ERR001 -> 14
[2025-10-12 16:12:43] [info] DB_WARN002 -> 159
[2025-10-12 16:12:43] [info] DB_WARN003 -> 161
[2025-10-12 16:12:43] [info] DRC_ERR9 -> 89
[2025-10-12 16:12:43] [info] DRC_ERR -> 67
[2025-10-12 16:12:43] [info] DRC_ERR5 -> 408
[2025-10-12 16:12:43] [info] DRC_ERR4 -> 241
[2025-10-12 16:12:43] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] Rule Violation Summary
```

```
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:12:43] [info] |-----|-----|-----|
[2025-10-12 16:12:43] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:12:43] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:12:43] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:12:43] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:12:43] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:12:43] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:12:43] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:12:43] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:12:43] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:12:43] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:12:43] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:12:43] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:12:43] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:12:43] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:12:43] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:12:43] [info] -----
```

```
[2025-10-12 16:12:43] [info] Performance Metrics
```

```
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] | Stage          | Time Taken |
[2025-10-12 16:12:43] [info] |-----|-----|
[2025-10-12 16:12:43] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:12:43] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 16:12:43] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:12:43] [info] -----
```

```
[2025-10-12 16:12:43] [info] Summary
```

```
[2025-10-12 16:12:43] [info] -----
[2025-10-12 16:12:43] [info] Total Errors:      1120
[2025-10-12 16:12:43] [info] Total Warnings:    321
[2025-10-12 16:12:43] [info] DRC Status:        FAILED
```

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```
[2025-10-12 16:13:41] [info] =====
[2025-10-12 16:13:41] [info]                      Design Rule Check Report
[2025-10-12 16:13:41] [info] =====

[2025-10-12 16:13:41] [info] Report Generated:      2025-10-12
[2025-10-12 16:13:41] [info] Run Start Time:       04:12:50 PM
[2025-10-12 16:13:41] [info] Run End Time:         04:13:41 PM
[2025-10-12 16:13:41] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 16:13:41] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:13:41] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:13:41] [info] User Name:            ubuntu
[2025-10-12 16:13:41] [info] Cores Used:           8
[2025-10-12 16:13:41] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info]                      Top Cell Information
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info] Layout Top Cell:      TOP
[2025-10-12 16:13:41] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:13:41] [info] Database Format:        GDSII
[2025-10-12 16:13:41] [info] Verification Mode:     Full Check

[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info]                      Type of Errors | Count
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info] DB_WARN -> 1
[2025-10-12 16:13:41] [info] LRF_ERR001 -> 14
[2025-10-12 16:13:41] [info] DB_WARN002 -> 159
[2025-10-12 16:13:41] [info] DB_WARN003 -> 161
[2025-10-12 16:13:41] [info] DRC_ERR9 -> 89
[2025-10-12 16:13:41] [info] DRC_ERR -> 67
[2025-10-12 16:13:41] [info] DRC_ERR5 -> 408
[2025-10-12 16:13:41] [info] DRC_ERR4 -> 241
[2025-10-12 16:13:41] [info] DRC_ERR1 -> 301
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info]                      Rule Violation Summary
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:13:41] [info] |-----|-----|-----|
[2025-10-12 16:13:41] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:13:41] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:13:41] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:13:41] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:13:41] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:13:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:13:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:13:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:13:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:13:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:13:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:13:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:13:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:13:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:13:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:13:41] [info]
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info]                      Performance Metrics
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:13:41] [info] |-----|-----|
[2025-10-12 16:13:41] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:13:41] [info] | Rule Checking        | 0 minutes 40 seconds |
[2025-10-12 16:13:41] [info] | Report Generation     | 0 minutes 0 seconds  |

[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info]                Summary
[2025-10-12 16:13:41] [info] -----
[2025-10-12 16:13:41] [info] Total Errors:    1120
[2025-10-12 16:13:41] [info] Total Warnings:    321
[2025-10-12 16:13:41] [info] DRC Status:      FAILED
```

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```
[2025-10-12 16:14:40] [info] =====
[2025-10-12 16:14:40] [info]                Design Rule Check Report
[2025-10-12 16:14:40] [info] =====

[2025-10-12 16:14:40] [info] Report Generated:    2025-10-12
[2025-10-12 16:14:40] [info] Run Start Time:      04:13:49 PM
[2025-10-12 16:14:40] [info] Run End Time:        04:14:40 PM
[2025-10-12 16:14:40] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:14:40] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:14:40] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:14:40] [info] User Name:           ubuntu
[2025-10-12 16:14:40] [info] Cores Used:          8
[2025-10-12 16:14:40] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info]                Top Cell Information
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] Layout Top Cell:     TOP
[2025-10-12 16:14:40] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:14:40] [info] Database Format:       GDSII
[2025-10-12 16:14:40] [info] Verification Mode:    Full Check

[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info]                Type of Errors | Count
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] DB_WARN -> 1
[2025-10-12 16:14:40] [info] LRF_ERR001 -> 14
[2025-10-12 16:14:40] [info] DB_WARN002 -> 159
[2025-10-12 16:14:40] [info] DB_WARN003 -> 161
[2025-10-12 16:14:40] [info] DRC_ERR9 -> 89
[2025-10-12 16:14:40] [info] DRC_ERR -> 67
[2025-10-12 16:14:40] [info] DRC_ERR5 -> 408
[2025-10-12 16:14:40] [info] DRC_ERR4 -> 241
[2025-10-12 16:14:40] [info] DRC_ERR1 -> 301
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info]                Rule Violation Summary
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:14:40] [info] |-----|-----|-----|
[2025-10-12 16:14:40] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:14:40] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:14:40] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:14:40] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:14:40] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:14:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:14:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:14:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:14:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:14:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:14:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:14:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:14:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:14:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:14:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:14:40] [info]
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] Performance Metrics
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] | Stage | Time Taken |
[2025-10-12 16:14:40] [info] |-----|-----|
[2025-10-12 16:14:40] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:14:40] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:14:40] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] Summary
[2025-10-12 16:14:40] [info] -----
[2025-10-12 16:14:40] [info] Total Errors: 1120
[2025-10-12 16:14:40] [info] Total Warnings: 321
[2025-10-12 16:14:40] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:15:37] [info] =====
[2025-10-12 16:15:37] [info] Design Rule Check Report
[2025-10-12 16:15:37] [info] =====

[2025-10-12 16:15:37] [info] Report Generated: 2025-10-12
[2025-10-12 16:15:37] [info] Run Start Time: 04:14:47 PM
[2025-10-12 16:15:37] [info] Run End Time: 04:15:37 PM
[2025-10-12 16:15:37] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:15:37] [info] Tool Version: LVR_2025.1
[2025-10-12 16:15:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:15:37] [info] User Name: ubuntu
[2025-10-12 16:15:37] [info] Cores Used: 8
[2025-10-12 16:15:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Top Cell Information
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Layout Top Cell: TOP
[2025-10-12 16:15:37] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:15:37] [info] Database Format: GDSII
[2025-10-12 16:15:37] [info] Verification Mode: Full Check

[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Type of Errors | Count
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] DB_WARN -> 1
[2025-10-12 16:15:37] [info] LRF_ERR001 -> 14
[2025-10-12 16:15:37] [info] DB_WARN002 -> 159
[2025-10-12 16:15:37] [info] DB_WARN003 -> 161
```

```

[2025-10-12 16:15:37] [info] DRC_ERR9 -> 89
[2025-10-12 16:15:37] [info] DRC_ERR -> 67
[2025-10-12 16:15:37] [info] DRC_ERR5 -> 408
[2025-10-12 16:15:37] [info] DRC_ERR4 -> 241
[2025-10-12 16:15:37] [info] DRC_ERR1 -> 301
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Rule Violation Summary
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:15:37] [info] |-----|-----|-----|
[2025-10-12 16:15:37] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:15:37] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:15:37] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:15:37] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:15:37] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:15:37] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:15:37] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:15:37] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:15:37] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:15:37] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:15:37] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:15:37] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:15:37] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:15:37] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:15:37] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Performance Metrics
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] | Stage | Time Taken |
[2025-10-12 16:15:37] [info] |-----|-----|
[2025-10-12 16:15:37] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:15:37] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:15:37] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Summary
[2025-10-12 16:15:37] [info] -----
[2025-10-12 16:15:37] [info] Total Errors: 1120
[2025-10-12 16:15:37] [info] Total Warnings: 321
[2025-10-12 16:15:37] [info] DRC Status: FAILED

```

lvr_summary_35.rpt

```

[2025-10-12 11:14:34] [info] =====
[2025-10-12 11:14:34] [info] Design Rule Check Report
[2025-10-12 11:14:34] [info] =====

[2025-10-12 11:14:34] [info] Report Generated: 2025-10-12
[2025-10-12 11:14:34] [info] Run Start Time: 11:13:42 AM
[2025-10-12 11:14:34] [info] Run End Time: 11:14:34 AM
[2025-10-12 11:14:34] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:14:34] [info] Tool Version: LVR_2025.1
[2025-10-12 11:14:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:14:34] [info] User Name: ubuntu
[2025-10-12 11:14:34] [info] Cores Used: 8
[2025-10-12 11:14:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Top Cell Information
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Layout Top Cell:      TOP
[2025-10-12 11:14:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:14:34] [info] Database Format:       GDSII
[2025-10-12 11:14:34] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Type of Errors | Count
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] DB_WARN -> 1
[2025-10-12 11:14:34] [info] LRF_ERROR01 -> 14
[2025-10-12 11:14:34] [info] DB_WARN002 -> 159
[2025-10-12 11:14:34] [info] DB_WARN003 -> 161
[2025-10-12 11:14:34] [info] DRC_ERR9 -> 89
[2025-10-12 11:14:34] [info] DRC_ERR -> 67
[2025-10-12 11:14:34] [info] DRC_ERR5 -> 408
[2025-10-12 11:14:34] [info] DRC_ERR4 -> 241
[2025-10-12 11:14:34] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Rule Violation Summary
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:14:34] [info] |-----|-----|-----|
[2025-10-12 11:14:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:14:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:14:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:14:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:14:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:14:34] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:14:34] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:14:34] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:14:34] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:14:34] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:14:34] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:14:34] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:14:34] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:14:34] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:14:34] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:14:34] [info] |
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Performance Metrics
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] | Stage          | Time Taken |
[2025-10-12 11:14:34] [info] |-----|-----|
[2025-10-12 11:14:34] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:14:34] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 11:14:34] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Summary
```

```
[2025-10-12 11:14:34] [info] -----
[2025-10-12 11:14:34] [info] Total Errors:      1120
[2025-10-12 11:14:34] [info] Total Warnings:    321
[2025-10-12 11:14:34] [info] DRC Status:        FAILED
```


lvr_summary_350.rpt

```
[2025-10-12 16:16:35] [info] =====
[2025-10-12 16:16:35] [info]                      Design Rule Check Report
[2025-10-12 16:16:35] [info] =====

[2025-10-12 16:16:35] [info] Report Generated:    2025-10-12
[2025-10-12 16:16:35] [info] Run Start Time:      04:15:45 PM
[2025-10-12 16:16:35] [info] Run End Time:        04:16:35 PM
[2025-10-12 16:16:35] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 16:16:35] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:16:35] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:16:35] [info] User Name:           ubuntu
[2025-10-12 16:16:35] [info] Cores Used:           8
[2025-10-12 16:16:35] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info]                      Top Cell Information
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info] Layout Top Cell:     TOP
[2025-10-12 16:16:35] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:16:35] [info] Database Format:      GDSII
[2025-10-12 16:16:35] [info] Verification Mode:    Full Check

[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info]                      Type of Errors | Count
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info] DB_WARN -> 1
[2025-10-12 16:16:35] [info] LRF_ERR001 -> 14
[2025-10-12 16:16:35] [info] DB_WARN002 -> 159
[2025-10-12 16:16:35] [info] DB_WARN003 -> 161
[2025-10-12 16:16:35] [info] DRC_ERR9 -> 89
[2025-10-12 16:16:35] [info] DRC_ERR -> 67
[2025-10-12 16:16:35] [info] DRC_ERR5 -> 408
[2025-10-12 16:16:35] [info] DRC_ERR4 -> 241
[2025-10-12 16:16:35] [info] DRC_ERR1 -> 301
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info]                      Rule Violation Summary
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:16:35] [info] |-----|-----|-----|
[2025-10-12 16:16:35] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:16:35] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:16:35] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:16:35] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:16:35] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:16:35] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:16:35] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:16:35] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:16:35] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:16:35] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:16:35] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:16:35] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:16:35] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:16:35] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:16:35] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:16:35] [info]
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info]                      Performance Metrics
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:16:35] [info] |-----|-----|
[2025-10-12 16:16:35] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:16:35] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 16:16:35] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info] Summary
[2025-10-12 16:16:35] [info] -----
[2025-10-12 16:16:35] [info] Total Errors: 1120
[2025-10-12 16:16:35] [info] Total Warnings: 321
[2025-10-12 16:16:35] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:17:34] [info] =====
[2025-10-12 16:17:34] [info] Design Rule Check Report
[2025-10-12 16:17:34] [info] =====

[2025-10-12 16:17:34] [info] Report Generated: 2025-10-12
[2025-10-12 16:17:34] [info] Run Start Time: 04:16:44 PM
[2025-10-12 16:17:34] [info] Run End Time: 04:17:34 PM
[2025-10-12 16:17:34] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:17:34] [info] Tool Version: LVR_2025.1
[2025-10-12 16:17:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:17:34] [info] User Name: ubuntu
[2025-10-12 16:17:34] [info] Cores Used: 8
[2025-10-12 16:17:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Top Cell Information
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Layout Top Cell: TOP
[2025-10-12 16:17:34] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:17:34] [info] Database Format: GDSII
[2025-10-12 16:17:34] [info] Verification Mode: Full Check

[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Type of Errors | Count
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] DB_WARN -> 1
[2025-10-12 16:17:34] [info] LRF_ERR001 -> 14
[2025-10-12 16:17:34] [info] DB_WARN002 -> 159
[2025-10-12 16:17:34] [info] DB_WARN003 -> 161
[2025-10-12 16:17:34] [info] DRC_ERR9 -> 89
[2025-10-12 16:17:34] [info] DRC_ERR -> 67
[2025-10-12 16:17:34] [info] DRC_ERR5 -> 408
[2025-10-12 16:17:34] [info] DRC_ERR4 -> 241
[2025-10-12 16:17:34] [info] DRC_ERR1 -> 301
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Rule Violation Summary
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:17:34] [info] |-----|-----|-----|
[2025-10-12 16:17:34] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:17:34] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:17:34] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:17:34] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:17:34] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 16:17:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:17:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:17:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:17:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:17:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:17:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:17:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:17:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:17:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:17:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:17:34] [info]
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Performance Metrics
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] | Stage | Time Taken |
[2025-10-12 16:17:34] [info] |-----|-----|
[2025-10-12 16:17:34] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:17:34] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:17:34] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Summary
[2025-10-12 16:17:34] [info] -----
[2025-10-12 16:17:34] [info] Total Errors: 1120
[2025-10-12 16:17:34] [info] Total Warnings: 321
[2025-10-12 16:17:34] [info] DRC Status: FAILED
```

lvr_summary_352.rpt

```
[2025-10-12 16:18:33] [info] =====
[2025-10-12 16:18:33] [info] Design Rule Check Report
[2025-10-12 16:18:33] [info] =====

[2025-10-12 16:18:33] [info] Report Generated: 2025-10-12
[2025-10-12 16:18:33] [info] Run Start Time: 04:17:43 PM
[2025-10-12 16:18:33] [info] Run End Time: 04:18:33 PM
[2025-10-12 16:18:33] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:18:33] [info] Tool Version: LVR_2025.1
[2025-10-12 16:18:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:18:33] [info] User Name: ubuntu
[2025-10-12 16:18:33] [info] Cores Used: 8
[2025-10-12 16:18:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Top Cell Information
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Layout Top Cell: TOP
[2025-10-12 16:18:33] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:18:33] [info] Database Format: GDSII
[2025-10-12 16:18:33] [info] Verification Mode: Full Check

[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Type of Errors | Count
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] DB_WARN -> 1
[2025-10-12 16:18:33] [info] LRF_ERR001 -> 14
[2025-10-12 16:18:33] [info] DB_WARN002 -> 159
[2025-10-12 16:18:33] [info] DB_WARN003 -> 161
```

```
[2025-10-12 16:18:33] [info] DRC_ERR9 -> 89
[2025-10-12 16:18:33] [info] DRC_ERR -> 67
[2025-10-12 16:18:33] [info] DRC_ERR5 -> 408
[2025-10-12 16:18:33] [info] DRC_ERR4 -> 241
[2025-10-12 16:18:33] [info] DRC_ERR1 -> 301
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Rule Violation Summary
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:18:33] [info] |-----|-----|-----|
[2025-10-12 16:18:33] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:18:33] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:18:33] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:18:33] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:18:33] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:18:33] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 16:18:33] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:18:33] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:18:33] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:18:33] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:18:33] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:18:33] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:18:33] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:18:33] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:18:33] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Performance Metrics
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] | Stage           | Time Taken |
[2025-10-12 16:18:33] [info] |-----|-----|
[2025-10-12 16:18:33] [info] | GDS Parsing     | 0 minutes 9 seconds |
[2025-10-12 16:18:33] [info] | Rule Checking   | 0 minutes 39 seconds |
[2025-10-12 16:18:33] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Summary
[2025-10-12 16:18:33] [info] -----
[2025-10-12 16:18:33] [info] Total Errors: 1120
[2025-10-12 16:18:33] [info] Total Warnings: 321
[2025-10-12 16:18:33] [info] DRC Status: FAILED
```

lvr_summary_353.rpt

```
[2025-10-12 16:19:34] [info] =====
[2025-10-12 16:19:34] [info] Design Rule Check Report
[2025-10-12 16:19:34] [info] =====

[2025-10-12 16:19:34] [info] Report Generated: 2025-10-12
[2025-10-12 16:19:34] [info] Run Start Time: 04:18:42 PM
[2025-10-12 16:19:34] [info] Run End Time: 04:19:34 PM
[2025-10-12 16:19:34] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:19:34] [info] Tool Version: LVR_2025.1
[2025-10-12 16:19:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:19:34] [info] User Name: ubuntu
[2025-10-12 16:19:34] [info] Cores Used: 8
[2025-10-12 16:19:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Top Cell Information
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Layout Top Cell:      TOP
[2025-10-12 16:19:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:19:34] [info] Database Format:       GDSII
[2025-10-12 16:19:34] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Type of Errors | Count
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] DB_WARN -> 1
[2025-10-12 16:19:34] [info] LRF_ERR001 -> 14
[2025-10-12 16:19:34] [info] DB_WARN002 -> 159
[2025-10-12 16:19:34] [info] DB_WARN003 -> 161
[2025-10-12 16:19:34] [info] DRC_ERR9 -> 89
[2025-10-12 16:19:34] [info] DRC_ERR -> 67
[2025-10-12 16:19:34] [info] DRC_ERR5 -> 408
[2025-10-12 16:19:34] [info] DRC_ERR4 -> 241
[2025-10-12 16:19:34] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Rule Violation Summary
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:19:34] [info] |-----|-----|-----|
[2025-10-12 16:19:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:19:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:19:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:19:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:19:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:19:34] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:19:34] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:19:34] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:19:34] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:19:34] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:19:34] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:19:34] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:19:34] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:19:34] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:19:34] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:19:34] [info] |
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Performance Metrics
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] | Stage          | Time Taken |
[2025-10-12 16:19:34] [info] |-----|-----|
[2025-10-12 16:19:34] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:19:34] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 16:19:34] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Summary
```

```
[2025-10-12 16:19:34] [info] -----
[2025-10-12 16:19:34] [info] Total Errors:      1120
[2025-10-12 16:19:34] [info] Total Warnings:    321
[2025-10-12 16:19:34] [info] DRC Status:        FAILED
```

lvr_summary_354.rpt

```
[2025-10-12 16:20:33] [info] =====
[2025-10-12 16:20:33] [info]                      Design Rule Check Report
[2025-10-12 16:20:33] [info] =====

[2025-10-12 16:20:33] [info] Report Generated:    2025-10-12
[2025-10-12 16:20:33] [info] Run Start Time:      04:19:42 PM
[2025-10-12 16:20:33] [info] Run End Time:        04:20:33 PM
[2025-10-12 16:20:33] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 16:20:33] [info] Tool Version:       LVR_2025.1
[2025-10-12 16:20:33] [info] Host Name:          ip-172-31-37-173
[2025-10-12 16:20:33] [info] User Name:          ubuntu
[2025-10-12 16:20:33] [info] Cores Used:         8
[2025-10-12 16:20:33] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info]                      Top Cell Information
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info] Layout Top Cell:    TOP
[2025-10-12 16:20:33] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 16:20:33] [info] Database Format:     GDSII
[2025-10-12 16:20:33] [info] Verification Mode:   Full Check

[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info]                      Type of Errors | Count
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info] DB_WARN -> 1
[2025-10-12 16:20:33] [info] LRF_ERR001 -> 14
[2025-10-12 16:20:33] [info] DB_WARN002 -> 159
[2025-10-12 16:20:33] [info] DB_WARN003 -> 161
[2025-10-12 16:20:33] [info] DRC_ERR9 -> 89
[2025-10-12 16:20:33] [info] DRC_ERR -> 67
[2025-10-12 16:20:33] [info] DRC_ERR5 -> 408
[2025-10-12 16:20:33] [info] DRC_ERR4 -> 241
[2025-10-12 16:20:33] [info] DRC_ERR1 -> 301
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info]                      Rule Violation Summary
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:20:33] [info] |-----|-----|-----|
[2025-10-12 16:20:33] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:20:33] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:20:33] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:20:33] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:20:33] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:20:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:20:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:20:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:20:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:20:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:20:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:20:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:20:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:20:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:20:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:20:33] [info]
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info]                      Performance Metrics
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:20:33] [info] |-----|-----|
[2025-10-12 16:20:33] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:20:33] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 16:20:33] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info]                Summary
[2025-10-12 16:20:33] [info] -----
[2025-10-12 16:20:33] [info] Total Errors:    1120
[2025-10-12 16:20:33] [info] Total Warnings:    321
[2025-10-12 16:20:33] [info] DRC Status:      FAILED
```

lvr_summary_355.rpt

```
[2025-10-12 16:21:34] [info] =====
[2025-10-12 16:21:34] [info]                Design Rule Check Report
[2025-10-12 16:21:34] [info] =====

[2025-10-12 16:21:34] [info] Report Generated:    2025-10-12
[2025-10-12 16:21:34] [info] Run Start Time:      04:20:43 PM
[2025-10-12 16:21:34] [info] Run End Time:        04:21:34 PM
[2025-10-12 16:21:34] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:21:34] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:21:34] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:21:34] [info] User Name:           ubuntu
[2025-10-12 16:21:34] [info] Cores Used:          8
[2025-10-12 16:21:34] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info]                Top Cell Information
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] Layout Top Cell:     TOP
[2025-10-12 16:21:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:21:34] [info] Database Format:       GDSII
[2025-10-12 16:21:34] [info] Verification Mode:    Full Check

[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info]                Type of Errors | Count
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] DB_WARN -> 1
[2025-10-12 16:21:34] [info] LRF_ERR001 -> 14
[2025-10-12 16:21:34] [info] DB_WARN002 -> 159
[2025-10-12 16:21:34] [info] DB_WARN003 -> 161
[2025-10-12 16:21:34] [info] DRC_ERR9 -> 89
[2025-10-12 16:21:34] [info] DRC_ERR -> 67
[2025-10-12 16:21:34] [info] DRC_ERR5 -> 408
[2025-10-12 16:21:34] [info] DRC_ERR4 -> 241
[2025-10-12 16:21:34] [info] DRC_ERR1 -> 301
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info]                Rule Violation Summary
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:21:34] [info] |-----|-----|-----|
[2025-10-12 16:21:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:21:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:21:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:21:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:21:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:21:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:21:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:21:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:21:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:21:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:21:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:21:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:21:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:21:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:21:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:21:34] [info]
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] Performance Metrics
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] | Stage | Time Taken |
[2025-10-12 16:21:34] [info] |-----|-----|
[2025-10-12 16:21:34] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:21:34] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:21:34] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] Summary
[2025-10-12 16:21:34] [info] -----
[2025-10-12 16:21:34] [info] Total Errors: 1120
[2025-10-12 16:21:34] [info] Total Warnings: 321
[2025-10-12 16:21:34] [info] DRC Status: FAILED
```

lvr_summary_356.rpt

```
[2025-10-12 16:22:32] [info] =====
[2025-10-12 16:22:32] [info] Design Rule Check Report
[2025-10-12 16:22:32] [info] =====

[2025-10-12 16:22:32] [info] Report Generated: 2025-10-12
[2025-10-12 16:22:32] [info] Run Start Time: 04:21:41 PM
[2025-10-12 16:22:32] [info] Run End Time: 04:22:32 PM
[2025-10-12 16:22:32] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:22:32] [info] Tool Version: LVR_2025.1
[2025-10-12 16:22:32] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:22:32] [info] User Name: ubuntu
[2025-10-12 16:22:32] [info] Cores Used: 8
[2025-10-12 16:22:32] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Top Cell Information
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Layout Top Cell: TOP
[2025-10-12 16:22:32] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:22:32] [info] Database Format: GDSII
[2025-10-12 16:22:32] [info] Verification Mode: Full Check

[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Type of Errors | Count
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] DB_WARN -> 1
[2025-10-12 16:22:32] [info] LRF_ERR001 -> 14
[2025-10-12 16:22:32] [info] DB_WARN002 -> 159
[2025-10-12 16:22:32] [info] DB_WARN003 -> 161
```



```

[2025-10-12 16:22:32] [info] DRC_ERR9 -> 89
[2025-10-12 16:22:32] [info] DRC_ERR -> 67
[2025-10-12 16:22:32] [info] DRC_ERR5 -> 408
[2025-10-12 16:22:32] [info] DRC_ERR4 -> 241
[2025-10-12 16:22:32] [info] DRC_ERR1 -> 301
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Rule Violation Summary
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:22:32] [info] |-----|-----|-----|
[2025-10-12 16:22:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:22:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:22:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:22:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:22:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:22:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:22:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:22:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:22:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:22:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:22:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:22:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:22:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:22:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:22:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Performance Metrics
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] | Stage | Time Taken |
[2025-10-12 16:22:32] [info] |-----|-----|
[2025-10-12 16:22:32] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:22:32] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:22:32] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Summary
[2025-10-12 16:22:32] [info] -----
[2025-10-12 16:22:32] [info] Total Errors: 1120
[2025-10-12 16:22:32] [info] Total Warnings: 321
[2025-10-12 16:22:32] [info] DRC Status: FAILED

```

lvr_summary_357.rpt

```

[2025-10-12 16:23:31] [info] =====
[2025-10-12 16:23:31] [info] Design Rule Check Report
[2025-10-12 16:23:31] [info] =====

[2025-10-12 16:23:31] [info] Report Generated: 2025-10-12
[2025-10-12 16:23:31] [info] Run Start Time: 04:22:40 PM
[2025-10-12 16:23:31] [info] Run End Time: 04:23:31 PM
[2025-10-12 16:23:31] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:23:31] [info] Tool Version: LVR_2025.1
[2025-10-12 16:23:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:23:31] [info] User Name: ubuntu
[2025-10-12 16:23:31] [info] Cores Used: 8
[2025-10-12 16:23:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Top Cell Information
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Layout Top Cell:      TOP
[2025-10-12 16:23:31] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:23:31] [info] Database Format:       GDSII
[2025-10-12 16:23:31] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Type of Errors | Count
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] DB_WARN -> 1
[2025-10-12 16:23:31] [info] LRF_ERR001 -> 14
[2025-10-12 16:23:31] [info] DB_WARN002 -> 159
[2025-10-12 16:23:31] [info] DB_WARN003 -> 161
[2025-10-12 16:23:31] [info] DRC_ERR9 -> 89
[2025-10-12 16:23:31] [info] DRC_ERR -> 67
[2025-10-12 16:23:31] [info] DRC_ERR5 -> 408
[2025-10-12 16:23:31] [info] DRC_ERR4 -> 241
[2025-10-12 16:23:31] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Rule Violation Summary
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:23:31] [info] |-----|-----|-----|
[2025-10-12 16:23:31] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:23:31] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:23:31] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:23:31] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:23:31] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:23:31] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:23:31] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:23:31] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:23:31] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:23:31] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:23:31] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:23:31] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:23:31] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:23:31] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:23:31] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:23:31] [info] |
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Performance Metrics
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] | Stage          | Time Taken |
[2025-10-12 16:23:31] [info] |-----|-----|
[2025-10-12 16:23:31] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:23:31] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 16:23:31] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Summary
```

```
[2025-10-12 16:23:31] [info] -----
[2025-10-12 16:23:31] [info] Total Errors:      1120
[2025-10-12 16:23:31] [info] Total Warnings:    321
[2025-10-12 16:23:31] [info] DRC Status:        FAILED
```

lvr_summary_358.rpt

```
[2025-10-12 16:24:30] [info] =====
[2025-10-12 16:24:30] [info] Design Rule Check Report
[2025-10-12 16:24:30] [info] =====

[2025-10-12 16:24:30] [info] Report Generated: 2025-10-12
[2025-10-12 16:24:30] [info] Run Start Time: 04:23:38 PM
[2025-10-12 16:24:30] [info] Run End Time: 04:24:30 PM
[2025-10-12 16:24:30] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:24:30] [info] Tool Version: LVR_2025.1
[2025-10-12 16:24:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:24:30] [info] User Name: ubuntu
[2025-10-12 16:24:30] [info] Cores Used: 8
[2025-10-12 16:24:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Top Cell Information
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Layout Top Cell: TOP
[2025-10-12 16:24:30] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:24:30] [info] Database Format: GDSII
[2025-10-12 16:24:30] [info] Verification Mode: Full Check

[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Type of Errors | Count
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] DB_WARN -> 1
[2025-10-12 16:24:30] [info] LRF_ERR001 -> 14
[2025-10-12 16:24:30] [info] DB_WARN002 -> 159
[2025-10-12 16:24:30] [info] DB_WARN003 -> 161
[2025-10-12 16:24:30] [info] DRC_ERR9 -> 89
[2025-10-12 16:24:30] [info] DRC_ERR -> 67
[2025-10-12 16:24:30] [info] DRC_ERR5 -> 408
[2025-10-12 16:24:30] [info] DRC_ERR4 -> 241
[2025-10-12 16:24:30] [info] DRC_ERR1 -> 301
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Rule Violation Summary
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:24:30] [info] |-----|-----|-----|
[2025-10-12 16:24:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:24:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:24:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:24:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:24:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:24:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:24:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:24:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:24:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:24:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:24:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:24:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:24:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:24:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:24:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:24:30] [info]
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Performance Metrics
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:24:30] [info] |-----|-----|
[2025-10-12 16:24:30] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:24:30] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 16:24:30] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Summary
[2025-10-12 16:24:30] [info] -----
[2025-10-12 16:24:30] [info] Total Errors: 1120
[2025-10-12 16:24:30] [info] Total Warnings: 321
[2025-10-12 16:24:30] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:25:28] [info] =====
[2025-10-12 16:25:28] [info] Design Rule Check Report
[2025-10-12 16:25:28] [info] =====

[2025-10-12 16:25:28] [info] Report Generated: 2025-10-12
[2025-10-12 16:25:28] [info] Run Start Time: 04:24:38 PM
[2025-10-12 16:25:28] [info] Run End Time: 04:25:28 PM
[2025-10-12 16:25:28] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:25:28] [info] Tool Version: LVR_2025.1
[2025-10-12 16:25:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:25:28] [info] User Name: ubuntu
[2025-10-12 16:25:28] [info] Cores Used: 8
[2025-10-12 16:25:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Top Cell Information
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Layout Top Cell: TOP
[2025-10-12 16:25:28] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:25:28] [info] Database Format: GDSII
[2025-10-12 16:25:28] [info] Verification Mode: Full Check

[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Type of Errors | Count
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] DB_WARN -> 1
[2025-10-12 16:25:28] [info] LRF_ERR001 -> 14
[2025-10-12 16:25:28] [info] DB_WARN002 -> 159
[2025-10-12 16:25:28] [info] DB_WARN003 -> 161
[2025-10-12 16:25:28] [info] DRC_ERR9 -> 89
[2025-10-12 16:25:28] [info] DRC_ERR -> 67
[2025-10-12 16:25:28] [info] DRC_ERR5 -> 408
[2025-10-12 16:25:28] [info] DRC_ERR4 -> 241
[2025-10-12 16:25:28] [info] DRC_ERR1 -> 301
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Rule Violation Summary
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:25:28] [info] |-----|-----|-----|
[2025-10-12 16:25:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:25:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:25:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:25:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:25:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 16:25:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:25:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:25:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:25:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:25:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:25:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:25:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:25:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:25:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:25:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:25:28] [info]
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Performance Metrics
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] | Stage | Time Taken |
[2025-10-12 16:25:28] [info] |-----|-----|
[2025-10-12 16:25:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:25:28] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:25:28] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Summary
[2025-10-12 16:25:28] [info] -----
[2025-10-12 16:25:28] [info] Total Errors: 1120
[2025-10-12 16:25:28] [info] Total Warnings: 321
[2025-10-12 16:25:28] [info] DRC Status: FAILED

```

lvr_summary_36.rpt

```

[2025-10-12 11:15:31] [info] =====
[2025-10-12 11:15:31] [info] Design Rule Check Report
[2025-10-12 11:15:31] [info] =====

[2025-10-12 11:15:31] [info] Report Generated: 2025-10-12
[2025-10-12 11:15:31] [info] Run Start Time: 11:14:39 AM
[2025-10-12 11:15:31] [info] Run End Time: 11:15:31 AM
[2025-10-12 11:15:31] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:15:31] [info] Tool Version: LVR_2025.1
[2025-10-12 11:15:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:15:31] [info] User Name: ubuntu
[2025-10-12 11:15:31] [info] Cores Used: 8
[2025-10-12 11:15:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Top Cell Information
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Layout Top Cell: TOP
[2025-10-12 11:15:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:15:31] [info] Database Format: GDSII
[2025-10-12 11:15:31] [info] Verification Mode: Full Check

[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Type of Errors | Count
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] DB_WARN -> 1
[2025-10-12 11:15:31] [info] LRF_ERR001 -> 14
[2025-10-12 11:15:31] [info] DB_WARN002 -> 159
[2025-10-12 11:15:31] [info] DB_WARN003 -> 161

```

```

[2025-10-12 11:15:31] [info] DRC_ERR9 -> 89
[2025-10-12 11:15:31] [info] DRC_ERR -> 67
[2025-10-12 11:15:31] [info] DRC_ERR5 -> 408
[2025-10-12 11:15:31] [info] DRC_ERR4 -> 241
[2025-10-12 11:15:31] [info] DRC_ERR1 -> 301
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Rule Violation Summary
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:15:31] [info] |-----|-----|-----|
[2025-10-12 11:15:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:15:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:15:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:15:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:15:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:15:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:15:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:15:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:15:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:15:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:15:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:15:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:15:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:15:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:15:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Performance Metrics
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] | Stage | Time Taken |
[2025-10-12 11:15:31] [info] |-----|-----|
[2025-10-12 11:15:31] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:15:31] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:15:31] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Summary
[2025-10-12 11:15:31] [info] -----
[2025-10-12 11:15:31] [info] Total Errors: 1120
[2025-10-12 11:15:31] [info] Total Warnings: 321
[2025-10-12 11:15:31] [info] DRC Status: FAILED

```

lvr_summary_360.rpt

```

[2025-10-12 16:26:26] [info] =====
[2025-10-12 16:26:26] [info] Design Rule Check Report
[2025-10-12 16:26:26] [info] =====

[2025-10-12 16:26:26] [info] Report Generated: 2025-10-12
[2025-10-12 16:26:26] [info] Run Start Time: 04:25:36 PM
[2025-10-12 16:26:26] [info] Run End Time: 04:26:26 PM
[2025-10-12 16:26:26] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:26:26] [info] Tool Version: LVR_2025.1
[2025-10-12 16:26:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:26:26] [info] User Name: ubuntu
[2025-10-12 16:26:26] [info] Cores Used: 8
[2025-10-12 16:26:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Top Cell Information
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Layout Top Cell:      TOP
[2025-10-12 16:26:26] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:26:26] [info] Database Format:      GDSII
[2025-10-12 16:26:26] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Type of Errors | Count
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] DB_WARN -> 1
[2025-10-12 16:26:26] [info] LRF_ERR001 -> 14
[2025-10-12 16:26:26] [info] DB_WARN002 -> 159
[2025-10-12 16:26:26] [info] DB_WARN003 -> 161
[2025-10-12 16:26:26] [info] DRC_ERR9 -> 89
[2025-10-12 16:26:26] [info] DRC_ERR -> 67
[2025-10-12 16:26:26] [info] DRC_ERR5 -> 408
[2025-10-12 16:26:26] [info] DRC_ERR4 -> 241
[2025-10-12 16:26:26] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Rule Violation Summary
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:26:26] [info] |-----|-----|-----|
[2025-10-12 16:26:26] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:26:26] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:26:26] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:26:26] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:26:26] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:26:26] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:26:26] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:26:26] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:26:26] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:26:26] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:26:26] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:26:26] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:26:26] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:26:26] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:26:26] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:26:26] [info] |
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Performance Metrics
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] | Stage          | Time Taken |
[2025-10-12 16:26:26] [info] |-----|-----|
[2025-10-12 16:26:26] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:26:26] [info] | Rule Checking   | 0 minutes 39 seconds |
[2025-10-12 16:26:26] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Summary
```

```
[2025-10-12 16:26:26] [info] -----
[2025-10-12 16:26:26] [info] Total Errors:      1120
[2025-10-12 16:26:26] [info] Total Warnings:    321
[2025-10-12 16:26:26] [info] DRC Status:        FAILED
```

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```
[2025-10-12 16:27:26] [info] =====
[2025-10-12 16:27:26] [info]                      Design Rule Check Report
[2025-10-12 16:27:26] [info] =====

[2025-10-12 16:27:26] [info] Report Generated:    2025-10-12
[2025-10-12 16:27:26] [info] Run Start Time:      04:26:36 PM
[2025-10-12 16:27:26] [info] Run End Time:        04:27:26 PM
[2025-10-12 16:27:26] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 16:27:26] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:27:26] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:27:26] [info] User Name:           ubuntu
[2025-10-12 16:27:26] [info] Cores Used:          8
[2025-10-12 16:27:26] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info]                      Top Cell Information
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info] Layout Top Cell:     TOP
[2025-10-12 16:27:26] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:27:26] [info] Database Format:       GDSII
[2025-10-12 16:27:26] [info] Verification Mode:    Full Check

[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info]                      Type of Errors | Count
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info] DB_WARN -> 1
[2025-10-12 16:27:26] [info] LRF_ERR001 -> 14
[2025-10-12 16:27:26] [info] DB_WARN002 -> 159
[2025-10-12 16:27:26] [info] DB_WARN003 -> 161
[2025-10-12 16:27:26] [info] DRC_ERR9 -> 89
[2025-10-12 16:27:26] [info] DRC_ERR -> 67
[2025-10-12 16:27:26] [info] DRC_ERR5 -> 408
[2025-10-12 16:27:26] [info] DRC_ERR4 -> 241
[2025-10-12 16:27:26] [info] DRC_ERR1 -> 301
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info]                      Rule Violation Summary
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:27:26] [info] |-----|-----|-----|
[2025-10-12 16:27:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:27:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:27:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:27:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:27:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:27:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:27:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:27:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:27:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:27:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:27:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:27:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:27:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:27:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:27:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:27:26] [info]
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info]                      Performance Metrics
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info] | Stage | Time Taken |
```



```
[2025-10-12 16:27:26] [info] |-----|-----|
[2025-10-12 16:27:26] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:27:26] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 16:27:26] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info] Summary
[2025-10-12 16:27:26] [info] -----
[2025-10-12 16:27:26] [info] Total Errors: 1120
[2025-10-12 16:27:26] [info] Total Warnings: 321
[2025-10-12 16:27:26] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:28:23] [info] =====
[2025-10-12 16:28:23] [info] Design Rule Check Report
[2025-10-12 16:28:23] [info] =====

[2025-10-12 16:28:23] [info] Report Generated: 2025-10-12
[2025-10-12 16:28:23] [info] Run Start Time: 04:27:32 PM
[2025-10-12 16:28:23] [info] Run End Time: 04:28:23 PM
[2025-10-12 16:28:23] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:28:23] [info] Tool Version: LVR_2025.1
[2025-10-12 16:28:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:28:23] [info] User Name: ubuntu
[2025-10-12 16:28:23] [info] Cores Used: 8
[2025-10-12 16:28:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Top Cell Information
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Layout Top Cell: TOP
[2025-10-12 16:28:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:28:23] [info] Database Format: GDSII
[2025-10-12 16:28:23] [info] Verification Mode: Full Check

[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Type of Errors | Count
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] DB_WARN -> 1
[2025-10-12 16:28:23] [info] LRF_ERR001 -> 14
[2025-10-12 16:28:23] [info] DB_WARN002 -> 159
[2025-10-12 16:28:23] [info] DB_WARN003 -> 161
[2025-10-12 16:28:23] [info] DRC_ERR9 -> 89
[2025-10-12 16:28:23] [info] DRC_ERR -> 67
[2025-10-12 16:28:23] [info] DRC_ERR5 -> 408
[2025-10-12 16:28:23] [info] DRC_ERR4 -> 241
[2025-10-12 16:28:23] [info] DRC_ERR1 -> 301
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Rule Violation Summary
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:28:23] [info] |-----|-----|-----|
[2025-10-12 16:28:23] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:28:23] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:28:23] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:28:23] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:28:23] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 16:28:23] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:28:23] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:28:23] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:28:23] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:28:23] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:28:23] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:28:23] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:28:23] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:28:23] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:28:23] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:28:23] [info]
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Performance Metrics
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] | Stage | Time Taken |
[2025-10-12 16:28:23] [info] |-----|-----|
[2025-10-12 16:28:23] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:28:23] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:28:23] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Summary
[2025-10-12 16:28:23] [info] -----
[2025-10-12 16:28:23] [info] Total Errors: 1120
[2025-10-12 16:28:23] [info] Total Warnings: 321
[2025-10-12 16:28:23] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:29:22] [info] =====
[2025-10-12 16:29:22] [info] Design Rule Check Report
[2025-10-12 16:29:22] [info] =====

[2025-10-12 16:29:22] [info] Report Generated: 2025-10-12
[2025-10-12 16:29:22] [info] Run Start Time: 04:28:30 PM
[2025-10-12 16:29:22] [info] Run End Time: 04:29:22 PM
[2025-10-12 16:29:22] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:29:22] [info] Tool Version: LVR_2025.1
[2025-10-12 16:29:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:29:22] [info] User Name: ubuntu
[2025-10-12 16:29:22] [info] Cores Used: 8
[2025-10-12 16:29:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Top Cell Information
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Layout Top Cell: TOP
[2025-10-12 16:29:22] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:29:22] [info] Database Format: GDSII
[2025-10-12 16:29:22] [info] Verification Mode: Full Check

[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Type of Errors | Count
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] DB_WARN -> 1
[2025-10-12 16:29:22] [info] LRF_ERR001 -> 14
[2025-10-12 16:29:22] [info] DB_WARN002 -> 159
[2025-10-12 16:29:22] [info] DB_WARN003 -> 161
```

```
[2025-10-12 16:29:22] [info] DRC_ERR9 -> 89
[2025-10-12 16:29:22] [info] DRC_ERR -> 67
[2025-10-12 16:29:22] [info] DRC_ERR5 -> 408
[2025-10-12 16:29:22] [info] DRC_ERR4 -> 241
[2025-10-12 16:29:22] [info] DRC_ERR1 -> 301
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Rule Violation Summary
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:29:22] [info] |-----|-----|-----|
[2025-10-12 16:29:22] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:29:22] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:29:22] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:29:22] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:29:22] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:29:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:29:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:29:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:29:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:29:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:29:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:29:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:29:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:29:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:29:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Performance Metrics
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] | Stage | Time Taken |
[2025-10-12 16:29:22] [info] |-----|-----|
[2025-10-12 16:29:22] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:29:22] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:29:22] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Summary
[2025-10-12 16:29:22] [info] -----
[2025-10-12 16:29:22] [info] Total Errors: 1120
[2025-10-12 16:29:22] [info] Total Warnings: 321
[2025-10-12 16:29:22] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:30:17] [info] =====
[2025-10-12 16:30:17] [info] Design Rule Check Report
[2025-10-12 16:30:17] [info] =====

[2025-10-12 16:30:17] [info] Report Generated: 2025-10-12
[2025-10-12 16:30:17] [info] Run Start Time: 04:29:26 PM
[2025-10-12 16:30:17] [info] Run End Time: 04:30:17 PM
[2025-10-12 16:30:17] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:30:17] [info] Tool Version: LVR_2025.1
[2025-10-12 16:30:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:30:17] [info] User Name: ubuntu
[2025-10-12 16:30:17] [info] Cores Used: 8
[2025-10-12 16:30:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] Top Cell Information
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] Layout Top Cell:      TOP
[2025-10-12 16:30:17] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:30:17] [info] Database Format:      GDSII
[2025-10-12 16:30:17] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] Type of Errors | Count
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] DB_WARN -> 1
[2025-10-12 16:30:17] [info] LRF_ERR001 -> 14
[2025-10-12 16:30:17] [info] DB_WARN002 -> 159
[2025-10-12 16:30:17] [info] DB_WARN003 -> 161
[2025-10-12 16:30:17] [info] DRC_ERR9 -> 89
[2025-10-12 16:30:17] [info] DRC_ERR -> 67
[2025-10-12 16:30:17] [info] DRC_ERR5 -> 408
[2025-10-12 16:30:17] [info] DRC_ERR4 -> 241
[2025-10-12 16:30:17] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] Rule Violation Summary
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:30:17] [info] |-----|-----|-----|
[2025-10-12 16:30:17] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:30:17] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:30:17] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:30:17] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:30:17] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:30:17] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:30:17] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:30:17] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:30:17] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:30:17] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:30:17] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:30:17] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:30:17] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:30:17] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:30:17] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:30:17] [info] -----
```

```
[2025-10-12 16:30:17] [info] Performance Metrics
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] | Stage          | Time Taken |
[2025-10-12 16:30:17] [info] |-----|-----|
[2025-10-12 16:30:17] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:30:17] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 16:30:17] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] Summary
```

```
[2025-10-12 16:30:17] [info] -----
[2025-10-12 16:30:17] [info] Total Errors:      1120
[2025-10-12 16:30:17] [info] Total Warnings:    321
[2025-10-12 16:30:17] [info] DRC Status:        FAILED
```

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```
[2025-10-12 16:31:17] [info] =====
[2025-10-12 16:31:17] [info]                      Design Rule Check Report
[2025-10-12 16:31:17] [info] =====

[2025-10-12 16:31:17] [info] Report Generated:      2025-10-12
[2025-10-12 16:31:17] [info] Run Start Time:       04:30:24 PM
[2025-10-12 16:31:17] [info] Run End Time:         04:31:17 PM
[2025-10-12 16:31:17] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 16:31:17] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:31:17] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:31:17] [info] User Name:            ubuntu
[2025-10-12 16:31:17] [info] Cores Used:           8
[2025-10-12 16:31:17] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info]                      Top Cell Information
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info] Layout Top Cell:      TOP
[2025-10-12 16:31:17] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:31:17] [info] Database Format:        GDSII
[2025-10-12 16:31:17] [info] Verification Mode:     Full Check

[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info]                      Type of Errors | Count
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info] DB_WARN -> 1
[2025-10-12 16:31:17] [info] LRF_ERR001 -> 14
[2025-10-12 16:31:17] [info] DB_WARN002 -> 159
[2025-10-12 16:31:17] [info] DB_WARN003 -> 161
[2025-10-12 16:31:17] [info] DRC_ERR9 -> 89
[2025-10-12 16:31:17] [info] DRC_ERR -> 67
[2025-10-12 16:31:17] [info] DRC_ERR5 -> 408
[2025-10-12 16:31:17] [info] DRC_ERR4 -> 241
[2025-10-12 16:31:17] [info] DRC_ERR1 -> 301
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info]                      Rule Violation Summary
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:31:17] [info] |-----|-----|-----|
[2025-10-12 16:31:17] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:31:17] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:31:17] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:31:17] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:31:17] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:31:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:31:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:31:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:31:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:31:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:31:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:31:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:31:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:31:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:31:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:31:17] [info]
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info]                      Performance Metrics
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:31:17] [info] |-----|-----|
[2025-10-12 16:31:17] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:31:17] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 16:31:17] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info] Summary
[2025-10-12 16:31:17] [info] -----
[2025-10-12 16:31:17] [info] Total Errors:   1120
[2025-10-12 16:31:17] [info] Total Warnings:    321
[2025-10-12 16:31:17] [info] DRC Status:      FAILED
```

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```
[2025-10-12 16:32:14] [info] =====
[2025-10-12 16:32:14] [info] Design Rule Check Report
[2025-10-12 16:32:14] [info] =====

[2025-10-12 16:32:14] [info] Report Generated:   2025-10-12
[2025-10-12 16:32:14] [info] Run Start Time:     04:31:22 PM
[2025-10-12 16:32:14] [info] Run End Time:       04:32:14 PM
[2025-10-12 16:32:14] [info] Time Taken:         0 minutes 52 seconds

[2025-10-12 16:32:14] [info] Tool Version:       LVR_2025.1
[2025-10-12 16:32:14] [info] Host Name:          ip-172-31-37-173
[2025-10-12 16:32:14] [info] User Name:          ubuntu
[2025-10-12 16:32:14] [info] Cores Used:         8
[2025-10-12 16:32:14] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Top Cell Information
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Layout Top Cell:    TOP
[2025-10-12 16:32:14] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 16:32:14] [info] Database Format:      GDSII
[2025-10-12 16:32:14] [info] Verification Mode:   Full Check

[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Type of Errors | Count
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] DB_WARN -> 1
[2025-10-12 16:32:14] [info] LRF_ERR001 -> 14
[2025-10-12 16:32:14] [info] DB_WARN002 -> 159
[2025-10-12 16:32:14] [info] DB_WARN003 -> 161
[2025-10-12 16:32:14] [info] DRC_ERR9 -> 89
[2025-10-12 16:32:14] [info] DRC_ERR -> 67
[2025-10-12 16:32:14] [info] DRC_ERR5 -> 408
[2025-10-12 16:32:14] [info] DRC_ERR4 -> 241
[2025-10-12 16:32:14] [info] DRC_ERR1 -> 301
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Rule Violation Summary
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:32:14] [info] |-----|-----|-----|
[2025-10-12 16:32:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:32:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:32:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:32:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:32:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 16:32:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:32:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:32:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:32:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:32:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:32:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:32:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:32:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:32:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:32:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:32:14] [info]
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Performance Metrics
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] | Stage | Time Taken |
[2025-10-12 16:32:14] [info] |-----|-----|
[2025-10-12 16:32:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:32:14] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:32:14] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Summary
[2025-10-12 16:32:14] [info] -----
[2025-10-12 16:32:14] [info] Total Errors: 1120
[2025-10-12 16:32:14] [info] Total Warnings: 321
[2025-10-12 16:32:14] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:33:10] [info] =====
[2025-10-12 16:33:10] [info] Design Rule Check Report
[2025-10-12 16:33:10] [info] =====

[2025-10-12 16:33:10] [info] Report Generated: 2025-10-12
[2025-10-12 16:33:10] [info] Run Start Time: 04:32:20 PM
[2025-10-12 16:33:10] [info] Run End Time: 04:33:10 PM
[2025-10-12 16:33:10] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:33:10] [info] Tool Version: LVR_2025.1
[2025-10-12 16:33:10] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:33:10] [info] User Name: ubuntu
[2025-10-12 16:33:10] [info] Cores Used: 8
[2025-10-12 16:33:10] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Top Cell Information
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Layout Top Cell: TOP
[2025-10-12 16:33:10] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:33:10] [info] Database Format: GDSII
[2025-10-12 16:33:10] [info] Verification Mode: Full Check

[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Type of Errors | Count
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] DB_WARN -> 1
[2025-10-12 16:33:10] [info] LRF_ERR001 -> 14
[2025-10-12 16:33:10] [info] DB_WARN002 -> 159
[2025-10-12 16:33:10] [info] DB_WARN003 -> 161
```

```

[2025-10-12 16:33:10] [info] DRC_ERR9 -> 89
[2025-10-12 16:33:10] [info] DRC_ERR -> 67
[2025-10-12 16:33:10] [info] DRC_ERR5 -> 408
[2025-10-12 16:33:10] [info] DRC_ERR4 -> 241
[2025-10-12 16:33:10] [info] DRC_ERR1 -> 301
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Rule Violation Summary
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:33:10] [info] |-----|-----|-----|
[2025-10-12 16:33:10] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:33:10] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:33:10] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:33:10] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:33:10] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:33:10] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:33:10] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:33:10] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:33:10] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:33:10] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:33:10] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:33:10] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:33:10] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:33:10] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:33:10] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Performance Metrics
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] | Stage | Time Taken |
[2025-10-12 16:33:10] [info] |-----|-----|
[2025-10-12 16:33:10] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:33:10] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:33:10] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Summary
[2025-10-12 16:33:10] [info] -----
[2025-10-12 16:33:10] [info] Total Errors: 1120
[2025-10-12 16:33:10] [info] Total Warnings: 321
[2025-10-12 16:33:10] [info] DRC Status: FAILED

```

lvr_summary_368.rpt

```

[2025-10-12 16:34:09] [info] =====
[2025-10-12 16:34:09] [info] Design Rule Check Report
[2025-10-12 16:34:09] [info] =====

[2025-10-12 16:34:09] [info] Report Generated: 2025-10-12
[2025-10-12 16:34:09] [info] Run Start Time: 04:33:17 PM
[2025-10-12 16:34:09] [info] Run End Time: 04:34:09 PM
[2025-10-12 16:34:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:34:09] [info] Tool Version: LVR_2025.1
[2025-10-12 16:34:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:34:09] [info] User Name: ubuntu
[2025-10-12 16:34:09] [info] Cores Used: 8
[2025-10-12 16:34:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] Top Cell Information
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] Layout Top Cell:      TOP
[2025-10-12 16:34:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:34:09] [info] Database Format:      GDSII
[2025-10-12 16:34:09] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] Type of Errors | Count
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] DB_WARN -> 1
[2025-10-12 16:34:09] [info] LRF_ERR001 -> 14
[2025-10-12 16:34:09] [info] DB_WARN002 -> 159
[2025-10-12 16:34:09] [info] DB_WARN003 -> 161
[2025-10-12 16:34:09] [info] DRC_ERR9 -> 89
[2025-10-12 16:34:09] [info] DRC_ERR -> 67
[2025-10-12 16:34:09] [info] DRC_ERR5 -> 408
[2025-10-12 16:34:09] [info] DRC_ERR4 -> 241
[2025-10-12 16:34:09] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] Rule Violation Summary
[2025-10-12 16:34:09] [info] -----
```

[2025-10-12 16:34:09] [info]	Rule ID	Description	Error Count
[2025-10-12 16:34:09] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 16:34:09] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 16:34:09] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 16:34:09] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 16:34:09] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 16:34:09] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 16:34:09] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 16:34:09] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 16:34:09] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 16:34:09] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 16:34:09] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 16:34:09] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" 15	
[2025-10-12 16:34:09] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 16:34:09] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 16:34:09] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

```
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] Performance Metrics
[2025-10-12 16:34:09] [info] -----
```

[2025-10-12 16:34:09] [info]	Stage	Time Taken
[2025-10-12 16:34:09] [info]	GDS Parsing	0 minutes 9 seconds
[2025-10-12 16:34:09] [info]	Rule Checking	0 minutes 40 seconds
[2025-10-12 16:34:09] [info]	Report Generation	0 minutes 0 seconds

```
[2025-10-12 16:34:09] [info] -----
[2025-10-12 16:34:09] [info] Summary
[2025-10-12 16:34:09] [info] -----
```

```
[2025-10-12 16:34:09] [info] Total Errors:      1120
[2025-10-12 16:34:09] [info] Total Warnings:    321
[2025-10-12 16:34:09] [info] DRC Status:        FAILED
```

lvr_summary_369.rpt

```
[2025-10-12 16:35:05] [info] =====
[2025-10-12 16:35:05] [info]                      Design Rule Check Report
[2025-10-12 16:35:05] [info] =====

[2025-10-12 16:35:05] [info] Report Generated:      2025-10-12
[2025-10-12 16:35:05] [info] Run Start Time:       04:34:15 PM
[2025-10-12 16:35:05] [info] Run End Time:         04:35:05 PM
[2025-10-12 16:35:05] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 16:35:05] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:35:05] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:35:05] [info] User Name:            ubuntu
[2025-10-12 16:35:05] [info] Cores Used:           8
[2025-10-12 16:35:05] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info]                      Top Cell Information
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info] Layout Top Cell:      TOP
[2025-10-12 16:35:05] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:35:05] [info] Database Format:        GDSII
[2025-10-12 16:35:05] [info] Verification Mode:     Full Check

[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info]                      Type of Errors | Count
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info] DB_WARN -> 1
[2025-10-12 16:35:05] [info] LRF_ERR001 -> 14
[2025-10-12 16:35:05] [info] DB_WARN002 -> 159
[2025-10-12 16:35:05] [info] DB_WARN003 -> 161
[2025-10-12 16:35:05] [info] DRC_ERR9 -> 89
[2025-10-12 16:35:05] [info] DRC_ERR -> 67
[2025-10-12 16:35:05] [info] DRC_ERR5 -> 408
[2025-10-12 16:35:05] [info] DRC_ERR4 -> 241
[2025-10-12 16:35:05] [info] DRC_ERR1 -> 301
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info]                      Rule Violation Summary
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:35:05] [info] |-----|-----|-----|
[2025-10-12 16:35:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:35:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:35:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:35:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:35:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:35:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:35:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:35:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:35:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:35:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:35:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:35:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:35:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:35:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:35:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:35:05] [info]
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info]                      Performance Metrics
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:35:05] [info] |-----|-----|
[2025-10-12 16:35:05] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:35:05] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 16:35:05] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info]              Summary
[2025-10-12 16:35:05] [info] -----
[2025-10-12 16:35:05] [info] Total Errors:   1120
[2025-10-12 16:35:05] [info] Total Warnings:    321
[2025-10-12 16:35:05] [info] DRC Status:      FAILED
```

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```
[2025-10-12 11:16:28] [info] =====
[2025-10-12 11:16:28] [info]              Design Rule Check Report
[2025-10-12 11:16:28] [info] =====

[2025-10-12 11:16:28] [info] Report Generated:   2025-10-12
[2025-10-12 11:16:28] [info] Run Start Time:     11:15:37 AM
[2025-10-12 11:16:28] [info] Run End Time:       11:16:28 AM
[2025-10-12 11:16:28] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 11:16:28] [info] Tool Version:       LVR_2025.1
[2025-10-12 11:16:28] [info] Host Name:          ip-172-31-37-173
[2025-10-12 11:16:28] [info] User Name:          ubuntu
[2025-10-12 11:16:28] [info] Cores Used:         8
[2025-10-12 11:16:28] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info]              Top Cell Information
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] Layout Top Cell:    TOP
[2025-10-12 11:16:28] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 11:16:28] [info] Database Format:      GDSII
[2025-10-12 11:16:28] [info] Verification Mode:   Full Check

[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info]              Type of Errors | Count
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] DB_WARN -> 1
[2025-10-12 11:16:28] [info] LRF_ERR001 -> 14
[2025-10-12 11:16:28] [info] DB_WARN002 -> 159
[2025-10-12 11:16:28] [info] DB_WARN003 -> 161
[2025-10-12 11:16:28] [info] DRC_ERR9 -> 89
[2025-10-12 11:16:28] [info] DRC_ERR -> 67
[2025-10-12 11:16:28] [info] DRC_ERR5 -> 408
[2025-10-12 11:16:28] [info] DRC_ERR4 -> 241
[2025-10-12 11:16:28] [info] DRC_ERR1 -> 301
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info]              Rule Violation Summary
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:16:28] [info] |-----|-----|-----|
[2025-10-12 11:16:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:16:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:16:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:16:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:16:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 11:16:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:16:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:16:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:16:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:16:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:16:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:16:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:16:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:16:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:16:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:16:28] [info]
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] Performance Metrics
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] | Stage | Time Taken |
[2025-10-12 11:16:28] [info] |-----|-----|
[2025-10-12 11:16:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:16:28] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:16:28] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] Summary
[2025-10-12 11:16:28] [info] -----
[2025-10-12 11:16:28] [info] Total Errors: 1120
[2025-10-12 11:16:28] [info] Total Warnings: 321
[2025-10-12 11:16:28] [info] DRC Status: FAILED

```

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```

[2025-10-12 16:36:03] [info] =====
[2025-10-12 16:36:03] [info] Design Rule Check Report
[2025-10-12 16:36:03] [info] =====

[2025-10-12 16:36:03] [info] Report Generated: 2025-10-12
[2025-10-12 16:36:03] [info] Run Start Time: 04:35:11 PM
[2025-10-12 16:36:03] [info] Run End Time: 04:36:03 PM
[2025-10-12 16:36:03] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:36:03] [info] Tool Version: LVR_2025.1
[2025-10-12 16:36:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:36:03] [info] User Name: ubuntu
[2025-10-12 16:36:03] [info] Cores Used: 8
[2025-10-12 16:36:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Top Cell Information
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Layout Top Cell: TOP
[2025-10-12 16:36:03] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:36:03] [info] Database Format: GDSII
[2025-10-12 16:36:03] [info] Verification Mode: Full Check

[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Type of Errors | Count
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] DB_WARN -> 1
[2025-10-12 16:36:03] [info] LRF_ERR001 -> 14
[2025-10-12 16:36:03] [info] DB_WARN002 -> 159
[2025-10-12 16:36:03] [info] DB_WARN003 -> 161

```

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[2025-10-12 16:36:03] [info] DRC_ERR9 -> 89
[2025-10-12 16:36:03] [info] DRC_ERR -> 67
[2025-10-12 16:36:03] [info] DRC_ERR5 -> 408
[2025-10-12 16:36:03] [info] DRC_ERR4 -> 241
[2025-10-12 16:36:03] [info] DRC_ERR1 -> 301
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Rule Violation Summary
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:36:03] [info] |-----|-----|-----|
[2025-10-12 16:36:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:36:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:36:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:36:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:36:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:36:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:36:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:36:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:36:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:36:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:36:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:36:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:36:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:36:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:36:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Performance Metrics
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] | Stage | Time Taken |
[2025-10-12 16:36:03] [info] |-----|-----|
[2025-10-12 16:36:03] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:36:03] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:36:03] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Summary
[2025-10-12 16:36:03] [info] -----
[2025-10-12 16:36:03] [info] Total Errors: 1120
[2025-10-12 16:36:03] [info] Total Warnings: 321
[2025-10-12 16:36:03] [info] DRC Status: FAILED

```

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```

[2025-10-12 16:37:01] [info] =====
[2025-10-12 16:37:01] [info] Design Rule Check Report
[2025-10-12 16:37:01] [info] =====

[2025-10-12 16:37:01] [info] Report Generated: 2025-10-12
[2025-10-12 16:37:01] [info] Run Start Time: 04:36:10 PM
[2025-10-12 16:37:01] [info] Run End Time: 04:37:01 PM
[2025-10-12 16:37:01] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:37:01] [info] Tool Version: LVR_2025.1
[2025-10-12 16:37:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:37:01] [info] User Name: ubuntu
[2025-10-12 16:37:01] [info] Cores Used: 8
[2025-10-12 16:37:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] Top Cell Information
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] Layout Top Cell:      TOP
[2025-10-12 16:37:01] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:37:01] [info] Database Format:      GDSII
[2025-10-12 16:37:01] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] Type of Errors | Count
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] DB_WARN -> 1
[2025-10-12 16:37:01] [info] LRF_ERROR01 -> 14
[2025-10-12 16:37:01] [info] DB_WARN002 -> 159
[2025-10-12 16:37:01] [info] DB_WARN003 -> 161
[2025-10-12 16:37:01] [info] DRC_ERR9 -> 89
[2025-10-12 16:37:01] [info] DRC_ERR -> 67
[2025-10-12 16:37:01] [info] DRC_ERR5 -> 408
[2025-10-12 16:37:01] [info] DRC_ERR4 -> 241
[2025-10-12 16:37:01] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] Rule Violation Summary
```

```
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:37:01] [info] |-----|-----|-----|
[2025-10-12 16:37:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:37:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:37:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:37:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:37:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:37:01] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:37:01] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:37:01] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:37:01] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:37:01] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:37:01] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:37:01] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:37:01] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:37:01] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:37:01] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:37:01] [info] -----
```

```
[2025-10-12 16:37:01] [info] Performance Metrics
```

```
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] | Stage          | Time Taken |
[2025-10-12 16:37:01] [info] |-----|-----|
[2025-10-12 16:37:01] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:37:01] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 16:37:01] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:37:01] [info] -----
```

```
[2025-10-12 16:37:01] [info] Summary
```

```
[2025-10-12 16:37:01] [info] -----
[2025-10-12 16:37:01] [info] Total Errors:      1120
[2025-10-12 16:37:01] [info] Total Warnings:    321
[2025-10-12 16:37:01] [info] DRC Status:        FAILED
```

lvr_summary_372.rpt

```
[2025-10-12 16:37:59] [info] =====
[2025-10-12 16:37:59] [info]                      Design Rule Check Report
[2025-10-12 16:37:59] [info] =====

[2025-10-12 16:37:59] [info] Report Generated:      2025-10-12
[2025-10-12 16:37:59] [info] Run Start Time:       04:37:07 PM
[2025-10-12 16:37:59] [info] Run End Time:         04:37:59 PM
[2025-10-12 16:37:59] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 16:37:59] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:37:59] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:37:59] [info] User Name:            ubuntu
[2025-10-12 16:37:59] [info] Cores Used:           8
[2025-10-12 16:37:59] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info]                      Top Cell Information
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info] Layout Top Cell:      TOP
[2025-10-12 16:37:59] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:37:59] [info] Database Format:        GDSII
[2025-10-12 16:37:59] [info] Verification Mode:     Full Check

[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info]                      Type of Errors | Count
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info] DB_WARN -> 1
[2025-10-12 16:37:59] [info] LRF_ERR001 -> 14
[2025-10-12 16:37:59] [info] DB_WARN002 -> 159
[2025-10-12 16:37:59] [info] DB_WARN003 -> 161
[2025-10-12 16:37:59] [info] DRC_ERR9 -> 89
[2025-10-12 16:37:59] [info] DRC_ERR -> 67
[2025-10-12 16:37:59] [info] DRC_ERR5 -> 408
[2025-10-12 16:37:59] [info] DRC_ERR4 -> 241
[2025-10-12 16:37:59] [info] DRC_ERR1 -> 301
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info]                      Rule Violation Summary
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:37:59] [info] |-----|-----|-----|
[2025-10-12 16:37:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:37:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:37:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:37:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:37:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:37:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:37:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:37:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:37:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:37:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:37:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:37:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:37:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:37:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:37:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:37:59] [info]
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info]                      Performance Metrics
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:37:59] [info] |-----|-----|
[2025-10-12 16:37:59] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:37:59] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 16:37:59] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info]                Summary
[2025-10-12 16:37:59] [info] -----
[2025-10-12 16:37:59] [info] Total Errors:    1120
[2025-10-12 16:37:59] [info] Total Warnings:    321
[2025-10-12 16:37:59] [info] DRC Status:      FAILED
```

lvr_summary_373.rpt

```
[2025-10-12 16:38:57] [info] =====
[2025-10-12 16:38:57] [info]                Design Rule Check Report
[2025-10-12 16:38:57] [info] =====

[2025-10-12 16:38:57] [info] Report Generated:    2025-10-12
[2025-10-12 16:38:57] [info] Run Start Time:      04:38:05 PM
[2025-10-12 16:38:57] [info] Run End Time:        04:38:57 PM
[2025-10-12 16:38:57] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:38:57] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:38:57] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:38:57] [info] User Name:           ubuntu
[2025-10-12 16:38:57] [info] Cores Used:          8
[2025-10-12 16:38:57] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info]                Top Cell Information
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] Layout Top Cell:     TOP
[2025-10-12 16:38:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:38:57] [info] Database Format:      GDSII
[2025-10-12 16:38:57] [info] Verification Mode:    Full Check

[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info]                Type of Errors | Count
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] DB_WARN -> 1
[2025-10-12 16:38:57] [info] LRF_ERR001 -> 14
[2025-10-12 16:38:57] [info] DB_WARN002 -> 159
[2025-10-12 16:38:57] [info] DB_WARN003 -> 161
[2025-10-12 16:38:57] [info] DRC_ERR9 -> 89
[2025-10-12 16:38:57] [info] DRC_ERR -> 67
[2025-10-12 16:38:57] [info] DRC_ERR5 -> 408
[2025-10-12 16:38:57] [info] DRC_ERR4 -> 241
[2025-10-12 16:38:57] [info] DRC_ERR1 -> 301
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info]                Rule Violation Summary
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:38:57] [info] |-----|-----|-----|
[2025-10-12 16:38:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:38:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:38:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:38:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:38:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 16:38:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:38:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:38:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:38:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:38:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:38:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:38:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:38:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:38:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:38:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:38:57] [info]
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] Performance Metrics
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] | Stage | Time Taken |
[2025-10-12 16:38:57] [info] |-----|-----|
[2025-10-12 16:38:57] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:38:57] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:38:57] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] Summary
[2025-10-12 16:38:57] [info] -----
[2025-10-12 16:38:57] [info] Total Errors: 1120
[2025-10-12 16:38:57] [info] Total Warnings: 321
[2025-10-12 16:38:57] [info] DRC Status: FAILED
```

lvr_summary_374.rpt

```
[2025-10-12 16:39:53] [info] =====
[2025-10-12 16:39:53] [info] Design Rule Check Report
[2025-10-12 16:39:53] [info] =====

[2025-10-12 16:39:53] [info] Report Generated: 2025-10-12
[2025-10-12 16:39:53] [info] Run Start Time: 04:39:01 PM
[2025-10-12 16:39:53] [info] Run End Time: 04:39:53 PM
[2025-10-12 16:39:53] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 16:39:53] [info] Tool Version: LVR_2025.1
[2025-10-12 16:39:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:39:53] [info] User Name: ubuntu
[2025-10-12 16:39:53] [info] Cores Used: 8
[2025-10-12 16:39:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Top Cell Information
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Layout Top Cell: TOP
[2025-10-12 16:39:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:39:53] [info] Database Format: GDSII
[2025-10-12 16:39:53] [info] Verification Mode: Full Check

[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Type of Errors | Count
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] DB_WARN -> 1
[2025-10-12 16:39:53] [info] LRF_ERR001 -> 14
[2025-10-12 16:39:53] [info] DB_WARN002 -> 159
[2025-10-12 16:39:53] [info] DB_WARN003 -> 161
```

```
[2025-10-12 16:39:53] [info] DRC_ERR9 -> 89
[2025-10-12 16:39:53] [info] DRC_ERR -> 67
[2025-10-12 16:39:53] [info] DRC_ERR5 -> 408
[2025-10-12 16:39:53] [info] DRC_ERR4 -> 241
[2025-10-12 16:39:53] [info] DRC_ERR1 -> 301
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Rule Violation Summary
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:39:53] [info] |-----|-----|-----|
[2025-10-12 16:39:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:39:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:39:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:39:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:39:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:39:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:39:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:39:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:39:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:39:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:39:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:39:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:39:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:39:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:39:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Performance Metrics
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] | Stage | Time Taken |
[2025-10-12 16:39:53] [info] |-----|-----|
[2025-10-12 16:39:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:39:53] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:39:53] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Summary
[2025-10-12 16:39:53] [info] -----
[2025-10-12 16:39:53] [info] Total Errors: 1120
[2025-10-12 16:39:53] [info] Total Warnings: 321
[2025-10-12 16:39:53] [info] DRC Status: FAILED
```

lvr_summary_375.rpt

```
[2025-10-12 16:40:51] [info] =====
[2025-10-12 16:40:51] [info] Design Rule Check Report
[2025-10-12 16:40:51] [info] =====

[2025-10-12 16:40:51] [info] Report Generated: 2025-10-12
[2025-10-12 16:40:51] [info] Run Start Time: 04:39:59 PM
[2025-10-12 16:40:51] [info] Run End Time: 04:40:51 PM
[2025-10-12 16:40:51] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:40:51] [info] Tool Version: LVR_2025.1
[2025-10-12 16:40:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:40:51] [info] User Name: ubuntu
[2025-10-12 16:40:51] [info] Cores Used: 8
[2025-10-12 16:40:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Top Cell Information
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Layout Top Cell:      TOP
[2025-10-12 16:40:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:40:51] [info] Database Format:      GDSII
[2025-10-12 16:40:51] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Type of Errors | Count
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] DB_WARN -> 1
[2025-10-12 16:40:51] [info] LRF_ERR001 -> 14
[2025-10-12 16:40:51] [info] DB_WARN002 -> 159
[2025-10-12 16:40:51] [info] DB_WARN003 -> 161
[2025-10-12 16:40:51] [info] DRC_ERR9 -> 89
[2025-10-12 16:40:51] [info] DRC_ERR -> 67
[2025-10-12 16:40:51] [info] DRC_ERR5 -> 408
[2025-10-12 16:40:51] [info] DRC_ERR4 -> 241
[2025-10-12 16:40:51] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Rule Violation Summary
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:40:51] [info] |-----|-----|-----|
[2025-10-12 16:40:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:40:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:40:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:40:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:40:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:40:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:40:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:40:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:40:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:40:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:40:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:40:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:40:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:40:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:40:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:40:51] [info] |
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Performance Metrics
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] | Stage          | Time Taken |
[2025-10-12 16:40:51] [info] |-----|-----|
[2025-10-12 16:40:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:40:51] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 16:40:51] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Summary
```

```
[2025-10-12 16:40:51] [info] -----
[2025-10-12 16:40:51] [info] Total Errors:      1120
[2025-10-12 16:40:51] [info] Total Warnings:    321
[2025-10-12 16:40:51] [info] DRC Status:        FAILED
```

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```
[2025-10-12 16:41:48] [info] =====
[2025-10-12 16:41:48] [info]                Design Rule Check Report
[2025-10-12 16:41:48] [info] =====

[2025-10-12 16:41:48] [info] Report Generated:    2025-10-12
[2025-10-12 16:41:48] [info] Run Start Time:      04:40:56 PM
[2025-10-12 16:41:48] [info] Run End Time:        04:41:48 PM
[2025-10-12 16:41:48] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 16:41:48] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:41:48] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:41:48] [info] User Name:           ubuntu
[2025-10-12 16:41:48] [info] Cores Used:          8
[2025-10-12 16:41:48] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info]                Top Cell Information
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info] Layout Top Cell:     TOP
[2025-10-12 16:41:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:41:48] [info] Database Format:      GDSII
[2025-10-12 16:41:48] [info] Verification Mode:    Full Check

[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info]                Type of Errors | Count
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info] DB_WARN -> 1
[2025-10-12 16:41:48] [info] LRF_ERR001 -> 14
[2025-10-12 16:41:48] [info] DB_WARN002 -> 159
[2025-10-12 16:41:48] [info] DB_WARN003 -> 161
[2025-10-12 16:41:48] [info] DRC_ERR9 -> 89
[2025-10-12 16:41:48] [info] DRC_ERR -> 67
[2025-10-12 16:41:48] [info] DRC_ERR5 -> 408
[2025-10-12 16:41:48] [info] DRC_ERR4 -> 241
[2025-10-12 16:41:48] [info] DRC_ERR1 -> 301
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info]                Rule Violation Summary
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:41:48] [info] |-----|-----|-----|
[2025-10-12 16:41:48] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:41:48] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:41:48] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:41:48] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:41:48] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:41:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:41:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:41:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:41:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:41:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:41:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:41:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:41:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:41:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:41:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:41:48] [info]
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info]                Performance Metrics
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:41:48] [info] |-----|-----|
[2025-10-12 16:41:48] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:41:48] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 16:41:48] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info]                Summary
[2025-10-12 16:41:48] [info] -----
[2025-10-12 16:41:48] [info] Total Errors:    1120
[2025-10-12 16:41:48] [info] Total Warnings:    321
[2025-10-12 16:41:48] [info] DRC Status:      FAILED
```

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```
[2025-10-12 16:42:45] [info] =====
[2025-10-12 16:42:45] [info]                Design Rule Check Report
[2025-10-12 16:42:45] [info] =====

[2025-10-12 16:42:45] [info] Report Generated:    2025-10-12
[2025-10-12 16:42:45] [info] Run Start Time:      04:41:54 PM
[2025-10-12 16:42:45] [info] Run End Time:        04:42:45 PM
[2025-10-12 16:42:45] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:42:45] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:42:45] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:42:45] [info] User Name:           ubuntu
[2025-10-12 16:42:45] [info] Cores Used:          8
[2025-10-12 16:42:45] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info]                Top Cell Information
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] Layout Top Cell:     TOP
[2025-10-12 16:42:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:42:45] [info] Database Format:       GDSII
[2025-10-12 16:42:45] [info] Verification Mode:    Full Check

[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info]                Type of Errors | Count
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] DB_WARN -> 1
[2025-10-12 16:42:45] [info] LRF_ERR001 -> 14
[2025-10-12 16:42:45] [info] DB_WARN002 -> 159
[2025-10-12 16:42:45] [info] DB_WARN003 -> 161
[2025-10-12 16:42:45] [info] DRC_ERR9 -> 89
[2025-10-12 16:42:45] [info] DRC_ERR -> 67
[2025-10-12 16:42:45] [info] DRC_ERR5 -> 408
[2025-10-12 16:42:45] [info] DRC_ERR4 -> 241
[2025-10-12 16:42:45] [info] DRC_ERR1 -> 301
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info]                Rule Violation Summary
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:42:45] [info] |-----|-----|-----|
[2025-10-12 16:42:45] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:42:45] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:42:45] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:42:45] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:42:45] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:42:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 16:42:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:42:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:42:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:42:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:42:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:42:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:42:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:42:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:42:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:42:45] [info]
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] Performance Metrics
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] | Stage | Time Taken |
[2025-10-12 16:42:45] [info] |-----|-----|
[2025-10-12 16:42:45] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:42:45] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:42:45] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] Summary
[2025-10-12 16:42:45] [info] -----
[2025-10-12 16:42:45] [info] Total Errors: 1120
[2025-10-12 16:42:45] [info] Total Warnings: 321
[2025-10-12 16:42:45] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:43:42] [info] =====
[2025-10-12 16:43:42] [info] Design Rule Check Report
[2025-10-12 16:43:42] [info] =====

[2025-10-12 16:43:42] [info] Report Generated: 2025-10-12
[2025-10-12 16:43:42] [info] Run Start Time: 04:42:51 PM
[2025-10-12 16:43:42] [info] Run End Time: 04:43:42 PM
[2025-10-12 16:43:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:43:42] [info] Tool Version: LVR_2025.1
[2025-10-12 16:43:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:43:42] [info] User Name: ubuntu
[2025-10-12 16:43:42] [info] Cores Used: 8
[2025-10-12 16:43:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Top Cell Information
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Layout Top Cell: TOP
[2025-10-12 16:43:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:43:42] [info] Database Format: GDSII
[2025-10-12 16:43:42] [info] Verification Mode: Full Check

[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Type of Errors | Count
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] DB_WARN -> 1
[2025-10-12 16:43:42] [info] LRF_ERR001 -> 14
[2025-10-12 16:43:42] [info] DB_WARN002 -> 159
[2025-10-12 16:43:42] [info] DB_WARN003 -> 161
```

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[2025-10-12 16:43:42] [info] DRC_ERR9 -> 89
[2025-10-12 16:43:42] [info] DRC_ERR -> 67
[2025-10-12 16:43:42] [info] DRC_ERR5 -> 408
[2025-10-12 16:43:42] [info] DRC_ERR4 -> 241
[2025-10-12 16:43:42] [info] DRC_ERR1 -> 301
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Rule Violation Summary
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:43:42] [info] |-----|-----|-----|
[2025-10-12 16:43:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:43:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:43:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:43:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:43:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:43:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:43:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:43:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:43:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:43:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:43:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:43:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:43:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:43:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:43:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Performance Metrics
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] | Stage | Time Taken |
[2025-10-12 16:43:42] [info] |-----|-----|
[2025-10-12 16:43:42] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:43:42] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:43:42] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Summary
[2025-10-12 16:43:42] [info] -----
[2025-10-12 16:43:42] [info] Total Errors: 1120
[2025-10-12 16:43:42] [info] Total Warnings: 321
[2025-10-12 16:43:42] [info] DRC Status: FAILED

```

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```

[2025-10-12 16:44:40] [info] =====
[2025-10-12 16:44:40] [info] Design Rule Check Report
[2025-10-12 16:44:40] [info] =====

[2025-10-12 16:44:40] [info] Report Generated: 2025-10-12
[2025-10-12 16:44:40] [info] Run Start Time: 04:43:48 PM
[2025-10-12 16:44:40] [info] Run End Time: 04:44:40 PM
[2025-10-12 16:44:40] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:44:40] [info] Tool Version: LVR_2025.1
[2025-10-12 16:44:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:44:40] [info] User Name: ubuntu
[2025-10-12 16:44:40] [info] Cores Used: 8
[2025-10-12 16:44:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] Top Cell Information
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] Layout Top Cell:      TOP
[2025-10-12 16:44:40] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:44:40] [info] Database Format:       GDSII
[2025-10-12 16:44:40] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] Type of Errors | Count
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] DB_WARN -> 1
[2025-10-12 16:44:40] [info] LRF_ERROR01 -> 14
[2025-10-12 16:44:40] [info] DB_WARN002 -> 159
[2025-10-12 16:44:40] [info] DB_WARN003 -> 161
[2025-10-12 16:44:40] [info] DRC_ERR9 -> 89
[2025-10-12 16:44:40] [info] DRC_ERR -> 67
[2025-10-12 16:44:40] [info] DRC_ERR5 -> 408
[2025-10-12 16:44:40] [info] DRC_ERR4 -> 241
[2025-10-12 16:44:40] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] Rule Violation Summary
```

```
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:44:40] [info] |-----|-----|-----|
[2025-10-12 16:44:40] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:44:40] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:44:40] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:44:40] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:44:40] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:44:40] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:44:40] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:44:40] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:44:40] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:44:40] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:44:40] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:44:40] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:44:40] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:44:40] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:44:40] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:44:40] [info] -----
```

```
[2025-10-12 16:44:40] [info] Performance Metrics
```

```
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] | Stage          | Time Taken |
[2025-10-12 16:44:40] [info] |-----|-----|
[2025-10-12 16:44:40] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:44:40] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 16:44:40] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:44:40] [info] -----
```

```
[2025-10-12 16:44:40] [info] Summary
```

```
[2025-10-12 16:44:40] [info] -----
[2025-10-12 16:44:40] [info] Total Errors:      1120
[2025-10-12 16:44:40] [info] Total Warnings:    321
[2025-10-12 16:44:40] [info] DRC Status:        FAILED
```


lvr_summary_38.rpt

```
[2025-10-12 11:17:25] [info] =====
[2025-10-12 11:17:25] [info]                      Design Rule Check Report
[2025-10-12 11:17:25] [info] =====

[2025-10-12 11:17:25] [info] Report Generated:    2025-10-12
[2025-10-12 11:17:25] [info] Run Start Time:      11:16:34 AM
[2025-10-12 11:17:25] [info] Run End Time:        11:17:25 AM
[2025-10-12 11:17:25] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 11:17:25] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:17:25] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:17:25] [info] User Name:           ubuntu
[2025-10-12 11:17:25] [info] Cores Used:          8
[2025-10-12 11:17:25] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info]                      Top Cell Information
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info] Layout Top Cell:     TOP
[2025-10-12 11:17:25] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:17:25] [info] Database Format:      GDSII
[2025-10-12 11:17:25] [info] Verification Mode:    Full Check

[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info]                      Type of Errors | Count
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info] DB_WARN -> 1
[2025-10-12 11:17:25] [info] LRF_ERR001 -> 14
[2025-10-12 11:17:25] [info] DB_WARN002 -> 159
[2025-10-12 11:17:25] [info] DB_WARN003 -> 161
[2025-10-12 11:17:25] [info] DRC_ERR9 -> 89
[2025-10-12 11:17:25] [info] DRC_ERR -> 67
[2025-10-12 11:17:25] [info] DRC_ERR5 -> 408
[2025-10-12 11:17:25] [info] DRC_ERR4 -> 241
[2025-10-12 11:17:25] [info] DRC_ERR1 -> 301
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info]                      Rule Violation Summary
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:17:25] [info] |-----|-----|-----|
[2025-10-12 11:17:25] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:17:25] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:17:25] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:17:25] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:17:25] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:17:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:17:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:17:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:17:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:17:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:17:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:17:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:17:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:17:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:17:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:17:25] [info]
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info]                      Performance Metrics
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:17:25] [info] |-----|-----|
[2025-10-12 11:17:25] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:17:25] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:17:25] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info]                Summary
[2025-10-12 11:17:25] [info] -----
[2025-10-12 11:17:25] [info] Total Errors:    1120
[2025-10-12 11:17:25] [info] Total Warnings:    321
[2025-10-12 11:17:25] [info] DRC Status:      FAILED
```

lvr_summary_380.rpt

```
[2025-10-12 16:45:37] [info] =====
[2025-10-12 16:45:37] [info]                Design Rule Check Report
[2025-10-12 16:45:37] [info] =====

[2025-10-12 16:45:37] [info] Report Generated:    2025-10-12
[2025-10-12 16:45:37] [info] Run Start Time:      04:44:46 PM
[2025-10-12 16:45:37] [info] Run End Time:        04:45:37 PM
[2025-10-12 16:45:37] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:45:37] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:45:37] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:45:37] [info] User Name:           ubuntu
[2025-10-12 16:45:37] [info] Cores Used:          8
[2025-10-12 16:45:37] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info]                Top Cell Information
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] Layout Top Cell:     TOP
[2025-10-12 16:45:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:45:37] [info] Database Format:       GDSII
[2025-10-12 16:45:37] [info] Verification Mode:    Full Check

[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info]                Type of Errors | Count
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] DB_WARN -> 1
[2025-10-12 16:45:37] [info] LRF_ERR001 -> 14
[2025-10-12 16:45:37] [info] DB_WARN002 -> 159
[2025-10-12 16:45:37] [info] DB_WARN003 -> 161
[2025-10-12 16:45:37] [info] DRC_ERR9 -> 89
[2025-10-12 16:45:37] [info] DRC_ERR -> 67
[2025-10-12 16:45:37] [info] DRC_ERR5 -> 408
[2025-10-12 16:45:37] [info] DRC_ERR4 -> 241
[2025-10-12 16:45:37] [info] DRC_ERR1 -> 301
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info]                Rule Violation Summary
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:45:37] [info] |-----|-----|-----|
[2025-10-12 16:45:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:45:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:45:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:45:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:45:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```

[2025-10-12 16:45:37] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:45:37] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:45:37] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:45:37] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:45:37] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:45:37] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:45:37] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:45:37] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:45:37] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:45:37] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:45:37] [info]
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] Performance Metrics
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] | Stage | Time Taken |
[2025-10-12 16:45:37] [info] |-----|-----|
[2025-10-12 16:45:37] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:45:37] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:45:37] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] Summary
[2025-10-12 16:45:37] [info] -----
[2025-10-12 16:45:37] [info] Total Errors: 1120
[2025-10-12 16:45:37] [info] Total Warnings: 321
[2025-10-12 16:45:37] [info] DRC Status: FAILED

```

lvr_summary_381.rpt

```

[2025-10-12 16:46:35] [info] =====
[2025-10-12 16:46:35] [info] Design Rule Check Report
[2025-10-12 16:46:35] [info] =====

[2025-10-12 16:46:35] [info] Report Generated: 2025-10-12
[2025-10-12 16:46:35] [info] Run Start Time: 04:45:44 PM
[2025-10-12 16:46:35] [info] Run End Time: 04:46:35 PM
[2025-10-12 16:46:35] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:46:35] [info] Tool Version: LVR_2025.1
[2025-10-12 16:46:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:46:35] [info] User Name: ubuntu
[2025-10-12 16:46:35] [info] Cores Used: 8
[2025-10-12 16:46:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Top Cell Information
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Layout Top Cell: TOP
[2025-10-12 16:46:35] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:46:35] [info] Database Format: GDSII
[2025-10-12 16:46:35] [info] Verification Mode: Full Check

[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Type of Errors | Count
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] DB_WARN -> 1
[2025-10-12 16:46:35] [info] LRF_ERR001 -> 14
[2025-10-12 16:46:35] [info] DB_WARN002 -> 159
[2025-10-12 16:46:35] [info] DB_WARN003 -> 161

```

```

[2025-10-12 16:46:35] [info] DRC_ERR9 -> 89
[2025-10-12 16:46:35] [info] DRC_ERR -> 67
[2025-10-12 16:46:35] [info] DRC_ERR5 -> 408
[2025-10-12 16:46:35] [info] DRC_ERR4 -> 241
[2025-10-12 16:46:35] [info] DRC_ERR1 -> 301
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Rule Violation Summary
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:46:35] [info] |-----|-----|-----|
[2025-10-12 16:46:35] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:46:35] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:46:35] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:46:35] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:46:35] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:46:35] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:46:35] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:46:35] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:46:35] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:46:35] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:46:35] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:46:35] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:46:35] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:46:35] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:46:35] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Performance Metrics
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] | Stage | Time Taken |
[2025-10-12 16:46:35] [info] |-----|-----|
[2025-10-12 16:46:35] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:46:35] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:46:35] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Summary
[2025-10-12 16:46:35] [info] -----
[2025-10-12 16:46:35] [info] Total Errors: 1120
[2025-10-12 16:46:35] [info] Total Warnings: 321
[2025-10-12 16:46:35] [info] DRC Status: FAILED

```

lvr_summary_382.rpt

```

[2025-10-12 16:47:32] [info] =====
[2025-10-12 16:47:32] [info] Design Rule Check Report
[2025-10-12 16:47:32] [info] =====

[2025-10-12 16:47:32] [info] Report Generated: 2025-10-12
[2025-10-12 16:47:32] [info] Run Start Time: 04:46:41 PM
[2025-10-12 16:47:32] [info] Run End Time: 04:47:32 PM
[2025-10-12 16:47:32] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:47:32] [info] Tool Version: LVR_2025.1
[2025-10-12 16:47:32] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:47:32] [info] User Name: ubuntu
[2025-10-12 16:47:32] [info] Cores Used: 8
[2025-10-12 16:47:32] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Top Cell Information
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Layout Top Cell:      TOP
[2025-10-12 16:47:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:47:32] [info] Database Format:      GDSII
[2025-10-12 16:47:32] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Type of Errors | Count
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] DB_WARN -> 1
[2025-10-12 16:47:32] [info] LRF_ERR001 -> 14
[2025-10-12 16:47:32] [info] DB_WARN002 -> 159
[2025-10-12 16:47:32] [info] DB_WARN003 -> 161
[2025-10-12 16:47:32] [info] DRC_ERR9 -> 89
[2025-10-12 16:47:32] [info] DRC_ERR -> 67
[2025-10-12 16:47:32] [info] DRC_ERR5 -> 408
[2025-10-12 16:47:32] [info] DRC_ERR4 -> 241
[2025-10-12 16:47:32] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Rule Violation Summary
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:47:32] [info] |-----|-----|-----|
[2025-10-12 16:47:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:47:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:47:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:47:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:47:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:47:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:47:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:47:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:47:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:47:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:47:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:47:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:47:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:47:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:47:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Performance Metrics
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] | Stage | Time Taken |
[2025-10-12 16:47:32] [info] |-----|-----|
[2025-10-12 16:47:32] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:47:32] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:47:32] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Summary
[2025-10-12 16:47:32] [info] -----
[2025-10-12 16:47:32] [info] Total Errors:      1120
[2025-10-12 16:47:32] [info] Total Warnings:    321
[2025-10-12 16:47:32] [info] DRC Status:        FAILED
```

lvr_summary_383.rpt

```
[2025-10-12 16:48:29] [info] =====
[2025-10-12 16:48:29] [info]                      Design Rule Check Report
[2025-10-12 16:48:29] [info] =====

[2025-10-12 16:48:29] [info] Report Generated:      2025-10-12
[2025-10-12 16:48:29] [info] Run Start Time:       04:47:39 PM
[2025-10-12 16:48:29] [info] Run End Time:         04:48:29 PM
[2025-10-12 16:48:29] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 16:48:29] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:48:29] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:48:29] [info] User Name:            ubuntu
[2025-10-12 16:48:29] [info] Cores Used:           8
[2025-10-12 16:48:29] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info]                      Top Cell Information
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info] Layout Top Cell:      TOP
[2025-10-12 16:48:29] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:48:29] [info] Database Format:        GDSII
[2025-10-12 16:48:29] [info] Verification Mode:     Full Check

[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info]                      Type of Errors | Count
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info] DB_WARN -> 1
[2025-10-12 16:48:29] [info] LRF_ERR001 -> 14
[2025-10-12 16:48:29] [info] DB_WARN002 -> 159
[2025-10-12 16:48:29] [info] DB_WARN003 -> 161
[2025-10-12 16:48:29] [info] DRC_ERR9 -> 89
[2025-10-12 16:48:29] [info] DRC_ERR -> 67
[2025-10-12 16:48:29] [info] DRC_ERR5 -> 408
[2025-10-12 16:48:29] [info] DRC_ERR4 -> 241
[2025-10-12 16:48:29] [info] DRC_ERR1 -> 301
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info]                      Rule Violation Summary
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:48:29] [info] |-----|-----|-----|
[2025-10-12 16:48:29] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:48:29] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:48:29] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:48:29] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:48:29] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:48:29] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:48:29] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:48:29] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:48:29] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:48:29] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:48:29] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:48:29] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:48:29] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:48:29] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:48:29] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:48:29] [info]
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info]                      Performance Metrics
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:48:29] [info] |-----|-----|
[2025-10-12 16:48:29] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:48:29] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 16:48:29] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info]                Summary
[2025-10-12 16:48:29] [info] -----
[2025-10-12 16:48:29] [info] Total Errors:    1120
[2025-10-12 16:48:29] [info] Total Warnings:    321
[2025-10-12 16:48:29] [info] DRC Status:      FAILED
```

lvr_summary_384.rpt

```
[2025-10-12 16:49:27] [info] =====
[2025-10-12 16:49:27] [info]                Design Rule Check Report
[2025-10-12 16:49:27] [info] =====

[2025-10-12 16:49:27] [info] Report Generated:    2025-10-12
[2025-10-12 16:49:27] [info] Run Start Time:      04:48:36 PM
[2025-10-12 16:49:27] [info] Run End Time:        04:49:27 PM
[2025-10-12 16:49:27] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 16:49:27] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:49:27] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:49:27] [info] User Name:           ubuntu
[2025-10-12 16:49:27] [info] Cores Used:          8
[2025-10-12 16:49:27] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info]                Top Cell Information
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] Layout Top Cell:     TOP
[2025-10-12 16:49:27] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:49:27] [info] Database Format:       GDSII
[2025-10-12 16:49:27] [info] Verification Mode:    Full Check

[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info]                Type of Errors | Count
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] DB_WARN -> 1
[2025-10-12 16:49:27] [info] LRF_ERR001 -> 14
[2025-10-12 16:49:27] [info] DB_WARN002 -> 159
[2025-10-12 16:49:27] [info] DB_WARN003 -> 161
[2025-10-12 16:49:27] [info] DRC_ERR9 -> 89
[2025-10-12 16:49:27] [info] DRC_ERR -> 67
[2025-10-12 16:49:27] [info] DRC_ERR5 -> 408
[2025-10-12 16:49:27] [info] DRC_ERR4 -> 241
[2025-10-12 16:49:27] [info] DRC_ERR1 -> 301
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info]                Rule Violation Summary
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:49:27] [info] |-----|-----|-----|
[2025-10-12 16:49:27] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:49:27] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:49:27] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:49:27] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:49:27] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:49:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:49:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:49:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:49:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:49:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:49:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:49:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:49:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:49:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:49:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:49:27] [info]
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] Performance Metrics
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] | Stage | Time Taken |
[2025-10-12 16:49:27] [info] |-----|-----|
[2025-10-12 16:49:27] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:49:27] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:49:27] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] Summary
[2025-10-12 16:49:27] [info] -----
[2025-10-12 16:49:27] [info] Total Errors: 1120
[2025-10-12 16:49:27] [info] Total Warnings: 321
[2025-10-12 16:49:27] [info] DRC Status: FAILED
```

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```
[2025-10-12 16:50:24] [info] =====
[2025-10-12 16:50:24] [info] Design Rule Check Report
[2025-10-12 16:50:24] [info] =====

[2025-10-12 16:50:24] [info] Report Generated: 2025-10-12
[2025-10-12 16:50:24] [info] Run Start Time: 04:49:33 PM
[2025-10-12 16:50:24] [info] Run End Time: 04:50:24 PM
[2025-10-12 16:50:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:50:24] [info] Tool Version: LVR_2025.1
[2025-10-12 16:50:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:50:24] [info] User Name: ubuntu
[2025-10-12 16:50:24] [info] Cores Used: 8
[2025-10-12 16:50:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Top Cell Information
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Layout Top Cell: TOP
[2025-10-12 16:50:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:50:24] [info] Database Format: GDSII
[2025-10-12 16:50:24] [info] Verification Mode: Full Check

[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Type of Errors | Count
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] DB_WARN -> 1
[2025-10-12 16:50:24] [info] LRF_ERR001 -> 14
[2025-10-12 16:50:24] [info] DB_WARN002 -> 159
[2025-10-12 16:50:24] [info] DB_WARN003 -> 161
```



```

[2025-10-12 16:50:24] [info] DRC_ERR9 -> 89
[2025-10-12 16:50:24] [info] DRC_ERR -> 67
[2025-10-12 16:50:24] [info] DRC_ERR5 -> 408
[2025-10-12 16:50:24] [info] DRC_ERR4 -> 241
[2025-10-12 16:50:24] [info] DRC_ERR1 -> 301
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Rule Violation Summary
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:50:24] [info] |-----|-----|-----|
[2025-10-12 16:50:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:50:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:50:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:50:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:50:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:50:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:50:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:50:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:50:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:50:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:50:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:50:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:50:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:50:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:50:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Performance Metrics
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] | Stage | Time Taken |
[2025-10-12 16:50:24] [info] |-----|-----|
[2025-10-12 16:50:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:50:24] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:50:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Summary
[2025-10-12 16:50:24] [info] -----
[2025-10-12 16:50:24] [info] Total Errors: 1120
[2025-10-12 16:50:24] [info] Total Warnings: 321
[2025-10-12 16:50:24] [info] DRC Status: FAILED

```

lvr_summary_386.rpt

```

[2025-10-12 16:51:21] [info] =====
[2025-10-12 16:51:21] [info] Design Rule Check Report
[2025-10-12 16:51:21] [info] =====

[2025-10-12 16:51:21] [info] Report Generated: 2025-10-12
[2025-10-12 16:51:21] [info] Run Start Time: 04:50:30 PM
[2025-10-12 16:51:21] [info] Run End Time: 04:51:21 PM
[2025-10-12 16:51:21] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:51:21] [info] Tool Version: LVR_2025.1
[2025-10-12 16:51:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:51:21] [info] User Name: ubuntu
[2025-10-12 16:51:21] [info] Cores Used: 8
[2025-10-12 16:51:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Top Cell Information
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Layout Top Cell:      TOP
[2025-10-12 16:51:21] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:51:21] [info] Database Format:       GDSII
[2025-10-12 16:51:21] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Type of Errors | Count
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] DB_WARN -> 1
[2025-10-12 16:51:21] [info] LRF_ERR001 -> 14
[2025-10-12 16:51:21] [info] DB_WARN002 -> 159
[2025-10-12 16:51:21] [info] DB_WARN003 -> 161
[2025-10-12 16:51:21] [info] DRC_ERR9 -> 89
[2025-10-12 16:51:21] [info] DRC_ERR -> 67
[2025-10-12 16:51:21] [info] DRC_ERR5 -> 408
[2025-10-12 16:51:21] [info] DRC_ERR4 -> 241
[2025-10-12 16:51:21] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Rule Violation Summary
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:51:21] [info] |-----|-----|-----|
[2025-10-12 16:51:21] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:51:21] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:51:21] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:51:21] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:51:21] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:51:21] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:51:21] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:51:21] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:51:21] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:51:21] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:51:21] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:51:21] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:51:21] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:51:21] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:51:21] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 16:51:21] [info] |
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Performance Metrics
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] | Stage          | Time Taken |
[2025-10-12 16:51:21] [info] |-----|-----|
[2025-10-12 16:51:21] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:51:21] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 16:51:21] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Summary
```

```
[2025-10-12 16:51:21] [info] -----
[2025-10-12 16:51:21] [info] Total Errors:      1120
[2025-10-12 16:51:21] [info] Total Warnings:    321
[2025-10-12 16:51:21] [info] DRC Status:        FAILED
```

lvr_summary_387.rpt

```
[2025-10-12 16:52:18] [info] =====
[2025-10-12 16:52:18] [info] Design Rule Check Report
[2025-10-12 16:52:18] [info] =====

[2025-10-12 16:52:18] [info] Report Generated: 2025-10-12
[2025-10-12 16:52:18] [info] Run Start Time: 04:51:28 PM
[2025-10-12 16:52:18] [info] Run End Time: 04:52:18 PM
[2025-10-12 16:52:18] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:52:18] [info] Tool Version: LVR_2025.1
[2025-10-12 16:52:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:52:18] [info] User Name: ubuntu
[2025-10-12 16:52:18] [info] Cores Used: 8
[2025-10-12 16:52:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] Top Cell Information
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] Layout Top Cell: TOP
[2025-10-12 16:52:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:52:18] [info] Database Format: GDSII
[2025-10-12 16:52:18] [info] Verification Mode: Full Check

[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] Type of Errors | Count
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] DB_WARN -> 1
[2025-10-12 16:52:18] [info] LRF_ERR001 -> 14
[2025-10-12 16:52:18] [info] DB_WARN002 -> 159
[2025-10-12 16:52:18] [info] DB_WARN003 -> 161
[2025-10-12 16:52:18] [info] DRC_ERR9 -> 89
[2025-10-12 16:52:18] [info] DRC_ERR -> 67
[2025-10-12 16:52:18] [info] DRC_ERR5 -> 408
[2025-10-12 16:52:18] [info] DRC_ERR4 -> 241
[2025-10-12 16:52:18] [info] DRC_ERR1 -> 301
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] Rule Violation Summary
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:52:18] [info] |-----|-----|-----|
[2025-10-12 16:52:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:52:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:52:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:52:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:52:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:52:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:52:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:52:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:52:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:52:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:52:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:52:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:52:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:52:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:52:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:52:18] [info]
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] Performance Metrics
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:52:18] [info] |-----|-----|
[2025-10-12 16:52:18] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:52:18] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 16:52:18] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info]                Summary
[2025-10-12 16:52:18] [info] -----
[2025-10-12 16:52:18] [info] Total Errors:    1120
[2025-10-12 16:52:18] [info] Total Warnings:    321
[2025-10-12 16:52:18] [info] DRC Status:      FAILED
```

lvr_summary_388.rpt

```
[2025-10-12 16:53:14] [info] =====
[2025-10-12 16:53:14] [info]                Design Rule Check Report
[2025-10-12 16:53:14] [info] =====

[2025-10-12 16:53:14] [info] Report Generated:    2025-10-12
[2025-10-12 16:53:14] [info] Run Start Time:      04:52:24 PM
[2025-10-12 16:53:14] [info] Run End Time:        04:53:14 PM
[2025-10-12 16:53:14] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 16:53:14] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:53:14] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:53:14] [info] User Name:           ubuntu
[2025-10-12 16:53:14] [info] Cores Used:          8
[2025-10-12 16:53:14] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info]                Top Cell Information
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] Layout Top Cell:     TOP
[2025-10-12 16:53:14] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:53:14] [info] Database Format:       GDSII
[2025-10-12 16:53:14] [info] Verification Mode:    Full Check

[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info]                Type of Errors | Count
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] DB_WARN -> 1
[2025-10-12 16:53:14] [info] LRF_ERR001 -> 14
[2025-10-12 16:53:14] [info] DB_WARN002 -> 159
[2025-10-12 16:53:14] [info] DB_WARN003 -> 161
[2025-10-12 16:53:14] [info] DRC_ERR9 -> 89
[2025-10-12 16:53:14] [info] DRC_ERR -> 67
[2025-10-12 16:53:14] [info] DRC_ERR5 -> 408
[2025-10-12 16:53:14] [info] DRC_ERR4 -> 241
[2025-10-12 16:53:14] [info] DRC_ERR1 -> 301
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info]                Rule Violation Summary
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:53:14] [info] |-----|-----|-----|
[2025-10-12 16:53:14] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:53:14] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:53:14] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:53:14] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:53:14] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:53:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:53:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:53:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:53:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:53:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:53:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:53:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:53:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:53:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:53:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:53:14] [info]
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] Performance Metrics
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] | Stage | Time Taken |
[2025-10-12 16:53:14] [info] |-----|-----|
[2025-10-12 16:53:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:53:14] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:53:14] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] Summary
[2025-10-12 16:53:14] [info] -----
[2025-10-12 16:53:14] [info] Total Errors: 1120
[2025-10-12 16:53:14] [info] Total Warnings: 321
[2025-10-12 16:53:14] [info] DRC Status: FAILED
```

lvr_summary_389.rpt

```
[2025-10-12 16:54:13] [info] =====
[2025-10-12 16:54:13] [info] Design Rule Check Report
[2025-10-12 16:54:13] [info] =====

[2025-10-12 16:54:13] [info] Report Generated: 2025-10-12
[2025-10-12 16:54:13] [info] Run Start Time: 04:53:21 PM
[2025-10-12 16:54:13] [info] Run End Time: 04:54:13 PM
[2025-10-12 16:54:13] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 16:54:13] [info] Tool Version: LVR_2025.1
[2025-10-12 16:54:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:54:13] [info] User Name: ubuntu
[2025-10-12 16:54:13] [info] Cores Used: 8
[2025-10-12 16:54:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Top Cell Information
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Layout Top Cell: TOP
[2025-10-12 16:54:13] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:54:13] [info] Database Format: GDSII
[2025-10-12 16:54:13] [info] Verification Mode: Full Check

[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Type of Errors | Count
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] DB_WARN -> 1
[2025-10-12 16:54:13] [info] LRF_ERR001 -> 14
[2025-10-12 16:54:13] [info] DB_WARN002 -> 159
[2025-10-12 16:54:13] [info] DB_WARN003 -> 161
```

```
[2025-10-12 16:54:13] [info] DRC_ERR9 -> 89
[2025-10-12 16:54:13] [info] DRC_ERR -> 67
[2025-10-12 16:54:13] [info] DRC_ERR5 -> 408
[2025-10-12 16:54:13] [info] DRC_ERR4 -> 241
[2025-10-12 16:54:13] [info] DRC_ERR1 -> 301
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Rule Violation Summary
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:54:13] [info] |-----|-----|-----|
[2025-10-12 16:54:13] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:54:13] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:54:13] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:54:13] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:54:13] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:54:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:54:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:54:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:54:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:54:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:54:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:54:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:54:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:54:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:54:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Performance Metrics
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] | Stage | Time Taken |
[2025-10-12 16:54:13] [info] |-----|-----|
[2025-10-12 16:54:13] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:54:13] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 16:54:13] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Summary
[2025-10-12 16:54:13] [info] -----
[2025-10-12 16:54:13] [info] Total Errors: 1120
[2025-10-12 16:54:13] [info] Total Warnings: 321
[2025-10-12 16:54:13] [info] DRC Status: FAILED
```

lvr_summary_39.rpt

```
[2025-10-12 11:18:23] [info] =====
[2025-10-12 11:18:23] [info] Design Rule Check Report
[2025-10-12 11:18:23] [info] =====

[2025-10-12 11:18:23] [info] Report Generated: 2025-10-12
[2025-10-12 11:18:23] [info] Run Start Time: 11:17:31 AM
[2025-10-12 11:18:23] [info] Run End Time: 11:18:23 AM
[2025-10-12 11:18:23] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:18:23] [info] Tool Version: LVR_2025.1
[2025-10-12 11:18:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:18:23] [info] User Name: ubuntu
[2025-10-12 11:18:23] [info] Cores Used: 8
[2025-10-12 11:18:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Top Cell Information
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Layout Top Cell:      TOP
[2025-10-12 11:18:23] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:18:23] [info] Database Format:       GDSII
[2025-10-12 11:18:23] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Type of Errors | Count
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] DB_WARN -> 1
[2025-10-12 11:18:23] [info] LRF_ERR001 -> 14
[2025-10-12 11:18:23] [info] DB_WARN002 -> 159
[2025-10-12 11:18:23] [info] DB_WARN003 -> 161
[2025-10-12 11:18:23] [info] DRC_ERR9 -> 89
[2025-10-12 11:18:23] [info] DRC_ERR -> 67
[2025-10-12 11:18:23] [info] DRC_ERR5 -> 408
[2025-10-12 11:18:23] [info] DRC_ERR4 -> 241
[2025-10-12 11:18:23] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Rule Violation Summary
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:18:23] [info] |-----|-----|-----|
[2025-10-12 11:18:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:18:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:18:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:18:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:18:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:18:23] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:18:23] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:18:23] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:18:23] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:18:23] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:18:23] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:18:23] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:18:23] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:18:23] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:18:23] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:18:23] [info] |
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Performance Metrics
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] | Stage          | Time Taken |
[2025-10-12 11:18:23] [info] |-----|-----|
[2025-10-12 11:18:23] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:18:23] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:18:23] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Summary
```

```
[2025-10-12 11:18:23] [info] -----
[2025-10-12 11:18:23] [info] Total Errors:      1120
[2025-10-12 11:18:23] [info] Total Warnings:    321
[2025-10-12 11:18:23] [info] DRC Status:        FAILED
```

lvr_summary_390.rpt

```
[2025-10-12 16:55:09] [info] =====
[2025-10-12 16:55:09] [info]                      Design Rule Check Report
[2025-10-12 16:55:09] [info] =====

[2025-10-12 16:55:09] [info] Report Generated:      2025-10-12
[2025-10-12 16:55:09] [info] Run Start Time:       04:54:17 PM
[2025-10-12 16:55:09] [info] Run End Time:         04:55:09 PM
[2025-10-12 16:55:09] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 16:55:09] [info] Tool Version:         LVR_2025.1
[2025-10-12 16:55:09] [info] Host Name:            ip-172-31-37-173
[2025-10-12 16:55:09] [info] User Name:            ubuntu
[2025-10-12 16:55:09] [info] Cores Used:           8
[2025-10-12 16:55:09] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info]                      Top Cell Information
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info] Layout Top Cell:      TOP
[2025-10-12 16:55:09] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 16:55:09] [info] Database Format:        GDSII
[2025-10-12 16:55:09] [info] Verification Mode:     Full Check

[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info]                      Type of Errors | Count
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info] DB_WARN -> 1
[2025-10-12 16:55:09] [info] LRF_ERR001 -> 14
[2025-10-12 16:55:09] [info] DB_WARN002 -> 159
[2025-10-12 16:55:09] [info] DB_WARN003 -> 161
[2025-10-12 16:55:09] [info] DRC_ERR9 -> 89
[2025-10-12 16:55:09] [info] DRC_ERR -> 67
[2025-10-12 16:55:09] [info] DRC_ERR5 -> 408
[2025-10-12 16:55:09] [info] DRC_ERR4 -> 241
[2025-10-12 16:55:09] [info] DRC_ERR1 -> 301
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info]                      Rule Violation Summary
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:55:09] [info] |-----|-----|-----|
[2025-10-12 16:55:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:55:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:55:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:55:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:55:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:55:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:55:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:55:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:55:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:55:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:55:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:55:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:55:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:55:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:55:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:55:09] [info]
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info]                      Performance Metrics
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info] | Stage | Time Taken |
```



```
[2025-10-12 16:55:09] [info] |-----|-----|
[2025-10-12 16:55:09] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:55:09] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 16:55:09] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info] Summary
[2025-10-12 16:55:09] [info] -----
[2025-10-12 16:55:09] [info] Total Errors: 1120
[2025-10-12 16:55:09] [info] Total Warnings: 321
[2025-10-12 16:55:09] [info] DRC Status: FAILED
```

lvr_summary_391.rpt

```
[2025-10-12 16:56:05] [info] =====
[2025-10-12 16:56:05] [info] Design Rule Check Report
[2025-10-12 16:56:05] [info] =====

[2025-10-12 16:56:05] [info] Report Generated: 2025-10-12
[2025-10-12 16:56:05] [info] Run Start Time: 04:55:15 PM
[2025-10-12 16:56:05] [info] Run End Time: 04:56:05 PM
[2025-10-12 16:56:05] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:56:05] [info] Tool Version: LVR_2025.1
[2025-10-12 16:56:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:56:05] [info] User Name: ubuntu
[2025-10-12 16:56:05] [info] Cores Used: 8
[2025-10-12 16:56:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Top Cell Information
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Layout Top Cell: TOP
[2025-10-12 16:56:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:56:05] [info] Database Format: GDSII
[2025-10-12 16:56:05] [info] Verification Mode: Full Check

[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Type of Errors | Count
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] DB_WARN -> 1
[2025-10-12 16:56:05] [info] LRF_ERR001 -> 14
[2025-10-12 16:56:05] [info] DB_WARN002 -> 159
[2025-10-12 16:56:05] [info] DB_WARN003 -> 161
[2025-10-12 16:56:05] [info] DRC_ERR9 -> 89
[2025-10-12 16:56:05] [info] DRC_ERR -> 67
[2025-10-12 16:56:05] [info] DRC_ERR5 -> 408
[2025-10-12 16:56:05] [info] DRC_ERR4 -> 241
[2025-10-12 16:56:05] [info] DRC_ERR1 -> 301
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Rule Violation Summary
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:56:05] [info] |-----|-----|-----|
[2025-10-12 16:56:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:56:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:56:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:56:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:56:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 16:56:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:56:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:56:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:56:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:56:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:56:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:56:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:56:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:56:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:56:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:56:05] [info]
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Performance Metrics
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] | Stage | Time Taken |
[2025-10-12 16:56:05] [info] |-----|-----|
[2025-10-12 16:56:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:56:05] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:56:05] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Summary
[2025-10-12 16:56:05] [info] -----
[2025-10-12 16:56:05] [info] Total Errors: 1120
[2025-10-12 16:56:05] [info] Total Warnings: 321
[2025-10-12 16:56:05] [info] DRC Status: FAILED
```

lvr_summary_392.rpt

```
[2025-10-12 16:57:02] [info] =====
[2025-10-12 16:57:02] [info] Design Rule Check Report
[2025-10-12 16:57:02] [info] =====

[2025-10-12 16:57:02] [info] Report Generated: 2025-10-12
[2025-10-12 16:57:02] [info] Run Start Time: 04:56:12 PM
[2025-10-12 16:57:02] [info] Run End Time: 04:57:02 PM
[2025-10-12 16:57:02] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:57:02] [info] Tool Version: LVR_2025.1
[2025-10-12 16:57:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:57:02] [info] User Name: ubuntu
[2025-10-12 16:57:02] [info] Cores Used: 8
[2025-10-12 16:57:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Top Cell Information
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Layout Top Cell: TOP
[2025-10-12 16:57:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 16:57:02] [info] Database Format: GDSII
[2025-10-12 16:57:02] [info] Verification Mode: Full Check

[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Type of Errors | Count
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] DB_WARN -> 1
[2025-10-12 16:57:02] [info] LRF_ERR001 -> 14
[2025-10-12 16:57:02] [info] DB_WARN002 -> 159
[2025-10-12 16:57:02] [info] DB_WARN003 -> 161
```

```
[2025-10-12 16:57:02] [info] DRC_ERR9 -> 89
[2025-10-12 16:57:02] [info] DRC_ERR -> 67
[2025-10-12 16:57:02] [info] DRC_ERR5 -> 408
[2025-10-12 16:57:02] [info] DRC_ERR4 -> 241
[2025-10-12 16:57:02] [info] DRC_ERR1 -> 301
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Rule Violation Summary
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:57:02] [info] |-----|-----|-----|
[2025-10-12 16:57:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:57:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:57:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:57:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:57:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:57:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:57:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:57:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:57:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:57:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:57:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:57:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:57:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:57:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:57:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Performance Metrics
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] | Stage | Time Taken |
[2025-10-12 16:57:02] [info] |-----|-----|
[2025-10-12 16:57:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:57:02] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 16:57:02] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Summary
[2025-10-12 16:57:02] [info] -----
[2025-10-12 16:57:02] [info] Total Errors: 1120
[2025-10-12 16:57:02] [info] Total Warnings: 321
[2025-10-12 16:57:02] [info] DRC Status: FAILED
```

lvr_summary_393.rpt

```
[2025-10-12 16:58:00] [info] =====
[2025-10-12 16:58:00] [info] Design Rule Check Report
[2025-10-12 16:58:00] [info] =====

[2025-10-12 16:58:00] [info] Report Generated: 2025-10-12
[2025-10-12 16:58:00] [info] Run Start Time: 04:57:09 PM
[2025-10-12 16:58:00] [info] Run End Time: 04:58:00 PM
[2025-10-12 16:58:00] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 16:58:00] [info] Tool Version: LVR_2025.1
[2025-10-12 16:58:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 16:58:00] [info] User Name: ubuntu
[2025-10-12 16:58:00] [info] Cores Used: 8
[2025-10-12 16:58:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Top Cell Information
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Layout Top Cell:      TOP
[2025-10-12 16:58:00] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:58:00] [info] Database Format:      GDSII
[2025-10-12 16:58:00] [info] Verification Mode:    Full Check
```

```
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Type of Errors | Count
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] DB_WARN -> 1
[2025-10-12 16:58:00] [info] LRF_ERR001 -> 14
[2025-10-12 16:58:00] [info] DB_WARN002 -> 159
[2025-10-12 16:58:00] [info] DB_WARN003 -> 161
[2025-10-12 16:58:00] [info] DRC_ERR9 -> 89
[2025-10-12 16:58:00] [info] DRC_ERR -> 67
[2025-10-12 16:58:00] [info] DRC_ERR5 -> 408
[2025-10-12 16:58:00] [info] DRC_ERR4 -> 241
[2025-10-12 16:58:00] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Rule Violation Summary
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 16:58:00] [info] |-----|-----|-----|
[2025-10-12 16:58:00] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:58:00] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:58:00] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:58:00] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:58:00] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 16:58:00] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 16:58:00] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 16:58:00] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 16:58:00] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 16:58:00] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 16:58:00] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 16:58:00] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 16:58:00] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 16:58:00] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 16:58:00] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Performance Metrics
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] | Stage          | Time Taken |
[2025-10-12 16:58:00] [info] |-----|-----|
[2025-10-12 16:58:00] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 16:58:00] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 16:58:00] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Summary
[2025-10-12 16:58:00] [info] -----
[2025-10-12 16:58:00] [info] Total Errors:      1120
[2025-10-12 16:58:00] [info] Total Warnings:    321
[2025-10-12 16:58:00] [info] DRC Status:        FAILED
```

lvr_summary_394.rpt

```
[2025-10-12 16:58:57] [info] =====
[2025-10-12 16:58:57] [info]                      Design Rule Check Report
[2025-10-12 16:58:57] [info] =====

[2025-10-12 16:58:57] [info] Report Generated:    2025-10-12
[2025-10-12 16:58:57] [info] Run Start Time:      04:58:06 PM
[2025-10-12 16:58:57] [info] Run End Time:        04:58:57 PM
[2025-10-12 16:58:57] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:58:57] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:58:57] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:58:57] [info] User Name:           ubuntu
[2025-10-12 16:58:57] [info] Cores Used:          8
[2025-10-12 16:58:57] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info]                      Top Cell Information
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info] Layout Top Cell:     TOP
[2025-10-12 16:58:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:58:57] [info] Database Format:      GDSII
[2025-10-12 16:58:57] [info] Verification Mode:    Full Check

[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info]                      Type of Errors | Count
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info] DB_WARN -> 1
[2025-10-12 16:58:57] [info] LRF_ERR001 -> 14
[2025-10-12 16:58:57] [info] DB_WARN002 -> 159
[2025-10-12 16:58:57] [info] DB_WARN003 -> 161
[2025-10-12 16:58:57] [info] DRC_ERR9 -> 89
[2025-10-12 16:58:57] [info] DRC_ERR -> 67
[2025-10-12 16:58:57] [info] DRC_ERR5 -> 408
[2025-10-12 16:58:57] [info] DRC_ERR4 -> 241
[2025-10-12 16:58:57] [info] DRC_ERR1 -> 301
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info]                      Rule Violation Summary
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info] | Rule ID | Description | Error Count |
[2025-10-12 16:58:57] [info] |-----|-----|-----|
[2025-10-12 16:58:57] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 16:58:57] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 16:58:57] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 16:58:57] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 16:58:57] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 16:58:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 16:58:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:58:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:58:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:58:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:58:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:58:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:58:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:58:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:58:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:58:57] [info]
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info]                      Performance Metrics
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info] | Stage | Time Taken |
```

```
[2025-10-12 16:58:57] [info] |-----|-----|
[2025-10-12 16:58:57] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 16:58:57] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 16:58:57] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info]                Summary
[2025-10-12 16:58:57] [info] -----
[2025-10-12 16:58:57] [info] Total Errors:    1120
[2025-10-12 16:58:57] [info] Total Warnings:    321
[2025-10-12 16:58:57] [info] DRC Status:      FAILED
```

lvr_summary_395.rpt

```
[2025-10-12 16:59:56] [info] =====
[2025-10-12 16:59:56] [info]                Design Rule Check Report
[2025-10-12 16:59:56] [info] =====

[2025-10-12 16:59:56] [info] Report Generated:    2025-10-12
[2025-10-12 16:59:56] [info] Run Start Time:      04:59:04 PM
[2025-10-12 16:59:56] [info] Run End Time:        04:59:56 PM
[2025-10-12 16:59:56] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 16:59:56] [info] Tool Version:        LVR_2025.1
[2025-10-12 16:59:56] [info] Host Name:           ip-172-31-37-173
[2025-10-12 16:59:56] [info] User Name:           ubuntu
[2025-10-12 16:59:56] [info] Cores Used:          8
[2025-10-12 16:59:56] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info]                Top Cell Information
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] Layout Top Cell:     TOP
[2025-10-12 16:59:56] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 16:59:56] [info] Database Format:       GDSII
[2025-10-12 16:59:56] [info] Verification Mode:    Full Check

[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info]                Type of Errors | Count
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] DB_WARN -> 1
[2025-10-12 16:59:56] [info] LRF_ERR001 -> 14
[2025-10-12 16:59:56] [info] DB_WARN002 -> 159
[2025-10-12 16:59:56] [info] DB_WARN003 -> 161
[2025-10-12 16:59:56] [info] DRC_ERR9 -> 89
[2025-10-12 16:59:56] [info] DRC_ERR -> 67
[2025-10-12 16:59:56] [info] DRC_ERR5 -> 408
[2025-10-12 16:59:56] [info] DRC_ERR4 -> 241
[2025-10-12 16:59:56] [info] DRC_ERR1 -> 301
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info]                Rule Violation Summary
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 16:59:56] [info] |-----|-----|-----|
[2025-10-12 16:59:56] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 16:59:56] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 16:59:56] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 16:59:56] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 16:59:56] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 16:59:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 16:59:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 16:59:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 16:59:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 16:59:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 16:59:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 16:59:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 16:59:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 16:59:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 16:59:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 16:59:56] [info]
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] Performance Metrics
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] | Stage | Time Taken |
[2025-10-12 16:59:56] [info] |-----|-----|
[2025-10-12 16:59:56] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 16:59:56] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 16:59:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] Summary
[2025-10-12 16:59:56] [info] -----
[2025-10-12 16:59:56] [info] Total Errors: 1120
[2025-10-12 16:59:56] [info] Total Warnings: 321
[2025-10-12 16:59:56] [info] DRC Status: FAILED
```

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```
[2025-10-12 17:00:52] [info] =====
[2025-10-12 17:00:52] [info] Design Rule Check Report
[2025-10-12 17:00:52] [info] =====

[2025-10-12 17:00:52] [info] Report Generated: 2025-10-12
[2025-10-12 17:00:52] [info] Run Start Time: 05:00:00 PM
[2025-10-12 17:00:52] [info] Run End Time: 05:00:52 PM
[2025-10-12 17:00:52] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:00:52] [info] Tool Version: LVR_2025.1
[2025-10-12 17:00:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:00:52] [info] User Name: ubuntu
[2025-10-12 17:00:52] [info] Cores Used: 8
[2025-10-12 17:00:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Top Cell Information
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Layout Top Cell: TOP
[2025-10-12 17:00:52] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:00:52] [info] Database Format: GDSII
[2025-10-12 17:00:52] [info] Verification Mode: Full Check

[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Type of Errors | Count
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] DB_WARN -> 1
[2025-10-12 17:00:52] [info] LRF_ERR001 -> 14
[2025-10-12 17:00:52] [info] DB_WARN002 -> 159
[2025-10-12 17:00:52] [info] DB_WARN003 -> 161
```

```

[2025-10-12 17:00:52] [info] DRC_ERR9 -> 89
[2025-10-12 17:00:52] [info] DRC_ERR -> 67
[2025-10-12 17:00:52] [info] DRC_ERR5 -> 408
[2025-10-12 17:00:52] [info] DRC_ERR4 -> 241
[2025-10-12 17:00:52] [info] DRC_ERR1 -> 301
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Rule Violation Summary
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:00:52] [info] |-----|-----|-----|
[2025-10-12 17:00:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:00:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:00:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:00:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:00:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:00:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:00:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:00:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:00:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:00:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:00:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:00:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:00:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:00:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:00:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Performance Metrics
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] | Stage          | Time Taken |
[2025-10-12 17:00:52] [info] |-----|-----|
[2025-10-12 17:00:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:00:52] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:00:52] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Summary
[2025-10-12 17:00:52] [info] -----
[2025-10-12 17:00:52] [info] Total Errors: 1120
[2025-10-12 17:00:52] [info] Total Warnings: 321
[2025-10-12 17:00:52] [info] DRC Status: FAILED

```

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```

[2025-10-12 17:01:51] [info] =====
[2025-10-12 17:01:51] [info] Design Rule Check Report
[2025-10-12 17:01:51] [info] =====

[2025-10-12 17:01:51] [info] Report Generated: 2025-10-12
[2025-10-12 17:01:51] [info] Run Start Time: 05:00:58 PM
[2025-10-12 17:01:51] [info] Run End Time: 05:01:51 PM
[2025-10-12 17:01:51] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:01:51] [info] Tool Version: LVR_2025.1
[2025-10-12 17:01:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:01:51] [info] User Name: ubuntu
[2025-10-12 17:01:51] [info] Cores Used: 8
[2025-10-12 17:01:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Top Cell Information
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Layout Top Cell:      TOP
[2025-10-12 17:01:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:01:51] [info] Database Format:      GDSII
[2025-10-12 17:01:51] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Type of Errors | Count
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] DB_WARN -> 1
[2025-10-12 17:01:51] [info] LRF_ERR001 -> 14
[2025-10-12 17:01:51] [info] DB_WARN002 -> 159
[2025-10-12 17:01:51] [info] DB_WARN003 -> 161
[2025-10-12 17:01:51] [info] DRC_ERR9 -> 89
[2025-10-12 17:01:51] [info] DRC_ERR -> 67
[2025-10-12 17:01:51] [info] DRC_ERR5 -> 408
[2025-10-12 17:01:51] [info] DRC_ERR4 -> 241
[2025-10-12 17:01:51] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Rule Violation Summary
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:01:51] [info] |-----|-----|-----|
[2025-10-12 17:01:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:01:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:01:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:01:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:01:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:01:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:01:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:01:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:01:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:01:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:01:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:01:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:01:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:01:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:01:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:01:51] [info] |
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Performance Metrics
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] | Stage          | Time Taken |
[2025-10-12 17:01:51] [info] |-----|-----|
[2025-10-12 17:01:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:01:51] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:01:51] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Summary
```

```
[2025-10-12 17:01:51] [info] -----
[2025-10-12 17:01:51] [info] Total Errors:      1120
[2025-10-12 17:01:51] [info] Total Warnings:    321
[2025-10-12 17:01:51] [info] DRC Status:        FAILED
```

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```
[2025-10-12 17:02:45] [info] =====
[2025-10-12 17:02:45] [info]                      Design Rule Check Report
[2025-10-12 17:02:45] [info] =====

[2025-10-12 17:02:45] [info] Report Generated:    2025-10-12
[2025-10-12 17:02:45] [info] Run Start Time:      05:01:54 PM
[2025-10-12 17:02:45] [info] Run End Time:        05:02:45 PM
[2025-10-12 17:02:45] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:02:45] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:02:45] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:02:45] [info] User Name:           ubuntu
[2025-10-12 17:02:45] [info] Cores Used:          8
[2025-10-12 17:02:45] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info]                      Top Cell Information
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info] Layout Top Cell:     TOP
[2025-10-12 17:02:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:02:45] [info] Database Format:      GDSII
[2025-10-12 17:02:45] [info] Verification Mode:    Full Check

[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info]                      Type of Errors | Count
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info] DB_WARN -> 1
[2025-10-12 17:02:45] [info] LRF_ERR001 -> 14
[2025-10-12 17:02:45] [info] DB_WARN002 -> 159
[2025-10-12 17:02:45] [info] DB_WARN003 -> 161
[2025-10-12 17:02:45] [info] DRC_ERR9 -> 89
[2025-10-12 17:02:45] [info] DRC_ERR -> 67
[2025-10-12 17:02:45] [info] DRC_ERR5 -> 408
[2025-10-12 17:02:45] [info] DRC_ERR4 -> 241
[2025-10-12 17:02:45] [info] DRC_ERR1 -> 301
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info]                      Rule Violation Summary
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:02:45] [info] |-----|-----|-----|
[2025-10-12 17:02:45] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:02:45] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:02:45] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:02:45] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:02:45] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:02:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:02:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:02:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:02:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:02:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:02:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:02:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:02:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:02:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:02:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:02:45] [info]
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info]                      Performance Metrics
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:02:45] [info] |-----|-----|
[2025-10-12 17:02:45] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:02:45] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:02:45] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info]                Summary
[2025-10-12 17:02:45] [info] -----
[2025-10-12 17:02:45] [info] Total Errors:    1120
[2025-10-12 17:02:45] [info] Total Warnings:    321
[2025-10-12 17:02:45] [info] DRC Status:      FAILED
```

lvr_summary_399.rpt

```
[2025-10-12 17:03:43] [info] =====
[2025-10-12 17:03:43] [info]                Design Rule Check Report
[2025-10-12 17:03:43] [info] =====

[2025-10-12 17:03:43] [info] Report Generated:    2025-10-12
[2025-10-12 17:03:43] [info] Run Start Time:      05:02:52 PM
[2025-10-12 17:03:43] [info] Run End Time:        05:03:43 PM
[2025-10-12 17:03:43] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 17:03:43] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:03:43] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:03:43] [info] User Name:           ubuntu
[2025-10-12 17:03:43] [info] Cores Used:          8
[2025-10-12 17:03:43] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info]                Top Cell Information
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] Layout Top Cell:     TOP
[2025-10-12 17:03:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:03:43] [info] Database Format:      GDSII
[2025-10-12 17:03:43] [info] Verification Mode:    Full Check

[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info]                Type of Errors | Count
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] DB_WARN -> 1
[2025-10-12 17:03:43] [info] LRF_ERR001 -> 14
[2025-10-12 17:03:43] [info] DB_WARN002 -> 159
[2025-10-12 17:03:43] [info] DB_WARN003 -> 161
[2025-10-12 17:03:43] [info] DRC_ERR9 -> 89
[2025-10-12 17:03:43] [info] DRC_ERR -> 67
[2025-10-12 17:03:43] [info] DRC_ERR5 -> 408
[2025-10-12 17:03:43] [info] DRC_ERR4 -> 241
[2025-10-12 17:03:43] [info] DRC_ERR1 -> 301
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info]                Rule Violation Summary
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:03:43] [info] |-----|-----|-----|
[2025-10-12 17:03:43] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:03:43] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:03:43] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:03:43] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:03:43] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:03:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:03:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:03:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:03:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:03:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:03:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:03:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:03:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:03:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:03:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:03:43] [info]
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] Performance Metrics
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] | Stage | Time Taken |
[2025-10-12 17:03:43] [info] |-----|-----|
[2025-10-12 17:03:43] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:03:43] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:03:43] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] Summary
[2025-10-12 17:03:43] [info] -----
[2025-10-12 17:03:43] [info] Total Errors: 1120
[2025-10-12 17:03:43] [info] Total Warnings: 321
[2025-10-12 17:03:43] [info] DRC Status: FAILED
```

lvr_summary_4.rpt

```
[2025-10-12 10:45:15] [info] =====
[2025-10-12 10:45:15] [info] Design Rule Check Report
[2025-10-12 10:45:15] [info] =====

[2025-10-12 10:45:15] [info] Report Generated: 2025-10-12
[2025-10-12 10:45:15] [info] Run Start Time: 10:44:24 AM
[2025-10-12 10:45:15] [info] Run End Time: 10:45:15 AM
[2025-10-12 10:45:15] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 10:45:15] [info] Tool Version: LVR_2025.1
[2025-10-12 10:45:15] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:45:15] [info] User Name: ubuntu
[2025-10-12 10:45:15] [info] Cores Used: 8
[2025-10-12 10:45:15] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Top Cell Information
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Layout Top Cell: TOP
[2025-10-12 10:45:15] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:45:15] [info] Database Format: GDSII
[2025-10-12 10:45:15] [info] Verification Mode: Full Check

[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Type of Errors | Count
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] DB_WARN -> 1
[2025-10-12 10:45:15] [info] LRF_ERR001 -> 14
[2025-10-12 10:45:15] [info] DB_WARN002 -> 159
[2025-10-12 10:45:15] [info] DB_WARN003 -> 161
```

```

[2025-10-12 10:45:15] [info] DRC_ERR9 -> 89
[2025-10-12 10:45:15] [info] DRC_ERR -> 67
[2025-10-12 10:45:15] [info] DRC_ERR5 -> 408
[2025-10-12 10:45:15] [info] DRC_ERR4 -> 241
[2025-10-12 10:45:15] [info] DRC_ERR1 -> 301
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Rule Violation Summary
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:45:15] [info] |-----|-----|-----|
[2025-10-12 10:45:15] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:45:15] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:45:15] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:45:15] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:45:15] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:45:15] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:45:15] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:45:15] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:45:15] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:45:15] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:45:15] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:45:15] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:45:15] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:45:15] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:45:15] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Performance Metrics
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] | Stage | Time Taken |
[2025-10-12 10:45:15] [info] |-----|-----|
[2025-10-12 10:45:15] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:45:15] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 10:45:15] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Summary
[2025-10-12 10:45:15] [info] -----
[2025-10-12 10:45:15] [info] Total Errors: 1120
[2025-10-12 10:45:15] [info] Total Warnings: 321
[2025-10-12 10:45:15] [info] DRC Status: FAILED

```

lvr_summary_40.rpt

```

[2025-10-12 11:19:20] [info] =====
[2025-10-12 11:19:20] [info] Design Rule Check Report
[2025-10-12 11:19:20] [info] =====

[2025-10-12 11:19:20] [info] Report Generated: 2025-10-12
[2025-10-12 11:19:20] [info] Run Start Time: 11:18:28 AM
[2025-10-12 11:19:20] [info] Run End Time: 11:19:20 AM
[2025-10-12 11:19:20] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:19:20] [info] Tool Version: LVR_2025.1
[2025-10-12 11:19:20] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:19:20] [info] User Name: ubuntu
[2025-10-12 11:19:20] [info] Cores Used: 8
[2025-10-12 11:19:20] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] Top Cell Information
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] Layout Top Cell:      TOP
[2025-10-12 11:19:20] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:19:20] [info] Database Format:       GDSII
[2025-10-12 11:19:20] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] Type of Errors | Count
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] DB_WARN -> 1
[2025-10-12 11:19:20] [info] LRF_ERR001 -> 14
[2025-10-12 11:19:20] [info] DB_WARN002 -> 159
[2025-10-12 11:19:20] [info] DB_WARN003 -> 161
[2025-10-12 11:19:20] [info] DRC_ERR9 -> 89
[2025-10-12 11:19:20] [info] DRC_ERR -> 67
[2025-10-12 11:19:20] [info] DRC_ERR5 -> 408
[2025-10-12 11:19:20] [info] DRC_ERR4 -> 241
[2025-10-12 11:19:20] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] Rule Violation Summary
```

```
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:19:20] [info] |-----|-----|-----|
[2025-10-12 11:19:20] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:19:20] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:19:20] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:19:20] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:19:20] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:19:20] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:19:20] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:19:20] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:19:20] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:19:20] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:19:20] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:19:20] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:19:20] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:19:20] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:19:20] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:19:20] [info] -----
```

```
[2025-10-12 11:19:20] [info] Performance Metrics
```

```
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] | Stage          | Time Taken |
[2025-10-12 11:19:20] [info] |-----|-----|
[2025-10-12 11:19:20] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:19:20] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:19:20] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:19:20] [info] -----
```

```
[2025-10-12 11:19:20] [info] Summary
```

```
[2025-10-12 11:19:20] [info] -----
[2025-10-12 11:19:20] [info] Total Errors:      1120
[2025-10-12 11:19:20] [info] Total Warnings:    321
[2025-10-12 11:19:20] [info] DRC Status:        FAILED
```

lvr_summary_400.rpt

```
[2025-10-12 17:04:41] [info] =====
[2025-10-12 17:04:41] [info] Design Rule Check Report
[2025-10-12 17:04:41] [info] =====

[2025-10-12 17:04:41] [info] Report Generated: 2025-10-12
[2025-10-12 17:04:41] [info] Run Start Time: 05:03:49 PM
[2025-10-12 17:04:41] [info] Run End Time: 05:04:41 PM
[2025-10-12 17:04:41] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:04:41] [info] Tool Version: LVR_2025.1
[2025-10-12 17:04:41] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:04:41] [info] User Name: ubuntu
[2025-10-12 17:04:41] [info] Cores Used: 8
[2025-10-12 17:04:41] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Top Cell Information
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Layout Top Cell: TOP
[2025-10-12 17:04:41] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:04:41] [info] Database Format: GDSII
[2025-10-12 17:04:41] [info] Verification Mode: Full Check

[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Type of Errors | Count
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] DB_WARN -> 1
[2025-10-12 17:04:41] [info] LRF_ERR001 -> 14
[2025-10-12 17:04:41] [info] DB_WARN002 -> 159
[2025-10-12 17:04:41] [info] DB_WARN003 -> 161
[2025-10-12 17:04:41] [info] DRC_ERR9 -> 89
[2025-10-12 17:04:41] [info] DRC_ERR -> 67
[2025-10-12 17:04:41] [info] DRC_ERR5 -> 408
[2025-10-12 17:04:41] [info] DRC_ERR4 -> 241
[2025-10-12 17:04:41] [info] DRC_ERR1 -> 301
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Rule Violation Summary
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:04:41] [info] |-----|-----|-----|
[2025-10-12 17:04:41] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:04:41] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:04:41] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:04:41] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:04:41] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:04:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:04:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:04:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:04:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:04:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:04:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:04:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:04:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:04:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:04:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:04:41] [info]
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Performance Metrics
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:04:41] [info] |-----|-----|
[2025-10-12 17:04:41] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:04:41] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 17:04:41] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Summary
[2025-10-12 17:04:41] [info] -----
[2025-10-12 17:04:41] [info] Total Errors: 1120
[2025-10-12 17:04:41] [info] Total Warnings: 321
[2025-10-12 17:04:41] [info] DRC Status: FAILED
```

lvr_summary_401.rpt

```
[2025-10-12 17:05:38] [info] =====
[2025-10-12 17:05:38] [info] Design Rule Check Report
[2025-10-12 17:05:38] [info] =====

[2025-10-12 17:05:38] [info] Report Generated: 2025-10-12
[2025-10-12 17:05:38] [info] Run Start Time: 05:04:46 PM
[2025-10-12 17:05:38] [info] Run End Time: 05:05:38 PM
[2025-10-12 17:05:38] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:05:38] [info] Tool Version: LVR_2025.1
[2025-10-12 17:05:38] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:05:38] [info] User Name: ubuntu
[2025-10-12 17:05:38] [info] Cores Used: 8
[2025-10-12 17:05:38] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Top Cell Information
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Layout Top Cell: TOP
[2025-10-12 17:05:38] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:05:38] [info] Database Format: GDSII
[2025-10-12 17:05:38] [info] Verification Mode: Full Check

[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Type of Errors | Count
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] DB_WARN -> 1
[2025-10-12 17:05:38] [info] LRF_ERR001 -> 14
[2025-10-12 17:05:38] [info] DB_WARN002 -> 159
[2025-10-12 17:05:38] [info] DB_WARN003 -> 161
[2025-10-12 17:05:38] [info] DRC_ERR9 -> 89
[2025-10-12 17:05:38] [info] DRC_ERR -> 67
[2025-10-12 17:05:38] [info] DRC_ERR5 -> 408
[2025-10-12 17:05:38] [info] DRC_ERR4 -> 241
[2025-10-12 17:05:38] [info] DRC_ERR1 -> 301
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Rule Violation Summary
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:05:38] [info] |-----|-----|-----|
[2025-10-12 17:05:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:05:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:05:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:05:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:05:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```
[2025-10-12 17:05:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:05:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:05:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:05:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:05:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:05:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:05:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:05:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:05:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:05:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:05:38] [info]
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Performance Metrics
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] | Stage | Time Taken |
[2025-10-12 17:05:38] [info] |-----|-----|
[2025-10-12 17:05:38] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:05:38] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:05:38] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Summary
[2025-10-12 17:05:38] [info] -----
[2025-10-12 17:05:38] [info] Total Errors: 1120
[2025-10-12 17:05:38] [info] Total Warnings: 321
[2025-10-12 17:05:38] [info] DRC Status: FAILED
```

lvr_summary_402.rpt

```
[2025-10-12 17:06:33] [info] =====
[2025-10-12 17:06:33] [info] Design Rule Check Report
[2025-10-12 17:06:33] [info] =====

[2025-10-12 17:06:33] [info] Report Generated: 2025-10-12
[2025-10-12 17:06:33] [info] Run Start Time: 05:05:43 PM
[2025-10-12 17:06:33] [info] Run End Time: 05:06:33 PM
[2025-10-12 17:06:33] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:06:33] [info] Tool Version: LVR_2025.1
[2025-10-12 17:06:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:06:33] [info] User Name: ubuntu
[2025-10-12 17:06:33] [info] Cores Used: 8
[2025-10-12 17:06:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Top Cell Information
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Layout Top Cell: TOP
[2025-10-12 17:06:33] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:06:33] [info] Database Format: GDSII
[2025-10-12 17:06:33] [info] Verification Mode: Full Check

[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Type of Errors | Count
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] DB_WARN -> 1
[2025-10-12 17:06:33] [info] LRF_ERR001 -> 14
[2025-10-12 17:06:33] [info] DB_WARN002 -> 159
[2025-10-12 17:06:33] [info] DB_WARN003 -> 161
```

```

[2025-10-12 17:06:33] [info] DRC_ERR9 -> 89
[2025-10-12 17:06:33] [info] DRC_ERR -> 67
[2025-10-12 17:06:33] [info] DRC_ERR5 -> 408
[2025-10-12 17:06:33] [info] DRC_ERR4 -> 241
[2025-10-12 17:06:33] [info] DRC_ERR1 -> 301
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Rule Violation Summary
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:06:33] [info] |-----|-----|-----|
[2025-10-12 17:06:33] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:06:33] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:06:33] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:06:33] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:06:33] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:06:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:06:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:06:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:06:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:06:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:06:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:06:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:06:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:06:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:06:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Performance Metrics
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] | Stage | Time Taken |
[2025-10-12 17:06:33] [info] |-----|-----|
[2025-10-12 17:06:33] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:06:33] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:06:33] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Summary
[2025-10-12 17:06:33] [info] -----
[2025-10-12 17:06:33] [info] Total Errors: 1120
[2025-10-12 17:06:33] [info] Total Warnings: 321
[2025-10-12 17:06:33] [info] DRC Status: FAILED

```

lvr_summary_403.rpt

```

[2025-10-12 17:07:31] [info] =====
[2025-10-12 17:07:31] [info] Design Rule Check Report
[2025-10-12 17:07:31] [info] =====

[2025-10-12 17:07:31] [info] Report Generated: 2025-10-12
[2025-10-12 17:07:31] [info] Run Start Time: 05:06:40 PM
[2025-10-12 17:07:31] [info] Run End Time: 05:07:31 PM
[2025-10-12 17:07:31] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:07:31] [info] Tool Version: LVR_2025.1
[2025-10-12 17:07:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:07:31] [info] User Name: ubuntu
[2025-10-12 17:07:31] [info] Cores Used: 8
[2025-10-12 17:07:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Top Cell Information
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Layout Top Cell:      TOP
[2025-10-12 17:07:31] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:07:31] [info] Database Format:      GDSII
[2025-10-12 17:07:31] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Type of Errors | Count
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] DB_WARN -> 1
[2025-10-12 17:07:31] [info] LRF_ERR001 -> 14
[2025-10-12 17:07:31] [info] DB_WARN002 -> 159
[2025-10-12 17:07:31] [info] DB_WARN003 -> 161
[2025-10-12 17:07:31] [info] DRC_ERR9 -> 89
[2025-10-12 17:07:31] [info] DRC_ERR -> 67
[2025-10-12 17:07:31] [info] DRC_ERR5 -> 408
[2025-10-12 17:07:31] [info] DRC_ERR4 -> 241
[2025-10-12 17:07:31] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Rule Violation Summary
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:07:31] [info] |-----|-----|-----|
[2025-10-12 17:07:31] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:07:31] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:07:31] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:07:31] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:07:31] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:07:31] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:07:31] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:07:31] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:07:31] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:07:31] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:07:31] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:07:31] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:07:31] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:07:31] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:07:31] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:07:31] [info] |
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Performance Metrics
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] | Stage          | Time Taken |
[2025-10-12 17:07:31] [info] |-----|-----|
[2025-10-12 17:07:31] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:07:31] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:07:31] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Summary
```

```
[2025-10-12 17:07:31] [info] -----
[2025-10-12 17:07:31] [info] Total Errors:      1120
[2025-10-12 17:07:31] [info] Total Warnings:    321
[2025-10-12 17:07:31] [info] DRC Status:        FAILED
```

lvr_summary_404.rpt

```
[2025-10-12 17:08:29] [info] =====
[2025-10-12 17:08:29] [info]                      Design Rule Check Report
[2025-10-12 17:08:29] [info] =====

[2025-10-12 17:08:29] [info] Report Generated:    2025-10-12
[2025-10-12 17:08:29] [info] Run Start Time:      05:07:38 PM
[2025-10-12 17:08:29] [info] Run End Time:        05:08:29 PM
[2025-10-12 17:08:29] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:08:29] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:08:29] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:08:29] [info] User Name:           ubuntu
[2025-10-12 17:08:29] [info] Cores Used:          8
[2025-10-12 17:08:29] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info]                      Top Cell Information
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info] Layout Top Cell:     TOP
[2025-10-12 17:08:29] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:08:29] [info] Database Format:       GDSII
[2025-10-12 17:08:29] [info] Verification Mode:    Full Check

[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info]                      Type of Errors | Count
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info] DB_WARN -> 1
[2025-10-12 17:08:29] [info] LRF_ERR001 -> 14
[2025-10-12 17:08:29] [info] DB_WARN002 -> 159
[2025-10-12 17:08:29] [info] DB_WARN003 -> 161
[2025-10-12 17:08:29] [info] DRC_ERR9 -> 89
[2025-10-12 17:08:29] [info] DRC_ERR -> 67
[2025-10-12 17:08:29] [info] DRC_ERR5 -> 408
[2025-10-12 17:08:29] [info] DRC_ERR4 -> 241
[2025-10-12 17:08:29] [info] DRC_ERR1 -> 301
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info]                      Rule Violation Summary
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:08:29] [info] |-----|-----|-----|
[2025-10-12 17:08:29] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:08:29] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:08:29] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:08:29] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:08:29] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:08:29] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:08:29] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:08:29] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:08:29] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:08:29] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:08:29] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:08:29] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:08:29] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:08:29] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:08:29] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:08:29] [info]
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info]                      Performance Metrics
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:08:29] [info] |-----|-----|
[2025-10-12 17:08:29] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:08:29] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 17:08:29] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info] Summary
[2025-10-12 17:08:29] [info] -----
[2025-10-12 17:08:29] [info] Total Errors: 1120
[2025-10-12 17:08:29] [info] Total Warnings: 321
[2025-10-12 17:08:29] [info] DRC Status: FAILED
```

lvr_summary_405.rpt

```
[2025-10-12 17:09:27] [info] =====
[2025-10-12 17:09:27] [info] Design Rule Check Report
[2025-10-12 17:09:27] [info] =====

[2025-10-12 17:09:27] [info] Report Generated: 2025-10-12
[2025-10-12 17:09:27] [info] Run Start Time: 05:08:36 PM
[2025-10-12 17:09:27] [info] Run End Time: 05:09:27 PM
[2025-10-12 17:09:27] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:09:27] [info] Tool Version: LVR_2025.1
[2025-10-12 17:09:27] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:09:27] [info] User Name: ubuntu
[2025-10-12 17:09:27] [info] Cores Used: 8
[2025-10-12 17:09:27] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Top Cell Information
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Layout Top Cell: TOP
[2025-10-12 17:09:27] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:09:27] [info] Database Format: GDSII
[2025-10-12 17:09:27] [info] Verification Mode: Full Check

[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Type of Errors | Count
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] DB_WARN -> 1
[2025-10-12 17:09:27] [info] LRF_ERR001 -> 14
[2025-10-12 17:09:27] [info] DB_WARN002 -> 159
[2025-10-12 17:09:27] [info] DB_WARN003 -> 161
[2025-10-12 17:09:27] [info] DRC_ERR9 -> 89
[2025-10-12 17:09:27] [info] DRC_ERR -> 67
[2025-10-12 17:09:27] [info] DRC_ERR5 -> 408
[2025-10-12 17:09:27] [info] DRC_ERR4 -> 241
[2025-10-12 17:09:27] [info] DRC_ERR1 -> 301
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Rule Violation Summary
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:09:27] [info] |-----|-----|-----|
[2025-10-12 17:09:27] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:09:27] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:09:27] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:09:27] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:09:27] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 17:09:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:09:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:09:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:09:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:09:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:09:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:09:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:09:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:09:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:09:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:09:27] [info]
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Performance Metrics
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] | Stage | Time Taken |
[2025-10-12 17:09:27] [info] |-----|-----|
[2025-10-12 17:09:27] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:09:27] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:09:27] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Summary
[2025-10-12 17:09:27] [info] -----
[2025-10-12 17:09:27] [info] Total Errors: 1120
[2025-10-12 17:09:27] [info] Total Warnings: 321
[2025-10-12 17:09:27] [info] DRC Status: FAILED
```

lvr_summary_406.rpt

```
[2025-10-12 17:10:25] [info] =====
[2025-10-12 17:10:25] [info] Design Rule Check Report
[2025-10-12 17:10:25] [info] =====

[2025-10-12 17:10:25] [info] Report Generated: 2025-10-12
[2025-10-12 17:10:25] [info] Run Start Time: 05:09:34 PM
[2025-10-12 17:10:25] [info] Run End Time: 05:10:25 PM
[2025-10-12 17:10:25] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:10:25] [info] Tool Version: LVR_2025.1
[2025-10-12 17:10:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:10:25] [info] User Name: ubuntu
[2025-10-12 17:10:25] [info] Cores Used: 8
[2025-10-12 17:10:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Top Cell Information
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Layout Top Cell: TOP
[2025-10-12 17:10:25] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:10:25] [info] Database Format: GDSII
[2025-10-12 17:10:25] [info] Verification Mode: Full Check

[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Type of Errors | Count
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] DB_WARN -> 1
[2025-10-12 17:10:25] [info] LRF_ERR001 -> 14
[2025-10-12 17:10:25] [info] DB_WARN002 -> 159
[2025-10-12 17:10:25] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:10:25] [info] DRC_ERR9 -> 89
[2025-10-12 17:10:25] [info] DRC_ERR -> 67
[2025-10-12 17:10:25] [info] DRC_ERR5 -> 408
[2025-10-12 17:10:25] [info] DRC_ERR4 -> 241
[2025-10-12 17:10:25] [info] DRC_ERR1 -> 301
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Rule Violation Summary
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:10:25] [info] |-----|-----|-----|
[2025-10-12 17:10:25] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:10:25] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:10:25] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:10:25] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:10:25] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:10:25] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:10:25] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:10:25] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:10:25] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:10:25] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:10:25] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:10:25] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:10:25] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:10:25] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:10:25] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Performance Metrics
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] | Stage          | Time Taken |
[2025-10-12 17:10:25] [info] |-----|-----|
[2025-10-12 17:10:25] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:10:25] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:10:25] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Summary
[2025-10-12 17:10:25] [info] -----
[2025-10-12 17:10:25] [info] Total Errors: 1120
[2025-10-12 17:10:25] [info] Total Warnings: 321
[2025-10-12 17:10:25] [info] DRC Status: FAILED
```

lvr_summary_407.rpt

```
[2025-10-12 17:11:25] [info] =====
[2025-10-12 17:11:25] [info] Design Rule Check Report
[2025-10-12 17:11:25] [info] =====

[2025-10-12 17:11:25] [info] Report Generated: 2025-10-12
[2025-10-12 17:11:25] [info] Run Start Time: 05:10:31 PM
[2025-10-12 17:11:25] [info] Run End Time: 05:11:25 PM
[2025-10-12 17:11:25] [info] Time Taken: 0 minutes 53 seconds

[2025-10-12 17:11:25] [info] Tool Version: LVR_2025.1
[2025-10-12 17:11:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:11:25] [info] User Name: ubuntu
[2025-10-12 17:11:25] [info] Cores Used: 8
[2025-10-12 17:11:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Top Cell Information
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Layout Top Cell:      TOP
[2025-10-12 17:11:25] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:11:25] [info] Database Format:      GDSII
[2025-10-12 17:11:25] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Type of Errors | Count
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] DB_WARN -> 1
[2025-10-12 17:11:25] [info] LRF_ERR001 -> 14
[2025-10-12 17:11:25] [info] DB_WARN002 -> 159
[2025-10-12 17:11:25] [info] DB_WARN003 -> 161
[2025-10-12 17:11:25] [info] DRC_ERR9 -> 89
[2025-10-12 17:11:25] [info] DRC_ERR -> 67
[2025-10-12 17:11:25] [info] DRC_ERR5 -> 408
[2025-10-12 17:11:25] [info] DRC_ERR4 -> 241
[2025-10-12 17:11:25] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Rule Violation Summary
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:11:25] [info] |-----|-----|-----|
[2025-10-12 17:11:25] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:11:25] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:11:25] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:11:25] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:11:25] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:11:25] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:11:25] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:11:25] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:11:25] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:11:25] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:11:25] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:11:25] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:11:25] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:11:25] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:11:25] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:11:25] [info] |
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Performance Metrics
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] | Stage          | Time Taken |
[2025-10-12 17:11:25] [info] |-----|-----|
[2025-10-12 17:11:25] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:11:25] [info] | Rule Checking  | 0 minutes 42 seconds |
[2025-10-12 17:11:25] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Summary
```

```
[2025-10-12 17:11:25] [info] -----
[2025-10-12 17:11:25] [info] Total Errors:      1120
[2025-10-12 17:11:25] [info] Total Warnings:    321
[2025-10-12 17:11:25] [info] DRC Status:        FAILED
```


lvr_summary_408.rpt

```
[2025-10-12 17:12:21] [info] =====
[2025-10-12 17:12:21] [info] Design Rule Check Report
[2025-10-12 17:12:21] [info] =====

[2025-10-12 17:12:21] [info] Report Generated: 2025-10-12
[2025-10-12 17:12:21] [info] Run Start Time: 05:11:30 PM
[2025-10-12 17:12:21] [info] Run End Time: 05:12:21 PM
[2025-10-12 17:12:21] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:12:21] [info] Tool Version: LVR_2025.1
[2025-10-12 17:12:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:12:21] [info] User Name: ubuntu
[2025-10-12 17:12:21] [info] Cores Used: 8
[2025-10-12 17:12:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Top Cell Information
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Layout Top Cell: TOP
[2025-10-12 17:12:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:12:21] [info] Database Format: GDSII
[2025-10-12 17:12:21] [info] Verification Mode: Full Check

[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Type of Errors | Count
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] DB_WARN -> 1
[2025-10-12 17:12:21] [info] LRF_ERR001 -> 14
[2025-10-12 17:12:21] [info] DB_WARN002 -> 159
[2025-10-12 17:12:21] [info] DB_WARN003 -> 161
[2025-10-12 17:12:21] [info] DRC_ERR9 -> 89
[2025-10-12 17:12:21] [info] DRC_ERR -> 67
[2025-10-12 17:12:21] [info] DRC_ERR5 -> 408
[2025-10-12 17:12:21] [info] DRC_ERR4 -> 241
[2025-10-12 17:12:21] [info] DRC_ERR1 -> 301
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Rule Violation Summary
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:12:21] [info] |-----|-----|-----|
[2025-10-12 17:12:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:12:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:12:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:12:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:12:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:12:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:12:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:12:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:12:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:12:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:12:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:12:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:12:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:12:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:12:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:12:21] [info]
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Performance Metrics
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:12:21] [info] |-----|-----|
[2025-10-12 17:12:21] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:12:21] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 17:12:21] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Summary
[2025-10-12 17:12:21] [info] -----
[2025-10-12 17:12:21] [info] Total Errors: 1120
[2025-10-12 17:12:21] [info] Total Warnings: 321
[2025-10-12 17:12:21] [info] DRC Status: FAILED
```

lvr_summary_409.rpt

```
[2025-10-12 17:13:18] [info] =====
[2025-10-12 17:13:18] [info] Design Rule Check Report
[2025-10-12 17:13:18] [info] =====

[2025-10-12 17:13:18] [info] Report Generated: 2025-10-12
[2025-10-12 17:13:18] [info] Run Start Time: 05:12:27 PM
[2025-10-12 17:13:18] [info] Run End Time: 05:13:18 PM
[2025-10-12 17:13:18] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:13:18] [info] Tool Version: LVR_2025.1
[2025-10-12 17:13:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:13:18] [info] User Name: ubuntu
[2025-10-12 17:13:18] [info] Cores Used: 8
[2025-10-12 17:13:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Top Cell Information
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Layout Top Cell: TOP
[2025-10-12 17:13:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:13:18] [info] Database Format: GDSII
[2025-10-12 17:13:18] [info] Verification Mode: Full Check

[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Type of Errors | Count
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] DB_WARN -> 1
[2025-10-12 17:13:18] [info] LRF_ERR001 -> 14
[2025-10-12 17:13:18] [info] DB_WARN002 -> 159
[2025-10-12 17:13:18] [info] DB_WARN003 -> 161
[2025-10-12 17:13:18] [info] DRC_ERR9 -> 89
[2025-10-12 17:13:18] [info] DRC_ERR -> 67
[2025-10-12 17:13:18] [info] DRC_ERR5 -> 408
[2025-10-12 17:13:18] [info] DRC_ERR4 -> 241
[2025-10-12 17:13:18] [info] DRC_ERR1 -> 301
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Rule Violation Summary
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:13:18] [info] |-----|-----|-----|
[2025-10-12 17:13:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:13:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:13:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:13:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:13:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 17:13:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:13:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:13:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:13:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:13:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:13:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:13:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:13:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:13:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:13:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:13:18] [info]
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Performance Metrics
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] | Stage | Time Taken |
[2025-10-12 17:13:18] [info] |-----|-----|
[2025-10-12 17:13:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:13:18] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:13:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Summary
[2025-10-12 17:13:18] [info] -----
[2025-10-12 17:13:18] [info] Total Errors: 1120
[2025-10-12 17:13:18] [info] Total Warnings: 321
[2025-10-12 17:13:18] [info] DRC Status: FAILED
```

lvr_summary_41.rpt

```
[2025-10-12 11:20:17] [info] =====
[2025-10-12 11:20:17] [info] Design Rule Check Report
[2025-10-12 11:20:17] [info] =====

[2025-10-12 11:20:17] [info] Report Generated: 2025-10-12
[2025-10-12 11:20:17] [info] Run Start Time: 11:19:25 AM
[2025-10-12 11:20:17] [info] Run End Time: 11:20:17 AM
[2025-10-12 11:20:17] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:20:17] [info] Tool Version: LVR_2025.1
[2025-10-12 11:20:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:20:17] [info] User Name: ubuntu
[2025-10-12 11:20:17] [info] Cores Used: 8
[2025-10-12 11:20:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Top Cell Information
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Layout Top Cell: TOP
[2025-10-12 11:20:17] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:20:17] [info] Database Format: GDSII
[2025-10-12 11:20:17] [info] Verification Mode: Full Check

[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Type of Errors | Count
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] DB_WARN -> 1
[2025-10-12 11:20:17] [info] LRF_ERR001 -> 14
[2025-10-12 11:20:17] [info] DB_WARN002 -> 159
[2025-10-12 11:20:17] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:20:17] [info] DRC_ERR9 -> 89
[2025-10-12 11:20:17] [info] DRC_ERR -> 67
[2025-10-12 11:20:17] [info] DRC_ERR5 -> 408
[2025-10-12 11:20:17] [info] DRC_ERR4 -> 241
[2025-10-12 11:20:17] [info] DRC_ERR1 -> 301
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Rule Violation Summary
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:20:17] [info] |-----|-----|-----|
[2025-10-12 11:20:17] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:20:17] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:20:17] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:20:17] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:20:17] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:20:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:20:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:20:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:20:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:20:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:20:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:20:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:20:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:20:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:20:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Performance Metrics
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] | Stage | Time Taken |
[2025-10-12 11:20:17] [info] |-----|-----|
[2025-10-12 11:20:17] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:20:17] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:20:17] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Summary
[2025-10-12 11:20:17] [info] -----
[2025-10-12 11:20:17] [info] Total Errors: 1120
[2025-10-12 11:20:17] [info] Total Warnings: 321
[2025-10-12 11:20:17] [info] DRC Status: FAILED
```

lvr_summary_410.rpt

```
[2025-10-12 17:14:16] [info] =====
[2025-10-12 17:14:16] [info] Design Rule Check Report
[2025-10-12 17:14:16] [info] =====

[2025-10-12 17:14:16] [info] Report Generated: 2025-10-12
[2025-10-12 17:14:16] [info] Run Start Time: 05:13:24 PM
[2025-10-12 17:14:16] [info] Run End Time: 05:14:16 PM
[2025-10-12 17:14:16] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:14:16] [info] Tool Version: LVR_2025.1
[2025-10-12 17:14:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:14:16] [info] User Name: ubuntu
[2025-10-12 17:14:16] [info] Cores Used: 8
[2025-10-12 17:14:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Top Cell Information
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Layout Top Cell:      TOP
[2025-10-12 17:14:16] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:14:16] [info] Database Format:      GDSII
[2025-10-12 17:14:16] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Type of Errors | Count
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] DB_WARN -> 1
[2025-10-12 17:14:16] [info] LRF_ERR001 -> 14
[2025-10-12 17:14:16] [info] DB_WARN002 -> 159
[2025-10-12 17:14:16] [info] DB_WARN003 -> 161
[2025-10-12 17:14:16] [info] DRC_ERR9 -> 89
[2025-10-12 17:14:16] [info] DRC_ERR -> 67
[2025-10-12 17:14:16] [info] DRC_ERR5 -> 408
[2025-10-12 17:14:16] [info] DRC_ERR4 -> 241
[2025-10-12 17:14:16] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Rule Violation Summary
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:14:16] [info] |-----|-----|-----|
[2025-10-12 17:14:16] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:14:16] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:14:16] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:14:16] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:14:16] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:14:16] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:14:16] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:14:16] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:14:16] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:14:16] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:14:16] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:14:16] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:14:16] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:14:16] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:14:16] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Performance Metrics
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] | Stage          | Time Taken |
[2025-10-12 17:14:16] [info] |-----|-----|
[2025-10-12 17:14:16] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:14:16] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:14:16] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Summary
[2025-10-12 17:14:16] [info] -----
[2025-10-12 17:14:16] [info] Total Errors:      1120
[2025-10-12 17:14:16] [info] Total Warnings:    321
[2025-10-12 17:14:16] [info] DRC Status:        FAILED
```

lvr_summary_411.rpt

```
[2025-10-12 17:15:12] [info] =====
[2025-10-12 17:15:12] [info]                Design Rule Check Report
[2025-10-12 17:15:12] [info] =====

[2025-10-12 17:15:12] [info] Report Generated:    2025-10-12
[2025-10-12 17:15:12] [info] Run Start Time:      05:14:21 PM
[2025-10-12 17:15:12] [info] Run End Time:        05:15:12 PM
[2025-10-12 17:15:12] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 17:15:12] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:15:12] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:15:12] [info] User Name:           ubuntu
[2025-10-12 17:15:12] [info] Cores Used:          8
[2025-10-12 17:15:12] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info]                Top Cell Information
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info] Layout Top Cell:     TOP
[2025-10-12 17:15:12] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:15:12] [info] Database Format:      GDSII
[2025-10-12 17:15:12] [info] Verification Mode:    Full Check

[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info]                Type of Errors | Count
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info] DB_WARN -> 1
[2025-10-12 17:15:12] [info] LRF_ERR001 -> 14
[2025-10-12 17:15:12] [info] DB_WARN002 -> 159
[2025-10-12 17:15:12] [info] DB_WARN003 -> 161
[2025-10-12 17:15:12] [info] DRC_ERR9 -> 89
[2025-10-12 17:15:12] [info] DRC_ERR -> 67
[2025-10-12 17:15:12] [info] DRC_ERR5 -> 408
[2025-10-12 17:15:12] [info] DRC_ERR4 -> 241
[2025-10-12 17:15:12] [info] DRC_ERR1 -> 301
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info]                Rule Violation Summary
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:15:12] [info] |-----|-----|-----|
[2025-10-12 17:15:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:15:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:15:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:15:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:15:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:15:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:15:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:15:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:15:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:15:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:15:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:15:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:15:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:15:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:15:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:15:12] [info]
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info]                Performance Metrics
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:15:12] [info] |-----|-----|
[2025-10-12 17:15:12] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:15:12] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:15:12] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info]                Summary
[2025-10-12 17:15:12] [info] -----
[2025-10-12 17:15:12] [info] Total Errors:   1120
[2025-10-12 17:15:12] [info] Total Warnings:    321
[2025-10-12 17:15:12] [info] DRC Status:      FAILED
```

lvr_summary_412.rpt

```
[2025-10-12 17:16:09] [info] =====
[2025-10-12 17:16:09] [info]                Design Rule Check Report
[2025-10-12 17:16:09] [info] =====

[2025-10-12 17:16:09] [info] Report Generated:   2025-10-12
[2025-10-12 17:16:09] [info] Run Start Time:     05:15:18 PM
[2025-10-12 17:16:09] [info] Run End Time:       05:16:09 PM
[2025-10-12 17:16:09] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 17:16:09] [info] Tool Version:       LVR_2025.1
[2025-10-12 17:16:09] [info] Host Name:          ip-172-31-37-173
[2025-10-12 17:16:09] [info] User Name:          ubuntu
[2025-10-12 17:16:09] [info] Cores Used:         8
[2025-10-12 17:16:09] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info]                Top Cell Information
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] Layout Top Cell:    TOP
[2025-10-12 17:16:09] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 17:16:09] [info] Database Format:      GDSII
[2025-10-12 17:16:09] [info] Verification Mode:   Full Check

[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info]                Type of Errors | Count
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] DB_WARN -> 1
[2025-10-12 17:16:09] [info] LRF_ERR001 -> 14
[2025-10-12 17:16:09] [info] DB_WARN002 -> 159
[2025-10-12 17:16:09] [info] DB_WARN003 -> 161
[2025-10-12 17:16:09] [info] DRC_ERR9 -> 89
[2025-10-12 17:16:09] [info] DRC_ERR -> 67
[2025-10-12 17:16:09] [info] DRC_ERR5 -> 408
[2025-10-12 17:16:09] [info] DRC_ERR4 -> 241
[2025-10-12 17:16:09] [info] DRC_ERR1 -> 301
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info]                Rule Violation Summary
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:16:09] [info] |-----|-----|-----|
[2025-10-12 17:16:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:16:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:16:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:16:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:16:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:16:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:16:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:16:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:16:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:16:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:16:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:16:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:16:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:16:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:16:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:16:09] [info]
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] Performance Metrics
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] | Stage | Time Taken |
[2025-10-12 17:16:09] [info] |-----|-----|
[2025-10-12 17:16:09] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:16:09] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:16:09] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] Summary
[2025-10-12 17:16:09] [info] -----
[2025-10-12 17:16:09] [info] Total Errors: 1120
[2025-10-12 17:16:09] [info] Total Warnings: 321
[2025-10-12 17:16:09] [info] DRC Status: FAILED
```

lvr_summary_413.rpt

```
[2025-10-12 17:17:07] [info] =====
[2025-10-12 17:17:07] [info] Design Rule Check Report
[2025-10-12 17:17:07] [info] =====

[2025-10-12 17:17:07] [info] Report Generated: 2025-10-12
[2025-10-12 17:17:07] [info] Run Start Time: 05:16:16 PM
[2025-10-12 17:17:07] [info] Run End Time: 05:17:07 PM
[2025-10-12 17:17:07] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:17:07] [info] Tool Version: LVR_2025.1
[2025-10-12 17:17:07] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:17:07] [info] User Name: ubuntu
[2025-10-12 17:17:07] [info] Cores Used: 8
[2025-10-12 17:17:07] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Top Cell Information
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Layout Top Cell: TOP
[2025-10-12 17:17:07] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:17:07] [info] Database Format: GDSII
[2025-10-12 17:17:07] [info] Verification Mode: Full Check

[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Type of Errors | Count
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] DB_WARN -> 1
[2025-10-12 17:17:07] [info] LRF_ERR001 -> 14
[2025-10-12 17:17:07] [info] DB_WARN002 -> 159
[2025-10-12 17:17:07] [info] DB_WARN003 -> 161
```



```
[2025-10-12 17:17:07] [info] DRC_ERR9 -> 89
[2025-10-12 17:17:07] [info] DRC_ERR -> 67
[2025-10-12 17:17:07] [info] DRC_ERR5 -> 408
[2025-10-12 17:17:07] [info] DRC_ERR4 -> 241
[2025-10-12 17:17:07] [info] DRC_ERR1 -> 301
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Rule Violation Summary
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:17:07] [info] |-----|-----|-----|
[2025-10-12 17:17:07] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:17:07] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:17:07] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:17:07] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:17:07] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:17:07] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:17:07] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:17:07] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:17:07] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:17:07] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:17:07] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:17:07] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:17:07] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:17:07] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:17:07] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Performance Metrics
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] | Stage          | Time Taken |
[2025-10-12 17:17:07] [info] |-----|-----|
[2025-10-12 17:17:07] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:17:07] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:17:07] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Summary
[2025-10-12 17:17:07] [info] -----
[2025-10-12 17:17:07] [info] Total Errors: 1120
[2025-10-12 17:17:07] [info] Total Warnings: 321
[2025-10-12 17:17:07] [info] DRC Status: FAILED
```

lvr_summary_414.rpt

```
[2025-10-12 17:18:04] [info] =====
[2025-10-12 17:18:04] [info] Design Rule Check Report
[2025-10-12 17:18:04] [info] =====

[2025-10-12 17:18:04] [info] Report Generated: 2025-10-12
[2025-10-12 17:18:04] [info] Run Start Time: 05:17:13 PM
[2025-10-12 17:18:04] [info] Run End Time: 05:18:04 PM
[2025-10-12 17:18:04] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:18:04] [info] Tool Version: LVR_2025.1
[2025-10-12 17:18:04] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:18:04] [info] User Name: ubuntu
[2025-10-12 17:18:04] [info] Cores Used: 8
[2025-10-12 17:18:04] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Top Cell Information
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Layout Top Cell:      TOP
[2025-10-12 17:18:04] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:18:04] [info] Database Format:      GDSII
[2025-10-12 17:18:04] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Type of Errors | Count
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] DB_WARN -> 1
[2025-10-12 17:18:04] [info] LRF_ERR001 -> 14
[2025-10-12 17:18:04] [info] DB_WARN002 -> 159
[2025-10-12 17:18:04] [info] DB_WARN003 -> 161
[2025-10-12 17:18:04] [info] DRC_ERR9 -> 89
[2025-10-12 17:18:04] [info] DRC_ERR -> 67
[2025-10-12 17:18:04] [info] DRC_ERR5 -> 408
[2025-10-12 17:18:04] [info] DRC_ERR4 -> 241
[2025-10-12 17:18:04] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Rule Violation Summary
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:18:04] [info] |-----|-----|-----|
[2025-10-12 17:18:04] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:18:04] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:18:04] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:18:04] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:18:04] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:18:04] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:18:04] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:18:04] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:18:04] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:18:04] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:18:04] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:18:04] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:18:04] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:18:04] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:18:04] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:18:04] [info] |
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Performance Metrics
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] | Stage          | Time Taken |
[2025-10-12 17:18:04] [info] |-----|-----|
[2025-10-12 17:18:04] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:18:04] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 17:18:04] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Summary
```

```
[2025-10-12 17:18:04] [info] -----
[2025-10-12 17:18:04] [info] Total Errors:      1120
[2025-10-12 17:18:04] [info] Total Warnings:    321
[2025-10-12 17:18:04] [info] DRC Status:        FAILED
```

lvr_summary_415.rpt

```
[2025-10-12 17:19:01] [info] =====
[2025-10-12 17:19:01] [info] Design Rule Check Report
[2025-10-12 17:19:01] [info] =====

[2025-10-12 17:19:01] [info] Report Generated: 2025-10-12
[2025-10-12 17:19:01] [info] Run Start Time: 05:18:11 PM
[2025-10-12 17:19:01] [info] Run End Time: 05:19:01 PM
[2025-10-12 17:19:01] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:19:01] [info] Tool Version: LVR_2025.1
[2025-10-12 17:19:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:19:01] [info] User Name: ubuntu
[2025-10-12 17:19:01] [info] Cores Used: 8
[2025-10-12 17:19:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Top Cell Information
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Layout Top Cell: TOP
[2025-10-12 17:19:01] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:19:01] [info] Database Format: GDSII
[2025-10-12 17:19:01] [info] Verification Mode: Full Check

[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Type of Errors | Count
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] DB_WARN -> 1
[2025-10-12 17:19:01] [info] LRF_ERR001 -> 14
[2025-10-12 17:19:01] [info] DB_WARN002 -> 159
[2025-10-12 17:19:01] [info] DB_WARN003 -> 161
[2025-10-12 17:19:01] [info] DRC_ERR9 -> 89
[2025-10-12 17:19:01] [info] DRC_ERR -> 67
[2025-10-12 17:19:01] [info] DRC_ERR5 -> 408
[2025-10-12 17:19:01] [info] DRC_ERR4 -> 241
[2025-10-12 17:19:01] [info] DRC_ERR1 -> 301
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Rule Violation Summary
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:19:01] [info] |-----|-----|-----|
[2025-10-12 17:19:01] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:19:01] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:19:01] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:19:01] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:19:01] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:19:01] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:19:01] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:19:01] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:19:01] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:19:01] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:19:01] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:19:01] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:19:01] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:19:01] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:19:01] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:19:01] [info]
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Performance Metrics
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:19:01] [info] |-----|-----|
[2025-10-12 17:19:01] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:19:01] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:19:01] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Summary
[2025-10-12 17:19:01] [info] -----
[2025-10-12 17:19:01] [info] Total Errors: 1120
[2025-10-12 17:19:01] [info] Total Warnings: 321
[2025-10-12 17:19:01] [info] DRC Status: FAILED
```

lvr_summary_416.rpt

```
[2025-10-12 17:19:59] [info] =====
[2025-10-12 17:19:59] [info] Design Rule Check Report
[2025-10-12 17:19:59] [info] =====

[2025-10-12 17:19:59] [info] Report Generated: 2025-10-12
[2025-10-12 17:19:59] [info] Run Start Time: 05:19:07 PM
[2025-10-12 17:19:59] [info] Run End Time: 05:19:59 PM
[2025-10-12 17:19:59] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:19:59] [info] Tool Version: LVR_2025.1
[2025-10-12 17:19:59] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:19:59] [info] User Name: ubuntu
[2025-10-12 17:19:59] [info] Cores Used: 8
[2025-10-12 17:19:59] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Top Cell Information
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Layout Top Cell: TOP
[2025-10-12 17:19:59] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:19:59] [info] Database Format: GDSII
[2025-10-12 17:19:59] [info] Verification Mode: Full Check

[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Type of Errors | Count
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] DB_WARN -> 1
[2025-10-12 17:19:59] [info] LRF_ERR001 -> 14
[2025-10-12 17:19:59] [info] DB_WARN002 -> 159
[2025-10-12 17:19:59] [info] DB_WARN003 -> 161
[2025-10-12 17:19:59] [info] DRC_ERR9 -> 89
[2025-10-12 17:19:59] [info] DRC_ERR -> 67
[2025-10-12 17:19:59] [info] DRC_ERR5 -> 408
[2025-10-12 17:19:59] [info] DRC_ERR4 -> 241
[2025-10-12 17:19:59] [info] DRC_ERR1 -> 301
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Rule Violation Summary
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:19:59] [info] |-----|-----|-----|
[2025-10-12 17:19:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:19:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:19:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:19:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:19:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 17:19:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:19:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:19:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:19:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:19:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:19:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:19:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:19:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:19:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:19:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:19:59] [info]
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Performance Metrics
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] | Stage | Time Taken |
[2025-10-12 17:19:59] [info] |-----|-----|
[2025-10-12 17:19:59] [info] | GDS Parsing | 0 minutes 10 seconds |
[2025-10-12 17:19:59] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:19:59] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Summary
[2025-10-12 17:19:59] [info] -----
[2025-10-12 17:19:59] [info] Total Errors: 1120
[2025-10-12 17:19:59] [info] Total Warnings: 321
[2025-10-12 17:19:59] [info] DRC Status: FAILED
```

lvr_summary_417.rpt

```
[2025-10-12 17:20:56] [info] =====
[2025-10-12 17:20:56] [info] Design Rule Check Report
[2025-10-12 17:20:56] [info] =====

[2025-10-12 17:20:56] [info] Report Generated: 2025-10-12
[2025-10-12 17:20:56] [info] Run Start Time: 05:20:05 PM
[2025-10-12 17:20:56] [info] Run End Time: 05:20:56 PM
[2025-10-12 17:20:56] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:20:56] [info] Tool Version: LVR_2025.1
[2025-10-12 17:20:56] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:20:56] [info] User Name: ubuntu
[2025-10-12 17:20:56] [info] Cores Used: 8
[2025-10-12 17:20:56] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Top Cell Information
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Layout Top Cell: TOP
[2025-10-12 17:20:56] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:20:56] [info] Database Format: GDSII
[2025-10-12 17:20:56] [info] Verification Mode: Full Check

[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Type of Errors | Count
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] DB_WARN -> 1
[2025-10-12 17:20:56] [info] LRF_ERR001 -> 14
[2025-10-12 17:20:56] [info] DB_WARN002 -> 159
[2025-10-12 17:20:56] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:20:56] [info] DRC_ERR9 -> 89
[2025-10-12 17:20:56] [info] DRC_ERR -> 67
[2025-10-12 17:20:56] [info] DRC_ERR5 -> 408
[2025-10-12 17:20:56] [info] DRC_ERR4 -> 241
[2025-10-12 17:20:56] [info] DRC_ERR1 -> 301
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Rule Violation Summary
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:20:56] [info] |-----|-----|-----|
[2025-10-12 17:20:56] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:20:56] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:20:56] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:20:56] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:20:56] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:20:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:20:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:20:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:20:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:20:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:20:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:20:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:20:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:20:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:20:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Performance Metrics
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] | Stage | Time Taken |
[2025-10-12 17:20:56] [info] |-----|-----|
[2025-10-12 17:20:56] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:20:56] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:20:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Summary
[2025-10-12 17:20:56] [info] -----
[2025-10-12 17:20:56] [info] Total Errors: 1120
[2025-10-12 17:20:56] [info] Total Warnings: 321
[2025-10-12 17:20:56] [info] DRC Status: FAILED
```

lvr_summary_418.rpt

```
[2025-10-12 17:21:52] [info] =====
[2025-10-12 17:21:52] [info] Design Rule Check Report
[2025-10-12 17:21:52] [info] =====

[2025-10-12 17:21:52] [info] Report Generated: 2025-10-12
[2025-10-12 17:21:52] [info] Run Start Time: 05:21:01 PM
[2025-10-12 17:21:52] [info] Run End Time: 05:21:52 PM
[2025-10-12 17:21:52] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:21:52] [info] Tool Version: LVR_2025.1
[2025-10-12 17:21:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:21:52] [info] User Name: ubuntu
[2025-10-12 17:21:52] [info] Cores Used: 8
[2025-10-12 17:21:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] Top Cell Information
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] Layout Top Cell:      TOP
[2025-10-12 17:21:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:21:52] [info] Database Format:      GDSII
[2025-10-12 17:21:52] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] Type of Errors | Count
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] DB_WARN -> 1
[2025-10-12 17:21:52] [info] LRF_ERR001 -> 14
[2025-10-12 17:21:52] [info] DB_WARN002 -> 159
[2025-10-12 17:21:52] [info] DB_WARN003 -> 161
[2025-10-12 17:21:52] [info] DRC_ERR9 -> 89
[2025-10-12 17:21:52] [info] DRC_ERR -> 67
[2025-10-12 17:21:52] [info] DRC_ERR5 -> 408
[2025-10-12 17:21:52] [info] DRC_ERR4 -> 241
[2025-10-12 17:21:52] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] Rule Violation Summary
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:21:52] [info] |-----|-----|-----|
[2025-10-12 17:21:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:21:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:21:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:21:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:21:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:21:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:21:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:21:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:21:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:21:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:21:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:21:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:21:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:21:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:21:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:21:52] [info] -----
```

```
[2025-10-12 17:21:52] [info] Performance Metrics
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] | Stage          | Time Taken |
[2025-10-12 17:21:52] [info] |-----|-----|
[2025-10-12 17:21:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:21:52] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 17:21:52] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] Summary
```

```
[2025-10-12 17:21:52] [info] -----
[2025-10-12 17:21:52] [info] Total Errors:      1120
[2025-10-12 17:21:52] [info] Total Warnings:    321
[2025-10-12 17:21:52] [info] DRC Status:        FAILED
```

lvr_summary_419.rpt

```
[2025-10-12 17:22:50] [info] =====
[2025-10-12 17:22:50] [info]                      Design Rule Check Report
[2025-10-12 17:22:50] [info] =====

[2025-10-12 17:22:50] [info] Report Generated:    2025-10-12
[2025-10-12 17:22:50] [info] Run Start Time:      05:22:00 PM
[2025-10-12 17:22:50] [info] Run End Time:        05:22:50 PM
[2025-10-12 17:22:50] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 17:22:50] [info] Tool Version:       LVR_2025.1
[2025-10-12 17:22:50] [info] Host Name:          ip-172-31-37-173
[2025-10-12 17:22:50] [info] User Name:          ubuntu
[2025-10-12 17:22:50] [info] Cores Used:         8
[2025-10-12 17:22:50] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info]                      Top Cell Information
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info] Layout Top Cell:    TOP
[2025-10-12 17:22:50] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 17:22:50] [info] Database Format:      GDSII
[2025-10-12 17:22:50] [info] Verification Mode:   Full Check

[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info]                      Type of Errors | Count
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info] DB_WARN -> 1
[2025-10-12 17:22:50] [info] LRF_ERR001 -> 14
[2025-10-12 17:22:50] [info] DB_WARN002 -> 159
[2025-10-12 17:22:50] [info] DB_WARN003 -> 161
[2025-10-12 17:22:50] [info] DRC_ERR9 -> 89
[2025-10-12 17:22:50] [info] DRC_ERR -> 67
[2025-10-12 17:22:50] [info] DRC_ERR5 -> 408
[2025-10-12 17:22:50] [info] DRC_ERR4 -> 241
[2025-10-12 17:22:50] [info] DRC_ERR1 -> 301
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info]                      Rule Violation Summary
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:22:50] [info] |-----|-----|-----|
[2025-10-12 17:22:50] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:22:50] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:22:50] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:22:50] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:22:50] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:22:50] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:22:50] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:22:50] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:22:50] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:22:50] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:22:50] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:22:50] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:22:50] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:22:50] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:22:50] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:22:50] [info]
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info]                      Performance Metrics
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info] | Stage | Time Taken |
```



```
[2025-10-12 17:22:50] [info] |-----|-----|
[2025-10-12 17:22:50] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:22:50] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:22:50] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info]                Summary
[2025-10-12 17:22:50] [info] -----
[2025-10-12 17:22:50] [info] Total Errors:    1120
[2025-10-12 17:22:50] [info] Total Warnings:    321
[2025-10-12 17:22:50] [info] DRC Status:      FAILED
```

lvr_summary_42.rpt

```
[2025-10-12 11:21:13] [info] =====
[2025-10-12 11:21:13] [info]                Design Rule Check Report
[2025-10-12 11:21:13] [info] =====

[2025-10-12 11:21:13] [info] Report Generated:    2025-10-12
[2025-10-12 11:21:13] [info] Run Start Time:      11:20:22 AM
[2025-10-12 11:21:13] [info] Run End Time:        11:21:13 AM
[2025-10-12 11:21:13] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:21:13] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:21:13] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:21:13] [info] User Name:           ubuntu
[2025-10-12 11:21:13] [info] Cores Used:          8
[2025-10-12 11:21:13] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info]                Top Cell Information
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] Layout Top Cell:     TOP
[2025-10-12 11:21:13] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:21:13] [info] Database Format:       GDSII
[2025-10-12 11:21:13] [info] Verification Mode:    Full Check

[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info]                Type of Errors | Count
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] DB_WARN -> 1
[2025-10-12 11:21:13] [info] LRF_ERR001 -> 14
[2025-10-12 11:21:13] [info] DB_WARN002 -> 159
[2025-10-12 11:21:13] [info] DB_WARN003 -> 161
[2025-10-12 11:21:13] [info] DRC_ERR9 -> 89
[2025-10-12 11:21:13] [info] DRC_ERR -> 67
[2025-10-12 11:21:13] [info] DRC_ERR5 -> 408
[2025-10-12 11:21:13] [info] DRC_ERR4 -> 241
[2025-10-12 11:21:13] [info] DRC_ERR1 -> 301
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info]                Rule Violation Summary
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:21:13] [info] |-----|-----|-----|
[2025-10-12 11:21:13] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:21:13] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:21:13] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:21:13] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:21:13] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:21:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:21:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:21:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:21:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:21:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:21:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:21:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:21:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:21:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:21:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:21:13] [info]
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] Performance Metrics
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] | Stage | Time Taken |
[2025-10-12 11:21:13] [info] |-----|-----|
[2025-10-12 11:21:13] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:21:13] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:21:13] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] Summary
[2025-10-12 11:21:13] [info] -----
[2025-10-12 11:21:13] [info] Total Errors: 1120
[2025-10-12 11:21:13] [info] Total Warnings: 321
[2025-10-12 11:21:13] [info] DRC Status: FAILED
```

lvr_summary_420.rpt

```
[2025-10-12 17:23:50] [info] =====
[2025-10-12 17:23:50] [info] Design Rule Check Report
[2025-10-12 17:23:50] [info] =====

[2025-10-12 17:23:50] [info] Report Generated: 2025-10-12
[2025-10-12 17:23:50] [info] Run Start Time: 05:22:58 PM
[2025-10-12 17:23:50] [info] Run End Time: 05:23:50 PM
[2025-10-12 17:23:50] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:23:50] [info] Tool Version: LVR_2025.1
[2025-10-12 17:23:50] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:23:50] [info] User Name: ubuntu
[2025-10-12 17:23:50] [info] Cores Used: 8
[2025-10-12 17:23:50] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Top Cell Information
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Layout Top Cell: TOP
[2025-10-12 17:23:50] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:23:50] [info] Database Format: GDSII
[2025-10-12 17:23:50] [info] Verification Mode: Full Check

[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Type of Errors | Count
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] DB_WARN -> 1
[2025-10-12 17:23:50] [info] LRF_ERR001 -> 14
[2025-10-12 17:23:50] [info] DB_WARN002 -> 159
[2025-10-12 17:23:50] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:23:50] [info] DRC_ERR9 -> 89
[2025-10-12 17:23:50] [info] DRC_ERR -> 67
[2025-10-12 17:23:50] [info] DRC_ERR5 -> 408
[2025-10-12 17:23:50] [info] DRC_ERR4 -> 241
[2025-10-12 17:23:50] [info] DRC_ERR1 -> 301
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Rule Violation Summary
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:23:50] [info] |-----|-----|-----|
[2025-10-12 17:23:50] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:23:50] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:23:50] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:23:50] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:23:50] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:23:50] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:23:50] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:23:50] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:23:50] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:23:50] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:23:50] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:23:50] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:23:50] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:23:50] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:23:50] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Performance Metrics
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] | Stage          | Time Taken |
[2025-10-12 17:23:50] [info] |-----|-----|
[2025-10-12 17:23:50] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:23:50] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:23:50] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Summary
[2025-10-12 17:23:50] [info] -----
[2025-10-12 17:23:50] [info] Total Errors:    1120
[2025-10-12 17:23:50] [info] Total Warnings:    321
[2025-10-12 17:23:50] [info] DRC Status:      FAILED
```

lvr_summary_421.rpt

```
[2025-10-12 17:24:46] [info] =====
[2025-10-12 17:24:46] [info] Design Rule Check Report
[2025-10-12 17:24:46] [info] =====

[2025-10-12 17:24:46] [info] Report Generated:    2025-10-12
[2025-10-12 17:24:46] [info] Run Start Time:      05:23:54 PM
[2025-10-12 17:24:46] [info] Run End Time:        05:24:46 PM
[2025-10-12 17:24:46] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:24:46] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:24:46] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:24:46] [info] User Name:           ubuntu
[2025-10-12 17:24:46] [info] Cores Used:          8
[2025-10-12 17:24:46] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Top Cell Information
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Layout Top Cell:      TOP
[2025-10-12 17:24:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:24:46] [info] Database Format:      GDSII
[2025-10-12 17:24:46] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Type of Errors | Count
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] DB_WARN -> 1
[2025-10-12 17:24:46] [info] LRF_ERR001 -> 14
[2025-10-12 17:24:46] [info] DB_WARN002 -> 159
[2025-10-12 17:24:46] [info] DB_WARN003 -> 161
[2025-10-12 17:24:46] [info] DRC_ERR9 -> 89
[2025-10-12 17:24:46] [info] DRC_ERR -> 67
[2025-10-12 17:24:46] [info] DRC_ERR5 -> 408
[2025-10-12 17:24:46] [info] DRC_ERR4 -> 241
[2025-10-12 17:24:46] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Rule Violation Summary
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:24:46] [info] |-----|-----|-----|
[2025-10-12 17:24:46] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:24:46] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:24:46] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:24:46] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:24:46] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:24:46] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:24:46] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:24:46] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:24:46] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:24:46] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:24:46] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:24:46] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:24:46] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:24:46] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:24:46] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:24:46] [info] |
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Performance Metrics
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] | Stage          | Time Taken |
[2025-10-12 17:24:46] [info] |-----|-----|
[2025-10-12 17:24:46] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:24:46] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:24:46] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Summary
```

```
[2025-10-12 17:24:46] [info] -----
[2025-10-12 17:24:46] [info] Total Errors:      1120
[2025-10-12 17:24:46] [info] Total Warnings:    321
[2025-10-12 17:24:46] [info] DRC Status:        FAILED
```

lvr_summary_422.rpt

```
[2025-10-12 17:25:42] [info] =====
[2025-10-12 17:25:42] [info] Design Rule Check Report
[2025-10-12 17:25:42] [info] =====

[2025-10-12 17:25:42] [info] Report Generated: 2025-10-12
[2025-10-12 17:25:42] [info] Run Start Time: 05:24:52 PM
[2025-10-12 17:25:42] [info] Run End Time: 05:25:42 PM
[2025-10-12 17:25:42] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:25:42] [info] Tool Version: LVR_2025.1
[2025-10-12 17:25:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:25:42] [info] User Name: ubuntu
[2025-10-12 17:25:42] [info] Cores Used: 8
[2025-10-12 17:25:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] Top Cell Information
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] Layout Top Cell: TOP
[2025-10-12 17:25:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:25:42] [info] Database Format: GDSII
[2025-10-12 17:25:42] [info] Verification Mode: Full Check

[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] Type of Errors | Count
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] DB_WARN -> 1
[2025-10-12 17:25:42] [info] LRF_ERR001 -> 14
[2025-10-12 17:25:42] [info] DB_WARN002 -> 159
[2025-10-12 17:25:42] [info] DB_WARN003 -> 161
[2025-10-12 17:25:42] [info] DRC_ERR9 -> 89
[2025-10-12 17:25:42] [info] DRC_ERR -> 67
[2025-10-12 17:25:42] [info] DRC_ERR5 -> 408
[2025-10-12 17:25:42] [info] DRC_ERR4 -> 241
[2025-10-12 17:25:42] [info] DRC_ERR1 -> 301
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] Rule Violation Summary
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:25:42] [info] |-----|-----|-----|
[2025-10-12 17:25:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:25:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:25:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:25:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:25:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:25:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:25:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:25:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:25:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:25:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:25:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:25:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:25:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:25:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:25:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:25:42] [info]
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] Performance Metrics
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:25:42] [info] |-----|-----|
[2025-10-12 17:25:42] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:25:42] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:25:42] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info]                Summary
[2025-10-12 17:25:42] [info] -----
[2025-10-12 17:25:42] [info] Total Errors:    1120
[2025-10-12 17:25:42] [info] Total Warnings:    321
[2025-10-12 17:25:42] [info] DRC Status:      FAILED
```

lvr_summary_423.rpt

```
[2025-10-12 17:26:41] [info] =====
[2025-10-12 17:26:41] [info]                Design Rule Check Report
[2025-10-12 17:26:41] [info] =====

[2025-10-12 17:26:41] [info] Report Generated:    2025-10-12
[2025-10-12 17:26:41] [info] Run Start Time:      05:25:49 PM
[2025-10-12 17:26:41] [info] Run End Time:        05:26:41 PM
[2025-10-12 17:26:41] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:26:41] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:26:41] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:26:41] [info] User Name:           ubuntu
[2025-10-12 17:26:41] [info] Cores Used:          8
[2025-10-12 17:26:41] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info]                Top Cell Information
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] Layout Top Cell:     TOP
[2025-10-12 17:26:41] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:26:41] [info] Database Format:       GDSII
[2025-10-12 17:26:41] [info] Verification Mode:    Full Check

[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info]                Type of Errors | Count
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] DB_WARN -> 1
[2025-10-12 17:26:41] [info] LRF_ERR001 -> 14
[2025-10-12 17:26:41] [info] DB_WARN002 -> 159
[2025-10-12 17:26:41] [info] DB_WARN003 -> 161
[2025-10-12 17:26:41] [info] DRC_ERR9 -> 89
[2025-10-12 17:26:41] [info] DRC_ERR -> 67
[2025-10-12 17:26:41] [info] DRC_ERR5 -> 408
[2025-10-12 17:26:41] [info] DRC_ERR4 -> 241
[2025-10-12 17:26:41] [info] DRC_ERR1 -> 301
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info]                Rule Violation Summary
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:26:41] [info] |-----|-----|-----|
[2025-10-12 17:26:41] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:26:41] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:26:41] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:26:41] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:26:41] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:26:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:26:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:26:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:26:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:26:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:26:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:26:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:26:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:26:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:26:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:26:41] [info]
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] Performance Metrics
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] | Stage | Time Taken |
[2025-10-12 17:26:41] [info] |-----|-----|
[2025-10-12 17:26:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:26:41] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:26:41] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:26:41] [info]
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] Summary
[2025-10-12 17:26:41] [info] -----
[2025-10-12 17:26:41] [info] Total Errors: 1120
[2025-10-12 17:26:41] [info] Total Warnings: 321
[2025-10-12 17:26:41] [info] DRC Status: FAILED
```

lvr_summary_424.rpt

```
[2025-10-12 17:27:36] [info] =====
[2025-10-12 17:27:36] [info] Design Rule Check Report
[2025-10-12 17:27:36] [info] =====

[2025-10-12 17:27:36] [info] Report Generated: 2025-10-12
[2025-10-12 17:27:36] [info] Run Start Time: 05:26:46 PM
[2025-10-12 17:27:36] [info] Run End Time: 05:27:36 PM
[2025-10-12 17:27:36] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:27:36] [info] Tool Version: LVR_2025.1
[2025-10-12 17:27:36] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:27:36] [info] User Name: ubuntu
[2025-10-12 17:27:36] [info] Cores Used: 8
[2025-10-12 17:27:36] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Top Cell Information
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Layout Top Cell: TOP
[2025-10-12 17:27:36] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:27:36] [info] Database Format: GDSII
[2025-10-12 17:27:36] [info] Verification Mode: Full Check

[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Type of Errors | Count
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] DB_WARN -> 1
[2025-10-12 17:27:36] [info] LRF_ERR001 -> 14
[2025-10-12 17:27:36] [info] DB_WARN002 -> 159
[2025-10-12 17:27:36] [info] DB_WARN003 -> 161
```

```

[2025-10-12 17:27:36] [info] DRC_ERR9 -> 89
[2025-10-12 17:27:36] [info] DRC_ERR -> 67
[2025-10-12 17:27:36] [info] DRC_ERR5 -> 408
[2025-10-12 17:27:36] [info] DRC_ERR4 -> 241
[2025-10-12 17:27:36] [info] DRC_ERR1 -> 301
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Rule Violation Summary
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:27:36] [info] |-----|-----|-----|
[2025-10-12 17:27:36] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:27:36] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:27:36] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:27:36] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:27:36] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:27:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:27:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:27:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:27:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:27:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:27:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:27:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:27:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:27:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:27:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Performance Metrics
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] | Stage | Time Taken |
[2025-10-12 17:27:36] [info] |-----|-----|
[2025-10-12 17:27:36] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:27:36] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:27:36] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Summary
[2025-10-12 17:27:36] [info] -----
[2025-10-12 17:27:36] [info] Total Errors: 1120
[2025-10-12 17:27:36] [info] Total Warnings: 321
[2025-10-12 17:27:36] [info] DRC Status: FAILED

```

lvr_summary_425.rpt

```

[2025-10-12 17:28:34] [info] =====
[2025-10-12 17:28:34] [info] Design Rule Check Report
[2025-10-12 17:28:34] [info] =====

[2025-10-12 17:28:34] [info] Report Generated: 2025-10-12
[2025-10-12 17:28:34] [info] Run Start Time: 05:27:43 PM
[2025-10-12 17:28:34] [info] Run End Time: 05:28:34 PM
[2025-10-12 17:28:34] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:28:34] [info] Tool Version: LVR_2025.1
[2025-10-12 17:28:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:28:34] [info] User Name: ubuntu
[2025-10-12 17:28:34] [info] Cores Used: 8
[2025-10-12 17:28:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Top Cell Information
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Layout Top Cell:      TOP
[2025-10-12 17:28:34] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:28:34] [info] Database Format:      GDSII
[2025-10-12 17:28:34] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Type of Errors | Count
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] DB_WARN -> 1
[2025-10-12 17:28:34] [info] LRF_ERR001 -> 14
[2025-10-12 17:28:34] [info] DB_WARN002 -> 159
[2025-10-12 17:28:34] [info] DB_WARN003 -> 161
[2025-10-12 17:28:34] [info] DRC_ERR9 -> 89
[2025-10-12 17:28:34] [info] DRC_ERR -> 67
[2025-10-12 17:28:34] [info] DRC_ERR5 -> 408
[2025-10-12 17:28:34] [info] DRC_ERR4 -> 241
[2025-10-12 17:28:34] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Rule Violation Summary
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:28:34] [info] |-----|-----|-----|
[2025-10-12 17:28:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:28:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:28:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:28:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:28:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:28:34] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:28:34] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:28:34] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:28:34] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:28:34] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:28:34] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:28:34] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:28:34] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:28:34] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:28:34] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:28:34] [info] |
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Performance Metrics
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] | Stage          | Time Taken |
[2025-10-12 17:28:34] [info] |-----|-----|
[2025-10-12 17:28:34] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:28:34] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:28:34] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Summary
```

```
[2025-10-12 17:28:34] [info] -----
[2025-10-12 17:28:34] [info] Total Errors:      1120
[2025-10-12 17:28:34] [info] Total Warnings:    321
[2025-10-12 17:28:34] [info] DRC Status:        FAILED
```

lvr_summary_426.rpt

```
[2025-10-12 17:29:31] [info] =====
[2025-10-12 17:29:31] [info] Design Rule Check Report
[2025-10-12 17:29:31] [info] =====

[2025-10-12 17:29:31] [info] Report Generated: 2025-10-12
[2025-10-12 17:29:31] [info] Run Start Time: 05:28:40 PM
[2025-10-12 17:29:31] [info] Run End Time: 05:29:31 PM
[2025-10-12 17:29:31] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:29:31] [info] Tool Version: LVR_2025.1
[2025-10-12 17:29:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:29:31] [info] User Name: ubuntu
[2025-10-12 17:29:31] [info] Cores Used: 8
[2025-10-12 17:29:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Top Cell Information
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Layout Top Cell: TOP
[2025-10-12 17:29:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:29:31] [info] Database Format: GDSII
[2025-10-12 17:29:31] [info] Verification Mode: Full Check

[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Type of Errors | Count
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] DB_WARN -> 1
[2025-10-12 17:29:31] [info] LRF_ERR001 -> 14
[2025-10-12 17:29:31] [info] DB_WARN002 -> 159
[2025-10-12 17:29:31] [info] DB_WARN003 -> 161
[2025-10-12 17:29:31] [info] DRC_ERR9 -> 89
[2025-10-12 17:29:31] [info] DRC_ERR -> 67
[2025-10-12 17:29:31] [info] DRC_ERR5 -> 408
[2025-10-12 17:29:31] [info] DRC_ERR4 -> 241
[2025-10-12 17:29:31] [info] DRC_ERR1 -> 301
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Rule Violation Summary
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:29:31] [info] |-----|-----|-----|
[2025-10-12 17:29:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:29:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:29:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:29:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:29:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:29:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:29:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:29:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:29:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:29:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:29:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:29:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:29:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:29:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:29:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:29:31] [info]
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Performance Metrics
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:29:31] [info] |-----|-----|
[2025-10-12 17:29:31] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:29:31] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 17:29:31] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Summary
[2025-10-12 17:29:31] [info] -----
[2025-10-12 17:29:31] [info] Total Errors: 1120
[2025-10-12 17:29:31] [info] Total Warnings: 321
[2025-10-12 17:29:31] [info] DRC Status: FAILED
```

lvr_summary_427.rpt

```
[2025-10-12 17:30:27] [info] =====
[2025-10-12 17:30:27] [info] Design Rule Check Report
[2025-10-12 17:30:27] [info] =====

[2025-10-12 17:30:27] [info] Report Generated: 2025-10-12
[2025-10-12 17:30:27] [info] Run Start Time: 05:29:36 PM
[2025-10-12 17:30:27] [info] Run End Time: 05:30:27 PM
[2025-10-12 17:30:27] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:30:27] [info] Tool Version: LVR_2025.1
[2025-10-12 17:30:27] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:30:27] [info] User Name: ubuntu
[2025-10-12 17:30:27] [info] Cores Used: 8
[2025-10-12 17:30:27] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Top Cell Information
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Layout Top Cell: TOP
[2025-10-12 17:30:27] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:30:27] [info] Database Format: GDSII
[2025-10-12 17:30:27] [info] Verification Mode: Full Check

[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Type of Errors | Count
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] DB_WARN -> 1
[2025-10-12 17:30:27] [info] LRF_ERR001 -> 14
[2025-10-12 17:30:27] [info] DB_WARN002 -> 159
[2025-10-12 17:30:27] [info] DB_WARN003 -> 161
[2025-10-12 17:30:27] [info] DRC_ERR9 -> 89
[2025-10-12 17:30:27] [info] DRC_ERR -> 67
[2025-10-12 17:30:27] [info] DRC_ERR5 -> 408
[2025-10-12 17:30:27] [info] DRC_ERR4 -> 241
[2025-10-12 17:30:27] [info] DRC_ERR1 -> 301
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Rule Violation Summary
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:30:27] [info] |-----|-----|-----|
[2025-10-12 17:30:27] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:30:27] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:30:27] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:30:27] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:30:27] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 17:30:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:30:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:30:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:30:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:30:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:30:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:30:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:30:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:30:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:30:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:30:27] [info]
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Performance Metrics
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] | Stage | Time Taken |
[2025-10-12 17:30:27] [info] |-----|-----|
[2025-10-12 17:30:27] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:30:27] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:30:27] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Summary
[2025-10-12 17:30:27] [info] -----
[2025-10-12 17:30:27] [info] Total Errors: 1120
[2025-10-12 17:30:27] [info] Total Warnings: 321
[2025-10-12 17:30:27] [info] DRC Status: FAILED
```

lvr_summary_428.rpt

```
[2025-10-12 17:31:24] [info] =====
[2025-10-12 17:31:24] [info] Design Rule Check Report
[2025-10-12 17:31:24] [info] =====

[2025-10-12 17:31:24] [info] Report Generated: 2025-10-12
[2025-10-12 17:31:24] [info] Run Start Time: 05:30:33 PM
[2025-10-12 17:31:24] [info] Run End Time: 05:31:24 PM
[2025-10-12 17:31:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:31:24] [info] Tool Version: LVR_2025.1
[2025-10-12 17:31:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:31:24] [info] User Name: ubuntu
[2025-10-12 17:31:24] [info] Cores Used: 8
[2025-10-12 17:31:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Top Cell Information
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Layout Top Cell: TOP
[2025-10-12 17:31:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:31:24] [info] Database Format: GDSII
[2025-10-12 17:31:24] [info] Verification Mode: Full Check

[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Type of Errors | Count
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] DB_WARN -> 1
[2025-10-12 17:31:24] [info] LRF_ERR001 -> 14
[2025-10-12 17:31:24] [info] DB_WARN002 -> 159
[2025-10-12 17:31:24] [info] DB_WARN003 -> 161
```

```

[2025-10-12 17:31:24] [info] DRC_ERR9 -> 89
[2025-10-12 17:31:24] [info] DRC_ERR -> 67
[2025-10-12 17:31:24] [info] DRC_ERR5 -> 408
[2025-10-12 17:31:24] [info] DRC_ERR4 -> 241
[2025-10-12 17:31:24] [info] DRC_ERR1 -> 301
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Rule Violation Summary
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:31:24] [info] |-----|-----|-----|
[2025-10-12 17:31:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:31:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:31:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:31:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:31:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:31:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:31:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:31:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:31:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:31:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:31:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:31:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:31:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:31:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:31:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Performance Metrics
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] | Stage | Time Taken |
[2025-10-12 17:31:24] [info] |-----|-----|
[2025-10-12 17:31:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:31:24] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:31:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Summary
[2025-10-12 17:31:24] [info] -----
[2025-10-12 17:31:24] [info] Total Errors: 1120
[2025-10-12 17:31:24] [info] Total Warnings: 321
[2025-10-12 17:31:24] [info] DRC Status: FAILED

```

lvr_summary_429.rpt

```

[2025-10-12 17:32:21] [info] =====
[2025-10-12 17:32:21] [info] Design Rule Check Report
[2025-10-12 17:32:21] [info] =====

[2025-10-12 17:32:21] [info] Report Generated: 2025-10-12
[2025-10-12 17:32:21] [info] Run Start Time: 05:31:31 PM
[2025-10-12 17:32:21] [info] Run End Time: 05:32:21 PM
[2025-10-12 17:32:21] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:32:21] [info] Tool Version: LVR_2025.1
[2025-10-12 17:32:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:32:21] [info] User Name: ubuntu
[2025-10-12 17:32:21] [info] Cores Used: 8
[2025-10-12 17:32:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Top Cell Information
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Layout Top Cell:      TOP
[2025-10-12 17:32:21] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:32:21] [info] Database Format:       GDSII
[2025-10-12 17:32:21] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Type of Errors | Count
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] DB_WARN -> 1
[2025-10-12 17:32:21] [info] LRF_ERR001 -> 14
[2025-10-12 17:32:21] [info] DB_WARN002 -> 159
[2025-10-12 17:32:21] [info] DB_WARN003 -> 161
[2025-10-12 17:32:21] [info] DRC_ERR9 -> 89
[2025-10-12 17:32:21] [info] DRC_ERR -> 67
[2025-10-12 17:32:21] [info] DRC_ERR5 -> 408
[2025-10-12 17:32:21] [info] DRC_ERR4 -> 241
[2025-10-12 17:32:21] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Rule Violation Summary
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:32:21] [info] |-----|-----|-----|
[2025-10-12 17:32:21] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:32:21] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:32:21] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:32:21] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:32:21] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:32:21] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:32:21] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:32:21] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:32:21] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:32:21] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:32:21] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:32:21] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:32:21] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:32:21] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:32:21] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:32:21] [info] |
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Performance Metrics
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] | Stage          | Time Taken |
[2025-10-12 17:32:21] [info] |-----|-----|
[2025-10-12 17:32:21] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:32:21] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:32:21] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Summary
```

```
[2025-10-12 17:32:21] [info] -----
[2025-10-12 17:32:21] [info] Total Errors:      1120
[2025-10-12 17:32:21] [info] Total Warnings:    321
[2025-10-12 17:32:21] [info] DRC Status:        FAILED
```

lvr_summary_43.rpt

```
[2025-10-12 11:22:09] [info] =====
[2025-10-12 11:22:09] [info] Design Rule Check Report
[2025-10-12 11:22:09] [info] =====

[2025-10-12 11:22:09] [info] Report Generated: 2025-10-12
[2025-10-12 11:22:09] [info] Run Start Time: 11:21:18 AM
[2025-10-12 11:22:09] [info] Run End Time: 11:22:09 AM
[2025-10-12 11:22:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:22:09] [info] Tool Version: LVR_2025.1
[2025-10-12 11:22:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:22:09] [info] User Name: ubuntu
[2025-10-12 11:22:09] [info] Cores Used: 8
[2025-10-12 11:22:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Top Cell Information
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Layout Top Cell: TOP
[2025-10-12 11:22:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:22:09] [info] Database Format: GDSII
[2025-10-12 11:22:09] [info] Verification Mode: Full Check

[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Type of Errors | Count
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] DB_WARN -> 1
[2025-10-12 11:22:09] [info] LRF_ERR001 -> 14
[2025-10-12 11:22:09] [info] DB_WARN002 -> 159
[2025-10-12 11:22:09] [info] DB_WARN003 -> 161
[2025-10-12 11:22:09] [info] DRC_ERR9 -> 89
[2025-10-12 11:22:09] [info] DRC_ERR -> 67
[2025-10-12 11:22:09] [info] DRC_ERR5 -> 408
[2025-10-12 11:22:09] [info] DRC_ERR4 -> 241
[2025-10-12 11:22:09] [info] DRC_ERR1 -> 301
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Rule Violation Summary
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:22:09] [info] |-----|-----|-----|
[2025-10-12 11:22:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:22:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:22:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:22:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:22:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:22:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:22:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:22:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:22:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:22:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:22:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:22:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:22:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:22:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:22:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:22:09] [info]
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Performance Metrics
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:22:09] [info] |-----|-----|
[2025-10-12 11:22:09] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:22:09] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:22:09] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Summary
[2025-10-12 11:22:09] [info] -----
[2025-10-12 11:22:09] [info] Total Errors: 1120
[2025-10-12 11:22:09] [info] Total Warnings: 321
[2025-10-12 11:22:09] [info] DRC Status: FAILED
```

lvr_summary_430.rpt

```
[2025-10-12 17:33:20] [info] =====
[2025-10-12 17:33:20] [info] Design Rule Check Report
[2025-10-12 17:33:20] [info] =====

[2025-10-12 17:33:20] [info] Report Generated: 2025-10-12
[2025-10-12 17:33:20] [info] Run Start Time: 05:32:27 PM
[2025-10-12 17:33:20] [info] Run End Time: 05:33:20 PM
[2025-10-12 17:33:20] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:33:20] [info] Tool Version: LVR_2025.1
[2025-10-12 17:33:20] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:33:20] [info] User Name: ubuntu
[2025-10-12 17:33:20] [info] Cores Used: 8
[2025-10-12 17:33:20] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Top Cell Information
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Layout Top Cell: TOP
[2025-10-12 17:33:20] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:33:20] [info] Database Format: GDSII
[2025-10-12 17:33:20] [info] Verification Mode: Full Check

[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Type of Errors | Count
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] DB_WARN -> 1
[2025-10-12 17:33:20] [info] LRF_ERR001 -> 14
[2025-10-12 17:33:20] [info] DB_WARN002 -> 159
[2025-10-12 17:33:20] [info] DB_WARN003 -> 161
[2025-10-12 17:33:20] [info] DRC_ERR9 -> 89
[2025-10-12 17:33:20] [info] DRC_ERR -> 67
[2025-10-12 17:33:20] [info] DRC_ERR5 -> 408
[2025-10-12 17:33:20] [info] DRC_ERR4 -> 241
[2025-10-12 17:33:20] [info] DRC_ERR1 -> 301
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Rule Violation Summary
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:33:20] [info] |-----|-----|-----|
[2025-10-12 17:33:20] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:33:20] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:33:20] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:33:20] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:33:20] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```
[2025-10-12 17:33:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:33:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:33:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:33:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:33:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:33:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:33:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:33:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:33:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:33:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:33:20] [info]
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Performance Metrics
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] | Stage | Time Taken |
[2025-10-12 17:33:20] [info] |-----|-----|
[2025-10-12 17:33:20] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:33:20] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:33:20] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Summary
[2025-10-12 17:33:20] [info] -----
[2025-10-12 17:33:20] [info] Total Errors: 1120
[2025-10-12 17:33:20] [info] Total Warnings: 321
[2025-10-12 17:33:20] [info] DRC Status: FAILED
```

lvr_summary_431.rpt

```
[2025-10-12 17:34:16] [info] =====
[2025-10-12 17:34:16] [info] Design Rule Check Report
[2025-10-12 17:34:16] [info] =====

[2025-10-12 17:34:16] [info] Report Generated: 2025-10-12
[2025-10-12 17:34:16] [info] Run Start Time: 05:33:24 PM
[2025-10-12 17:34:16] [info] Run End Time: 05:34:16 PM
[2025-10-12 17:34:16] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:34:16] [info] Tool Version: LVR_2025.1
[2025-10-12 17:34:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:34:16] [info] User Name: ubuntu
[2025-10-12 17:34:16] [info] Cores Used: 8
[2025-10-12 17:34:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Top Cell Information
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Layout Top Cell: TOP
[2025-10-12 17:34:16] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:34:16] [info] Database Format: GDSII
[2025-10-12 17:34:16] [info] Verification Mode: Full Check

[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Type of Errors | Count
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] DB_WARN -> 1
[2025-10-12 17:34:16] [info] LRF_ERR001 -> 14
[2025-10-12 17:34:16] [info] DB_WARN002 -> 159
[2025-10-12 17:34:16] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:34:16] [info] DRC_ERR9 -> 89
[2025-10-12 17:34:16] [info] DRC_ERR -> 67
[2025-10-12 17:34:16] [info] DRC_ERR5 -> 408
[2025-10-12 17:34:16] [info] DRC_ERR4 -> 241
[2025-10-12 17:34:16] [info] DRC_ERR1 -> 301
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Rule Violation Summary
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:34:16] [info] |-----|-----|-----|
[2025-10-12 17:34:16] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:34:16] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:34:16] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:34:16] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:34:16] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:34:16] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:34:16] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:34:16] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:34:16] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:34:16] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:34:16] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:34:16] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:34:16] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:34:16] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:34:16] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:34:16] [info] |
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Performance Metrics
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] | Stage          | Time Taken |
[2025-10-12 17:34:16] [info] |-----|-----|
[2025-10-12 17:34:16] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:34:16] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:34:16] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:34:16] [info] |
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Summary
[2025-10-12 17:34:16] [info] -----
[2025-10-12 17:34:16] [info] Total Errors: 1120
[2025-10-12 17:34:16] [info] Total Warnings: 321
[2025-10-12 17:34:16] [info] DRC Status: FAILED
```

lvr_summary_432.rpt

```
[2025-10-12 17:35:11] [info] =====
[2025-10-12 17:35:11] [info] Design Rule Check Report
[2025-10-12 17:35:11] [info] =====

[2025-10-12 17:35:11] [info] Report Generated: 2025-10-12
[2025-10-12 17:35:11] [info] Run Start Time: 05:34:21 PM
[2025-10-12 17:35:11] [info] Run End Time: 05:35:11 PM
[2025-10-12 17:35:11] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:35:11] [info] Tool Version: LVR_2025.1
[2025-10-12 17:35:11] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:35:11] [info] User Name: ubuntu
[2025-10-12 17:35:11] [info] Cores Used: 8
[2025-10-12 17:35:11] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Top Cell Information
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Layout Top Cell:      TOP
[2025-10-12 17:35:11] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:35:11] [info] Database Format:      GDSII
[2025-10-12 17:35:11] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Type of Errors | Count
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] DB_WARN -> 1
[2025-10-12 17:35:11] [info] LRF_ERR001 -> 14
[2025-10-12 17:35:11] [info] DB_WARN002 -> 159
[2025-10-12 17:35:11] [info] DB_WARN003 -> 161
[2025-10-12 17:35:11] [info] DRC_ERR9 -> 89
[2025-10-12 17:35:11] [info] DRC_ERR -> 67
[2025-10-12 17:35:11] [info] DRC_ERR5 -> 408
[2025-10-12 17:35:11] [info] DRC_ERR4 -> 241
[2025-10-12 17:35:11] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Rule Violation Summary
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:35:11] [info] |-----|-----|-----|
[2025-10-12 17:35:11] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:35:11] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:35:11] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:35:11] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:35:11] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:35:11] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:35:11] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:35:11] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:35:11] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:35:11] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:35:11] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:35:11] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:35:11] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:35:11] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:35:11] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:35:11] [info] |
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Performance Metrics
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] | Stage          | Time Taken |
[2025-10-12 17:35:11] [info] |-----|-----|
[2025-10-12 17:35:11] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:35:11] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 17:35:11] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Summary
```

```
[2025-10-12 17:35:11] [info] -----
[2025-10-12 17:35:11] [info] Total Errors:      1120
[2025-10-12 17:35:11] [info] Total Warnings:    321
[2025-10-12 17:35:11] [info] DRC Status:        FAILED
```

lvr_summary_433.rpt

```
[2025-10-12 17:36:11] [info] =====
[2025-10-12 17:36:11] [info]                      Design Rule Check Report
[2025-10-12 17:36:11] [info] =====

[2025-10-12 17:36:11] [info] Report Generated:      2025-10-12
[2025-10-12 17:36:11] [info] Run Start Time:       05:35:20 PM
[2025-10-12 17:36:11] [info] Run End Time:         05:36:11 PM
[2025-10-12 17:36:11] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 17:36:11] [info] Tool Version:         LVR_2025.1
[2025-10-12 17:36:11] [info] Host Name:            ip-172-31-37-173
[2025-10-12 17:36:11] [info] User Name:            ubuntu
[2025-10-12 17:36:11] [info] Cores Used:           8
[2025-10-12 17:36:11] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info]                      Top Cell Information
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info] Layout Top Cell:      TOP
[2025-10-12 17:36:11] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 17:36:11] [info] Database Format:        GDSII
[2025-10-12 17:36:11] [info] Verification Mode:     Full Check

[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info]                      Type of Errors | Count
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info] DB_WARN -> 1
[2025-10-12 17:36:11] [info] LRF_ERR001 -> 14
[2025-10-12 17:36:11] [info] DB_WARN002 -> 159
[2025-10-12 17:36:11] [info] DB_WARN003 -> 161
[2025-10-12 17:36:11] [info] DRC_ERR9 -> 89
[2025-10-12 17:36:11] [info] DRC_ERR -> 67
[2025-10-12 17:36:11] [info] DRC_ERR5 -> 408
[2025-10-12 17:36:11] [info] DRC_ERR4 -> 241
[2025-10-12 17:36:11] [info] DRC_ERR1 -> 301
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info]                      Rule Violation Summary
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:36:11] [info] |-----|-----|-----|
[2025-10-12 17:36:11] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:36:11] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:36:11] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:36:11] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:36:11] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:36:11] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:36:11] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:36:11] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:36:11] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:36:11] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:36:11] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:36:11] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:36:11] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:36:11] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:36:11] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:36:11] [info]
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info]                      Performance Metrics
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:36:11] [info] |-----|-----|
[2025-10-12 17:36:11] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:36:11] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:36:11] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info]                Summary
[2025-10-12 17:36:11] [info] -----
[2025-10-12 17:36:11] [info] Total Errors:   1120
[2025-10-12 17:36:11] [info] Total Warnings:    321
[2025-10-12 17:36:11] [info] DRC Status:      FAILED
```

lvr_summary_434.rpt

```
[2025-10-12 17:37:08] [info] =====
[2025-10-12 17:37:08] [info]                Design Rule Check Report
[2025-10-12 17:37:08] [info] =====

[2025-10-12 17:37:08] [info] Report Generated:   2025-10-12
[2025-10-12 17:37:08] [info] Run Start Time:     05:36:18 PM
[2025-10-12 17:37:08] [info] Run End Time:       05:37:08 PM
[2025-10-12 17:37:08] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 17:37:08] [info] Tool Version:       LVR_2025.1
[2025-10-12 17:37:08] [info] Host Name:          ip-172-31-37-173
[2025-10-12 17:37:08] [info] User Name:          ubuntu
[2025-10-12 17:37:08] [info] Cores Used:         8
[2025-10-12 17:37:08] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info]                Top Cell Information
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] Layout Top Cell:    TOP
[2025-10-12 17:37:08] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 17:37:08] [info] Database Format:      GDSII
[2025-10-12 17:37:08] [info] Verification Mode:   Full Check

[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info]                Type of Errors | Count
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] DB_WARN -> 1
[2025-10-12 17:37:08] [info] LRF_ERR001 -> 14
[2025-10-12 17:37:08] [info] DB_WARN002 -> 159
[2025-10-12 17:37:08] [info] DB_WARN003 -> 161
[2025-10-12 17:37:08] [info] DRC_ERR9 -> 89
[2025-10-12 17:37:08] [info] DRC_ERR -> 67
[2025-10-12 17:37:08] [info] DRC_ERR5 -> 408
[2025-10-12 17:37:08] [info] DRC_ERR4 -> 241
[2025-10-12 17:37:08] [info] DRC_ERR1 -> 301
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info]                Rule Violation Summary
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:37:08] [info] |-----|-----|-----|
[2025-10-12 17:37:08] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:37:08] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:37:08] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:37:08] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:37:08] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:37:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:37:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:37:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:37:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:37:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:37:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:37:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:37:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:37:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:37:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:37:08] [info]
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] Performance Metrics
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] | Stage | Time Taken |
[2025-10-12 17:37:08] [info] |-----|-----|
[2025-10-12 17:37:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:37:08] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:37:08] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] Summary
[2025-10-12 17:37:08] [info] -----
[2025-10-12 17:37:08] [info] Total Errors: 1120
[2025-10-12 17:37:08] [info] Total Warnings: 321
[2025-10-12 17:37:08] [info] DRC Status: FAILED
```

lvr_summary_435.rpt

```
[2025-10-12 17:38:06] [info] =====
[2025-10-12 17:38:06] [info] Design Rule Check Report
[2025-10-12 17:38:06] [info] =====

[2025-10-12 17:38:06] [info] Report Generated: 2025-10-12
[2025-10-12 17:38:06] [info] Run Start Time: 05:37:15 PM
[2025-10-12 17:38:06] [info] Run End Time: 05:38:06 PM
[2025-10-12 17:38:06] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:38:06] [info] Tool Version: LVR_2025.1
[2025-10-12 17:38:06] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:38:06] [info] User Name: ubuntu
[2025-10-12 17:38:06] [info] Cores Used: 8
[2025-10-12 17:38:06] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Top Cell Information
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Layout Top Cell: TOP
[2025-10-12 17:38:06] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:38:06] [info] Database Format: GDSII
[2025-10-12 17:38:06] [info] Verification Mode: Full Check

[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Type of Errors | Count
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] DB_WARN -> 1
[2025-10-12 17:38:06] [info] LRF_ERR001 -> 14
[2025-10-12 17:38:06] [info] DB_WARN002 -> 159
[2025-10-12 17:38:06] [info] DB_WARN003 -> 161
```

```

[2025-10-12 17:38:06] [info] DRC_ERR9 -> 89
[2025-10-12 17:38:06] [info] DRC_ERR -> 67
[2025-10-12 17:38:06] [info] DRC_ERR5 -> 408
[2025-10-12 17:38:06] [info] DRC_ERR4 -> 241
[2025-10-12 17:38:06] [info] DRC_ERR1 -> 301
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Rule Violation Summary
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:38:06] [info] |-----|-----|-----|
[2025-10-12 17:38:06] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:38:06] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:38:06] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:38:06] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:38:06] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:38:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:38:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:38:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:38:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:38:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:38:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:38:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:38:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:38:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:38:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Performance Metrics
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] | Stage | Time Taken |
[2025-10-12 17:38:06] [info] |-----|-----|
[2025-10-12 17:38:06] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:38:06] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:38:06] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Summary
[2025-10-12 17:38:06] [info] -----
[2025-10-12 17:38:06] [info] Total Errors: 1120
[2025-10-12 17:38:06] [info] Total Warnings: 321
[2025-10-12 17:38:06] [info] DRC Status: FAILED

```

lvr_summary_436.rpt

```

[2025-10-12 17:39:05] [info] =====
[2025-10-12 17:39:05] [info] Design Rule Check Report
[2025-10-12 17:39:05] [info] =====

[2025-10-12 17:39:05] [info] Report Generated: 2025-10-12
[2025-10-12 17:39:05] [info] Run Start Time: 05:38:13 PM
[2025-10-12 17:39:05] [info] Run End Time: 05:39:05 PM
[2025-10-12 17:39:05] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:39:05] [info] Tool Version: LVR_2025.1
[2025-10-12 17:39:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:39:05] [info] User Name: ubuntu
[2025-10-12 17:39:05] [info] Cores Used: 8
[2025-10-12 17:39:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Top Cell Information
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Layout Top Cell:      TOP
[2025-10-12 17:39:05] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:39:05] [info] Database Format:      GDSII
[2025-10-12 17:39:05] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Type of Errors | Count
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] DB_WARN -> 1
[2025-10-12 17:39:05] [info] LRF_ERR001 -> 14
[2025-10-12 17:39:05] [info] DB_WARN002 -> 159
[2025-10-12 17:39:05] [info] DB_WARN003 -> 161
[2025-10-12 17:39:05] [info] DRC_ERR9 -> 89
[2025-10-12 17:39:05] [info] DRC_ERR -> 67
[2025-10-12 17:39:05] [info] DRC_ERR5 -> 408
[2025-10-12 17:39:05] [info] DRC_ERR4 -> 241
[2025-10-12 17:39:05] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Rule Violation Summary
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:39:05] [info] |-----|-----|-----|
[2025-10-12 17:39:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:39:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:39:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:39:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:39:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:39:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:39:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:39:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:39:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:39:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:39:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:39:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:39:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:39:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:39:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Performance Metrics
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] | Stage | Time Taken |
[2025-10-12 17:39:05] [info] |-----|-----|
[2025-10-12 17:39:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:39:05] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:39:05] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Summary
[2025-10-12 17:39:05] [info] -----
[2025-10-12 17:39:05] [info] Total Errors:      1120
[2025-10-12 17:39:05] [info] Total Warnings:    321
[2025-10-12 17:39:05] [info] DRC Status:        FAILED
```


lvr_summary_437.rpt

```
[2025-10-12 17:40:00] [info] =====
[2025-10-12 17:40:00] [info] Design Rule Check Report
[2025-10-12 17:40:00] [info] =====

[2025-10-12 17:40:00] [info] Report Generated: 2025-10-12
[2025-10-12 17:40:00] [info] Run Start Time: 05:39:09 PM
[2025-10-12 17:40:00] [info] Run End Time: 05:40:00 PM
[2025-10-12 17:40:00] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:40:00] [info] Tool Version: LVR_2025.1
[2025-10-12 17:40:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:40:00] [info] User Name: ubuntu
[2025-10-12 17:40:00] [info] Cores Used: 8
[2025-10-12 17:40:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] Top Cell Information
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] Layout Top Cell: TOP
[2025-10-12 17:40:00] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:40:00] [info] Database Format: GDSII
[2025-10-12 17:40:00] [info] Verification Mode: Full Check

[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] Type of Errors | Count
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] DB_WARN -> 1
[2025-10-12 17:40:00] [info] LRF_ERR001 -> 14
[2025-10-12 17:40:00] [info] DB_WARN002 -> 159
[2025-10-12 17:40:00] [info] DB_WARN003 -> 161
[2025-10-12 17:40:00] [info] DRC_ERR9 -> 89
[2025-10-12 17:40:00] [info] DRC_ERR -> 67
[2025-10-12 17:40:00] [info] DRC_ERR5 -> 408
[2025-10-12 17:40:00] [info] DRC_ERR4 -> 241
[2025-10-12 17:40:00] [info] DRC_ERR1 -> 301
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] Rule Violation Summary
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:40:00] [info] |-----|-----|-----|
[2025-10-12 17:40:00] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:40:00] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:40:00] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:40:00] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:40:00] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:40:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:40:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:40:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:40:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:40:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:40:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:40:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:40:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:40:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:40:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:40:00] [info]
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] Performance Metrics
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:40:00] [info] |-----|-----|
[2025-10-12 17:40:00] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:40:00] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:40:00] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info]                Summary
[2025-10-12 17:40:00] [info] -----
[2025-10-12 17:40:00] [info] Total Errors:    1120
[2025-10-12 17:40:00] [info] Total Warnings:    321
[2025-10-12 17:40:00] [info] DRC Status:      FAILED
```

lvr_summary_438.rpt

```
[2025-10-12 17:40:57] [info] =====
[2025-10-12 17:40:57] [info]                Design Rule Check Report
[2025-10-12 17:40:57] [info] =====

[2025-10-12 17:40:57] [info] Report Generated:    2025-10-12
[2025-10-12 17:40:57] [info] Run Start Time:      05:40:06 PM
[2025-10-12 17:40:57] [info] Run End Time:        05:40:57 PM
[2025-10-12 17:40:57] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 17:40:57] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:40:57] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:40:57] [info] User Name:           ubuntu
[2025-10-12 17:40:57] [info] Cores Used:          8
[2025-10-12 17:40:57] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info]                Top Cell Information
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] Layout Top Cell:     TOP
[2025-10-12 17:40:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:40:57] [info] Database Format:       GDSII
[2025-10-12 17:40:57] [info] Verification Mode:    Full Check

[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info]                Type of Errors | Count
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] DB_WARN -> 1
[2025-10-12 17:40:57] [info] LRF_ERR001 -> 14
[2025-10-12 17:40:57] [info] DB_WARN002 -> 159
[2025-10-12 17:40:57] [info] DB_WARN003 -> 161
[2025-10-12 17:40:57] [info] DRC_ERR9 -> 89
[2025-10-12 17:40:57] [info] DRC_ERR -> 67
[2025-10-12 17:40:57] [info] DRC_ERR5 -> 408
[2025-10-12 17:40:57] [info] DRC_ERR4 -> 241
[2025-10-12 17:40:57] [info] DRC_ERR1 -> 301
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info]                Rule Violation Summary
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:40:57] [info] |-----|-----|-----|
[2025-10-12 17:40:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:40:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:40:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:40:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:40:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:40:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:40:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:40:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:40:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:40:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:40:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:40:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:40:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:40:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:40:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:40:57] [info]
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] Performance Metrics
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] | Stage | Time Taken |
[2025-10-12 17:40:57] [info] |-----|-----|
[2025-10-12 17:40:57] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:40:57] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:40:57] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] Summary
[2025-10-12 17:40:57] [info] -----
[2025-10-12 17:40:57] [info] Total Errors: 1120
[2025-10-12 17:40:57] [info] Total Warnings: 321
[2025-10-12 17:40:57] [info] DRC Status: FAILED
```

lvr_summary_439.rpt

```
[2025-10-12 17:41:55] [info] =====
[2025-10-12 17:41:55] [info] Design Rule Check Report
[2025-10-12 17:41:55] [info] =====

[2025-10-12 17:41:55] [info] Report Generated: 2025-10-12
[2025-10-12 17:41:55] [info] Run Start Time: 05:41:04 PM
[2025-10-12 17:41:55] [info] Run End Time: 05:41:55 PM
[2025-10-12 17:41:55] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:41:55] [info] Tool Version: LVR_2025.1
[2025-10-12 17:41:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:41:55] [info] User Name: ubuntu
[2025-10-12 17:41:55] [info] Cores Used: 8
[2025-10-12 17:41:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Top Cell Information
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Layout Top Cell: TOP
[2025-10-12 17:41:55] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:41:55] [info] Database Format: GDSII
[2025-10-12 17:41:55] [info] Verification Mode: Full Check

[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Type of Errors | Count
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] DB_WARN -> 1
[2025-10-12 17:41:55] [info] LRF_ERR001 -> 14
[2025-10-12 17:41:55] [info] DB_WARN002 -> 159
[2025-10-12 17:41:55] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:41:55] [info] DRC_ERR9 -> 89
[2025-10-12 17:41:55] [info] DRC_ERR -> 67
[2025-10-12 17:41:55] [info] DRC_ERR5 -> 408
[2025-10-12 17:41:55] [info] DRC_ERR4 -> 241
[2025-10-12 17:41:55] [info] DRC_ERR1 -> 301
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Rule Violation Summary
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:41:55] [info] |-----|-----|-----|
[2025-10-12 17:41:55] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:41:55] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:41:55] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:41:55] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:41:55] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:41:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:41:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:41:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:41:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:41:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:41:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:41:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:41:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:41:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:41:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Performance Metrics
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] | Stage | Time Taken |
[2025-10-12 17:41:55] [info] |-----|-----|
[2025-10-12 17:41:55] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:41:55] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:41:55] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Summary
[2025-10-12 17:41:55] [info] -----
[2025-10-12 17:41:55] [info] Total Errors: 1120
[2025-10-12 17:41:55] [info] Total Warnings: 321
[2025-10-12 17:41:55] [info] DRC Status: FAILED
```

lvr_summary_44.rpt

```
[2025-10-12 11:23:08] [info] =====
[2025-10-12 11:23:08] [info] Design Rule Check Report
[2025-10-12 11:23:08] [info] =====

[2025-10-12 11:23:08] [info] Report Generated: 2025-10-12
[2025-10-12 11:23:08] [info] Run Start Time: 11:22:16 AM
[2025-10-12 11:23:08] [info] Run End Time: 11:23:08 AM
[2025-10-12 11:23:08] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:23:08] [info] Tool Version: LVR_2025.1
[2025-10-12 11:23:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:23:08] [info] User Name: ubuntu
[2025-10-12 11:23:08] [info] Cores Used: 8
[2025-10-12 11:23:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Top Cell Information
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Layout Top Cell:      TOP
[2025-10-12 11:23:08] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:23:08] [info] Database Format:      GDSII
[2025-10-12 11:23:08] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Type of Errors | Count
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] DB_WARN -> 1
[2025-10-12 11:23:08] [info] LRF_ERR001 -> 14
[2025-10-12 11:23:08] [info] DB_WARN002 -> 159
[2025-10-12 11:23:08] [info] DB_WARN003 -> 161
[2025-10-12 11:23:08] [info] DRC_ERR9 -> 89
[2025-10-12 11:23:08] [info] DRC_ERR -> 67
[2025-10-12 11:23:08] [info] DRC_ERR5 -> 408
[2025-10-12 11:23:08] [info] DRC_ERR4 -> 241
[2025-10-12 11:23:08] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Rule Violation Summary
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:23:08] [info] |-----|-----|-----|
[2025-10-12 11:23:08] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:23:08] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:23:08] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:23:08] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:23:08] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:23:08] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:23:08] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:23:08] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:23:08] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:23:08] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:23:08] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:23:08] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:23:08] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:23:08] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:23:08] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:23:08] [info] |
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Performance Metrics
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] | Stage          | Time Taken |
[2025-10-12 11:23:08] [info] |-----|-----|
[2025-10-12 11:23:08] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:23:08] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 11:23:08] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Summary
```

```
[2025-10-12 11:23:08] [info] -----
[2025-10-12 11:23:08] [info] Total Errors:      1120
[2025-10-12 11:23:08] [info] Total Warnings:    321
[2025-10-12 11:23:08] [info] DRC Status:        FAILED
```

lvr_summary_440.rpt

```
[2025-10-12 17:42:53] [info] =====
[2025-10-12 17:42:53] [info] Design Rule Check Report
[2025-10-12 17:42:53] [info] =====

[2025-10-12 17:42:53] [info] Report Generated: 2025-10-12
[2025-10-12 17:42:53] [info] Run Start Time: 05:42:02 PM
[2025-10-12 17:42:53] [info] Run End Time: 05:42:53 PM
[2025-10-12 17:42:53] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:42:53] [info] Tool Version: LVR_2025.1
[2025-10-12 17:42:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:42:53] [info] User Name: ubuntu
[2025-10-12 17:42:53] [info] Cores Used: 8
[2025-10-12 17:42:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] Top Cell Information
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] Layout Top Cell: TOP
[2025-10-12 17:42:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:42:53] [info] Database Format: GDSII
[2025-10-12 17:42:53] [info] Verification Mode: Full Check

[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] Type of Errors | Count
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] DB_WARN -> 1
[2025-10-12 17:42:53] [info] LRF_ERR001 -> 14
[2025-10-12 17:42:53] [info] DB_WARN002 -> 159
[2025-10-12 17:42:53] [info] DB_WARN003 -> 161
[2025-10-12 17:42:53] [info] DRC_ERR9 -> 89
[2025-10-12 17:42:53] [info] DRC_ERR -> 67
[2025-10-12 17:42:53] [info] DRC_ERR5 -> 408
[2025-10-12 17:42:53] [info] DRC_ERR4 -> 241
[2025-10-12 17:42:53] [info] DRC_ERR1 -> 301
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] Rule Violation Summary
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:42:53] [info] |-----|-----|-----|
[2025-10-12 17:42:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:42:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:42:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:42:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:42:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:42:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:42:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:42:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:42:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:42:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:42:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:42:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:42:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:42:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:42:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:42:53] [info]
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] Performance Metrics
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:42:53] [info] |-----|-----|
[2025-10-12 17:42:53] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:42:53] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 17:42:53] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info]                Summary
[2025-10-12 17:42:53] [info] -----
[2025-10-12 17:42:53] [info] Total Errors:    1120
[2025-10-12 17:42:53] [info] Total Warnings:    321
[2025-10-12 17:42:53] [info] DRC Status:      FAILED
```

lvr_summary_441.rpt

```
[2025-10-12 17:43:49] [info] =====
[2025-10-12 17:43:49] [info]                Design Rule Check Report
[2025-10-12 17:43:49] [info] =====

[2025-10-12 17:43:49] [info] Report Generated:    2025-10-12
[2025-10-12 17:43:49] [info] Run Start Time:      05:42:59 PM
[2025-10-12 17:43:49] [info] Run End Time:        05:43:49 PM
[2025-10-12 17:43:49] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 17:43:49] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:43:49] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:43:49] [info] User Name:           ubuntu
[2025-10-12 17:43:49] [info] Cores Used:          8
[2025-10-12 17:43:49] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info]                Top Cell Information
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] Layout Top Cell:     TOP
[2025-10-12 17:43:49] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:43:49] [info] Database Format:      GDSII
[2025-10-12 17:43:49] [info] Verification Mode:    Full Check

[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info]                Type of Errors | Count
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] DB_WARN -> 1
[2025-10-12 17:43:49] [info] LRF_ERR001 -> 14
[2025-10-12 17:43:49] [info] DB_WARN002 -> 159
[2025-10-12 17:43:49] [info] DB_WARN003 -> 161
[2025-10-12 17:43:49] [info] DRC_ERR9 -> 89
[2025-10-12 17:43:49] [info] DRC_ERR -> 67
[2025-10-12 17:43:49] [info] DRC_ERR5 -> 408
[2025-10-12 17:43:49] [info] DRC_ERR4 -> 241
[2025-10-12 17:43:49] [info] DRC_ERR1 -> 301
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info]                Rule Violation Summary
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:43:49] [info] |-----|-----|-----|
[2025-10-12 17:43:49] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:43:49] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:43:49] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:43:49] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:43:49] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:43:49] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:43:49] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:43:49] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:43:49] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:43:49] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:43:49] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:43:49] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:43:49] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:43:49] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:43:49] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:43:49] [info]
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] Performance Metrics
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] | Stage | Time Taken |
[2025-10-12 17:43:49] [info] |-----|-----|
[2025-10-12 17:43:49] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:43:49] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:43:49] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] Summary
[2025-10-12 17:43:49] [info] -----
[2025-10-12 17:43:49] [info] Total Errors: 1120
[2025-10-12 17:43:49] [info] Total Warnings: 321
[2025-10-12 17:43:49] [info] DRC Status: FAILED
```

lvr_summary_442.rpt

```
[2025-10-12 17:44:46] [info] =====
[2025-10-12 17:44:46] [info] Design Rule Check Report
[2025-10-12 17:44:46] [info] =====

[2025-10-12 17:44:46] [info] Report Generated: 2025-10-12
[2025-10-12 17:44:46] [info] Run Start Time: 05:43:56 PM
[2025-10-12 17:44:46] [info] Run End Time: 05:44:46 PM
[2025-10-12 17:44:46] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:44:46] [info] Tool Version: LVR_2025.1
[2025-10-12 17:44:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:44:46] [info] User Name: ubuntu
[2025-10-12 17:44:46] [info] Cores Used: 8
[2025-10-12 17:44:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Top Cell Information
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Layout Top Cell: TOP
[2025-10-12 17:44:46] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:44:46] [info] Database Format: GDSII
[2025-10-12 17:44:46] [info] Verification Mode: Full Check

[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Type of Errors | Count
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] DB_WARN -> 1
[2025-10-12 17:44:46] [info] LRF_ERR001 -> 14
[2025-10-12 17:44:46] [info] DB_WARN002 -> 159
[2025-10-12 17:44:46] [info] DB_WARN003 -> 161
```



```
[2025-10-12 17:44:46] [info] DRC_ERR9 -> 89
[2025-10-12 17:44:46] [info] DRC_ERR -> 67
[2025-10-12 17:44:46] [info] DRC_ERR5 -> 408
[2025-10-12 17:44:46] [info] DRC_ERR4 -> 241
[2025-10-12 17:44:46] [info] DRC_ERR1 -> 301
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Rule Violation Summary
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:44:46] [info] |-----|-----|-----|
[2025-10-12 17:44:46] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:44:46] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:44:46] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:44:46] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:44:46] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:44:46] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:44:46] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:44:46] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:44:46] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:44:46] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:44:46] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:44:46] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:44:46] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:44:46] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:44:46] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Performance Metrics
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] | Stage          | Time Taken |
[2025-10-12 17:44:46] [info] |-----|-----|
[2025-10-12 17:44:46] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:44:46] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 17:44:46] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Summary
[2025-10-12 17:44:46] [info] -----
[2025-10-12 17:44:46] [info] Total Errors: 1120
[2025-10-12 17:44:46] [info] Total Warnings: 321
[2025-10-12 17:44:46] [info] DRC Status: FAILED
```

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```
[2025-10-12 17:45:43] [info] =====
[2025-10-12 17:45:43] [info] Design Rule Check Report
[2025-10-12 17:45:43] [info] =====

[2025-10-12 17:45:43] [info] Report Generated: 2025-10-12
[2025-10-12 17:45:43] [info] Run Start Time: 05:44:52 PM
[2025-10-12 17:45:43] [info] Run End Time: 05:45:43 PM
[2025-10-12 17:45:43] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:45:43] [info] Tool Version: LVR_2025.1
[2025-10-12 17:45:43] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:45:43] [info] User Name: ubuntu
[2025-10-12 17:45:43] [info] Cores Used: 8
[2025-10-12 17:45:43] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Top Cell Information
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Layout Top Cell:      TOP
[2025-10-12 17:45:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:45:43] [info] Database Format:       GDSII
[2025-10-12 17:45:43] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Type of Errors | Count
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] DB_WARN -> 1
[2025-10-12 17:45:43] [info] LRF_ERR001 -> 14
[2025-10-12 17:45:43] [info] DB_WARN002 -> 159
[2025-10-12 17:45:43] [info] DB_WARN003 -> 161
[2025-10-12 17:45:43] [info] DRC_ERR9 -> 89
[2025-10-12 17:45:43] [info] DRC_ERR -> 67
[2025-10-12 17:45:43] [info] DRC_ERR5 -> 408
[2025-10-12 17:45:43] [info] DRC_ERR4 -> 241
[2025-10-12 17:45:43] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Rule Violation Summary
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:45:43] [info] |-----|-----|-----|
[2025-10-12 17:45:43] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:45:43] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:45:43] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:45:43] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:45:43] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:45:43] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:45:43] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:45:43] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:45:43] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:45:43] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:45:43] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:45:43] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:45:43] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:45:43] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:45:43] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:45:43] [info] |
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Performance Metrics
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] | Stage          | Time Taken |
[2025-10-12 17:45:43] [info] |-----|-----|
[2025-10-12 17:45:43] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:45:43] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:45:43] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Summary
```

```
[2025-10-12 17:45:43] [info] -----
[2025-10-12 17:45:43] [info] Total Errors:      1120
[2025-10-12 17:45:43] [info] Total Warnings:    321
[2025-10-12 17:45:43] [info] DRC Status:        FAILED
```

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```
[2025-10-12 17:46:42] [info] =====
[2025-10-12 17:46:42] [info] Design Rule Check Report
[2025-10-12 17:46:42] [info] =====

[2025-10-12 17:46:42] [info] Report Generated: 2025-10-12
[2025-10-12 17:46:42] [info] Run Start Time: 05:45:50 PM
[2025-10-12 17:46:42] [info] Run End Time: 05:46:42 PM
[2025-10-12 17:46:42] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:46:42] [info] Tool Version: LVR_2025.1
[2025-10-12 17:46:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:46:42] [info] User Name: ubuntu
[2025-10-12 17:46:42] [info] Cores Used: 8
[2025-10-12 17:46:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] Top Cell Information
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] Layout Top Cell: TOP
[2025-10-12 17:46:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:46:42] [info] Database Format: GDSII
[2025-10-12 17:46:42] [info] Verification Mode: Full Check

[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] Type of Errors | Count
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] DB_WARN -> 1
[2025-10-12 17:46:42] [info] LRF_ERR001 -> 14
[2025-10-12 17:46:42] [info] DB_WARN002 -> 159
[2025-10-12 17:46:42] [info] DB_WARN003 -> 161
[2025-10-12 17:46:42] [info] DRC_ERR9 -> 89
[2025-10-12 17:46:42] [info] DRC_ERR -> 67
[2025-10-12 17:46:42] [info] DRC_ERR5 -> 408
[2025-10-12 17:46:42] [info] DRC_ERR4 -> 241
[2025-10-12 17:46:42] [info] DRC_ERR1 -> 301
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] Rule Violation Summary
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:46:42] [info] |-----|-----|-----|
[2025-10-12 17:46:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:46:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:46:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:46:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:46:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:46:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:46:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:46:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:46:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:46:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:46:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:46:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:46:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:46:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:46:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:46:42] [info]
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] Performance Metrics
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:46:42] [info] |-----|-----|
[2025-10-12 17:46:42] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:46:42] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 17:46:42] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info]                Summary
[2025-10-12 17:46:42] [info] -----
[2025-10-12 17:46:42] [info] Total Errors:    1120
[2025-10-12 17:46:42] [info] Total Warnings:    321
[2025-10-12 17:46:42] [info] DRC Status:      FAILED
```

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```
[2025-10-12 17:47:39] [info] =====
[2025-10-12 17:47:39] [info]                Design Rule Check Report
[2025-10-12 17:47:39] [info] =====

[2025-10-12 17:47:39] [info] Report Generated:    2025-10-12
[2025-10-12 17:47:39] [info] Run Start Time:      05:46:48 PM
[2025-10-12 17:47:39] [info] Run End Time:        05:47:39 PM
[2025-10-12 17:47:39] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:47:39] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:47:39] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:47:39] [info] User Name:           ubuntu
[2025-10-12 17:47:39] [info] Cores Used:          8
[2025-10-12 17:47:39] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info]                Top Cell Information
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] Layout Top Cell:     TOP
[2025-10-12 17:47:39] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:47:39] [info] Database Format:       GDSII
[2025-10-12 17:47:39] [info] Verification Mode:    Full Check

[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info]                Type of Errors | Count
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] DB_WARN -> 1
[2025-10-12 17:47:39] [info] LRF_ERR001 -> 14
[2025-10-12 17:47:39] [info] DB_WARN002 -> 159
[2025-10-12 17:47:39] [info] DB_WARN003 -> 161
[2025-10-12 17:47:39] [info] DRC_ERR9 -> 89
[2025-10-12 17:47:39] [info] DRC_ERR -> 67
[2025-10-12 17:47:39] [info] DRC_ERR5 -> 408
[2025-10-12 17:47:39] [info] DRC_ERR4 -> 241
[2025-10-12 17:47:39] [info] DRC_ERR1 -> 301
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info]                Rule Violation Summary
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:47:39] [info] |-----|-----|-----|
[2025-10-12 17:47:39] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:47:39] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:47:39] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:47:39] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:47:39] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:47:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 17:47:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:47:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:47:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:47:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:47:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:47:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:47:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:47:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:47:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:47:39] [info]
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] Performance Metrics
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] | Stage | Time Taken |
[2025-10-12 17:47:39] [info] |-----|-----|
[2025-10-12 17:47:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:47:39] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:47:39] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] Summary
[2025-10-12 17:47:39] [info] -----
[2025-10-12 17:47:39] [info] Total Errors: 1120
[2025-10-12 17:47:39] [info] Total Warnings: 321
[2025-10-12 17:47:39] [info] DRC Status: FAILED
```

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```
[2025-10-12 17:48:35] [info] =====
[2025-10-12 17:48:35] [info] Design Rule Check Report
[2025-10-12 17:48:35] [info] =====

[2025-10-12 17:48:35] [info] Report Generated: 2025-10-12
[2025-10-12 17:48:35] [info] Run Start Time: 05:47:44 PM
[2025-10-12 17:48:35] [info] Run End Time: 05:48:35 PM
[2025-10-12 17:48:35] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:48:35] [info] Tool Version: LVR_2025.1
[2025-10-12 17:48:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:48:35] [info] User Name: ubuntu
[2025-10-12 17:48:35] [info] Cores Used: 8
[2025-10-12 17:48:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Top Cell Information
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Layout Top Cell: TOP
[2025-10-12 17:48:35] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:48:35] [info] Database Format: GDSII
[2025-10-12 17:48:35] [info] Verification Mode: Full Check

[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Type of Errors | Count
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] DB_WARN -> 1
[2025-10-12 17:48:35] [info] LRF_ERR001 -> 14
[2025-10-12 17:48:35] [info] DB_WARN002 -> 159
[2025-10-12 17:48:35] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:48:35] [info] DRC_ERR9 -> 89
[2025-10-12 17:48:35] [info] DRC_ERR -> 67
[2025-10-12 17:48:35] [info] DRC_ERR5 -> 408
[2025-10-12 17:48:35] [info] DRC_ERR4 -> 241
[2025-10-12 17:48:35] [info] DRC_ERR1 -> 301
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Rule Violation Summary
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:48:35] [info] |-----|-----|-----|
[2025-10-12 17:48:35] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:48:35] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:48:35] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:48:35] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:48:35] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:48:35] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 17:48:35] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:48:35] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:48:35] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:48:35] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:48:35] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:48:35] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:48:35] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:48:35] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:48:35] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Performance Metrics
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] | Stage          | Time Taken |
[2025-10-12 17:48:35] [info] |-----|-----|
[2025-10-12 17:48:35] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:48:35] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 17:48:35] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Summary
[2025-10-12 17:48:35] [info] -----
[2025-10-12 17:48:35] [info] Total Errors: 1120
[2025-10-12 17:48:35] [info] Total Warnings: 321
[2025-10-12 17:48:35] [info] DRC Status: FAILED
```

lvr_summary_447.rpt

```
[2025-10-12 17:49:33] [info] =====
[2025-10-12 17:49:33] [info] Design Rule Check Report
[2025-10-12 17:49:33] [info] =====

[2025-10-12 17:49:33] [info] Report Generated: 2025-10-12
[2025-10-12 17:49:33] [info] Run Start Time: 05:48:41 PM
[2025-10-12 17:49:33] [info] Run End Time: 05:49:33 PM
[2025-10-12 17:49:33] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:49:33] [info] Tool Version: LVR_2025.1
[2025-10-12 17:49:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:49:33] [info] User Name: ubuntu
[2025-10-12 17:49:33] [info] Cores Used: 8
[2025-10-12 17:49:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] Top Cell Information
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] Layout Top Cell:      TOP
[2025-10-12 17:49:33] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:49:33] [info] Database Format:      GDSII
[2025-10-12 17:49:33] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] Type of Errors | Count
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] DB_WARN -> 1
[2025-10-12 17:49:33] [info] LRF_ERR001 -> 14
[2025-10-12 17:49:33] [info] DB_WARN002 -> 159
[2025-10-12 17:49:33] [info] DB_WARN003 -> 161
[2025-10-12 17:49:33] [info] DRC_ERR9 -> 89
[2025-10-12 17:49:33] [info] DRC_ERR -> 67
[2025-10-12 17:49:33] [info] DRC_ERR5 -> 408
[2025-10-12 17:49:33] [info] DRC_ERR4 -> 241
[2025-10-12 17:49:33] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] Rule Violation Summary
```

```
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:49:33] [info] |-----|-----|-----|
[2025-10-12 17:49:33] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:49:33] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:49:33] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:49:33] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:49:33] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:49:33] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:49:33] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:49:33] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:49:33] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:49:33] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:49:33] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:49:33] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:49:33] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:49:33] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:49:33] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:49:33] [info] -----
```

```
[2025-10-12 17:49:33] [info] Performance Metrics
```

```
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] | Stage          | Time Taken |
[2025-10-12 17:49:33] [info] |-----|-----|
[2025-10-12 17:49:33] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:49:33] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 17:49:33] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:49:33] [info] -----
```

```
[2025-10-12 17:49:33] [info] Summary
```

```
[2025-10-12 17:49:33] [info] -----
[2025-10-12 17:49:33] [info] Total Errors:      1120
[2025-10-12 17:49:33] [info] Total Warnings:    321
[2025-10-12 17:49:33] [info] DRC Status:        FAILED
```

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```
[2025-10-12 17:50:30] [info] =====
[2025-10-12 17:50:30] [info] Design Rule Check Report
[2025-10-12 17:50:30] [info] =====

[2025-10-12 17:50:30] [info] Report Generated: 2025-10-12
[2025-10-12 17:50:30] [info] Run Start Time: 05:49:38 PM
[2025-10-12 17:50:30] [info] Run End Time: 05:50:30 PM
[2025-10-12 17:50:30] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:50:30] [info] Tool Version: LVR_2025.1
[2025-10-12 17:50:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:50:30] [info] User Name: ubuntu
[2025-10-12 17:50:30] [info] Cores Used: 8
[2025-10-12 17:50:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Top Cell Information
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Layout Top Cell: TOP
[2025-10-12 17:50:30] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:50:30] [info] Database Format: GDSII
[2025-10-12 17:50:30] [info] Verification Mode: Full Check

[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Type of Errors | Count
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] DB_WARN -> 1
[2025-10-12 17:50:30] [info] LRF_ERR001 -> 14
[2025-10-12 17:50:30] [info] DB_WARN002 -> 159
[2025-10-12 17:50:30] [info] DB_WARN003 -> 161
[2025-10-12 17:50:30] [info] DRC_ERR9 -> 89
[2025-10-12 17:50:30] [info] DRC_ERR -> 67
[2025-10-12 17:50:30] [info] DRC_ERR5 -> 408
[2025-10-12 17:50:30] [info] DRC_ERR4 -> 241
[2025-10-12 17:50:30] [info] DRC_ERR1 -> 301
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Rule Violation Summary
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:50:30] [info] |-----|-----|-----|
[2025-10-12 17:50:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:50:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:50:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:50:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:50:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:50:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:50:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:50:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:50:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:50:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:50:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:50:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:50:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:50:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:50:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:50:30] [info]
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Performance Metrics
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] | Stage | Time Taken |
```



```
[2025-10-12 17:50:30] [info] |-----|-----|
[2025-10-12 17:50:30] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:50:30] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 17:50:30] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Summary
[2025-10-12 17:50:30] [info] -----
[2025-10-12 17:50:30] [info] Total Errors:    1120
[2025-10-12 17:50:30] [info] Total Warnings:    321
[2025-10-12 17:50:30] [info] DRC Status:      FAILED
```

lvr_summary_449.rpt

```
[2025-10-12 17:51:26] [info] =====
[2025-10-12 17:51:26] [info] Design Rule Check Report
[2025-10-12 17:51:26] [info] =====

[2025-10-12 17:51:26] [info] Report Generated:    2025-10-12
[2025-10-12 17:51:26] [info] Run Start Time:      05:50:35 PM
[2025-10-12 17:51:26] [info] Run End Time:        05:51:26 PM
[2025-10-12 17:51:26] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 17:51:26] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:51:26] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:51:26] [info] User Name:           ubuntu
[2025-10-12 17:51:26] [info] Cores Used:          8
[2025-10-12 17:51:26] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Top Cell Information
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Layout Top Cell:     TOP
[2025-10-12 17:51:26] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:51:26] [info] Database Format:       GDSII
[2025-10-12 17:51:26] [info] Verification Mode:    Full Check

[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Type of Errors | Count
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] DB_WARN -> 1
[2025-10-12 17:51:26] [info] LRF_ERR001 -> 14
[2025-10-12 17:51:26] [info] DB_WARN002 -> 159
[2025-10-12 17:51:26] [info] DB_WARN003 -> 161
[2025-10-12 17:51:26] [info] DRC_ERR9 -> 89
[2025-10-12 17:51:26] [info] DRC_ERR -> 67
[2025-10-12 17:51:26] [info] DRC_ERR5 -> 408
[2025-10-12 17:51:26] [info] DRC_ERR4 -> 241
[2025-10-12 17:51:26] [info] DRC_ERR1 -> 301
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Rule Violation Summary
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:51:26] [info] |-----|-----|-----|
[2025-10-12 17:51:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:51:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:51:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:51:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:51:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 17:51:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 17:51:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:51:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:51:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:51:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:51:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:51:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:51:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:51:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:51:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:51:26] [info]
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Performance Metrics
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] | Stage | Time Taken |
[2025-10-12 17:51:26] [info] |-----|-----|
[2025-10-12 17:51:26] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:51:26] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:51:26] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Summary
[2025-10-12 17:51:26] [info] -----
[2025-10-12 17:51:26] [info] Total Errors: 1120
[2025-10-12 17:51:26] [info] Total Warnings: 321
[2025-10-12 17:51:26] [info] DRC Status: FAILED

```

lvr_summary_45.rpt

```

[2025-10-12 11:24:03] [info] =====
[2025-10-12 11:24:03] [info] Design Rule Check Report
[2025-10-12 11:24:03] [info] =====

[2025-10-12 11:24:03] [info] Report Generated: 2025-10-12
[2025-10-12 11:24:03] [info] Run Start Time: 11:23:11 AM
[2025-10-12 11:24:03] [info] Run End Time: 11:24:03 AM
[2025-10-12 11:24:03] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:24:03] [info] Tool Version: LVR_2025.1
[2025-10-12 11:24:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:24:03] [info] User Name: ubuntu
[2025-10-12 11:24:03] [info] Cores Used: 8
[2025-10-12 11:24:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Top Cell Information
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Layout Top Cell: TOP
[2025-10-12 11:24:03] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:24:03] [info] Database Format: GDSII
[2025-10-12 11:24:03] [info] Verification Mode: Full Check

[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Type of Errors | Count
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] DB_WARN -> 1
[2025-10-12 11:24:03] [info] LRF_ERR001 -> 14
[2025-10-12 11:24:03] [info] DB_WARN002 -> 159
[2025-10-12 11:24:03] [info] DB_WARN003 -> 161

```

```
[2025-10-12 11:24:03] [info] DRC_ERR9 -> 89
[2025-10-12 11:24:03] [info] DRC_ERR -> 67
[2025-10-12 11:24:03] [info] DRC_ERR5 -> 408
[2025-10-12 11:24:03] [info] DRC_ERR4 -> 241
[2025-10-12 11:24:03] [info] DRC_ERR1 -> 301
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Rule Violation Summary
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:24:03] [info] |-----|-----|-----|
[2025-10-12 11:24:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:24:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:24:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:24:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:24:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:24:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:24:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:24:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:24:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:24:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:24:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:24:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:24:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:24:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:24:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Performance Metrics
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] | Stage | Time Taken |
[2025-10-12 11:24:03] [info] |-----|-----|
[2025-10-12 11:24:03] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:24:03] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:24:03] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Summary
[2025-10-12 11:24:03] [info] -----
[2025-10-12 11:24:03] [info] Total Errors: 1120
[2025-10-12 11:24:03] [info] Total Warnings: 321
[2025-10-12 11:24:03] [info] DRC Status: FAILED
```

lvr_summary_450.rpt

```
[2025-10-12 17:52:24] [info] =====
[2025-10-12 17:52:24] [info] Design Rule Check Report
[2025-10-12 17:52:24] [info] =====

[2025-10-12 17:52:24] [info] Report Generated: 2025-10-12
[2025-10-12 17:52:24] [info] Run Start Time: 05:51:33 PM
[2025-10-12 17:52:24] [info] Run End Time: 05:52:24 PM
[2025-10-12 17:52:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:52:24] [info] Tool Version: LVR_2025.1
[2025-10-12 17:52:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:52:24] [info] User Name: ubuntu
[2025-10-12 17:52:24] [info] Cores Used: 8
[2025-10-12 17:52:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Top Cell Information
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Layout Top Cell:      TOP
[2025-10-12 17:52:24] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:52:24] [info] Database Format:       GDSII
[2025-10-12 17:52:24] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Type of Errors | Count
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] DB_WARN -> 1
[2025-10-12 17:52:24] [info] LRF_ERR001 -> 14
[2025-10-12 17:52:24] [info] DB_WARN002 -> 159
[2025-10-12 17:52:24] [info] DB_WARN003 -> 161
[2025-10-12 17:52:24] [info] DRC_ERR9 -> 89
[2025-10-12 17:52:24] [info] DRC_ERR -> 67
[2025-10-12 17:52:24] [info] DRC_ERR5 -> 408
[2025-10-12 17:52:24] [info] DRC_ERR4 -> 241
[2025-10-12 17:52:24] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Rule Violation Summary
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:52:24] [info] |-----|-----|-----|
[2025-10-12 17:52:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:52:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:52:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:52:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:52:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:52:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:52:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:52:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:52:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:52:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:52:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:52:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:52:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:52:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:52:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Performance Metrics
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] | Stage | Time Taken |
[2025-10-12 17:52:24] [info] |-----|-----|
[2025-10-12 17:52:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:52:24] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 17:52:24] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Summary
[2025-10-12 17:52:24] [info] -----
[2025-10-12 17:52:24] [info] Total Errors:      1120
[2025-10-12 17:52:24] [info] Total Warnings:    321
[2025-10-12 17:52:24] [info] DRC Status:        FAILED
```

lvr_summary_451.rpt

```
[2025-10-12 17:53:20] [info] =====
[2025-10-12 17:53:20] [info]                      Design Rule Check Report
[2025-10-12 17:53:20] [info] =====

[2025-10-12 17:53:20] [info] Report Generated:      2025-10-12
[2025-10-12 17:53:20] [info] Run Start Time:       05:52:29 PM
[2025-10-12 17:53:20] [info] Run End Time:         05:53:20 PM
[2025-10-12 17:53:20] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 17:53:20] [info] Tool Version:         LVR_2025.1
[2025-10-12 17:53:20] [info] Host Name:            ip-172-31-37-173
[2025-10-12 17:53:20] [info] User Name:            ubuntu
[2025-10-12 17:53:20] [info] Cores Used:           8
[2025-10-12 17:53:20] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info]                      Top Cell Information
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info] Layout Top Cell:      TOP
[2025-10-12 17:53:20] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 17:53:20] [info] Database Format:        GDSII
[2025-10-12 17:53:20] [info] Verification Mode:     Full Check

[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info]                      Type of Errors | Count
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info] DB_WARN -> 1
[2025-10-12 17:53:20] [info] LRF_ERR001 -> 14
[2025-10-12 17:53:20] [info] DB_WARN002 -> 159
[2025-10-12 17:53:20] [info] DB_WARN003 -> 161
[2025-10-12 17:53:20] [info] DRC_ERR9 -> 89
[2025-10-12 17:53:20] [info] DRC_ERR -> 67
[2025-10-12 17:53:20] [info] DRC_ERR5 -> 408
[2025-10-12 17:53:20] [info] DRC_ERR4 -> 241
[2025-10-12 17:53:20] [info] DRC_ERR1 -> 301
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info]                      Rule Violation Summary
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:53:20] [info] |-----|-----|-----|
[2025-10-12 17:53:20] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:53:20] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:53:20] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:53:20] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:53:20] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:53:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:53:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:53:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:53:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:53:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:53:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:53:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:53:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:53:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:53:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:53:20] [info]
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info]                      Performance Metrics
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:53:20] [info] |-----|-----|
[2025-10-12 17:53:20] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:53:20] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 17:53:20] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info]                Summary
[2025-10-12 17:53:20] [info] -----
[2025-10-12 17:53:20] [info] Total Errors:    1120
[2025-10-12 17:53:20] [info] Total Warnings:    321
[2025-10-12 17:53:20] [info] DRC Status:      FAILED
```

lvr_summary_452.rpt

```
[2025-10-12 17:54:18] [info] =====
[2025-10-12 17:54:18] [info]                Design Rule Check Report
[2025-10-12 17:54:18] [info] =====

[2025-10-12 17:54:18] [info] Report Generated:    2025-10-12
[2025-10-12 17:54:18] [info] Run Start Time:      05:53:27 PM
[2025-10-12 17:54:18] [info] Run End Time:        05:54:18 PM
[2025-10-12 17:54:18] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:54:18] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:54:18] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:54:18] [info] User Name:           ubuntu
[2025-10-12 17:54:18] [info] Cores Used:          8
[2025-10-12 17:54:18] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info]                Top Cell Information
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] Layout Top Cell:     TOP
[2025-10-12 17:54:18] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:54:18] [info] Database Format:      GDSII
[2025-10-12 17:54:18] [info] Verification Mode:    Full Check

[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info]                Type of Errors | Count
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] DB_WARN -> 1
[2025-10-12 17:54:18] [info] LRF_ERR001 -> 14
[2025-10-12 17:54:18] [info] DB_WARN002 -> 159
[2025-10-12 17:54:18] [info] DB_WARN003 -> 161
[2025-10-12 17:54:18] [info] DRC_ERR9 -> 89
[2025-10-12 17:54:18] [info] DRC_ERR -> 67
[2025-10-12 17:54:18] [info] DRC_ERR5 -> 408
[2025-10-12 17:54:18] [info] DRC_ERR4 -> 241
[2025-10-12 17:54:18] [info] DRC_ERR1 -> 301
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info]                Rule Violation Summary
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:54:18] [info] |-----|-----|-----|
[2025-10-12 17:54:18] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:54:18] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:54:18] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:54:18] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:54:18] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:54:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:54:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:54:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:54:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:54:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:54:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:54:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:54:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:54:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:54:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:54:18] [info]
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] Performance Metrics
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] | Stage | Time Taken |
[2025-10-12 17:54:18] [info] |-----|-----|
[2025-10-12 17:54:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:54:18] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:54:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] Summary
[2025-10-12 17:54:18] [info] -----
[2025-10-12 17:54:18] [info] Total Errors: 1120
[2025-10-12 17:54:18] [info] Total Warnings: 321
[2025-10-12 17:54:18] [info] DRC Status: FAILED
```

lvr_summary_453.rpt

```
[2025-10-12 17:55:13] [info] =====
[2025-10-12 17:55:13] [info] Design Rule Check Report
[2025-10-12 17:55:13] [info] =====

[2025-10-12 17:55:13] [info] Report Generated: 2025-10-12
[2025-10-12 17:55:13] [info] Run Start Time: 05:54:23 PM
[2025-10-12 17:55:13] [info] Run End Time: 05:55:13 PM
[2025-10-12 17:55:13] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 17:55:13] [info] Tool Version: LVR_2025.1
[2025-10-12 17:55:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:55:13] [info] User Name: ubuntu
[2025-10-12 17:55:13] [info] Cores Used: 8
[2025-10-12 17:55:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Top Cell Information
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Layout Top Cell: TOP
[2025-10-12 17:55:13] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:55:13] [info] Database Format: GDSII
[2025-10-12 17:55:13] [info] Verification Mode: Full Check

[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Type of Errors | Count
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] DB_WARN -> 1
[2025-10-12 17:55:13] [info] LRF_ERR001 -> 14
[2025-10-12 17:55:13] [info] DB_WARN002 -> 159
[2025-10-12 17:55:13] [info] DB_WARN003 -> 161
```

```

[2025-10-12 17:55:13] [info] DRC_ERR9 -> 89
[2025-10-12 17:55:13] [info] DRC_ERR -> 67
[2025-10-12 17:55:13] [info] DRC_ERR5 -> 408
[2025-10-12 17:55:13] [info] DRC_ERR4 -> 241
[2025-10-12 17:55:13] [info] DRC_ERR1 -> 301
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Rule Violation Summary
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:55:13] [info] |-----|-----|-----|
[2025-10-12 17:55:13] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:55:13] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:55:13] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:55:13] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:55:13] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:55:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:55:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:55:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:55:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:55:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:55:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:55:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:55:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:55:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:55:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Performance Metrics
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] | Stage | Time Taken |
[2025-10-12 17:55:13] [info] |-----|-----|
[2025-10-12 17:55:13] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:55:13] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 17:55:13] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Summary
[2025-10-12 17:55:13] [info] -----
[2025-10-12 17:55:13] [info] Total Errors: 1120
[2025-10-12 17:55:13] [info] Total Warnings: 321
[2025-10-12 17:55:13] [info] DRC Status: FAILED

```

lvr_summary_454.rpt

```

[2025-10-12 17:56:11] [info] =====
[2025-10-12 17:56:11] [info] Design Rule Check Report
[2025-10-12 17:56:11] [info] =====

[2025-10-12 17:56:11] [info] Report Generated: 2025-10-12
[2025-10-12 17:56:11] [info] Run Start Time: 05:55:20 PM
[2025-10-12 17:56:11] [info] Run End Time: 05:56:11 PM
[2025-10-12 17:56:11] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:56:11] [info] Tool Version: LVR_2025.1
[2025-10-12 17:56:11] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:56:11] [info] User Name: ubuntu
[2025-10-12 17:56:11] [info] Cores Used: 8
[2025-10-12 17:56:11] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Top Cell Information
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Layout Top Cell:      TOP
[2025-10-12 17:56:11] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:56:11] [info] Database Format:      GDSII
[2025-10-12 17:56:11] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Type of Errors | Count
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] DB_WARN -> 1
[2025-10-12 17:56:11] [info] LRF_ERR001 -> 14
[2025-10-12 17:56:11] [info] DB_WARN002 -> 159
[2025-10-12 17:56:11] [info] DB_WARN003 -> 161
[2025-10-12 17:56:11] [info] DRC_ERR9 -> 89
[2025-10-12 17:56:11] [info] DRC_ERR -> 67
[2025-10-12 17:56:11] [info] DRC_ERR5 -> 408
[2025-10-12 17:56:11] [info] DRC_ERR4 -> 241
[2025-10-12 17:56:11] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Rule Violation Summary
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:56:11] [info] |-----|-----|-----|
[2025-10-12 17:56:11] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:56:11] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:56:11] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:56:11] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:56:11] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:56:11] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:56:11] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:56:11] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:56:11] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:56:11] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:56:11] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:56:11] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:56:11] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:56:11] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:56:11] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:56:11] [info] |
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Performance Metrics
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] | Stage          | Time Taken |
[2025-10-12 17:56:11] [info] |-----|-----|
[2025-10-12 17:56:11] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:56:11] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 17:56:11] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Summary
```

```
[2025-10-12 17:56:11] [info] -----
[2025-10-12 17:56:11] [info] Total Errors:      1120
[2025-10-12 17:56:11] [info] Total Warnings:    321
[2025-10-12 17:56:11] [info] DRC Status:        FAILED
```

lvr_summary_455.rpt

```
[2025-10-12 17:57:07] [info] =====
[2025-10-12 17:57:07] [info]                      Design Rule Check Report
[2025-10-12 17:57:07] [info] =====

[2025-10-12 17:57:07] [info] Report Generated:    2025-10-12
[2025-10-12 17:57:07] [info] Run Start Time:      05:56:16 PM
[2025-10-12 17:57:07] [info] Run End Time:        05:57:07 PM
[2025-10-12 17:57:07] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 17:57:07] [info] Tool Version:       LVR_2025.1
[2025-10-12 17:57:07] [info] Host Name:          ip-172-31-37-173
[2025-10-12 17:57:07] [info] User Name:          ubuntu
[2025-10-12 17:57:07] [info] Cores Used:         8
[2025-10-12 17:57:07] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info]                      Top Cell Information
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info] Layout Top Cell:    TOP
[2025-10-12 17:57:07] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 17:57:07] [info] Database Format:      GDSII
[2025-10-12 17:57:07] [info] Verification Mode:   Full Check

[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info]                      Type of Errors | Count
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info] DB_WARN -> 1
[2025-10-12 17:57:07] [info] LRF_ERR001 -> 14
[2025-10-12 17:57:07] [info] DB_WARN002 -> 159
[2025-10-12 17:57:07] [info] DB_WARN003 -> 161
[2025-10-12 17:57:07] [info] DRC_ERR9 -> 89
[2025-10-12 17:57:07] [info] DRC_ERR -> 67
[2025-10-12 17:57:07] [info] DRC_ERR5 -> 408
[2025-10-12 17:57:07] [info] DRC_ERR4 -> 241
[2025-10-12 17:57:07] [info] DRC_ERR1 -> 301
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info]                      Rule Violation Summary
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:57:07] [info] |-----|-----|-----|
[2025-10-12 17:57:07] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:57:07] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:57:07] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:57:07] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:57:07] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:57:07] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 17:57:07] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:57:07] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:57:07] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:57:07] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:57:07] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:57:07] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:57:07] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:57:07] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:57:07] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:57:07] [info]
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info]                      Performance Metrics
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info] | Stage | Time Taken |
```

```
[2025-10-12 17:57:07] [info] |-----|-----|
[2025-10-12 17:57:07] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 17:57:07] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 17:57:07] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info]                Summary
[2025-10-12 17:57:07] [info] -----
[2025-10-12 17:57:07] [info] Total Errors:    1120
[2025-10-12 17:57:07] [info] Total Warnings:    321
[2025-10-12 17:57:07] [info] DRC Status:      FAILED
```

lvr_summary_456.rpt

```
[2025-10-12 17:58:05] [info] =====
[2025-10-12 17:58:05] [info]                Design Rule Check Report
[2025-10-12 17:58:05] [info] =====

[2025-10-12 17:58:05] [info] Report Generated:    2025-10-12
[2025-10-12 17:58:05] [info] Run Start Time:      05:57:13 PM
[2025-10-12 17:58:05] [info] Run End Time:        05:58:05 PM
[2025-10-12 17:58:05] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 17:58:05] [info] Tool Version:        LVR_2025.1
[2025-10-12 17:58:05] [info] Host Name:           ip-172-31-37-173
[2025-10-12 17:58:05] [info] User Name:           ubuntu
[2025-10-12 17:58:05] [info] Cores Used:          8
[2025-10-12 17:58:05] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info]                Top Cell Information
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] Layout Top Cell:     TOP
[2025-10-12 17:58:05] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:58:05] [info] Database Format:       GDSII
[2025-10-12 17:58:05] [info] Verification Mode:    Full Check

[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info]                Type of Errors | Count
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] DB_WARN -> 1
[2025-10-12 17:58:05] [info] LRF_ERR001 -> 14
[2025-10-12 17:58:05] [info] DB_WARN002 -> 159
[2025-10-12 17:58:05] [info] DB_WARN003 -> 161
[2025-10-12 17:58:05] [info] DRC_ERR9 -> 89
[2025-10-12 17:58:05] [info] DRC_ERR -> 67
[2025-10-12 17:58:05] [info] DRC_ERR5 -> 408
[2025-10-12 17:58:05] [info] DRC_ERR4 -> 241
[2025-10-12 17:58:05] [info] DRC_ERR1 -> 301
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info]                Rule Violation Summary
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 17:58:05] [info] |-----|-----|-----|
[2025-10-12 17:58:05] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:58:05] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:58:05] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:58:05] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:58:05] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 17:58:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 17:58:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:58:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:58:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:58:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:58:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:58:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:58:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:58:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:58:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:58:05] [info]
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] Performance Metrics
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] | Stage | Time Taken |
[2025-10-12 17:58:05] [info] |-----|-----|
[2025-10-12 17:58:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:58:05] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:58:05] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] Summary
[2025-10-12 17:58:05] [info] -----
[2025-10-12 17:58:05] [info] Total Errors: 1120
[2025-10-12 17:58:05] [info] Total Warnings: 321
[2025-10-12 17:58:05] [info] DRC Status: FAILED
```

lvr_summary_457.rpt

```
[2025-10-12 17:59:02] [info] =====
[2025-10-12 17:59:02] [info] Design Rule Check Report
[2025-10-12 17:59:02] [info] =====

[2025-10-12 17:59:02] [info] Report Generated: 2025-10-12
[2025-10-12 17:59:02] [info] Run Start Time: 05:58:10 PM
[2025-10-12 17:59:02] [info] Run End Time: 05:59:02 PM
[2025-10-12 17:59:02] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 17:59:02] [info] Tool Version: LVR_2025.1
[2025-10-12 17:59:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:59:02] [info] User Name: ubuntu
[2025-10-12 17:59:02] [info] Cores Used: 8
[2025-10-12 17:59:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Top Cell Information
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Layout Top Cell: TOP
[2025-10-12 17:59:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 17:59:02] [info] Database Format: GDSII
[2025-10-12 17:59:02] [info] Verification Mode: Full Check

[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Type of Errors | Count
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] DB_WARN -> 1
[2025-10-12 17:59:02] [info] LRF_ERR001 -> 14
[2025-10-12 17:59:02] [info] DB_WARN002 -> 159
[2025-10-12 17:59:02] [info] DB_WARN003 -> 161
```

```
[2025-10-12 17:59:02] [info] DRC_ERR9 -> 89
[2025-10-12 17:59:02] [info] DRC_ERR -> 67
[2025-10-12 17:59:02] [info] DRC_ERR5 -> 408
[2025-10-12 17:59:02] [info] DRC_ERR4 -> 241
[2025-10-12 17:59:02] [info] DRC_ERR1 -> 301
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Rule Violation Summary
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 17:59:02] [info] |-----|-----|-----|
[2025-10-12 17:59:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 17:59:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 17:59:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 17:59:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 17:59:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 17:59:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 17:59:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 17:59:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 17:59:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 17:59:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 17:59:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 17:59:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 17:59:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 17:59:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 17:59:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Performance Metrics
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] | Stage | Time Taken |
[2025-10-12 17:59:02] [info] |-----|-----|
[2025-10-12 17:59:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 17:59:02] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 17:59:02] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Summary
[2025-10-12 17:59:02] [info] -----
[2025-10-12 17:59:02] [info] Total Errors: 1120
[2025-10-12 17:59:02] [info] Total Warnings: 321
[2025-10-12 17:59:02] [info] DRC Status: FAILED
```

lvr_summary_458.rpt

```
[2025-10-12 17:59:59] [info] =====
[2025-10-12 17:59:59] [info] Design Rule Check Report
[2025-10-12 17:59:59] [info] =====

[2025-10-12 17:59:59] [info] Report Generated: 2025-10-12
[2025-10-12 17:59:59] [info] Run Start Time: 05:59:07 PM
[2025-10-12 17:59:59] [info] Run End Time: 05:59:59 PM
[2025-10-12 17:59:59] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 17:59:59] [info] Tool Version: LVR_2025.1
[2025-10-12 17:59:59] [info] Host Name: ip-172-31-37-173
[2025-10-12 17:59:59] [info] User Name: ubuntu
[2025-10-12 17:59:59] [info] Cores Used: 8
[2025-10-12 17:59:59] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Top Cell Information
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Layout Top Cell:      TOP
[2025-10-12 17:59:59] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 17:59:59] [info] Database Format:       GDSII
[2025-10-12 17:59:59] [info] Verification Mode:    Full Check
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Type of Errors | Count
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] DB_WARN -> 1
[2025-10-12 17:59:59] [info] LRF_ERR001 -> 14
[2025-10-12 17:59:59] [info] DB_WARN002 -> 159
[2025-10-12 17:59:59] [info] DB_WARN003 -> 161
[2025-10-12 17:59:59] [info] DRC_ERR9 -> 89
[2025-10-12 17:59:59] [info] DRC_ERR -> 67
[2025-10-12 17:59:59] [info] DRC_ERR5 -> 408
[2025-10-12 17:59:59] [info] DRC_ERR4 -> 241
[2025-10-12 17:59:59] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Rule Violation Summary
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 17:59:59] [info] |-----|-----|-----|
[2025-10-12 17:59:59] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 17:59:59] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 17:59:59] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 17:59:59] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 17:59:59] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 17:59:59] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 17:59:59] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 17:59:59] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 17:59:59] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 17:59:59] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 17:59:59] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 17:59:59] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 17:59:59] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 17:59:59] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 17:59:59] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 17:59:59] [info] |
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Performance Metrics
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] | Stage          | Time Taken |
[2025-10-12 17:59:59] [info] |-----|-----|
[2025-10-12 17:59:59] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 17:59:59] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 17:59:59] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Summary
```

```
[2025-10-12 17:59:59] [info] -----
[2025-10-12 17:59:59] [info] Total Errors:      1120
[2025-10-12 17:59:59] [info] Total Warnings:    321
[2025-10-12 17:59:59] [info] DRC Status:        FAILED
```

lvr_summary_459.rpt

```
[2025-10-12 18:00:56] [info] =====
[2025-10-12 18:00:56] [info]                      Design Rule Check Report
[2025-10-12 18:00:56] [info] =====

[2025-10-12 18:00:56] [info] Report Generated:      2025-10-12
[2025-10-12 18:00:56] [info] Run Start Time:       06:00:04 PM
[2025-10-12 18:00:56] [info] Run End Time:         06:00:56 PM
[2025-10-12 18:00:56] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 18:00:56] [info] Tool Version:         LVR_2025.1
[2025-10-12 18:00:56] [info] Host Name:            ip-172-31-37-173
[2025-10-12 18:00:56] [info] User Name:            ubuntu
[2025-10-12 18:00:56] [info] Cores Used:           8
[2025-10-12 18:00:56] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info]                      Top Cell Information
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info] Layout Top Cell:      TOP
[2025-10-12 18:00:56] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 18:00:56] [info] Database Format:        GDSII
[2025-10-12 18:00:56] [info] Verification Mode:     Full Check

[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info]                      Type of Errors | Count
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info] DB_WARN -> 1
[2025-10-12 18:00:56] [info] LRF_ERR001 -> 14
[2025-10-12 18:00:56] [info] DB_WARN002 -> 159
[2025-10-12 18:00:56] [info] DB_WARN003 -> 161
[2025-10-12 18:00:56] [info] DRC_ERR9 -> 89
[2025-10-12 18:00:56] [info] DRC_ERR -> 67
[2025-10-12 18:00:56] [info] DRC_ERR5 -> 408
[2025-10-12 18:00:56] [info] DRC_ERR4 -> 241
[2025-10-12 18:00:56] [info] DRC_ERR1 -> 301
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info]                      Rule Violation Summary
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:00:56] [info] |-----|-----|-----|
[2025-10-12 18:00:56] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:00:56] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:00:56] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:00:56] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:00:56] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:00:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:00:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:00:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:00:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:00:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:00:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:00:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:00:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:00:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:00:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:00:56] [info]
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info]                      Performance Metrics
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:00:56] [info] |-----|-----|
[2025-10-12 18:00:56] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:00:56] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:00:56] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info]                Summary
[2025-10-12 18:00:56] [info] -----
[2025-10-12 18:00:56] [info] Total Errors:    1120
[2025-10-12 18:00:56] [info] Total Warnings:    321
[2025-10-12 18:00:56] [info] DRC Status:      FAILED
```

lvr_summary_46.rpt

```
[2025-10-12 11:24:59] [info] =====
[2025-10-12 11:24:59] [info]                Design Rule Check Report
[2025-10-12 11:24:59] [info] =====

[2025-10-12 11:24:59] [info] Report Generated:    2025-10-12
[2025-10-12 11:24:59] [info] Run Start Time:      11:24:09 AM
[2025-10-12 11:24:59] [info] Run End Time:        11:24:59 AM
[2025-10-12 11:24:59] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 11:24:59] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:24:59] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:24:59] [info] User Name:           ubuntu
[2025-10-12 11:24:59] [info] Cores Used:          8
[2025-10-12 11:24:59] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info]                Top Cell Information
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] Layout Top Cell:     TOP
[2025-10-12 11:24:59] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:24:59] [info] Database Format:       GDSII
[2025-10-12 11:24:59] [info] Verification Mode:    Full Check

[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info]                Type of Errors | Count
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] DB_WARN -> 1
[2025-10-12 11:24:59] [info] LRF_ERR001 -> 14
[2025-10-12 11:24:59] [info] DB_WARN002 -> 159
[2025-10-12 11:24:59] [info] DB_WARN003 -> 161
[2025-10-12 11:24:59] [info] DRC_ERR9 -> 89
[2025-10-12 11:24:59] [info] DRC_ERR -> 67
[2025-10-12 11:24:59] [info] DRC_ERR5 -> 408
[2025-10-12 11:24:59] [info] DRC_ERR4 -> 241
[2025-10-12 11:24:59] [info] DRC_ERR1 -> 301
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info]                Rule Violation Summary
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:24:59] [info] |-----|-----|-----|
[2025-10-12 11:24:59] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:24:59] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:24:59] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:24:59] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:24:59] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 11:24:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 11:24:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:24:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:24:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:24:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:24:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:24:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:24:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:24:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:24:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:24:59] [info]
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] Performance Metrics
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] | Stage | Time Taken |
[2025-10-12 11:24:59] [info] |-----|-----|
[2025-10-12 11:24:59] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:24:59] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 11:24:59] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] Summary
[2025-10-12 11:24:59] [info] -----
[2025-10-12 11:24:59] [info] Total Errors: 1120
[2025-10-12 11:24:59] [info] Total Warnings: 321
[2025-10-12 11:24:59] [info] DRC Status: FAILED
```

lvr_summary_460.rpt

```
[2025-10-12 18:01:53] [info] =====
[2025-10-12 18:01:53] [info] Design Rule Check Report
[2025-10-12 18:01:53] [info] =====

[2025-10-12 18:01:53] [info] Report Generated: 2025-10-12
[2025-10-12 18:01:53] [info] Run Start Time: 06:01:01 PM
[2025-10-12 18:01:53] [info] Run End Time: 06:01:53 PM
[2025-10-12 18:01:53] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:01:53] [info] Tool Version: LVR_2025.1
[2025-10-12 18:01:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:01:53] [info] User Name: ubuntu
[2025-10-12 18:01:53] [info] Cores Used: 8
[2025-10-12 18:01:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Top Cell Information
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Layout Top Cell: TOP
[2025-10-12 18:01:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:01:53] [info] Database Format: GDSII
[2025-10-12 18:01:53] [info] Verification Mode: Full Check

[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Type of Errors | Count
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] DB_WARN -> 1
[2025-10-12 18:01:53] [info] LRF_ERR001 -> 14
[2025-10-12 18:01:53] [info] DB_WARN002 -> 159
[2025-10-12 18:01:53] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:01:53] [info] DRC_ERR9 -> 89
[2025-10-12 18:01:53] [info] DRC_ERR -> 67
[2025-10-12 18:01:53] [info] DRC_ERR5 -> 408
[2025-10-12 18:01:53] [info] DRC_ERR4 -> 241
[2025-10-12 18:01:53] [info] DRC_ERR1 -> 301
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Rule Violation Summary
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:01:53] [info] |-----|-----|-----|
[2025-10-12 18:01:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:01:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:01:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:01:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:01:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:01:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:01:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:01:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:01:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:01:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:01:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:01:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:01:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:01:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:01:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Performance Metrics
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] | Stage | Time Taken |
[2025-10-12 18:01:53] [info] |-----|-----|
[2025-10-12 18:01:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:01:53] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:01:53] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Summary
[2025-10-12 18:01:53] [info] -----
[2025-10-12 18:01:53] [info] Total Errors: 1120
[2025-10-12 18:01:53] [info] Total Warnings: 321
[2025-10-12 18:01:53] [info] DRC Status: FAILED

```

lvr_summary_461.rpt

```

[2025-10-12 18:02:50] [info] =====
[2025-10-12 18:02:50] [info] Design Rule Check Report
[2025-10-12 18:02:50] [info] =====

[2025-10-12 18:02:50] [info] Report Generated: 2025-10-12
[2025-10-12 18:02:50] [info] Run Start Time: 06:01:58 PM
[2025-10-12 18:02:50] [info] Run End Time: 06:02:50 PM
[2025-10-12 18:02:50] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:02:50] [info] Tool Version: LVR_2025.1
[2025-10-12 18:02:50] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:02:50] [info] User Name: ubuntu
[2025-10-12 18:02:50] [info] Cores Used: 8
[2025-10-12 18:02:50] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] Top Cell Information
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] Layout Top Cell:      TOP
[2025-10-12 18:02:50] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:02:50] [info] Database Format:      GDSII
[2025-10-12 18:02:50] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] Type of Errors | Count
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] DB_WARN -> 1
[2025-10-12 18:02:50] [info] LRF_ERR001 -> 14
[2025-10-12 18:02:50] [info] DB_WARN002 -> 159
[2025-10-12 18:02:50] [info] DB_WARN003 -> 161
[2025-10-12 18:02:50] [info] DRC_ERR9 -> 89
[2025-10-12 18:02:50] [info] DRC_ERR -> 67
[2025-10-12 18:02:50] [info] DRC_ERR5 -> 408
[2025-10-12 18:02:50] [info] DRC_ERR4 -> 241
[2025-10-12 18:02:50] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] Rule Violation Summary
```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:02:50] [info] |-----|-----|-----|
[2025-10-12 18:02:50] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:02:50] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:02:50] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:02:50] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:02:50] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:02:50] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:02:50] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:02:50] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:02:50] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:02:50] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:02:50] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:02:50] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:02:50] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:02:50] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:02:50] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:02:50] [info] -----
```

```
[2025-10-12 18:02:50] [info] Performance Metrics
```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] | Stage          | Time Taken |
[2025-10-12 18:02:50] [info] |-----|-----|
[2025-10-12 18:02:50] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:02:50] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 18:02:50] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] Summary
```

```
[2025-10-12 18:02:50] [info] -----
[2025-10-12 18:02:50] [info] Total Errors:      1120
[2025-10-12 18:02:50] [info] Total Warnings:    321
[2025-10-12 18:02:50] [info] DRC Status:        FAILED
```

lvr_summary_462.rpt

```
[2025-10-12 18:03:46] [info] =====
[2025-10-12 18:03:46] [info]                      Design Rule Check Report
[2025-10-12 18:03:46] [info] =====

[2025-10-12 18:03:46] [info] Report Generated:    2025-10-12
[2025-10-12 18:03:46] [info] Run Start Time:      06:02:55 PM
[2025-10-12 18:03:46] [info] Run End Time:        06:03:46 PM
[2025-10-12 18:03:46] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 18:03:46] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:03:46] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:03:46] [info] User Name:           ubuntu
[2025-10-12 18:03:46] [info] Cores Used:          8
[2025-10-12 18:03:46] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info]                      Top Cell Information
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info] Layout Top Cell:     TOP
[2025-10-12 18:03:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:03:46] [info] Database Format:       GDSII
[2025-10-12 18:03:46] [info] Verification Mode:    Full Check

[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info]                      Type of Errors | Count
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info] DB_WARN -> 1
[2025-10-12 18:03:46] [info] LRF_ERR001 -> 14
[2025-10-12 18:03:46] [info] DB_WARN002 -> 159
[2025-10-12 18:03:46] [info] DB_WARN003 -> 161
[2025-10-12 18:03:46] [info] DRC_ERR9 -> 89
[2025-10-12 18:03:46] [info] DRC_ERR -> 67
[2025-10-12 18:03:46] [info] DRC_ERR5 -> 408
[2025-10-12 18:03:46] [info] DRC_ERR4 -> 241
[2025-10-12 18:03:46] [info] DRC_ERR1 -> 301
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info]                      Rule Violation Summary
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:03:46] [info] |-----|-----|-----|
[2025-10-12 18:03:46] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:03:46] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:03:46] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:03:46] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:03:46] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:03:46] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:03:46] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:03:46] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:03:46] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:03:46] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:03:46] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:03:46] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:03:46] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:03:46] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:03:46] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:03:46] [info]
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info]                      Performance Metrics
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:03:46] [info] |-----|-----|
[2025-10-12 18:03:46] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:03:46] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:03:46] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info]                Summary
[2025-10-12 18:03:46] [info] -----
[2025-10-12 18:03:46] [info] Total Errors:    1120
[2025-10-12 18:03:46] [info] Total Warnings:    321
[2025-10-12 18:03:46] [info] DRC Status:      FAILED
```

lvr_summary_463.rpt

```
[2025-10-12 18:04:43] [info] =====
[2025-10-12 18:04:43] [info]                Design Rule Check Report
[2025-10-12 18:04:43] [info] =====

[2025-10-12 18:04:43] [info] Report Generated:    2025-10-12
[2025-10-12 18:04:43] [info] Run Start Time:      06:03:52 PM
[2025-10-12 18:04:43] [info] Run End Time:        06:04:43 PM
[2025-10-12 18:04:43] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 18:04:43] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:04:43] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:04:43] [info] User Name:           ubuntu
[2025-10-12 18:04:43] [info] Cores Used:          8
[2025-10-12 18:04:43] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info]                Top Cell Information
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] Layout Top Cell:     TOP
[2025-10-12 18:04:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:04:43] [info] Database Format:       GDSII
[2025-10-12 18:04:43] [info] Verification Mode:    Full Check

[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info]                Type of Errors | Count
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] DB_WARN -> 1
[2025-10-12 18:04:43] [info] LRF_ERR001 -> 14
[2025-10-12 18:04:43] [info] DB_WARN002 -> 159
[2025-10-12 18:04:43] [info] DB_WARN003 -> 161
[2025-10-12 18:04:43] [info] DRC_ERR9 -> 89
[2025-10-12 18:04:43] [info] DRC_ERR -> 67
[2025-10-12 18:04:43] [info] DRC_ERR5 -> 408
[2025-10-12 18:04:43] [info] DRC_ERR4 -> 241
[2025-10-12 18:04:43] [info] DRC_ERR1 -> 301
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info]                Rule Violation Summary
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:04:43] [info] |-----|-----|-----|
[2025-10-12 18:04:43] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:04:43] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:04:43] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:04:43] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:04:43] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:04:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:04:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:04:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:04:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:04:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:04:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:04:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:04:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:04:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:04:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:04:43] [info]
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] Performance Metrics
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] | Stage | Time Taken |
[2025-10-12 18:04:43] [info] |-----|-----|
[2025-10-12 18:04:43] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:04:43] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:04:43] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] Summary
[2025-10-12 18:04:43] [info] -----
[2025-10-12 18:04:43] [info] Total Errors: 1120
[2025-10-12 18:04:43] [info] Total Warnings: 321
[2025-10-12 18:04:43] [info] DRC Status: FAILED
```

lvr_summary_464.rpt

```
[2025-10-12 18:05:40] [info] =====
[2025-10-12 18:05:40] [info] Design Rule Check Report
[2025-10-12 18:05:40] [info] =====

[2025-10-12 18:05:40] [info] Report Generated: 2025-10-12
[2025-10-12 18:05:40] [info] Run Start Time: 06:04:49 PM
[2025-10-12 18:05:40] [info] Run End Time: 06:05:40 PM
[2025-10-12 18:05:40] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:05:40] [info] Tool Version: LVR_2025.1
[2025-10-12 18:05:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:05:40] [info] User Name: ubuntu
[2025-10-12 18:05:40] [info] Cores Used: 8
[2025-10-12 18:05:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Top Cell Information
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Layout Top Cell: TOP
[2025-10-12 18:05:40] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:05:40] [info] Database Format: GDSII
[2025-10-12 18:05:40] [info] Verification Mode: Full Check

[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Type of Errors | Count
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] DB_WARN -> 1
[2025-10-12 18:05:40] [info] LRF_ERR001 -> 14
[2025-10-12 18:05:40] [info] DB_WARN002 -> 159
[2025-10-12 18:05:40] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:05:40] [info] DRC_ERR9 -> 89
[2025-10-12 18:05:40] [info] DRC_ERR -> 67
[2025-10-12 18:05:40] [info] DRC_ERR5 -> 408
[2025-10-12 18:05:40] [info] DRC_ERR4 -> 241
[2025-10-12 18:05:40] [info] DRC_ERR1 -> 301
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Rule Violation Summary
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:05:40] [info] |-----|-----|-----|
[2025-10-12 18:05:40] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:05:40] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:05:40] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:05:40] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:05:40] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:05:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:05:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:05:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:05:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:05:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:05:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:05:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:05:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:05:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:05:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Performance Metrics
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] | Stage | Time Taken |
[2025-10-12 18:05:40] [info] |-----|-----|
[2025-10-12 18:05:40] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:05:40] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:05:40] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Summary
[2025-10-12 18:05:40] [info] -----
[2025-10-12 18:05:40] [info] Total Errors: 1120
[2025-10-12 18:05:40] [info] Total Warnings: 321
[2025-10-12 18:05:40] [info] DRC Status: FAILED

```

lvr_summary_465.rpt

```

[2025-10-12 18:06:37] [info] =====
[2025-10-12 18:06:37] [info] Design Rule Check Report
[2025-10-12 18:06:37] [info] =====

[2025-10-12 18:06:37] [info] Report Generated: 2025-10-12
[2025-10-12 18:06:37] [info] Run Start Time: 06:05:46 PM
[2025-10-12 18:06:37] [info] Run End Time: 06:06:37 PM
[2025-10-12 18:06:37] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:06:37] [info] Tool Version: LVR_2025.1
[2025-10-12 18:06:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:06:37] [info] User Name: ubuntu
[2025-10-12 18:06:37] [info] Cores Used: 8
[2025-10-12 18:06:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Top Cell Information
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Layout Top Cell:      TOP
[2025-10-12 18:06:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:06:37] [info] Database Format:      GDSII
[2025-10-12 18:06:37] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Type of Errors | Count
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] DB_WARN -> 1
[2025-10-12 18:06:37] [info] LRF_ERR001 -> 14
[2025-10-12 18:06:37] [info] DB_WARN002 -> 159
[2025-10-12 18:06:37] [info] DB_WARN003 -> 161
[2025-10-12 18:06:37] [info] DRC_ERR9 -> 89
[2025-10-12 18:06:37] [info] DRC_ERR -> 67
[2025-10-12 18:06:37] [info] DRC_ERR5 -> 408
[2025-10-12 18:06:37] [info] DRC_ERR4 -> 241
[2025-10-12 18:06:37] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Rule Violation Summary
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:06:37] [info] |-----|-----|-----|
[2025-10-12 18:06:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:06:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:06:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:06:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:06:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:06:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:06:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:06:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:06:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:06:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:06:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:06:37] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:06:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:06:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:06:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Performance Metrics
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] | Stage          | Time Taken |
[2025-10-12 18:06:37] [info] |-----|-----|
[2025-10-12 18:06:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:06:37] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 18:06:37] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Summary
[2025-10-12 18:06:37] [info] -----
[2025-10-12 18:06:37] [info] Total Errors:      1120
[2025-10-12 18:06:37] [info] Total Warnings:    321
[2025-10-12 18:06:37] [info] DRC Status:        FAILED
```


lvr_summary_466.rpt

```
[2025-10-12 18:07:33] [info] =====
[2025-10-12 18:07:33] [info] Design Rule Check Report
[2025-10-12 18:07:33] [info] =====

[2025-10-12 18:07:33] [info] Report Generated: 2025-10-12
[2025-10-12 18:07:33] [info] Run Start Time: 06:06:42 PM
[2025-10-12 18:07:33] [info] Run End Time: 06:07:33 PM
[2025-10-12 18:07:33] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:07:33] [info] Tool Version: LVR_2025.1
[2025-10-12 18:07:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:07:33] [info] User Name: ubuntu
[2025-10-12 18:07:33] [info] Cores Used: 8
[2025-10-12 18:07:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] Top Cell Information
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] Layout Top Cell: TOP
[2025-10-12 18:07:33] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:07:33] [info] Database Format: GDSII
[2025-10-12 18:07:33] [info] Verification Mode: Full Check

[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] Type of Errors | Count
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] DB_WARN -> 1
[2025-10-12 18:07:33] [info] LRF_ERR001 -> 14
[2025-10-12 18:07:33] [info] DB_WARN002 -> 159
[2025-10-12 18:07:33] [info] DB_WARN003 -> 161
[2025-10-12 18:07:33] [info] DRC_ERR9 -> 89
[2025-10-12 18:07:33] [info] DRC_ERR -> 67
[2025-10-12 18:07:33] [info] DRC_ERR5 -> 408
[2025-10-12 18:07:33] [info] DRC_ERR4 -> 241
[2025-10-12 18:07:33] [info] DRC_ERR1 -> 301
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] Rule Violation Summary
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:07:33] [info] |-----|-----|-----|
[2025-10-12 18:07:33] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:07:33] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:07:33] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:07:33] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:07:33] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:07:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:07:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:07:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:07:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:07:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:07:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:07:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:07:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:07:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:07:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:07:33] [info]
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] Performance Metrics
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:07:33] [info] |-----|-----|
[2025-10-12 18:07:33] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:07:33] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 18:07:33] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info]                Summary
[2025-10-12 18:07:33] [info] -----
[2025-10-12 18:07:33] [info] Total Errors:   1120
[2025-10-12 18:07:33] [info] Total Warnings:    321
[2025-10-12 18:07:33] [info] DRC Status:      FAILED
```

lvr_summary_467.rpt

```
[2025-10-12 18:08:30] [info] =====
[2025-10-12 18:08:30] [info]                Design Rule Check Report
[2025-10-12 18:08:30] [info] =====

[2025-10-12 18:08:30] [info] Report Generated:   2025-10-12
[2025-10-12 18:08:30] [info] Run Start Time:     06:07:39 PM
[2025-10-12 18:08:30] [info] Run End Time:       06:08:30 PM
[2025-10-12 18:08:30] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 18:08:30] [info] Tool Version:       LVR_2025.1
[2025-10-12 18:08:30] [info] Host Name:          ip-172-31-37-173
[2025-10-12 18:08:30] [info] User Name:          ubuntu
[2025-10-12 18:08:30] [info] Cores Used:         8
[2025-10-12 18:08:30] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info]                Top Cell Information
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] Layout Top Cell:    TOP
[2025-10-12 18:08:30] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 18:08:30] [info] Database Format:      GDSII
[2025-10-12 18:08:30] [info] Verification Mode:   Full Check

[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info]                Type of Errors | Count
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] DB_WARN -> 1
[2025-10-12 18:08:30] [info] LRF_ERR001 -> 14
[2025-10-12 18:08:30] [info] DB_WARN002 -> 159
[2025-10-12 18:08:30] [info] DB_WARN003 -> 161
[2025-10-12 18:08:30] [info] DRC_ERR9 -> 89
[2025-10-12 18:08:30] [info] DRC_ERR -> 67
[2025-10-12 18:08:30] [info] DRC_ERR5 -> 408
[2025-10-12 18:08:30] [info] DRC_ERR4 -> 241
[2025-10-12 18:08:30] [info] DRC_ERR1 -> 301
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info]                Rule Violation Summary
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:08:30] [info] |-----|-----|-----|
[2025-10-12 18:08:30] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:08:30] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:08:30] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:08:30] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:08:30] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:08:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:08:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:08:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:08:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:08:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:08:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:08:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:08:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:08:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:08:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:08:30] [info]
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] Performance Metrics
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] | Stage | Time Taken |
[2025-10-12 18:08:30] [info] |-----|-----|
[2025-10-12 18:08:30] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:08:30] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:08:30] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] Summary
[2025-10-12 18:08:30] [info] -----
[2025-10-12 18:08:30] [info] Total Errors: 1120
[2025-10-12 18:08:30] [info] Total Warnings: 321
[2025-10-12 18:08:30] [info] DRC Status: FAILED
```

lvr_summary_468.rpt

```
[2025-10-12 18:09:28] [info] =====
[2025-10-12 18:09:28] [info] Design Rule Check Report
[2025-10-12 18:09:28] [info] =====

[2025-10-12 18:09:28] [info] Report Generated: 2025-10-12
[2025-10-12 18:09:28] [info] Run Start Time: 06:08:36 PM
[2025-10-12 18:09:28] [info] Run End Time: 06:09:28 PM
[2025-10-12 18:09:28] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:09:28] [info] Tool Version: LVR_2025.1
[2025-10-12 18:09:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:09:28] [info] User Name: ubuntu
[2025-10-12 18:09:28] [info] Cores Used: 8
[2025-10-12 18:09:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Top Cell Information
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Layout Top Cell: TOP
[2025-10-12 18:09:28] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:09:28] [info] Database Format: GDSII
[2025-10-12 18:09:28] [info] Verification Mode: Full Check

[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Type of Errors | Count
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] DB_WARN -> 1
[2025-10-12 18:09:28] [info] LRF_ERR001 -> 14
[2025-10-12 18:09:28] [info] DB_WARN002 -> 159
[2025-10-12 18:09:28] [info] DB_WARN003 -> 161
```

```
[2025-10-12 18:09:28] [info] DRC_ERR9 -> 89
[2025-10-12 18:09:28] [info] DRC_ERR -> 67
[2025-10-12 18:09:28] [info] DRC_ERR5 -> 408
[2025-10-12 18:09:28] [info] DRC_ERR4 -> 241
[2025-10-12 18:09:28] [info] DRC_ERR1 -> 301
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Rule Violation Summary
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:09:28] [info] |-----|-----|-----|
[2025-10-12 18:09:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:09:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:09:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:09:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:09:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:09:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:09:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:09:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:09:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:09:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:09:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:09:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:09:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:09:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:09:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Performance Metrics
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] | Stage | Time Taken |
[2025-10-12 18:09:28] [info] |-----|-----|
[2025-10-12 18:09:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:09:28] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:09:28] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Summary
[2025-10-12 18:09:28] [info] -----
[2025-10-12 18:09:28] [info] Total Errors: 1120
[2025-10-12 18:09:28] [info] Total Warnings: 321
[2025-10-12 18:09:28] [info] DRC Status: FAILED
```

lvr_summary_469.rpt

```
[2025-10-12 18:10:25] [info] =====
[2025-10-12 18:10:25] [info] Design Rule Check Report
[2025-10-12 18:10:25] [info] =====

[2025-10-12 18:10:25] [info] Report Generated: 2025-10-12
[2025-10-12 18:10:25] [info] Run Start Time: 06:09:33 PM
[2025-10-12 18:10:25] [info] Run End Time: 06:10:25 PM
[2025-10-12 18:10:25] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:10:25] [info] Tool Version: LVR_2025.1
[2025-10-12 18:10:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:10:25] [info] User Name: ubuntu
[2025-10-12 18:10:25] [info] Cores Used: 8
[2025-10-12 18:10:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] Top Cell Information
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] Layout Top Cell:      TOP
[2025-10-12 18:10:25] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:10:25] [info] Database Format:      GDSII
[2025-10-12 18:10:25] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] Type of Errors | Count
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] DB_WARN -> 1
[2025-10-12 18:10:25] [info] LRF_ERR001 -> 14
[2025-10-12 18:10:25] [info] DB_WARN002 -> 159
[2025-10-12 18:10:25] [info] DB_WARN003 -> 161
[2025-10-12 18:10:25] [info] DRC_ERR9 -> 89
[2025-10-12 18:10:25] [info] DRC_ERR -> 67
[2025-10-12 18:10:25] [info] DRC_ERR5 -> 408
[2025-10-12 18:10:25] [info] DRC_ERR4 -> 241
[2025-10-12 18:10:25] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] Rule Violation Summary
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:10:25] [info] |-----|-----|-----|
[2025-10-12 18:10:25] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:10:25] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:10:25] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:10:25] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:10:25] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:10:25] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:10:25] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:10:25] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:10:25] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:10:25] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:10:25] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:10:25] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:10:25] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:10:25] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:10:25] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:10:25] [info] -----
```

```
[2025-10-12 18:10:25] [info] Performance Metrics
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] | Stage          | Time Taken |
[2025-10-12 18:10:25] [info] |-----|-----|
[2025-10-12 18:10:25] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:10:25] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 18:10:25] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] Summary
```

```
[2025-10-12 18:10:25] [info] -----
[2025-10-12 18:10:25] [info] Total Errors:      1120
[2025-10-12 18:10:25] [info] Total Warnings:    321
[2025-10-12 18:10:25] [info] DRC Status:        FAILED
```

lvr_summary_47.rpt

```
[2025-10-12 11:25:57] [info] =====
[2025-10-12 11:25:57] [info]                      Design Rule Check Report
[2025-10-12 11:25:57] [info] =====

[2025-10-12 11:25:57] [info] Report Generated:    2025-10-12
[2025-10-12 11:25:57] [info] Run Start Time:      11:25:06 AM
[2025-10-12 11:25:57] [info] Run End Time:        11:25:57 AM
[2025-10-12 11:25:57] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:25:57] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:25:57] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:25:57] [info] User Name:           ubuntu
[2025-10-12 11:25:57] [info] Cores Used:          8
[2025-10-12 11:25:57] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info]                      Top Cell Information
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info] Layout Top Cell:     TOP
[2025-10-12 11:25:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:25:57] [info] Database Format:      GDSII
[2025-10-12 11:25:57] [info] Verification Mode:    Full Check

[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info]                      Type of Errors | Count
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info] DB_WARN -> 1
[2025-10-12 11:25:57] [info] LRF_ERR001 -> 14
[2025-10-12 11:25:57] [info] DB_WARN002 -> 159
[2025-10-12 11:25:57] [info] DB_WARN003 -> 161
[2025-10-12 11:25:57] [info] DRC_ERR9 -> 89
[2025-10-12 11:25:57] [info] DRC_ERR -> 67
[2025-10-12 11:25:57] [info] DRC_ERR5 -> 408
[2025-10-12 11:25:57] [info] DRC_ERR4 -> 241
[2025-10-12 11:25:57] [info] DRC_ERR1 -> 301
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info]                      Rule Violation Summary
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:25:57] [info] |-----|-----|-----|
[2025-10-12 11:25:57] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:25:57] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:25:57] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:25:57] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:25:57] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:25:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:25:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:25:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:25:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:25:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:25:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:25:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:25:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:25:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:25:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:25:57] [info]
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info]                      Performance Metrics
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:25:57] [info] |-----|-----|
[2025-10-12 11:25:57] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:25:57] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 11:25:57] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info] Summary
[2025-10-12 11:25:57] [info] -----
[2025-10-12 11:25:57] [info] Total Errors: 1120
[2025-10-12 11:25:57] [info] Total Warnings: 321
[2025-10-12 11:25:57] [info] DRC Status: FAILED
```

lvr_summary_470.rpt

```
[2025-10-12 18:11:21] [info] =====
[2025-10-12 18:11:21] [info] Design Rule Check Report
[2025-10-12 18:11:21] [info] =====

[2025-10-12 18:11:21] [info] Report Generated: 2025-10-12
[2025-10-12 18:11:21] [info] Run Start Time: 06:10:30 PM
[2025-10-12 18:11:21] [info] Run End Time: 06:11:21 PM
[2025-10-12 18:11:21] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:11:21] [info] Tool Version: LVR_2025.1
[2025-10-12 18:11:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:11:21] [info] User Name: ubuntu
[2025-10-12 18:11:21] [info] Cores Used: 8
[2025-10-12 18:11:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Top Cell Information
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Layout Top Cell: TOP
[2025-10-12 18:11:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:11:21] [info] Database Format: GDSII
[2025-10-12 18:11:21] [info] Verification Mode: Full Check

[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Type of Errors | Count
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] DB_WARN -> 1
[2025-10-12 18:11:21] [info] LRF_ERR001 -> 14
[2025-10-12 18:11:21] [info] DB_WARN002 -> 159
[2025-10-12 18:11:21] [info] DB_WARN003 -> 161
[2025-10-12 18:11:21] [info] DRC_ERR9 -> 89
[2025-10-12 18:11:21] [info] DRC_ERR -> 67
[2025-10-12 18:11:21] [info] DRC_ERR5 -> 408
[2025-10-12 18:11:21] [info] DRC_ERR4 -> 241
[2025-10-12 18:11:21] [info] DRC_ERR1 -> 301
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Rule Violation Summary
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:11:21] [info] |-----|-----|-----|
[2025-10-12 18:11:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:11:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:11:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:11:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:11:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 18:11:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:11:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:11:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:11:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:11:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:11:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:11:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:11:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:11:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:11:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:11:21] [info]
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Performance Metrics
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] | Stage | Time Taken |
[2025-10-12 18:11:21] [info] |-----|-----|
[2025-10-12 18:11:21] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:11:21] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:11:21] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Summary
[2025-10-12 18:11:21] [info] -----
[2025-10-12 18:11:21] [info] Total Errors: 1120
[2025-10-12 18:11:21] [info] Total Warnings: 321
[2025-10-12 18:11:21] [info] DRC Status: FAILED
```

lvr_summary_471.rpt

```
[2025-10-12 18:12:19] [info] =====
[2025-10-12 18:12:19] [info] Design Rule Check Report
[2025-10-12 18:12:19] [info] =====

[2025-10-12 18:12:19] [info] Report Generated: 2025-10-12
[2025-10-12 18:12:19] [info] Run Start Time: 06:11:27 PM
[2025-10-12 18:12:19] [info] Run End Time: 06:12:19 PM
[2025-10-12 18:12:19] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:12:19] [info] Tool Version: LVR_2025.1
[2025-10-12 18:12:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:12:19] [info] User Name: ubuntu
[2025-10-12 18:12:19] [info] Cores Used: 8
[2025-10-12 18:12:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Top Cell Information
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Layout Top Cell: TOP
[2025-10-12 18:12:19] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:12:19] [info] Database Format: GDSII
[2025-10-12 18:12:19] [info] Verification Mode: Full Check

[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Type of Errors | Count
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] DB_WARN -> 1
[2025-10-12 18:12:19] [info] LRF_ERR001 -> 14
[2025-10-12 18:12:19] [info] DB_WARN002 -> 159
[2025-10-12 18:12:19] [info] DB_WARN003 -> 161
```



```

[2025-10-12 18:12:19] [info] DRC_ERR9 -> 89
[2025-10-12 18:12:19] [info] DRC_ERR -> 67
[2025-10-12 18:12:19] [info] DRC_ERR5 -> 408
[2025-10-12 18:12:19] [info] DRC_ERR4 -> 241
[2025-10-12 18:12:19] [info] DRC_ERR1 -> 301
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Rule Violation Summary
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:12:19] [info] |-----|-----|-----|
[2025-10-12 18:12:19] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:12:19] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:12:19] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:12:19] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:12:19] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:12:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:12:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:12:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:12:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:12:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:12:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:12:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:12:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:12:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:12:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Performance Metrics
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] | Stage | Time Taken |
[2025-10-12 18:12:19] [info] |-----|-----|
[2025-10-12 18:12:19] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:12:19] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:12:19] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Summary
[2025-10-12 18:12:19] [info] -----
[2025-10-12 18:12:19] [info] Total Errors: 1120
[2025-10-12 18:12:19] [info] Total Warnings: 321
[2025-10-12 18:12:19] [info] DRC Status: FAILED

```

lvr_summary_472.rpt

```

[2025-10-12 18:13:14] [info] =====
[2025-10-12 18:13:14] [info] Design Rule Check Report
[2025-10-12 18:13:14] [info] =====

[2025-10-12 18:13:14] [info] Report Generated: 2025-10-12
[2025-10-12 18:13:14] [info] Run Start Time: 06:12:24 PM
[2025-10-12 18:13:14] [info] Run End Time: 06:13:14 PM
[2025-10-12 18:13:14] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:13:14] [info] Tool Version: LVR_2025.1
[2025-10-12 18:13:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:13:14] [info] User Name: ubuntu
[2025-10-12 18:13:14] [info] Cores Used: 8
[2025-10-12 18:13:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Top Cell Information
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Layout Top Cell:      TOP
[2025-10-12 18:13:14] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:13:14] [info] Database Format:      GDSII
[2025-10-12 18:13:14] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Type of Errors | Count
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] DB_WARN -> 1
[2025-10-12 18:13:14] [info] LRF_ERR001 -> 14
[2025-10-12 18:13:14] [info] DB_WARN002 -> 159
[2025-10-12 18:13:14] [info] DB_WARN003 -> 161
[2025-10-12 18:13:14] [info] DRC_ERR9 -> 89
[2025-10-12 18:13:14] [info] DRC_ERR -> 67
[2025-10-12 18:13:14] [info] DRC_ERR5 -> 408
[2025-10-12 18:13:14] [info] DRC_ERR4 -> 241
[2025-10-12 18:13:14] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Rule Violation Summary
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:13:14] [info] |-----|-----|-----|
[2025-10-12 18:13:14] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:13:14] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:13:14] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:13:14] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:13:14] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:13:14] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:13:14] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:13:14] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:13:14] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:13:14] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:13:14] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:13:14] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:13:14] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:13:14] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:13:14] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:13:14] [info] |
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Performance Metrics
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] | Stage          | Time Taken |
[2025-10-12 18:13:14] [info] |-----|-----|
[2025-10-12 18:13:14] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:13:14] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 18:13:14] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Summary
```

```
[2025-10-12 18:13:14] [info] -----
[2025-10-12 18:13:14] [info] Total Errors:      1120
[2025-10-12 18:13:14] [info] Total Warnings:    321
[2025-10-12 18:13:14] [info] DRC Status:        FAILED
```

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```
[2025-10-12 18:14:12] [info] =====
[2025-10-12 18:14:12] [info] Design Rule Check Report
[2025-10-12 18:14:12] [info] =====

[2025-10-12 18:14:12] [info] Report Generated: 2025-10-12
[2025-10-12 18:14:12] [info] Run Start Time: 06:13:21 PM
[2025-10-12 18:14:12] [info] Run End Time: 06:14:12 PM
[2025-10-12 18:14:12] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:14:12] [info] Tool Version: LVR_2025.1
[2025-10-12 18:14:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:14:12] [info] User Name: ubuntu
[2025-10-12 18:14:12] [info] Cores Used: 8
[2025-10-12 18:14:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] Top Cell Information
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] Layout Top Cell: TOP
[2025-10-12 18:14:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:14:12] [info] Database Format: GDSII
[2025-10-12 18:14:12] [info] Verification Mode: Full Check

[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] Type of Errors | Count
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] DB_WARN -> 1
[2025-10-12 18:14:12] [info] LRF_ERR001 -> 14
[2025-10-12 18:14:12] [info] DB_WARN002 -> 159
[2025-10-12 18:14:12] [info] DB_WARN003 -> 161
[2025-10-12 18:14:12] [info] DRC_ERR9 -> 89
[2025-10-12 18:14:12] [info] DRC_ERR -> 67
[2025-10-12 18:14:12] [info] DRC_ERR5 -> 408
[2025-10-12 18:14:12] [info] DRC_ERR4 -> 241
[2025-10-12 18:14:12] [info] DRC_ERR1 -> 301
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] Rule Violation Summary
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:14:12] [info] |-----|-----|-----|
[2025-10-12 18:14:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:14:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:14:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:14:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:14:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:14:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:14:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:14:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:14:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:14:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:14:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:14:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:14:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:14:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:14:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:14:12] [info]
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] Performance Metrics
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:14:12] [info] |-----|
[2025-10-12 18:14:12] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:14:12] [info] | Rule Checking        | 0 minutes 39 seconds |
[2025-10-12 18:14:12] [info] | Report Generation     | 0 minutes 0 seconds  |

[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info]                Summary
[2025-10-12 18:14:12] [info] -----
[2025-10-12 18:14:12] [info] Total Errors:    1120
[2025-10-12 18:14:12] [info] Total Warnings:      321
[2025-10-12 18:14:12] [info] DRC Status:      FAILED
```

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```
[2025-10-12 18:15:10] [info] =====
[2025-10-12 18:15:10] [info]                Design Rule Check Report
[2025-10-12 18:15:10] [info] =====

[2025-10-12 18:15:10] [info] Report Generated:    2025-10-12
[2025-10-12 18:15:10] [info] Run Start Time:      06:14:18 PM
[2025-10-12 18:15:10] [info] Run End Time:        06:15:10 PM
[2025-10-12 18:15:10] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 18:15:10] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:15:10] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:15:10] [info] User Name:           ubuntu
[2025-10-12 18:15:10] [info] Cores Used:          8
[2025-10-12 18:15:10] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info]                Top Cell Information
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] Layout Top Cell:     TOP
[2025-10-12 18:15:10] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:15:10] [info] Database Format:       GDSII
[2025-10-12 18:15:10] [info] Verification Mode:    Full Check

[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info]                Type of Errors | Count
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] DB_WARN -> 1
[2025-10-12 18:15:10] [info] LRF_ERR001 -> 14
[2025-10-12 18:15:10] [info] DB_WARN002 -> 159
[2025-10-12 18:15:10] [info] DB_WARN003 -> 161
[2025-10-12 18:15:10] [info] DRC_ERR9 -> 89
[2025-10-12 18:15:10] [info] DRC_ERR -> 67
[2025-10-12 18:15:10] [info] DRC_ERR5 -> 408
[2025-10-12 18:15:10] [info] DRC_ERR4 -> 241
[2025-10-12 18:15:10] [info] DRC_ERR1 -> 301
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info]                Rule Violation Summary
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:15:10] [info] |-----|-----|-----|
[2025-10-12 18:15:10] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:15:10] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:15:10] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:15:10] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:15:10] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:15:10] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:15:10] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:15:10] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:15:10] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:15:10] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:15:10] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:15:10] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:15:10] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:15:10] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:15:10] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:15:10] [info]
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] Performance Metrics
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] | Stage | Time Taken |
[2025-10-12 18:15:10] [info] |-----|-----|
[2025-10-12 18:15:10] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:15:10] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:15:10] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] Summary
[2025-10-12 18:15:10] [info] -----
[2025-10-12 18:15:10] [info] Total Errors: 1120
[2025-10-12 18:15:10] [info] Total Warnings: 321
[2025-10-12 18:15:10] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:16:05] [info] =====
[2025-10-12 18:16:05] [info] Design Rule Check Report
[2025-10-12 18:16:05] [info] =====

[2025-10-12 18:16:05] [info] Report Generated: 2025-10-12
[2025-10-12 18:16:05] [info] Run Start Time: 06:15:15 PM
[2025-10-12 18:16:05] [info] Run End Time: 06:16:05 PM
[2025-10-12 18:16:05] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:16:05] [info] Tool Version: LVR_2025.1
[2025-10-12 18:16:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:16:05] [info] User Name: ubuntu
[2025-10-12 18:16:05] [info] Cores Used: 8
[2025-10-12 18:16:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Top Cell Information
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Layout Top Cell: TOP
[2025-10-12 18:16:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:16:05] [info] Database Format: GDSII
[2025-10-12 18:16:05] [info] Verification Mode: Full Check

[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Type of Errors | Count
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] DB_WARN -> 1
[2025-10-12 18:16:05] [info] LRF_ERR001 -> 14
[2025-10-12 18:16:05] [info] DB_WARN002 -> 159
[2025-10-12 18:16:05] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:16:05] [info] DRC_ERR9 -> 89
[2025-10-12 18:16:05] [info] DRC_ERR -> 67
[2025-10-12 18:16:05] [info] DRC_ERR5 -> 408
[2025-10-12 18:16:05] [info] DRC_ERR4 -> 241
[2025-10-12 18:16:05] [info] DRC_ERR1 -> 301
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Rule Violation Summary
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:16:05] [info] |-----|-----|-----|
[2025-10-12 18:16:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:16:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:16:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:16:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:16:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:16:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:16:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:16:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:16:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:16:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:16:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:16:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:16:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:16:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:16:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Performance Metrics
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] | Stage | Time Taken |
[2025-10-12 18:16:05] [info] |-----|-----|
[2025-10-12 18:16:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:16:05] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:16:05] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Summary
[2025-10-12 18:16:05] [info] -----
[2025-10-12 18:16:05] [info] Total Errors: 1120
[2025-10-12 18:16:05] [info] Total Warnings: 321
[2025-10-12 18:16:05] [info] DRC Status: FAILED

```

lvr_summary_476.rpt

```

[2025-10-12 18:17:03] [info] =====
[2025-10-12 18:17:03] [info] Design Rule Check Report
[2025-10-12 18:17:03] [info] =====

[2025-10-12 18:17:03] [info] Report Generated: 2025-10-12
[2025-10-12 18:17:03] [info] Run Start Time: 06:16:12 PM
[2025-10-12 18:17:03] [info] Run End Time: 06:17:03 PM
[2025-10-12 18:17:03] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:17:03] [info] Tool Version: LVR_2025.1
[2025-10-12 18:17:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:17:03] [info] User Name: ubuntu
[2025-10-12 18:17:03] [info] Cores Used: 8
[2025-10-12 18:17:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Top Cell Information
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Layout Top Cell:      TOP
[2025-10-12 18:17:03] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:17:03] [info] Database Format:       GDSII
[2025-10-12 18:17:03] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Type of Errors | Count
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] DB_WARN -> 1
[2025-10-12 18:17:03] [info] LRF_ERR001 -> 14
[2025-10-12 18:17:03] [info] DB_WARN002 -> 159
[2025-10-12 18:17:03] [info] DB_WARN003 -> 161
[2025-10-12 18:17:03] [info] DRC_ERR9 -> 89
[2025-10-12 18:17:03] [info] DRC_ERR -> 67
[2025-10-12 18:17:03] [info] DRC_ERR5 -> 408
[2025-10-12 18:17:03] [info] DRC_ERR4 -> 241
[2025-10-12 18:17:03] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Rule Violation Summary
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:17:03] [info] |-----|-----|-----|
[2025-10-12 18:17:03] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:17:03] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:17:03] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:17:03] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:17:03] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:17:03] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:17:03] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:17:03] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:17:03] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:17:03] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:17:03] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:17:03] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:17:03] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:17:03] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:17:03] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:17:03] [info] |
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Performance Metrics
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] | Stage          | Time Taken |
[2025-10-12 18:17:03] [info] |-----|-----|
[2025-10-12 18:17:03] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:17:03] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 18:17:03] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Summary
```

```
[2025-10-12 18:17:03] [info] -----
[2025-10-12 18:17:03] [info] Total Errors:      1120
[2025-10-12 18:17:03] [info] Total Warnings:    321
[2025-10-12 18:17:03] [info] DRC Status:        FAILED
```

lvr_summary_477.rpt

```
[2025-10-12 18:18:00] [info] =====
[2025-10-12 18:18:00] [info] Design Rule Check Report
[2025-10-12 18:18:00] [info] =====

[2025-10-12 18:18:00] [info] Report Generated: 2025-10-12
[2025-10-12 18:18:00] [info] Run Start Time: 06:17:09 PM
[2025-10-12 18:18:00] [info] Run End Time: 06:18:00 PM
[2025-10-12 18:18:00] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:18:00] [info] Tool Version: LVR_2025.1
[2025-10-12 18:18:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:18:00] [info] User Name: ubuntu
[2025-10-12 18:18:00] [info] Cores Used: 8
[2025-10-12 18:18:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] Top Cell Information
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] Layout Top Cell: TOP
[2025-10-12 18:18:00] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:18:00] [info] Database Format: GDSII
[2025-10-12 18:18:00] [info] Verification Mode: Full Check

[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] Type of Errors | Count
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] DB_WARN -> 1
[2025-10-12 18:18:00] [info] LRF_ERR001 -> 14
[2025-10-12 18:18:00] [info] DB_WARN002 -> 159
[2025-10-12 18:18:00] [info] DB_WARN003 -> 161
[2025-10-12 18:18:00] [info] DRC_ERR9 -> 89
[2025-10-12 18:18:00] [info] DRC_ERR -> 67
[2025-10-12 18:18:00] [info] DRC_ERR5 -> 408
[2025-10-12 18:18:00] [info] DRC_ERR4 -> 241
[2025-10-12 18:18:00] [info] DRC_ERR1 -> 301
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] Rule Violation Summary
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:18:00] [info] |-----|-----|-----|
[2025-10-12 18:18:00] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:18:00] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:18:00] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:18:00] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:18:00] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:18:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:18:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:18:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:18:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:18:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:18:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:18:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:18:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:18:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:18:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:18:00] [info]
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] Performance Metrics
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] | Stage | Time Taken |
```



```
[2025-10-12 18:18:00] [info] |-----|-----|
[2025-10-12 18:18:00] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:18:00] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 18:18:00] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info]                Summary
[2025-10-12 18:18:00] [info] -----
[2025-10-12 18:18:00] [info] Total Errors:    1120
[2025-10-12 18:18:00] [info] Total Warnings:    321
[2025-10-12 18:18:00] [info] DRC Status:      FAILED
```

lvr_summary_478.rpt

```
[2025-10-12 18:18:57] [info] =====
[2025-10-12 18:18:57] [info]                Design Rule Check Report
[2025-10-12 18:18:57] [info] =====

[2025-10-12 18:18:57] [info] Report Generated:    2025-10-12
[2025-10-12 18:18:57] [info] Run Start Time:      06:18:07 PM
[2025-10-12 18:18:57] [info] Run End Time:        06:18:57 PM
[2025-10-12 18:18:57] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 18:18:57] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:18:57] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:18:57] [info] User Name:           ubuntu
[2025-10-12 18:18:57] [info] Cores Used:          8
[2025-10-12 18:18:57] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info]                Top Cell Information
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] Layout Top Cell:     TOP
[2025-10-12 18:18:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:18:57] [info] Database Format:       GDSII
[2025-10-12 18:18:57] [info] Verification Mode:    Full Check

[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info]                Type of Errors | Count
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] DB_WARN -> 1
[2025-10-12 18:18:57] [info] LRF_ERR001 -> 14
[2025-10-12 18:18:57] [info] DB_WARN002 -> 159
[2025-10-12 18:18:57] [info] DB_WARN003 -> 161
[2025-10-12 18:18:57] [info] DRC_ERR9 -> 89
[2025-10-12 18:18:57] [info] DRC_ERR -> 67
[2025-10-12 18:18:57] [info] DRC_ERR5 -> 408
[2025-10-12 18:18:57] [info] DRC_ERR4 -> 241
[2025-10-12 18:18:57] [info] DRC_ERR1 -> 301
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info]                Rule Violation Summary
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:18:57] [info] |-----|-----|-----|
[2025-10-12 18:18:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:18:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:18:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:18:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:18:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:18:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:18:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:18:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:18:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:18:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:18:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:18:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:18:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:18:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:18:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:18:57] [info]
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] Performance Metrics
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] | Stage | Time Taken |
[2025-10-12 18:18:57] [info] |-----|-----|
[2025-10-12 18:18:57] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:18:57] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:18:57] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] Summary
[2025-10-12 18:18:57] [info] -----
[2025-10-12 18:18:57] [info] Total Errors: 1120
[2025-10-12 18:18:57] [info] Total Warnings: 321
[2025-10-12 18:18:57] [info] DRC Status: FAILED
```

lvr_summary_479.rpt

```
[2025-10-12 18:19:55] [info] =====
[2025-10-12 18:19:55] [info] Design Rule Check Report
[2025-10-12 18:19:55] [info] =====

[2025-10-12 18:19:55] [info] Report Generated: 2025-10-12
[2025-10-12 18:19:55] [info] Run Start Time: 06:19:05 PM
[2025-10-12 18:19:55] [info] Run End Time: 06:19:55 PM
[2025-10-12 18:19:55] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:19:55] [info] Tool Version: LVR_2025.1
[2025-10-12 18:19:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:19:55] [info] User Name: ubuntu
[2025-10-12 18:19:55] [info] Cores Used: 8
[2025-10-12 18:19:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Top Cell Information
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Layout Top Cell: TOP
[2025-10-12 18:19:55] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:19:55] [info] Database Format: GDSII
[2025-10-12 18:19:55] [info] Verification Mode: Full Check

[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Type of Errors | Count
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] DB_WARN -> 1
[2025-10-12 18:19:55] [info] LRF_ERR001 -> 14
[2025-10-12 18:19:55] [info] DB_WARN002 -> 159
[2025-10-12 18:19:55] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:19:55] [info] DRC_ERR9 -> 89
[2025-10-12 18:19:55] [info] DRC_ERR -> 67
[2025-10-12 18:19:55] [info] DRC_ERR5 -> 408
[2025-10-12 18:19:55] [info] DRC_ERR4 -> 241
[2025-10-12 18:19:55] [info] DRC_ERR1 -> 301
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Rule Violation Summary
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:19:55] [info] |-----|-----|-----|
[2025-10-12 18:19:55] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:19:55] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:19:55] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:19:55] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:19:55] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:19:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:19:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:19:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:19:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:19:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:19:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:19:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:19:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:19:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:19:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Performance Metrics
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] | Stage | Time Taken |
[2025-10-12 18:19:55] [info] |-----|-----|
[2025-10-12 18:19:55] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:19:55] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:19:55] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Summary
[2025-10-12 18:19:55] [info] -----
[2025-10-12 18:19:55] [info] Total Errors: 1120
[2025-10-12 18:19:55] [info] Total Warnings: 321
[2025-10-12 18:19:55] [info] DRC Status: FAILED

```

lvr_summary_48.rpt

```

[2025-10-12 11:26:53] [info] =====
[2025-10-12 11:26:53] [info] Design Rule Check Report
[2025-10-12 11:26:53] [info] =====

[2025-10-12 11:26:53] [info] Report Generated: 2025-10-12
[2025-10-12 11:26:53] [info] Run Start Time: 11:26:03 AM
[2025-10-12 11:26:53] [info] Run End Time: 11:26:53 AM
[2025-10-12 11:26:53] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:26:53] [info] Tool Version: LVR_2025.1
[2025-10-12 11:26:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:26:53] [info] User Name: ubuntu
[2025-10-12 11:26:53] [info] Cores Used: 8
[2025-10-12 11:26:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Top Cell Information
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Layout Top Cell:      TOP
[2025-10-12 11:26:53] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:26:53] [info] Database Format:      GDSII
[2025-10-12 11:26:53] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Type of Errors | Count
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] DB_WARN -> 1
[2025-10-12 11:26:53] [info] LRF_ERR001 -> 14
[2025-10-12 11:26:53] [info] DB_WARN002 -> 159
[2025-10-12 11:26:53] [info] DB_WARN003 -> 161
[2025-10-12 11:26:53] [info] DRC_ERR9 -> 89
[2025-10-12 11:26:53] [info] DRC_ERR -> 67
[2025-10-12 11:26:53] [info] DRC_ERR5 -> 408
[2025-10-12 11:26:53] [info] DRC_ERR4 -> 241
[2025-10-12 11:26:53] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Rule Violation Summary
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:26:53] [info] |-----|-----|-----|
[2025-10-12 11:26:53] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:26:53] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:26:53] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:26:53] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:26:53] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:26:53] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:26:53] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:26:53] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:26:53] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:26:53] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:26:53] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:26:53] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:26:53] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:26:53] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:26:53] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:26:53] [info] |
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Performance Metrics
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] | Stage          | Time Taken |
[2025-10-12 11:26:53] [info] |-----|-----|
[2025-10-12 11:26:53] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:26:53] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 11:26:53] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Summary
```

```
[2025-10-12 11:26:53] [info] -----
[2025-10-12 11:26:53] [info] Total Errors:      1120
[2025-10-12 11:26:53] [info] Total Warnings:    321
[2025-10-12 11:26:53] [info] DRC Status:        FAILED
```

lvr_summary_480.rpt

```
[2025-10-12 18:20:53] [info] =====
[2025-10-12 18:20:53] [info]                      Design Rule Check Report
[2025-10-12 18:20:53] [info] =====

[2025-10-12 18:20:53] [info] Report Generated:    2025-10-12
[2025-10-12 18:20:53] [info] Run Start Time:      06:20:02 PM
[2025-10-12 18:20:53] [info] Run End Time:        06:20:53 PM
[2025-10-12 18:20:53] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 18:20:53] [info] Tool Version:       LVR_2025.1
[2025-10-12 18:20:53] [info] Host Name:         ip-172-31-37-173
[2025-10-12 18:20:53] [info] User Name:        ubuntu
[2025-10-12 18:20:53] [info] Cores Used:       8
[2025-10-12 18:20:53] [info] Memory Utilized:   670.22 MB / 61.77812957763672 GB

[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info]                      Top Cell Information
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info] Layout Top Cell:    TOP
[2025-10-12 18:20:53] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 18:20:53] [info] Database Format:     GDSII
[2025-10-12 18:20:53] [info] Verification Mode:   Full Check

[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info]                      Type of Errors | Count
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info] DB_WARN -> 1
[2025-10-12 18:20:53] [info] LRF_ERR001 -> 14
[2025-10-12 18:20:53] [info] DB_WARN002 -> 159
[2025-10-12 18:20:53] [info] DB_WARN003 -> 161
[2025-10-12 18:20:53] [info] DRC_ERR9 -> 89
[2025-10-12 18:20:53] [info] DRC_ERR -> 67
[2025-10-12 18:20:53] [info] DRC_ERR5 -> 408
[2025-10-12 18:20:53] [info] DRC_ERR4 -> 241
[2025-10-12 18:20:53] [info] DRC_ERR1 -> 301
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info]                      Rule Violation Summary
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:20:53] [info] |-----|-----|-----|
[2025-10-12 18:20:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:20:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:20:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:20:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:20:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:20:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:20:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:20:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:20:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:20:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:20:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:20:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:20:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:20:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:20:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:20:53] [info]
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info]                      Performance Metrics
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:20:53] [info] |-----|-----|
[2025-10-12 18:20:53] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:20:53] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:20:53] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info] Summary
[2025-10-12 18:20:53] [info] -----
[2025-10-12 18:20:53] [info] Total Errors: 1120
[2025-10-12 18:20:53] [info] Total Warnings: 321
[2025-10-12 18:20:53] [info] DRC Status: FAILED
```

lvr_summary_481.rpt

```
[2025-10-12 18:21:53] [info] =====
[2025-10-12 18:21:53] [info] Design Rule Check Report
[2025-10-12 18:21:53] [info] =====

[2025-10-12 18:21:53] [info] Report Generated: 2025-10-12
[2025-10-12 18:21:53] [info] Run Start Time: 06:21:01 PM
[2025-10-12 18:21:53] [info] Run End Time: 06:21:53 PM
[2025-10-12 18:21:53] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:21:53] [info] Tool Version: LVR_2025.1
[2025-10-12 18:21:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:21:53] [info] User Name: ubuntu
[2025-10-12 18:21:53] [info] Cores Used: 8
[2025-10-12 18:21:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Top Cell Information
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Layout Top Cell: TOP
[2025-10-12 18:21:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:21:53] [info] Database Format: GDSII
[2025-10-12 18:21:53] [info] Verification Mode: Full Check

[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Type of Errors | Count
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] DB_WARN -> 1
[2025-10-12 18:21:53] [info] LRF_ERR001 -> 14
[2025-10-12 18:21:53] [info] DB_WARN002 -> 159
[2025-10-12 18:21:53] [info] DB_WARN003 -> 161
[2025-10-12 18:21:53] [info] DRC_ERR9 -> 89
[2025-10-12 18:21:53] [info] DRC_ERR -> 67
[2025-10-12 18:21:53] [info] DRC_ERR5 -> 408
[2025-10-12 18:21:53] [info] DRC_ERR4 -> 241
[2025-10-12 18:21:53] [info] DRC_ERR1 -> 301
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Rule Violation Summary
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:21:53] [info] |-----|-----|-----|
[2025-10-12 18:21:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:21:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:21:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:21:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:21:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 18:21:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:21:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:21:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:21:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:21:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:21:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:21:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:21:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:21:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:21:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:21:53] [info]
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Performance Metrics
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] | Stage | Time Taken |
[2025-10-12 18:21:53] [info] |-----|-----|
[2025-10-12 18:21:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:21:53] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:21:53] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Summary
[2025-10-12 18:21:53] [info] -----
[2025-10-12 18:21:53] [info] Total Errors: 1120
[2025-10-12 18:21:53] [info] Total Warnings: 321
[2025-10-12 18:21:53] [info] DRC Status: FAILED
```

lvr_summary_482.rpt

```
[2025-10-12 18:22:51] [info] =====
[2025-10-12 18:22:51] [info] Design Rule Check Report
[2025-10-12 18:22:51] [info] =====

[2025-10-12 18:22:51] [info] Report Generated: 2025-10-12
[2025-10-12 18:22:51] [info] Run Start Time: 06:21:59 PM
[2025-10-12 18:22:51] [info] Run End Time: 06:22:51 PM
[2025-10-12 18:22:51] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:22:51] [info] Tool Version: LVR_2025.1
[2025-10-12 18:22:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:22:51] [info] User Name: ubuntu
[2025-10-12 18:22:51] [info] Cores Used: 8
[2025-10-12 18:22:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Top Cell Information
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Layout Top Cell: TOP
[2025-10-12 18:22:51] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:22:51] [info] Database Format: GDSII
[2025-10-12 18:22:51] [info] Verification Mode: Full Check

[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Type of Errors | Count
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] DB_WARN -> 1
[2025-10-12 18:22:51] [info] LRF_ERR001 -> 14
[2025-10-12 18:22:51] [info] DB_WARN002 -> 159
[2025-10-12 18:22:51] [info] DB_WARN003 -> 161
```

```
[2025-10-12 18:22:51] [info] DRC_ERR9 -> 89
[2025-10-12 18:22:51] [info] DRC_ERR -> 67
[2025-10-12 18:22:51] [info] DRC_ERR5 -> 408
[2025-10-12 18:22:51] [info] DRC_ERR4 -> 241
[2025-10-12 18:22:51] [info] DRC_ERR1 -> 301
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Rule Violation Summary
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:22:51] [info] |-----|-----|-----|
[2025-10-12 18:22:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:22:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:22:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:22:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:22:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:22:51] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 18:22:51] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:22:51] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:22:51] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:22:51] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:22:51] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:22:51] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:22:51] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:22:51] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:22:51] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Performance Metrics
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] | Stage          | Time Taken |
[2025-10-12 18:22:51] [info] |-----|-----|
[2025-10-12 18:22:51] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:22:51] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 18:22:51] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Summary
[2025-10-12 18:22:51] [info] -----
[2025-10-12 18:22:51] [info] Total Errors: 1120
[2025-10-12 18:22:51] [info] Total Warnings: 321
[2025-10-12 18:22:51] [info] DRC Status: FAILED
```

lvr_summary_483.rpt

```
[2025-10-12 18:23:48] [info] =====
[2025-10-12 18:23:48] [info] Design Rule Check Report
[2025-10-12 18:23:48] [info] =====

[2025-10-12 18:23:48] [info] Report Generated: 2025-10-12
[2025-10-12 18:23:48] [info] Run Start Time: 06:22:56 PM
[2025-10-12 18:23:48] [info] Run End Time: 06:23:48 PM
[2025-10-12 18:23:48] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:23:48] [info] Tool Version: LVR_2025.1
[2025-10-12 18:23:48] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:23:48] [info] User Name: ubuntu
[2025-10-12 18:23:48] [info] Cores Used: 8
[2025-10-12 18:23:48] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] Top Cell Information
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] Layout Top Cell:      TOP
[2025-10-12 18:23:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:23:48] [info] Database Format:       GDSII
[2025-10-12 18:23:48] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] Type of Errors | Count
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] DB_WARN -> 1
[2025-10-12 18:23:48] [info] LRF_ERR001 -> 14
[2025-10-12 18:23:48] [info] DB_WARN002 -> 159
[2025-10-12 18:23:48] [info] DB_WARN003 -> 161
[2025-10-12 18:23:48] [info] DRC_ERR9 -> 89
[2025-10-12 18:23:48] [info] DRC_ERR -> 67
[2025-10-12 18:23:48] [info] DRC_ERR5 -> 408
[2025-10-12 18:23:48] [info] DRC_ERR4 -> 241
[2025-10-12 18:23:48] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] Rule Violation Summary
[2025-10-12 18:23:48] [info] -----
```

[2025-10-12 18:23:48] [info]	Rule ID	Description	Error Count
[2025-10-12 18:23:48] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 18:23:48] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 18:23:48] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 18:23:48] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 18:23:48] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 18:23:48] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 18:23:48] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 18:23:48] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 18:23:48] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 18:23:48] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 18:23:48] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 18:23:48] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" 15	
[2025-10-12 18:23:48] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 18:23:48] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 18:23:48] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

```
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] Performance Metrics
[2025-10-12 18:23:48] [info] -----
```

[2025-10-12 18:23:48] [info]	Stage	Time Taken
[2025-10-12 18:23:48] [info]	GDS Parsing	0 minutes 9 seconds
[2025-10-12 18:23:48] [info]	Rule Checking	0 minutes 41 seconds
[2025-10-12 18:23:48] [info]	Report Generation	0 minutes 0 seconds

```
[2025-10-12 18:23:48] [info] -----
[2025-10-12 18:23:48] [info] Summary
[2025-10-12 18:23:48] [info] -----
```

```
[2025-10-12 18:23:48] [info] Total Errors:      1120
[2025-10-12 18:23:48] [info] Total Warnings:    321
[2025-10-12 18:23:48] [info] DRC Status:        FAILED
```

lvr_summary_484.rpt

```
[2025-10-12 18:24:44] [info] =====
[2025-10-12 18:24:44] [info]                      Design Rule Check Report
[2025-10-12 18:24:44] [info] =====

[2025-10-12 18:24:44] [info] Report Generated:      2025-10-12
[2025-10-12 18:24:44] [info] Run Start Time:       06:23:53 PM
[2025-10-12 18:24:44] [info] Run End Time:         06:24:44 PM
[2025-10-12 18:24:44] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 18:24:44] [info] Tool Version:         LVR_2025.1
[2025-10-12 18:24:44] [info] Host Name:            ip-172-31-37-173
[2025-10-12 18:24:44] [info] User Name:            ubuntu
[2025-10-12 18:24:44] [info] Cores Used:           8
[2025-10-12 18:24:44] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info]                      Top Cell Information
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info] Layout Top Cell:      TOP
[2025-10-12 18:24:44] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 18:24:44] [info] Database Format:        GDSII
[2025-10-12 18:24:44] [info] Verification Mode:     Full Check

[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info]                      Type of Errors | Count
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info] DB_WARN -> 1
[2025-10-12 18:24:44] [info] LRF_ERR001 -> 14
[2025-10-12 18:24:44] [info] DB_WARN002 -> 159
[2025-10-12 18:24:44] [info] DB_WARN003 -> 161
[2025-10-12 18:24:44] [info] DRC_ERR9 -> 89
[2025-10-12 18:24:44] [info] DRC_ERR -> 67
[2025-10-12 18:24:44] [info] DRC_ERR5 -> 408
[2025-10-12 18:24:44] [info] DRC_ERR4 -> 241
[2025-10-12 18:24:44] [info] DRC_ERR1 -> 301
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info]                      Rule Violation Summary
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:24:44] [info] |-----|-----|-----|
[2025-10-12 18:24:44] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:24:44] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:24:44] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:24:44] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:24:44] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:24:44] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:24:44] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:24:44] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:24:44] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:24:44] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:24:44] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:24:44] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:24:44] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:24:44] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:24:44] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info]                      Performance Metrics
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:24:44] [info] |-----|-----|
[2025-10-12 18:24:44] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:24:44] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:24:44] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info] Summary
[2025-10-12 18:24:44] [info] -----
[2025-10-12 18:24:44] [info] Total Errors: 1120
[2025-10-12 18:24:44] [info] Total Warnings: 321
[2025-10-12 18:24:44] [info] DRC Status: FAILED
```

lvr_summary_485.rpt

```
[2025-10-12 18:25:41] [info] =====
[2025-10-12 18:25:41] [info] Design Rule Check Report
[2025-10-12 18:25:41] [info] =====

[2025-10-12 18:25:41] [info] Report Generated: 2025-10-12
[2025-10-12 18:25:41] [info] Run Start Time: 06:24:50 PM
[2025-10-12 18:25:41] [info] Run End Time: 06:25:41 PM
[2025-10-12 18:25:41] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:25:41] [info] Tool Version: LVR_2025.1
[2025-10-12 18:25:41] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:25:41] [info] User Name: ubuntu
[2025-10-12 18:25:41] [info] Cores Used: 8
[2025-10-12 18:25:41] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Top Cell Information
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Layout Top Cell: TOP
[2025-10-12 18:25:41] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:25:41] [info] Database Format: GDSII
[2025-10-12 18:25:41] [info] Verification Mode: Full Check

[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Type of Errors | Count
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] DB_WARN -> 1
[2025-10-12 18:25:41] [info] LRF_ERR001 -> 14
[2025-10-12 18:25:41] [info] DB_WARN002 -> 159
[2025-10-12 18:25:41] [info] DB_WARN003 -> 161
[2025-10-12 18:25:41] [info] DRC_ERR9 -> 89
[2025-10-12 18:25:41] [info] DRC_ERR -> 67
[2025-10-12 18:25:41] [info] DRC_ERR5 -> 408
[2025-10-12 18:25:41] [info] DRC_ERR4 -> 241
[2025-10-12 18:25:41] [info] DRC_ERR1 -> 301
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Rule Violation Summary
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:25:41] [info] |-----|-----|-----|
[2025-10-12 18:25:41] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:25:41] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:25:41] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:25:41] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:25:41] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 18:25:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:25:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:25:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:25:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:25:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:25:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:25:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:25:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:25:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:25:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:25:41] [info]
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Performance Metrics
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] | Stage | Time Taken |
[2025-10-12 18:25:41] [info] |-----|-----|
[2025-10-12 18:25:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:25:41] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:25:41] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Summary
[2025-10-12 18:25:41] [info] -----
[2025-10-12 18:25:41] [info] Total Errors: 1120
[2025-10-12 18:25:41] [info] Total Warnings: 321
[2025-10-12 18:25:41] [info] DRC Status: FAILED
```

lvr_summary_486.rpt

```
[2025-10-12 18:26:38] [info] =====
[2025-10-12 18:26:38] [info] Design Rule Check Report
[2025-10-12 18:26:38] [info] =====

[2025-10-12 18:26:38] [info] Report Generated: 2025-10-12
[2025-10-12 18:26:38] [info] Run Start Time: 06:25:47 PM
[2025-10-12 18:26:38] [info] Run End Time: 06:26:38 PM
[2025-10-12 18:26:38] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:26:38] [info] Tool Version: LVR_2025.1
[2025-10-12 18:26:38] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:26:38] [info] User Name: ubuntu
[2025-10-12 18:26:38] [info] Cores Used: 8
[2025-10-12 18:26:38] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Top Cell Information
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Layout Top Cell: TOP
[2025-10-12 18:26:38] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:26:38] [info] Database Format: GDSII
[2025-10-12 18:26:38] [info] Verification Mode: Full Check

[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Type of Errors | Count
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] DB_WARN -> 1
[2025-10-12 18:26:38] [info] LRF_ERR001 -> 14
[2025-10-12 18:26:38] [info] DB_WARN002 -> 159
[2025-10-12 18:26:38] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:26:38] [info] DRC_ERR9 -> 89
[2025-10-12 18:26:38] [info] DRC_ERR -> 67
[2025-10-12 18:26:38] [info] DRC_ERR5 -> 408
[2025-10-12 18:26:38] [info] DRC_ERR4 -> 241
[2025-10-12 18:26:38] [info] DRC_ERR1 -> 301
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Rule Violation Summary
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:26:38] [info] |-----|-----|-----|
[2025-10-12 18:26:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:26:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:26:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:26:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:26:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:26:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:26:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:26:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:26:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:26:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:26:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:26:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:26:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:26:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:26:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Performance Metrics
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] | Stage | Time Taken |
[2025-10-12 18:26:38] [info] |-----|-----|
[2025-10-12 18:26:38] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:26:38] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:26:38] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Summary
[2025-10-12 18:26:38] [info] -----
[2025-10-12 18:26:38] [info] Total Errors: 1120
[2025-10-12 18:26:38] [info] Total Warnings: 321
[2025-10-12 18:26:38] [info] DRC Status: FAILED

```

lvr_summary_487.rpt

```

[2025-10-12 18:27:35] [info] =====
[2025-10-12 18:27:35] [info] Design Rule Check Report
[2025-10-12 18:27:35] [info] =====

[2025-10-12 18:27:35] [info] Report Generated: 2025-10-12
[2025-10-12 18:27:35] [info] Run Start Time: 06:26:45 PM
[2025-10-12 18:27:35] [info] Run End Time: 06:27:35 PM
[2025-10-12 18:27:35] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:27:35] [info] Tool Version: LVR_2025.1
[2025-10-12 18:27:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:27:35] [info] User Name: ubuntu
[2025-10-12 18:27:35] [info] Cores Used: 8
[2025-10-12 18:27:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Top Cell Information
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Layout Top Cell:      TOP
[2025-10-12 18:27:35] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:27:35] [info] Database Format:      GDSII
[2025-10-12 18:27:35] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Type of Errors | Count
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] DB_WARN -> 1
[2025-10-12 18:27:35] [info] LRF_ERR001 -> 14
[2025-10-12 18:27:35] [info] DB_WARN002 -> 159
[2025-10-12 18:27:35] [info] DB_WARN003 -> 161
[2025-10-12 18:27:35] [info] DRC_ERR9 -> 89
[2025-10-12 18:27:35] [info] DRC_ERR -> 67
[2025-10-12 18:27:35] [info] DRC_ERR5 -> 408
[2025-10-12 18:27:35] [info] DRC_ERR4 -> 241
[2025-10-12 18:27:35] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Rule Violation Summary
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:27:35] [info] |-----|-----|-----|
[2025-10-12 18:27:35] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:27:35] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:27:35] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:27:35] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:27:35] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:27:35] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:27:35] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:27:35] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:27:35] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:27:35] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:27:35] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:27:35] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:27:35] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:27:35] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:27:35] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:27:35] [info] |
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Performance Metrics
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] | Stage          | Time Taken |
[2025-10-12 18:27:35] [info] |-----|-----|
[2025-10-12 18:27:35] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:27:35] [info] | Rule Checking   | 0 minutes 39 seconds |
[2025-10-12 18:27:35] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Summary
```

```
[2025-10-12 18:27:35] [info] -----
[2025-10-12 18:27:35] [info] Total Errors:      1120
[2025-10-12 18:27:35] [info] Total Warnings:    321
[2025-10-12 18:27:35] [info] DRC Status:        FAILED
```

lvr_summary_488.rpt

```
[2025-10-12 18:28:32] [info] =====
[2025-10-12 18:28:32] [info]                      Design Rule Check Report
[2025-10-12 18:28:32] [info] =====

[2025-10-12 18:28:32] [info] Report Generated:    2025-10-12
[2025-10-12 18:28:32] [info] Run Start Time:      06:27:41 PM
[2025-10-12 18:28:32] [info] Run End Time:        06:28:32 PM
[2025-10-12 18:28:32] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 18:28:32] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:28:32] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:28:32] [info] User Name:           ubuntu
[2025-10-12 18:28:32] [info] Cores Used:          8
[2025-10-12 18:28:32] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info]                      Top Cell Information
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info] Layout Top Cell:     TOP
[2025-10-12 18:28:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:28:32] [info] Database Format:       GDSII
[2025-10-12 18:28:32] [info] Verification Mode:    Full Check

[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info]                      Type of Errors | Count
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info] DB_WARN -> 1
[2025-10-12 18:28:32] [info] LRF_ERR001 -> 14
[2025-10-12 18:28:32] [info] DB_WARN002 -> 159
[2025-10-12 18:28:32] [info] DB_WARN003 -> 161
[2025-10-12 18:28:32] [info] DRC_ERR9 -> 89
[2025-10-12 18:28:32] [info] DRC_ERR -> 67
[2025-10-12 18:28:32] [info] DRC_ERR5 -> 408
[2025-10-12 18:28:32] [info] DRC_ERR4 -> 241
[2025-10-12 18:28:32] [info] DRC_ERR1 -> 301
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info]                      Rule Violation Summary
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:28:32] [info] |-----|-----|-----|
[2025-10-12 18:28:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:28:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:28:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:28:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:28:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:28:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:28:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:28:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:28:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:28:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:28:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:28:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:28:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:28:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:28:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:28:32] [info]
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info]                      Performance Metrics
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:28:32] [info] |-----|-----|
[2025-10-12 18:28:32] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:28:32] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 18:28:32] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info] Summary
[2025-10-12 18:28:32] [info] -----
[2025-10-12 18:28:32] [info] Total Errors:   1120
[2025-10-12 18:28:32] [info] Total Warnings:    321
[2025-10-12 18:28:32] [info] DRC Status:      FAILED
```

lvr_summary_489.rpt

```
[2025-10-12 18:29:31] [info] =====
[2025-10-12 18:29:31] [info] Design Rule Check Report
[2025-10-12 18:29:31] [info] =====

[2025-10-12 18:29:31] [info] Report Generated:   2025-10-12
[2025-10-12 18:29:31] [info] Run Start Time:     06:28:39 PM
[2025-10-12 18:29:31] [info] Run End Time:       06:29:31 PM
[2025-10-12 18:29:31] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 18:29:31] [info] Tool Version:       LVR_2025.1
[2025-10-12 18:29:31] [info] Host Name:          ip-172-31-37-173
[2025-10-12 18:29:31] [info] User Name:          ubuntu
[2025-10-12 18:29:31] [info] Cores Used:         8
[2025-10-12 18:29:31] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Top Cell Information
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Layout Top Cell:    TOP
[2025-10-12 18:29:31] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 18:29:31] [info] Database Format:      GDSII
[2025-10-12 18:29:31] [info] Verification Mode:   Full Check

[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Type of Errors | Count
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] DB_WARN -> 1
[2025-10-12 18:29:31] [info] LRF_ERR001 -> 14
[2025-10-12 18:29:31] [info] DB_WARN002 -> 159
[2025-10-12 18:29:31] [info] DB_WARN003 -> 161
[2025-10-12 18:29:31] [info] DRC_ERR9 -> 89
[2025-10-12 18:29:31] [info] DRC_ERR -> 67
[2025-10-12 18:29:31] [info] DRC_ERR5 -> 408
[2025-10-12 18:29:31] [info] DRC_ERR4 -> 241
[2025-10-12 18:29:31] [info] DRC_ERR1 -> 301
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Rule Violation Summary
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:29:31] [info] |-----|-----|-----|
[2025-10-12 18:29:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:29:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:29:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:29:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:29:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```
[2025-10-12 18:29:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:29:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:29:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:29:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:29:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:29:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:29:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:29:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:29:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:29:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:29:31] [info]
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Performance Metrics
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] | Stage | Time Taken |
[2025-10-12 18:29:31] [info] |-----|-----|
[2025-10-12 18:29:31] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:29:31] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:29:31] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Summary
[2025-10-12 18:29:31] [info] -----
[2025-10-12 18:29:31] [info] Total Errors: 1120
[2025-10-12 18:29:31] [info] Total Warnings: 321
[2025-10-12 18:29:31] [info] DRC Status: FAILED
```

lvr_summary_49.rpt

```
[2025-10-12 11:27:51] [info] =====
[2025-10-12 11:27:51] [info] Design Rule Check Report
[2025-10-12 11:27:51] [info] =====

[2025-10-12 11:27:51] [info] Report Generated: 2025-10-12
[2025-10-12 11:27:51] [info] Run Start Time: 11:27:00 AM
[2025-10-12 11:27:51] [info] Run End Time: 11:27:51 AM
[2025-10-12 11:27:51] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:27:51] [info] Tool Version: LVR_2025.1
[2025-10-12 11:27:51] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:27:51] [info] User Name: ubuntu
[2025-10-12 11:27:51] [info] Cores Used: 8
[2025-10-12 11:27:51] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Top Cell Information
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Layout Top Cell: TOP
[2025-10-12 11:27:51] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:27:51] [info] Database Format: GDSII
[2025-10-12 11:27:51] [info] Verification Mode: Full Check

[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Type of Errors | Count
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] DB_WARN -> 1
[2025-10-12 11:27:51] [info] LRF_ERR001 -> 14
[2025-10-12 11:27:51] [info] DB_WARN002 -> 159
[2025-10-12 11:27:51] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:27:51] [info] DRC_ERR9 -> 89
[2025-10-12 11:27:51] [info] DRC_ERR -> 67
[2025-10-12 11:27:51] [info] DRC_ERR5 -> 408
[2025-10-12 11:27:51] [info] DRC_ERR4 -> 241
[2025-10-12 11:27:51] [info] DRC_ERR1 -> 301
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Rule Violation Summary
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:27:51] [info] |-----|-----|-----|
[2025-10-12 11:27:51] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:27:51] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:27:51] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:27:51] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:27:51] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:27:51] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:27:51] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:27:51] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:27:51] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:27:51] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:27:51] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:27:51] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:27:51] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:27:51] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:27:51] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Performance Metrics
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] | Stage | Time Taken |
[2025-10-12 11:27:51] [info] |-----|-----|
[2025-10-12 11:27:51] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:27:51] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:27:51] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Summary
[2025-10-12 11:27:51] [info] -----
[2025-10-12 11:27:51] [info] Total Errors: 1120
[2025-10-12 11:27:51] [info] Total Warnings: 321
[2025-10-12 11:27:51] [info] DRC Status: FAILED
```

lvr_summary_490.rpt

```
[2025-10-12 18:30:28] [info] =====
[2025-10-12 18:30:28] [info] Design Rule Check Report
[2025-10-12 18:30:28] [info] =====

[2025-10-12 18:30:28] [info] Report Generated: 2025-10-12
[2025-10-12 18:30:28] [info] Run Start Time: 06:29:36 PM
[2025-10-12 18:30:28] [info] Run End Time: 06:30:28 PM
[2025-10-12 18:30:28] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:30:28] [info] Tool Version: LVR_2025.1
[2025-10-12 18:30:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:30:28] [info] User Name: ubuntu
[2025-10-12 18:30:28] [info] Cores Used: 8
[2025-10-12 18:30:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Top Cell Information
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Layout Top Cell:      TOP
[2025-10-12 18:30:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:30:28] [info] Database Format:      GDSII
[2025-10-12 18:30:28] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Type of Errors | Count
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] DB_WARN -> 1
[2025-10-12 18:30:28] [info] LRF_ERR001 -> 14
[2025-10-12 18:30:28] [info] DB_WARN002 -> 159
[2025-10-12 18:30:28] [info] DB_WARN003 -> 161
[2025-10-12 18:30:28] [info] DRC_ERR9 -> 89
[2025-10-12 18:30:28] [info] DRC_ERR -> 67
[2025-10-12 18:30:28] [info] DRC_ERR5 -> 408
[2025-10-12 18:30:28] [info] DRC_ERR4 -> 241
[2025-10-12 18:30:28] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Rule Violation Summary
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:30:28] [info] |-----|-----|-----|
[2025-10-12 18:30:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:30:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:30:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:30:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:30:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:30:28] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:30:28] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:30:28] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:30:28] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:30:28] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:30:28] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:30:28] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:30:28] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:30:28] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:30:28] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:30:28] [info] |
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Performance Metrics
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] | Stage          | Time Taken |
[2025-10-12 18:30:28] [info] |-----|-----|
[2025-10-12 18:30:28] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:30:28] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 18:30:28] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Summary
```

```
[2025-10-12 18:30:28] [info] -----
[2025-10-12 18:30:28] [info] Total Errors:      1120
[2025-10-12 18:30:28] [info] Total Warnings:    321
[2025-10-12 18:30:28] [info] DRC Status:        FAILED
```

lvr_summary_491.rpt

```
[2025-10-12 18:31:24] [info] =====
[2025-10-12 18:31:24] [info]                Design Rule Check Report
[2025-10-12 18:31:24] [info] =====

[2025-10-12 18:31:24] [info] Report Generated:    2025-10-12
[2025-10-12 18:31:24] [info] Run Start Time:      06:30:33 PM
[2025-10-12 18:31:24] [info] Run End Time:        06:31:24 PM
[2025-10-12 18:31:24] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 18:31:24] [info] Tool Version:       LVR_2025.1
[2025-10-12 18:31:24] [info] Host Name:          ip-172-31-37-173
[2025-10-12 18:31:24] [info] User Name:          ubuntu
[2025-10-12 18:31:24] [info] Cores Used:         8
[2025-10-12 18:31:24] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info]                Top Cell Information
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info] Layout Top Cell:    TOP
[2025-10-12 18:31:24] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 18:31:24] [info] Database Format:      GDSII
[2025-10-12 18:31:24] [info] Verification Mode:   Full Check

[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info]                Type of Errors | Count
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info] DB_WARN -> 1
[2025-10-12 18:31:24] [info] LRF_ERR001 -> 14
[2025-10-12 18:31:24] [info] DB_WARN002 -> 159
[2025-10-12 18:31:24] [info] DB_WARN003 -> 161
[2025-10-12 18:31:24] [info] DRC_ERR9 -> 89
[2025-10-12 18:31:24] [info] DRC_ERR -> 67
[2025-10-12 18:31:24] [info] DRC_ERR5 -> 408
[2025-10-12 18:31:24] [info] DRC_ERR4 -> 241
[2025-10-12 18:31:24] [info] DRC_ERR1 -> 301
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info]                Rule Violation Summary
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:31:24] [info] |-----|-----|-----|
[2025-10-12 18:31:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:31:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:31:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:31:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:31:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:31:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:31:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:31:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:31:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:31:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:31:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:31:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:31:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:31:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:31:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:31:24] [info]
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info]                Performance Metrics
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:31:24] [info] |-----|-----|
[2025-10-12 18:31:24] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:31:24] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:31:24] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info]                Summary
[2025-10-12 18:31:24] [info] -----
[2025-10-12 18:31:24] [info] Total Errors:    1120
[2025-10-12 18:31:24] [info] Total Warnings:    321
[2025-10-12 18:31:24] [info] DRC Status:      FAILED
```

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```
[2025-10-12 18:32:20] [info] =====
[2025-10-12 18:32:20] [info]                Design Rule Check Report
[2025-10-12 18:32:20] [info] =====

[2025-10-12 18:32:20] [info] Report Generated:    2025-10-12
[2025-10-12 18:32:20] [info] Run Start Time:      06:31:29 PM
[2025-10-12 18:32:20] [info] Run End Time:        06:32:20 PM
[2025-10-12 18:32:20] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 18:32:20] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:32:20] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:32:20] [info] User Name:           ubuntu
[2025-10-12 18:32:20] [info] Cores Used:          8
[2025-10-12 18:32:20] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info]                Top Cell Information
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] Layout Top Cell:     TOP
[2025-10-12 18:32:20] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:32:20] [info] Database Format:       GDSII
[2025-10-12 18:32:20] [info] Verification Mode:    Full Check

[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info]                Type of Errors | Count
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] DB_WARN -> 1
[2025-10-12 18:32:20] [info] LRF_ERR001 -> 14
[2025-10-12 18:32:20] [info] DB_WARN002 -> 159
[2025-10-12 18:32:20] [info] DB_WARN003 -> 161
[2025-10-12 18:32:20] [info] DRC_ERR9 -> 89
[2025-10-12 18:32:20] [info] DRC_ERR -> 67
[2025-10-12 18:32:20] [info] DRC_ERR5 -> 408
[2025-10-12 18:32:20] [info] DRC_ERR4 -> 241
[2025-10-12 18:32:20] [info] DRC_ERR1 -> 301
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info]                Rule Violation Summary
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:32:20] [info] |-----|-----|-----|
[2025-10-12 18:32:20] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:32:20] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:32:20] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:32:20] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:32:20] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:32:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:32:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:32:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:32:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:32:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:32:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:32:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:32:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:32:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:32:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:32:20] [info]
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] Performance Metrics
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] | Stage | Time Taken |
[2025-10-12 18:32:20] [info] |-----|-----|
[2025-10-12 18:32:20] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:32:20] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:32:20] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] Summary
[2025-10-12 18:32:20] [info] -----
[2025-10-12 18:32:20] [info] Total Errors: 1120
[2025-10-12 18:32:20] [info] Total Warnings: 321
[2025-10-12 18:32:20] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:33:17] [info] =====
[2025-10-12 18:33:17] [info] Design Rule Check Report
[2025-10-12 18:33:17] [info] =====

[2025-10-12 18:33:17] [info] Report Generated: 2025-10-12
[2025-10-12 18:33:17] [info] Run Start Time: 06:32:27 PM
[2025-10-12 18:33:17] [info] Run End Time: 06:33:17 PM
[2025-10-12 18:33:17] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:33:17] [info] Tool Version: LVR_2025.1
[2025-10-12 18:33:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:33:17] [info] User Name: ubuntu
[2025-10-12 18:33:17] [info] Cores Used: 8
[2025-10-12 18:33:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Top Cell Information
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Layout Top Cell: TOP
[2025-10-12 18:33:17] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:33:17] [info] Database Format: GDSII
[2025-10-12 18:33:17] [info] Verification Mode: Full Check

[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Type of Errors | Count
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] DB_WARN -> 1
[2025-10-12 18:33:17] [info] LRF_ERROR01 -> 14
[2025-10-12 18:33:17] [info] DB_WARN002 -> 159
[2025-10-12 18:33:17] [info] DB_WARN003 -> 161
```

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[2025-10-12 18:33:17] [info] DRC_ERR9 -> 89
[2025-10-12 18:33:17] [info] DRC_ERR -> 67
[2025-10-12 18:33:17] [info] DRC_ERR5 -> 408
[2025-10-12 18:33:17] [info] DRC_ERR4 -> 241
[2025-10-12 18:33:17] [info] DRC_ERR1 -> 301
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Rule Violation Summary
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:33:17] [info] |-----|-----|-----|
[2025-10-12 18:33:17] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:33:17] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:33:17] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:33:17] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:33:17] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:33:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:33:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:33:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:33:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:33:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:33:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:33:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:33:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:33:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:33:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Performance Metrics
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] | Stage | Time Taken |
[2025-10-12 18:33:17] [info] |-----|-----|
[2025-10-12 18:33:17] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:33:17] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:33:17] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Summary
[2025-10-12 18:33:17] [info] -----
[2025-10-12 18:33:17] [info] Total Errors: 1120
[2025-10-12 18:33:17] [info] Total Warnings: 321
[2025-10-12 18:33:17] [info] DRC Status: FAILED

```

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```

[2025-10-12 18:34:14] [info] =====
[2025-10-12 18:34:14] [info] Design Rule Check Report
[2025-10-12 18:34:14] [info] =====

[2025-10-12 18:34:14] [info] Report Generated: 2025-10-12
[2025-10-12 18:34:14] [info] Run Start Time: 06:33:23 PM
[2025-10-12 18:34:14] [info] Run End Time: 06:34:14 PM
[2025-10-12 18:34:14] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:34:14] [info] Tool Version: LVR_2025.1
[2025-10-12 18:34:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:34:14] [info] User Name: ubuntu
[2025-10-12 18:34:14] [info] Cores Used: 8
[2025-10-12 18:34:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Top Cell Information
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Layout Top Cell:      TOP
[2025-10-12 18:34:14] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:34:14] [info] Database Format:      GDSII
[2025-10-12 18:34:14] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Type of Errors | Count
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] DB_WARN -> 1
[2025-10-12 18:34:14] [info] LRF_ERR001 -> 14
[2025-10-12 18:34:14] [info] DB_WARN002 -> 159
[2025-10-12 18:34:14] [info] DB_WARN003 -> 161
[2025-10-12 18:34:14] [info] DRC_ERR9 -> 89
[2025-10-12 18:34:14] [info] DRC_ERR -> 67
[2025-10-12 18:34:14] [info] DRC_ERR5 -> 408
[2025-10-12 18:34:14] [info] DRC_ERR4 -> 241
[2025-10-12 18:34:14] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Rule Violation Summary
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:34:14] [info] |-----|-----|-----|
[2025-10-12 18:34:14] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:34:14] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:34:14] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:34:14] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:34:14] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:34:14] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:34:14] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:34:14] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:34:14] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:34:14] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:34:14] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:34:14] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:34:14] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:34:14] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:34:14] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:34:14] [info] |
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Performance Metrics
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] | Stage          | Time Taken |
[2025-10-12 18:34:14] [info] |-----|-----|
[2025-10-12 18:34:14] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:34:14] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 18:34:14] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Summary
```

```
[2025-10-12 18:34:14] [info] -----
[2025-10-12 18:34:14] [info] Total Errors:      1120
[2025-10-12 18:34:14] [info] Total Warnings:    321
[2025-10-12 18:34:14] [info] DRC Status:        FAILED
```


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```
[2025-10-12 18:35:13] [info] =====
[2025-10-12 18:35:13] [info] Design Rule Check Report
[2025-10-12 18:35:13] [info] =====

[2025-10-12 18:35:13] [info] Report Generated: 2025-10-12
[2025-10-12 18:35:13] [info] Run Start Time: 06:34:21 PM
[2025-10-12 18:35:13] [info] Run End Time: 06:35:13 PM
[2025-10-12 18:35:13] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:35:13] [info] Tool Version: LVR_2025.1
[2025-10-12 18:35:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:35:13] [info] User Name: ubuntu
[2025-10-12 18:35:13] [info] Cores Used: 8
[2025-10-12 18:35:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Top Cell Information
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Layout Top Cell: TOP
[2025-10-12 18:35:13] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:35:13] [info] Database Format: GDSII
[2025-10-12 18:35:13] [info] Verification Mode: Full Check

[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Type of Errors | Count
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] DB_WARN -> 1
[2025-10-12 18:35:13] [info] LRF_ERR001 -> 14
[2025-10-12 18:35:13] [info] DB_WARN002 -> 159
[2025-10-12 18:35:13] [info] DB_WARN003 -> 161
[2025-10-12 18:35:13] [info] DRC_ERR9 -> 89
[2025-10-12 18:35:13] [info] DRC_ERR -> 67
[2025-10-12 18:35:13] [info] DRC_ERR5 -> 408
[2025-10-12 18:35:13] [info] DRC_ERR4 -> 241
[2025-10-12 18:35:13] [info] DRC_ERR1 -> 301
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Rule Violation Summary
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:35:13] [info] |-----|-----|-----|
[2025-10-12 18:35:13] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:35:13] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:35:13] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:35:13] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:35:13] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:35:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:35:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:35:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:35:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:35:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:35:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:35:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:35:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:35:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:35:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:35:13] [info]
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Performance Metrics
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:35:13] [info] |-----|-----|
[2025-10-12 18:35:13] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:35:13] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 18:35:13] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Summary
[2025-10-12 18:35:13] [info] -----
[2025-10-12 18:35:13] [info] Total Errors: 1120
[2025-10-12 18:35:13] [info] Total Warnings: 321
[2025-10-12 18:35:13] [info] DRC Status: FAILED
```

lvr_summary_496.rpt

```
[2025-10-12 18:36:09] [info] =====
[2025-10-12 18:36:09] [info] Design Rule Check Report
[2025-10-12 18:36:09] [info] =====

[2025-10-12 18:36:09] [info] Report Generated: 2025-10-12
[2025-10-12 18:36:09] [info] Run Start Time: 06:35:17 PM
[2025-10-12 18:36:09] [info] Run End Time: 06:36:09 PM
[2025-10-12 18:36:09] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:36:09] [info] Tool Version: LVR_2025.1
[2025-10-12 18:36:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:36:09] [info] User Name: ubuntu
[2025-10-12 18:36:09] [info] Cores Used: 8
[2025-10-12 18:36:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Top Cell Information
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Layout Top Cell: TOP
[2025-10-12 18:36:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:36:09] [info] Database Format: GDSII
[2025-10-12 18:36:09] [info] Verification Mode: Full Check

[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Type of Errors | Count
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] DB_WARN -> 1
[2025-10-12 18:36:09] [info] LRF_ERR001 -> 14
[2025-10-12 18:36:09] [info] DB_WARN002 -> 159
[2025-10-12 18:36:09] [info] DB_WARN003 -> 161
[2025-10-12 18:36:09] [info] DRC_ERR9 -> 89
[2025-10-12 18:36:09] [info] DRC_ERR -> 67
[2025-10-12 18:36:09] [info] DRC_ERR5 -> 408
[2025-10-12 18:36:09] [info] DRC_ERR4 -> 241
[2025-10-12 18:36:09] [info] DRC_ERR1 -> 301
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Rule Violation Summary
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:36:09] [info] |-----|-----|-----|
[2025-10-12 18:36:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:36:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:36:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:36:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:36:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 18:36:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:36:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:36:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:36:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:36:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:36:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:36:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:36:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:36:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:36:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:36:09] [info]
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Performance Metrics
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] | Stage | Time Taken |
[2025-10-12 18:36:09] [info] |-----|-----|
[2025-10-12 18:36:09] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:36:09] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:36:09] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Summary
[2025-10-12 18:36:09] [info] -----
[2025-10-12 18:36:09] [info] Total Errors: 1120
[2025-10-12 18:36:09] [info] Total Warnings: 321
[2025-10-12 18:36:09] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:37:05] [info] =====
[2025-10-12 18:37:05] [info] Design Rule Check Report
[2025-10-12 18:37:05] [info] =====

[2025-10-12 18:37:05] [info] Report Generated: 2025-10-12
[2025-10-12 18:37:05] [info] Run Start Time: 06:36:14 PM
[2025-10-12 18:37:05] [info] Run End Time: 06:37:05 PM
[2025-10-12 18:37:05] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:37:05] [info] Tool Version: LVR_2025.1
[2025-10-12 18:37:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:37:05] [info] User Name: ubuntu
[2025-10-12 18:37:05] [info] Cores Used: 8
[2025-10-12 18:37:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Top Cell Information
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Layout Top Cell: TOP
[2025-10-12 18:37:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:37:05] [info] Database Format: GDSII
[2025-10-12 18:37:05] [info] Verification Mode: Full Check

[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Type of Errors | Count
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] DB_WARN -> 1
[2025-10-12 18:37:05] [info] LRF_ERR001 -> 14
[2025-10-12 18:37:05] [info] DB_WARN002 -> 159
[2025-10-12 18:37:05] [info] DB_WARN003 -> 161
```

```
[2025-10-12 18:37:05] [info] DRC_ERR9 -> 89
[2025-10-12 18:37:05] [info] DRC_ERR -> 67
[2025-10-12 18:37:05] [info] DRC_ERR5 -> 408
[2025-10-12 18:37:05] [info] DRC_ERR4 -> 241
[2025-10-12 18:37:05] [info] DRC_ERR1 -> 301
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Rule Violation Summary
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:37:05] [info] |-----|-----|-----|
[2025-10-12 18:37:05] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:37:05] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:37:05] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:37:05] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:37:05] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:37:05] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 18:37:05] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:37:05] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:37:05] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:37:05] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:37:05] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:37:05] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:37:05] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:37:05] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:37:05] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Performance Metrics
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] | Stage          | Time Taken |
[2025-10-12 18:37:05] [info] |-----|-----|
[2025-10-12 18:37:05] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:37:05] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 18:37:05] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Summary
[2025-10-12 18:37:05] [info] -----
[2025-10-12 18:37:05] [info] Total Errors: 1120
[2025-10-12 18:37:05] [info] Total Warnings: 321
[2025-10-12 18:37:05] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:38:02] [info] =====
[2025-10-12 18:38:02] [info] Design Rule Check Report
[2025-10-12 18:38:02] [info] =====

[2025-10-12 18:38:02] [info] Report Generated: 2025-10-12
[2025-10-12 18:38:02] [info] Run Start Time: 06:37:10 PM
[2025-10-12 18:38:02] [info] Run End Time: 06:38:02 PM
[2025-10-12 18:38:02] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:38:02] [info] Tool Version: LVR_2025.1
[2025-10-12 18:38:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:38:02] [info] User Name: ubuntu
[2025-10-12 18:38:02] [info] Cores Used: 8
[2025-10-12 18:38:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Top Cell Information
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Layout Top Cell:      TOP
[2025-10-12 18:38:02] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:38:02] [info] Database Format:      GDSII
[2025-10-12 18:38:02] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Type of Errors | Count
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] DB_WARN -> 1
[2025-10-12 18:38:02] [info] LRF_ERR001 -> 14
[2025-10-12 18:38:02] [info] DB_WARN002 -> 159
[2025-10-12 18:38:02] [info] DB_WARN003 -> 161
[2025-10-12 18:38:02] [info] DRC_ERR9 -> 89
[2025-10-12 18:38:02] [info] DRC_ERR -> 67
[2025-10-12 18:38:02] [info] DRC_ERR5 -> 408
[2025-10-12 18:38:02] [info] DRC_ERR4 -> 241
[2025-10-12 18:38:02] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Rule Violation Summary
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:38:02] [info] |-----|-----|-----|
[2025-10-12 18:38:02] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:38:02] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:38:02] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:38:02] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:38:02] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:38:02] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:38:02] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:38:02] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:38:02] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:38:02] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:38:02] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:38:02] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:38:02] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:38:02] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:38:02] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Performance Metrics
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] | Stage          | Time Taken |
[2025-10-12 18:38:02] [info] |-----|-----|
[2025-10-12 18:38:02] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:38:02] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 18:38:02] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Summary
[2025-10-12 18:38:02] [info] -----
[2025-10-12 18:38:02] [info] Total Errors:      1120
[2025-10-12 18:38:02] [info] Total Warnings:    321
[2025-10-12 18:38:02] [info] DRC Status:        FAILED
```

lvr_summary_499.rpt

```
[2025-10-12 18:39:00] [info] =====
[2025-10-12 18:39:00] [info] Design Rule Check Report
[2025-10-12 18:39:00] [info] =====

[2025-10-12 18:39:00] [info] Report Generated: 2025-10-12
[2025-10-12 18:39:00] [info] Run Start Time: 06:38:08 PM
[2025-10-12 18:39:00] [info] Run End Time: 06:39:00 PM
[2025-10-12 18:39:00] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:39:00] [info] Tool Version: LVR_2025.1
[2025-10-12 18:39:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:39:00] [info] User Name: ubuntu
[2025-10-12 18:39:00] [info] Cores Used: 8
[2025-10-12 18:39:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Top Cell Information
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Layout Top Cell: TOP
[2025-10-12 18:39:00] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:39:00] [info] Database Format: GDSII
[2025-10-12 18:39:00] [info] Verification Mode: Full Check

[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Type of Errors | Count
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] DB_WARN -> 1
[2025-10-12 18:39:00] [info] LRF_ERR001 -> 14
[2025-10-12 18:39:00] [info] DB_WARN002 -> 159
[2025-10-12 18:39:00] [info] DB_WARN003 -> 161
[2025-10-12 18:39:00] [info] DRC_ERR9 -> 89
[2025-10-12 18:39:00] [info] DRC_ERR -> 67
[2025-10-12 18:39:00] [info] DRC_ERR5 -> 408
[2025-10-12 18:39:00] [info] DRC_ERR4 -> 241
[2025-10-12 18:39:00] [info] DRC_ERR1 -> 301
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Rule Violation Summary
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:39:00] [info] |-----|-----|-----|
[2025-10-12 18:39:00] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:39:00] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:39:00] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:39:00] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:39:00] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:39:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:39:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:39:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:39:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:39:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:39:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:39:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:39:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:39:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:39:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:39:00] [info]
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Performance Metrics
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:39:00] [info] |-----|-----|
[2025-10-12 18:39:00] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:39:00] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 18:39:00] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Summary
[2025-10-12 18:39:00] [info] -----
[2025-10-12 18:39:00] [info] Total Errors: 1120
[2025-10-12 18:39:00] [info] Total Warnings: 321
[2025-10-12 18:39:00] [info] DRC Status: FAILED
```

lvr_summary_5.rpt

```
[2025-10-12 10:46:13] [info] =====
[2025-10-12 10:46:13] [info] Design Rule Check Report
[2025-10-12 10:46:13] [info] =====

[2025-10-12 10:46:13] [info] Report Generated: 2025-10-12
[2025-10-12 10:46:13] [info] Run Start Time: 10:45:21 AM
[2025-10-12 10:46:13] [info] Run End Time: 10:46:13 AM
[2025-10-12 10:46:13] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:46:13] [info] Tool Version: LVR_2025.1
[2025-10-12 10:46:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:46:13] [info] User Name: ubuntu
[2025-10-12 10:46:13] [info] Cores Used: 8
[2025-10-12 10:46:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Top Cell Information
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Layout Top Cell: TOP
[2025-10-12 10:46:13] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:46:13] [info] Database Format: GDSII
[2025-10-12 10:46:13] [info] Verification Mode: Full Check

[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Type of Errors | Count
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] DB_WARN -> 1
[2025-10-12 10:46:13] [info] LRF_ERR001 -> 14
[2025-10-12 10:46:13] [info] DB_WARN002 -> 159
[2025-10-12 10:46:13] [info] DB_WARN003 -> 161
[2025-10-12 10:46:13] [info] DRC_ERR9 -> 89
[2025-10-12 10:46:13] [info] DRC_ERR -> 67
[2025-10-12 10:46:13] [info] DRC_ERR5 -> 408
[2025-10-12 10:46:13] [info] DRC_ERR4 -> 241
[2025-10-12 10:46:13] [info] DRC_ERR1 -> 301
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Rule Violation Summary
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:46:13] [info] |-----|-----|-----|
[2025-10-12 10:46:13] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:46:13] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:46:13] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:46:13] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:46:13] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 10:46:13] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 10:46:13] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:46:13] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:46:13] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:46:13] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:46:13] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:46:13] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:46:13] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:46:13] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:46:13] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:46:13] [info]
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Performance Metrics
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] | Stage | Time Taken |
[2025-10-12 10:46:13] [info] |-----|-----|
[2025-10-12 10:46:13] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:46:13] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 10:46:13] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Summary
[2025-10-12 10:46:13] [info] -----
[2025-10-12 10:46:13] [info] Total Errors: 1120
[2025-10-12 10:46:13] [info] Total Warnings: 321
[2025-10-12 10:46:13] [info] DRC Status: FAILED
```

lvr_summary_50.rpt

```
[2025-10-12 11:28:47] [info] =====
[2025-10-12 11:28:47] [info] Design Rule Check Report
[2025-10-12 11:28:47] [info] =====

[2025-10-12 11:28:47] [info] Report Generated: 2025-10-12
[2025-10-12 11:28:47] [info] Run Start Time: 11:27:56 AM
[2025-10-12 11:28:47] [info] Run End Time: 11:28:47 AM
[2025-10-12 11:28:47] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:28:47] [info] Tool Version: LVR_2025.1
[2025-10-12 11:28:47] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:28:47] [info] User Name: ubuntu
[2025-10-12 11:28:47] [info] Cores Used: 8
[2025-10-12 11:28:47] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Top Cell Information
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Layout Top Cell: TOP
[2025-10-12 11:28:47] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:28:47] [info] Database Format: GDSII
[2025-10-12 11:28:47] [info] Verification Mode: Full Check

[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Type of Errors | Count
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] DB_WARN -> 1
[2025-10-12 11:28:47] [info] LRF_ERR001 -> 14
[2025-10-12 11:28:47] [info] DB_WARN002 -> 159
[2025-10-12 11:28:47] [info] DB_WARN003 -> 161
```



```

[2025-10-12 11:28:47] [info] DRC_ERR9 -> 89
[2025-10-12 11:28:47] [info] DRC_ERR -> 67
[2025-10-12 11:28:47] [info] DRC_ERR5 -> 408
[2025-10-12 11:28:47] [info] DRC_ERR4 -> 241
[2025-10-12 11:28:47] [info] DRC_ERR1 -> 301
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Rule Violation Summary
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:28:47] [info] |-----|-----|-----|
[2025-10-12 11:28:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:28:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:28:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:28:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:28:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:28:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:28:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:28:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:28:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:28:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:28:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:28:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:28:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:28:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:28:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Performance Metrics
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] | Stage | Time Taken |
[2025-10-12 11:28:47] [info] |-----|-----|
[2025-10-12 11:28:47] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:28:47] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:28:47] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Summary
[2025-10-12 11:28:47] [info] -----
[2025-10-12 11:28:47] [info] Total Errors: 1120
[2025-10-12 11:28:47] [info] Total Warnings: 321
[2025-10-12 11:28:47] [info] DRC Status: FAILED

```

lvr_summary_500.rpt

```

[2025-10-12 18:39:57] [info] =====
[2025-10-12 18:39:57] [info] Design Rule Check Report
[2025-10-12 18:39:57] [info] =====

[2025-10-12 18:39:57] [info] Report Generated: 2025-10-12
[2025-10-12 18:39:57] [info] Run Start Time: 06:39:05 PM
[2025-10-12 18:39:57] [info] Run End Time: 06:39:57 PM
[2025-10-12 18:39:57] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:39:57] [info] Tool Version: LVR_2025.1
[2025-10-12 18:39:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:39:57] [info] User Name: ubuntu
[2025-10-12 18:39:57] [info] Cores Used: 8
[2025-10-12 18:39:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Top Cell Information
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Layout Top Cell:      TOP
[2025-10-12 18:39:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:39:57] [info] Database Format:      GDSII
[2025-10-12 18:39:57] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Type of Errors | Count
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] DB_WARN -> 1
[2025-10-12 18:39:57] [info] LRF_ERR001 -> 14
[2025-10-12 18:39:57] [info] DB_WARN002 -> 159
[2025-10-12 18:39:57] [info] DB_WARN003 -> 161
[2025-10-12 18:39:57] [info] DRC_ERR9 -> 89
[2025-10-12 18:39:57] [info] DRC_ERR -> 67
[2025-10-12 18:39:57] [info] DRC_ERR5 -> 408
[2025-10-12 18:39:57] [info] DRC_ERR4 -> 241
[2025-10-12 18:39:57] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Rule Violation Summary
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:39:57] [info] |-----|-----|-----|
[2025-10-12 18:39:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:39:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:39:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:39:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:39:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:39:57] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:39:57] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:39:57] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:39:57] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:39:57] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:39:57] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:39:57] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:39:57] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:39:57] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:39:57] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:39:57] [info] |
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Performance Metrics
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] | Stage          | Time Taken |
[2025-10-12 18:39:57] [info] |-----|-----|
[2025-10-12 18:39:57] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:39:57] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 18:39:57] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Summary
```

```
[2025-10-12 18:39:57] [info] -----
[2025-10-12 18:39:57] [info] Total Errors:      1120
[2025-10-12 18:39:57] [info] Total Warnings:    321
[2025-10-12 18:39:57] [info] DRC Status:        FAILED
```

lvr_summary_501.rpt

```
[2025-10-12 18:40:54] [info] =====
[2025-10-12 18:40:54] [info] Design Rule Check Report
[2025-10-12 18:40:54] [info] =====

[2025-10-12 18:40:54] [info] Report Generated: 2025-10-12
[2025-10-12 18:40:54] [info] Run Start Time: 06:40:02 PM
[2025-10-12 18:40:54] [info] Run End Time: 06:40:54 PM
[2025-10-12 18:40:54] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:40:54] [info] Tool Version: LVR_2025.1
[2025-10-12 18:40:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:40:54] [info] User Name: ubuntu
[2025-10-12 18:40:54] [info] Cores Used: 8
[2025-10-12 18:40:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] Top Cell Information
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] Layout Top Cell: TOP
[2025-10-12 18:40:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:40:54] [info] Database Format: GDSII
[2025-10-12 18:40:54] [info] Verification Mode: Full Check

[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] Type of Errors | Count
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] DB_WARN -> 1
[2025-10-12 18:40:54] [info] LRF_ERR001 -> 14
[2025-10-12 18:40:54] [info] DB_WARN002 -> 159
[2025-10-12 18:40:54] [info] DB_WARN003 -> 161
[2025-10-12 18:40:54] [info] DRC_ERR9 -> 89
[2025-10-12 18:40:54] [info] DRC_ERR -> 67
[2025-10-12 18:40:54] [info] DRC_ERR5 -> 408
[2025-10-12 18:40:54] [info] DRC_ERR4 -> 241
[2025-10-12 18:40:54] [info] DRC_ERR1 -> 301
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] Rule Violation Summary
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:40:54] [info] |-----|-----|-----|
[2025-10-12 18:40:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:40:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:40:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:40:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:40:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:40:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:40:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:40:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:40:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:40:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:40:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:40:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:40:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:40:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:40:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:40:54] [info]
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] Performance Metrics
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:40:54] [info] |-----|-----|
[2025-10-12 18:40:54] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:40:54] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 18:40:54] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info]                Summary
[2025-10-12 18:40:54] [info] -----
[2025-10-12 18:40:54] [info] Total Errors:    1120
[2025-10-12 18:40:54] [info] Total Warnings:    321
[2025-10-12 18:40:54] [info] DRC Status:      FAILED
```

lvr_summary_502.rpt

```
[2025-10-12 18:41:51] [info] =====
[2025-10-12 18:41:51] [info]                Design Rule Check Report
[2025-10-12 18:41:51] [info] =====

[2025-10-12 18:41:51] [info] Report Generated:    2025-10-12
[2025-10-12 18:41:51] [info] Run Start Time:      06:40:58 PM
[2025-10-12 18:41:51] [info] Run End Time:        06:41:51 PM
[2025-10-12 18:41:51] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 18:41:51] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:41:51] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:41:51] [info] User Name:           ubuntu
[2025-10-12 18:41:51] [info] Cores Used:          8
[2025-10-12 18:41:51] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info]                Top Cell Information
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] Layout Top Cell:     TOP
[2025-10-12 18:41:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:41:51] [info] Database Format:       GDSII
[2025-10-12 18:41:51] [info] Verification Mode:    Full Check

[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info]                Type of Errors | Count
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] DB_WARN -> 1
[2025-10-12 18:41:51] [info] LRF_ERR001 -> 14
[2025-10-12 18:41:51] [info] DB_WARN002 -> 159
[2025-10-12 18:41:51] [info] DB_WARN003 -> 161
[2025-10-12 18:41:51] [info] DRC_ERR9 -> 89
[2025-10-12 18:41:51] [info] DRC_ERR -> 67
[2025-10-12 18:41:51] [info] DRC_ERR5 -> 408
[2025-10-12 18:41:51] [info] DRC_ERR4 -> 241
[2025-10-12 18:41:51] [info] DRC_ERR1 -> 301
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info]                Rule Violation Summary
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:41:51] [info] |-----|-----|-----|
[2025-10-12 18:41:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:41:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:41:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:41:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:41:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:41:51] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:41:51] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:41:51] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:41:51] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:41:51] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:41:51] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:41:51] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:41:51] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:41:51] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:41:51] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:41:51] [info]
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] Performance Metrics
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] | Stage | Time Taken |
[2025-10-12 18:41:51] [info] |-----|-----|
[2025-10-12 18:41:51] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:41:51] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:41:51] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] Summary
[2025-10-12 18:41:51] [info] -----
[2025-10-12 18:41:51] [info] Total Errors: 1120
[2025-10-12 18:41:51] [info] Total Warnings: 321
[2025-10-12 18:41:51] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:42:47] [info] =====
[2025-10-12 18:42:47] [info] Design Rule Check Report
[2025-10-12 18:42:47] [info] =====

[2025-10-12 18:42:47] [info] Report Generated: 2025-10-12
[2025-10-12 18:42:47] [info] Run Start Time: 06:41:56 PM
[2025-10-12 18:42:47] [info] Run End Time: 06:42:47 PM
[2025-10-12 18:42:47] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:42:47] [info] Tool Version: LVR_2025.1
[2025-10-12 18:42:47] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:42:47] [info] User Name: ubuntu
[2025-10-12 18:42:47] [info] Cores Used: 8
[2025-10-12 18:42:47] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Top Cell Information
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Layout Top Cell: TOP
[2025-10-12 18:42:47] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:42:47] [info] Database Format: GDSII
[2025-10-12 18:42:47] [info] Verification Mode: Full Check

[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Type of Errors | Count
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] DB_WARN -> 1
[2025-10-12 18:42:47] [info] LRF_ERR001 -> 14
[2025-10-12 18:42:47] [info] DB_WARN002 -> 159
[2025-10-12 18:42:47] [info] DB_WARN003 -> 161
```

```
[2025-10-12 18:42:47] [info] DRC_ERR9 -> 89
[2025-10-12 18:42:47] [info] DRC_ERR -> 67
[2025-10-12 18:42:47] [info] DRC_ERR5 -> 408
[2025-10-12 18:42:47] [info] DRC_ERR4 -> 241
[2025-10-12 18:42:47] [info] DRC_ERR1 -> 301
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Rule Violation Summary
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:42:47] [info] |-----|-----|-----|
[2025-10-12 18:42:47] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:42:47] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:42:47] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:42:47] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:42:47] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:42:47] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:42:47] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:42:47] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:42:47] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:42:47] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:42:47] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:42:47] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:42:47] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:42:47] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:42:47] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Performance Metrics
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] | Stage | Time Taken |
[2025-10-12 18:42:47] [info] |-----|-----|
[2025-10-12 18:42:47] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:42:47] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:42:47] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Summary
[2025-10-12 18:42:47] [info] -----
[2025-10-12 18:42:47] [info] Total Errors: 1120
[2025-10-12 18:42:47] [info] Total Warnings: 321
[2025-10-12 18:42:47] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:43:43] [info] =====
[2025-10-12 18:43:43] [info] Design Rule Check Report
[2025-10-12 18:43:43] [info] =====

[2025-10-12 18:43:43] [info] Report Generated: 2025-10-12
[2025-10-12 18:43:43] [info] Run Start Time: 06:42:52 PM
[2025-10-12 18:43:43] [info] Run End Time: 06:43:43 PM
[2025-10-12 18:43:43] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:43:43] [info] Tool Version: LVR_2025.1
[2025-10-12 18:43:43] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:43:43] [info] User Name: ubuntu
[2025-10-12 18:43:43] [info] Cores Used: 8
[2025-10-12 18:43:43] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] Top Cell Information
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] Layout Top Cell:      TOP
[2025-10-12 18:43:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:43:43] [info] Database Format:      GDSII
[2025-10-12 18:43:43] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] Type of Errors | Count
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] DB_WARN -> 1
[2025-10-12 18:43:43] [info] LRF_ERR001 -> 14
[2025-10-12 18:43:43] [info] DB_WARN002 -> 159
[2025-10-12 18:43:43] [info] DB_WARN003 -> 161
[2025-10-12 18:43:43] [info] DRC_ERR9 -> 89
[2025-10-12 18:43:43] [info] DRC_ERR -> 67
[2025-10-12 18:43:43] [info] DRC_ERR5 -> 408
[2025-10-12 18:43:43] [info] DRC_ERR4 -> 241
[2025-10-12 18:43:43] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] Rule Violation Summary
```

```
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:43:43] [info] |-----|-----|-----|
[2025-10-12 18:43:43] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:43:43] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:43:43] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:43:43] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:43:43] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:43:43] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:43:43] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:43:43] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:43:43] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:43:43] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:43:43] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:43:43] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:43:43] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:43:43] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:43:43] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:43:43] [info] -----
```

```
[2025-10-12 18:43:43] [info] Performance Metrics
```

```
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] | Stage          | Time Taken |
[2025-10-12 18:43:43] [info] |-----|-----|
[2025-10-12 18:43:43] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:43:43] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 18:43:43] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:43:43] [info] -----
```

```
[2025-10-12 18:43:43] [info] Summary
```

```
[2025-10-12 18:43:43] [info] -----
[2025-10-12 18:43:43] [info] Total Errors:      1120
[2025-10-12 18:43:43] [info] Total Warnings:    321
[2025-10-12 18:43:43] [info] DRC Status:        FAILED
```

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```
[2025-10-12 18:44:40] [info] =====
[2025-10-12 18:44:40] [info] Design Rule Check Report
[2025-10-12 18:44:40] [info] =====

[2025-10-12 18:44:40] [info] Report Generated: 2025-10-12
[2025-10-12 18:44:40] [info] Run Start Time: 06:43:49 PM
[2025-10-12 18:44:40] [info] Run End Time: 06:44:40 PM
[2025-10-12 18:44:40] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 18:44:40] [info] Tool Version: LVR_2025.1
[2025-10-12 18:44:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:44:40] [info] User Name: ubuntu
[2025-10-12 18:44:40] [info] Cores Used: 8
[2025-10-12 18:44:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] Top Cell Information
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] Layout Top Cell: TOP
[2025-10-12 18:44:40] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:44:40] [info] Database Format: GDSII
[2025-10-12 18:44:40] [info] Verification Mode: Full Check

[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] Type of Errors | Count
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] DB_WARN -> 1
[2025-10-12 18:44:40] [info] LRF_ERR001 -> 14
[2025-10-12 18:44:40] [info] DB_WARN002 -> 159
[2025-10-12 18:44:40] [info] DB_WARN003 -> 161
[2025-10-12 18:44:40] [info] DRC_ERR9 -> 89
[2025-10-12 18:44:40] [info] DRC_ERR -> 67
[2025-10-12 18:44:40] [info] DRC_ERR5 -> 408
[2025-10-12 18:44:40] [info] DRC_ERR4 -> 241
[2025-10-12 18:44:40] [info] DRC_ERR1 -> 301
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] Rule Violation Summary
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:44:40] [info] |-----|-----|-----|
[2025-10-12 18:44:40] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:44:40] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:44:40] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:44:40] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:44:40] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:44:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:44:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:44:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:44:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:44:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:44:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:44:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:44:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:44:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:44:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:44:40] [info]
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] Performance Metrics
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] | Stage | Time Taken |
```



```
[2025-10-12 18:44:40] [info] |-----|-----|
[2025-10-12 18:44:40] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:44:40] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:44:40] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info]                Summary
[2025-10-12 18:44:40] [info] -----
[2025-10-12 18:44:40] [info] Total Errors:    1120
[2025-10-12 18:44:40] [info] Total Warnings:    321
[2025-10-12 18:44:40] [info] DRC Status:      FAILED
```

lvr_summary_506.rpt

```
[2025-10-12 18:45:37] [info] =====
[2025-10-12 18:45:37] [info]                Design Rule Check Report
[2025-10-12 18:45:37] [info] =====

[2025-10-12 18:45:37] [info] Report Generated:    2025-10-12
[2025-10-12 18:45:37] [info] Run Start Time:      06:44:46 PM
[2025-10-12 18:45:37] [info] Run End Time:        06:45:37 PM
[2025-10-12 18:45:37] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 18:45:37] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:45:37] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:45:37] [info] User Name:           ubuntu
[2025-10-12 18:45:37] [info] Cores Used:          8
[2025-10-12 18:45:37] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info]                Top Cell Information
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] Layout Top Cell:     TOP
[2025-10-12 18:45:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:45:37] [info] Database Format:       GDSII
[2025-10-12 18:45:37] [info] Verification Mode:    Full Check

[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info]                Type of Errors | Count
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] DB_WARN -> 1
[2025-10-12 18:45:37] [info] LRF_ERR001 -> 14
[2025-10-12 18:45:37] [info] DB_WARN002 -> 159
[2025-10-12 18:45:37] [info] DB_WARN003 -> 161
[2025-10-12 18:45:37] [info] DRC_ERR9 -> 89
[2025-10-12 18:45:37] [info] DRC_ERR -> 67
[2025-10-12 18:45:37] [info] DRC_ERR5 -> 408
[2025-10-12 18:45:37] [info] DRC_ERR4 -> 241
[2025-10-12 18:45:37] [info] DRC_ERR1 -> 301
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info]                Rule Violation Summary
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:45:37] [info] |-----|-----|-----|
[2025-10-12 18:45:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:45:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:45:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:45:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:45:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:45:37] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:45:37] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:45:37] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:45:37] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:45:37] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:45:37] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:45:37] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:45:37] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:45:37] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:45:37] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:45:37] [info]
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] Performance Metrics
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] | Stage | Time Taken |
[2025-10-12 18:45:37] [info] |-----|-----|
[2025-10-12 18:45:37] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:45:37] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:45:37] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] Summary
[2025-10-12 18:45:37] [info] -----
[2025-10-12 18:45:37] [info] Total Errors: 1120
[2025-10-12 18:45:37] [info] Total Warnings: 321
[2025-10-12 18:45:37] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:46:36] [info] =====
[2025-10-12 18:46:36] [info] Design Rule Check Report
[2025-10-12 18:46:36] [info] =====

[2025-10-12 18:46:36] [info] Report Generated: 2025-10-12
[2025-10-12 18:46:36] [info] Run Start Time: 06:45:44 PM
[2025-10-12 18:46:36] [info] Run End Time: 06:46:36 PM
[2025-10-12 18:46:36] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:46:36] [info] Tool Version: LVR_2025.1
[2025-10-12 18:46:36] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:46:36] [info] User Name: ubuntu
[2025-10-12 18:46:36] [info] Cores Used: 8
[2025-10-12 18:46:36] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Top Cell Information
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Layout Top Cell: TOP
[2025-10-12 18:46:36] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:46:36] [info] Database Format: GDSII
[2025-10-12 18:46:36] [info] Verification Mode: Full Check

[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Type of Errors | Count
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] DB_WARN -> 1
[2025-10-12 18:46:36] [info] LRF_ERR001 -> 14
[2025-10-12 18:46:36] [info] DB_WARN002 -> 159
[2025-10-12 18:46:36] [info] DB_WARN003 -> 161
```

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[2025-10-12 18:46:36] [info] DRC_ERR9 -> 89
[2025-10-12 18:46:36] [info] DRC_ERR -> 67
[2025-10-12 18:46:36] [info] DRC_ERR5 -> 408
[2025-10-12 18:46:36] [info] DRC_ERR4 -> 241
[2025-10-12 18:46:36] [info] DRC_ERR1 -> 301
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Rule Violation Summary
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:46:36] [info] |-----|-----|-----|
[2025-10-12 18:46:36] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:46:36] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:46:36] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:46:36] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:46:36] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:46:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:46:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:46:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:46:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:46:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:46:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:46:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:46:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:46:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:46:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Performance Metrics
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] | Stage | Time Taken |
[2025-10-12 18:46:36] [info] |-----|-----|
[2025-10-12 18:46:36] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:46:36] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:46:36] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Summary
[2025-10-12 18:46:36] [info] -----
[2025-10-12 18:46:36] [info] Total Errors: 1120
[2025-10-12 18:46:36] [info] Total Warnings: 321
[2025-10-12 18:46:36] [info] DRC Status: FAILED

```

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[2025-10-12 18:47:32] [info] =====
[2025-10-12 18:47:32] [info] Design Rule Check Report
[2025-10-12 18:47:32] [info] =====

[2025-10-12 18:47:32] [info] Report Generated: 2025-10-12
[2025-10-12 18:47:32] [info] Run Start Time: 06:46:40 PM
[2025-10-12 18:47:32] [info] Run End Time: 06:47:32 PM
[2025-10-12 18:47:32] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:47:32] [info] Tool Version: LVR_2025.1
[2025-10-12 18:47:32] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:47:32] [info] User Name: ubuntu
[2025-10-12 18:47:32] [info] Cores Used: 8
[2025-10-12 18:47:32] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Top Cell Information
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Layout Top Cell:      TOP
[2025-10-12 18:47:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:47:32] [info] Database Format:      GDSII
[2025-10-12 18:47:32] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Type of Errors | Count
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] DB_WARN -> 1
[2025-10-12 18:47:32] [info] LRF_ERR001 -> 14
[2025-10-12 18:47:32] [info] DB_WARN002 -> 159
[2025-10-12 18:47:32] [info] DB_WARN003 -> 161
[2025-10-12 18:47:32] [info] DRC_ERR9 -> 89
[2025-10-12 18:47:32] [info] DRC_ERR -> 67
[2025-10-12 18:47:32] [info] DRC_ERR5 -> 408
[2025-10-12 18:47:32] [info] DRC_ERR4 -> 241
[2025-10-12 18:47:32] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Rule Violation Summary
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:47:32] [info] |-----|-----|-----|
[2025-10-12 18:47:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:47:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:47:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:47:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:47:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:47:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:47:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:47:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:47:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:47:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:47:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:47:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:47:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:47:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:47:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Performance Metrics
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] | Stage | Time Taken |
[2025-10-12 18:47:32] [info] |-----|-----|
[2025-10-12 18:47:32] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:47:32] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:47:32] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Summary
[2025-10-12 18:47:32] [info] -----
[2025-10-12 18:47:32] [info] Total Errors: 1120
[2025-10-12 18:47:32] [info] Total Warnings: 321
[2025-10-12 18:47:32] [info] DRC Status: FAILED
```

lvr_summary_509.rpt

```
[2025-10-12 18:48:30] [info] =====
[2025-10-12 18:48:30] [info] Design Rule Check Report
[2025-10-12 18:48:30] [info] =====

[2025-10-12 18:48:30] [info] Report Generated: 2025-10-12
[2025-10-12 18:48:30] [info] Run Start Time: 06:47:38 PM
[2025-10-12 18:48:30] [info] Run End Time: 06:48:30 PM
[2025-10-12 18:48:30] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:48:30] [info] Tool Version: LVR_2025.1
[2025-10-12 18:48:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:48:30] [info] User Name: ubuntu
[2025-10-12 18:48:30] [info] Cores Used: 8
[2025-10-12 18:48:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Top Cell Information
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Layout Top Cell: TOP
[2025-10-12 18:48:30] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:48:30] [info] Database Format: GDSII
[2025-10-12 18:48:30] [info] Verification Mode: Full Check

[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Type of Errors | Count
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] DB_WARN -> 1
[2025-10-12 18:48:30] [info] LRF_ERR001 -> 14
[2025-10-12 18:48:30] [info] DB_WARN002 -> 159
[2025-10-12 18:48:30] [info] DB_WARN003 -> 161
[2025-10-12 18:48:30] [info] DRC_ERR9 -> 89
[2025-10-12 18:48:30] [info] DRC_ERR -> 67
[2025-10-12 18:48:30] [info] DRC_ERR5 -> 408
[2025-10-12 18:48:30] [info] DRC_ERR4 -> 241
[2025-10-12 18:48:30] [info] DRC_ERR1 -> 301
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Rule Violation Summary
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:48:30] [info] |-----|-----|-----|
[2025-10-12 18:48:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:48:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:48:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:48:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:48:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:48:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:48:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:48:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:48:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:48:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:48:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:48:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:48:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:48:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:48:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:48:30] [info]
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Performance Metrics
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:48:30] [info] |-----|-----|
[2025-10-12 18:48:30] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:48:30] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 18:48:30] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Summary
[2025-10-12 18:48:30] [info] -----
[2025-10-12 18:48:30] [info] Total Errors:   1120
[2025-10-12 18:48:30] [info] Total Warnings:    321
[2025-10-12 18:48:30] [info] DRC Status:      FAILED
```

lvr_summary_51.rpt

```
[2025-10-12 11:29:44] [info] =====
[2025-10-12 11:29:44] [info] Design Rule Check Report
[2025-10-12 11:29:44] [info] =====

[2025-10-12 11:29:44] [info] Report Generated:   2025-10-12
[2025-10-12 11:29:44] [info] Run Start Time:     11:28:53 AM
[2025-10-12 11:29:44] [info] Run End Time:       11:29:44 AM
[2025-10-12 11:29:44] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 11:29:44] [info] Tool Version:       LVR_2025.1
[2025-10-12 11:29:44] [info] Host Name:          ip-172-31-37-173
[2025-10-12 11:29:44] [info] User Name:          ubuntu
[2025-10-12 11:29:44] [info] Cores Used:         8
[2025-10-12 11:29:44] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Top Cell Information
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Layout Top Cell:    TOP
[2025-10-12 11:29:44] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 11:29:44] [info] Database Format:      GDSII
[2025-10-12 11:29:44] [info] Verification Mode:   Full Check

[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Type of Errors | Count
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] DB_WARN -> 1
[2025-10-12 11:29:44] [info] LRF_ERR001 -> 14
[2025-10-12 11:29:44] [info] DB_WARN002 -> 159
[2025-10-12 11:29:44] [info] DB_WARN003 -> 161
[2025-10-12 11:29:44] [info] DRC_ERR9 -> 89
[2025-10-12 11:29:44] [info] DRC_ERR -> 67
[2025-10-12 11:29:44] [info] DRC_ERR5 -> 408
[2025-10-12 11:29:44] [info] DRC_ERR4 -> 241
[2025-10-12 11:29:44] [info] DRC_ERR1 -> 301
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Rule Violation Summary
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:29:44] [info] |-----|-----|-----|
[2025-10-12 11:29:44] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:29:44] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:29:44] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:29:44] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:29:44] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 11:29:44] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:29:44] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:29:44] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:29:44] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:29:44] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:29:44] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:29:44] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:29:44] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:29:44] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:29:44] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:29:44] [info]
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Performance Metrics
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] | Stage | Time Taken |
[2025-10-12 11:29:44] [info] |-----|-----|
[2025-10-12 11:29:44] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:29:44] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:29:44] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Summary
[2025-10-12 11:29:44] [info] -----
[2025-10-12 11:29:44] [info] Total Errors: 1120
[2025-10-12 11:29:44] [info] Total Warnings: 321
[2025-10-12 11:29:44] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:49:26] [info] =====
[2025-10-12 18:49:26] [info] Design Rule Check Report
[2025-10-12 18:49:26] [info] =====

[2025-10-12 18:49:26] [info] Report Generated: 2025-10-12
[2025-10-12 18:49:26] [info] Run Start Time: 06:48:35 PM
[2025-10-12 18:49:26] [info] Run End Time: 06:49:26 PM
[2025-10-12 18:49:26] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:49:26] [info] Tool Version: LVR_2025.1
[2025-10-12 18:49:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:49:26] [info] User Name: ubuntu
[2025-10-12 18:49:26] [info] Cores Used: 8
[2025-10-12 18:49:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Top Cell Information
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Layout Top Cell: TOP
[2025-10-12 18:49:26] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:49:26] [info] Database Format: GDSII
[2025-10-12 18:49:26] [info] Verification Mode: Full Check

[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Type of Errors | Count
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] DB_WARN -> 1
[2025-10-12 18:49:26] [info] LRF_ERR001 -> 14
[2025-10-12 18:49:26] [info] DB_WARN002 -> 159
[2025-10-12 18:49:26] [info] DB_WARN003 -> 161
```

```
[2025-10-12 18:49:26] [info] DRC_ERR9 -> 89
[2025-10-12 18:49:26] [info] DRC_ERR -> 67
[2025-10-12 18:49:26] [info] DRC_ERR5 -> 408
[2025-10-12 18:49:26] [info] DRC_ERR4 -> 241
[2025-10-12 18:49:26] [info] DRC_ERR1 -> 301
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Rule Violation Summary
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:49:26] [info] |-----|-----|-----|
[2025-10-12 18:49:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:49:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:49:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:49:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:49:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:49:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:49:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:49:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:49:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:49:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:49:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:49:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:49:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:49:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:49:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Performance Metrics
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] | Stage | Time Taken |
[2025-10-12 18:49:26] [info] |-----|-----|
[2025-10-12 18:49:26] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:49:26] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:49:26] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Summary
[2025-10-12 18:49:26] [info] -----
[2025-10-12 18:49:26] [info] Total Errors: 1120
[2025-10-12 18:49:26] [info] Total Warnings: 321
[2025-10-12 18:49:26] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:50:25] [info] =====
[2025-10-12 18:50:25] [info] Design Rule Check Report
[2025-10-12 18:50:25] [info] =====

[2025-10-12 18:50:25] [info] Report Generated: 2025-10-12
[2025-10-12 18:50:25] [info] Run Start Time: 06:49:33 PM
[2025-10-12 18:50:25] [info] Run End Time: 06:50:25 PM
[2025-10-12 18:50:25] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:50:25] [info] Tool Version: LVR_2025.1
[2025-10-12 18:50:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:50:25] [info] User Name: ubuntu
[2025-10-12 18:50:25] [info] Cores Used: 8
[2025-10-12 18:50:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Top Cell Information
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Layout Top Cell:      TOP
[2025-10-12 18:50:25] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:50:25] [info] Database Format:       GDSII
[2025-10-12 18:50:25] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Type of Errors | Count
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] DB_WARN -> 1
[2025-10-12 18:50:25] [info] LRF_ERR001 -> 14
[2025-10-12 18:50:25] [info] DB_WARN002 -> 159
[2025-10-12 18:50:25] [info] DB_WARN003 -> 161
[2025-10-12 18:50:25] [info] DRC_ERR9 -> 89
[2025-10-12 18:50:25] [info] DRC_ERR -> 67
[2025-10-12 18:50:25] [info] DRC_ERR5 -> 408
[2025-10-12 18:50:25] [info] DRC_ERR4 -> 241
[2025-10-12 18:50:25] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Rule Violation Summary
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:50:25] [info] |-----|-----|-----|
[2025-10-12 18:50:25] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:50:25] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:50:25] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:50:25] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:50:25] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:50:25] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:50:25] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:50:25] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:50:25] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:50:25] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:50:25] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:50:25] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:50:25] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:50:25] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:50:25] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:50:25] [info] |
```

```
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Performance Metrics
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] | Stage          | Time Taken |
[2025-10-12 18:50:25] [info] |-----|-----|
[2025-10-12 18:50:25] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:50:25] [info] | Rule Checking   | 0 minutes 40 seconds |
[2025-10-12 18:50:25] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Summary
[2025-10-12 18:50:25] [info] -----
[2025-10-12 18:50:25] [info] Total Errors:      1120
[2025-10-12 18:50:25] [info] Total Warnings:    321
[2025-10-12 18:50:25] [info] DRC Status:        FAILED
```

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```
[2025-10-12 18:51:22] [info] =====
[2025-10-12 18:51:22] [info] Design Rule Check Report
[2025-10-12 18:51:22] [info] =====

[2025-10-12 18:51:22] [info] Report Generated: 2025-10-12
[2025-10-12 18:51:22] [info] Run Start Time: 06:50:30 PM
[2025-10-12 18:51:22] [info] Run End Time: 06:51:22 PM
[2025-10-12 18:51:22] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:51:22] [info] Tool Version: LVR_2025.1
[2025-10-12 18:51:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:51:22] [info] User Name: ubuntu
[2025-10-12 18:51:22] [info] Cores Used: 8
[2025-10-12 18:51:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] Top Cell Information
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] Layout Top Cell: TOP
[2025-10-12 18:51:22] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:51:22] [info] Database Format: GDSII
[2025-10-12 18:51:22] [info] Verification Mode: Full Check

[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] Type of Errors | Count
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] DB_WARN -> 1
[2025-10-12 18:51:22] [info] LRF_ERR001 -> 14
[2025-10-12 18:51:22] [info] DB_WARN002 -> 159
[2025-10-12 18:51:22] [info] DB_WARN003 -> 161
[2025-10-12 18:51:22] [info] DRC_ERR9 -> 89
[2025-10-12 18:51:22] [info] DRC_ERR -> 67
[2025-10-12 18:51:22] [info] DRC_ERR5 -> 408
[2025-10-12 18:51:22] [info] DRC_ERR4 -> 241
[2025-10-12 18:51:22] [info] DRC_ERR1 -> 301
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] Rule Violation Summary
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:51:22] [info] |-----|-----|-----|
[2025-10-12 18:51:22] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:51:22] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:51:22] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:51:22] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:51:22] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:51:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:51:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:51:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:51:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:51:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:51:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:51:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:51:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:51:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:51:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:51:22] [info]
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] Performance Metrics
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:51:22] [info] |-----|-----|
[2025-10-12 18:51:22] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:51:22] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:51:22] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info]                Summary
[2025-10-12 18:51:22] [info] -----
[2025-10-12 18:51:22] [info] Total Errors:    1120
[2025-10-12 18:51:22] [info] Total Warnings:    321
[2025-10-12 18:51:22] [info] DRC Status:      FAILED
```

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```
[2025-10-12 18:52:19] [info] =====
[2025-10-12 18:52:19] [info]                Design Rule Check Report
[2025-10-12 18:52:19] [info] =====

[2025-10-12 18:52:19] [info] Report Generated:    2025-10-12
[2025-10-12 18:52:19] [info] Run Start Time:      06:51:27 PM
[2025-10-12 18:52:19] [info] Run End Time:        06:52:19 PM
[2025-10-12 18:52:19] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 18:52:19] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:52:19] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:52:19] [info] User Name:           ubuntu
[2025-10-12 18:52:19] [info] Cores Used:          8
[2025-10-12 18:52:19] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info]                Top Cell Information
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] Layout Top Cell:     TOP
[2025-10-12 18:52:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:52:19] [info] Database Format:       GDSII
[2025-10-12 18:52:19] [info] Verification Mode:    Full Check

[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info]                Type of Errors | Count
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] DB_WARN -> 1
[2025-10-12 18:52:19] [info] LRF_ERR001 -> 14
[2025-10-12 18:52:19] [info] DB_WARN002 -> 159
[2025-10-12 18:52:19] [info] DB_WARN003 -> 161
[2025-10-12 18:52:19] [info] DRC_ERR9 -> 89
[2025-10-12 18:52:19] [info] DRC_ERR -> 67
[2025-10-12 18:52:19] [info] DRC_ERR5 -> 408
[2025-10-12 18:52:19] [info] DRC_ERR4 -> 241
[2025-10-12 18:52:19] [info] DRC_ERR1 -> 301
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info]                Rule Violation Summary
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:52:19] [info] |-----|-----|-----|
[2025-10-12 18:52:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:52:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:52:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:52:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:52:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:52:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:52:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:52:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:52:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:52:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:52:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:52:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:52:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:52:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:52:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:52:19] [info]
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] Performance Metrics
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] | Stage | Time Taken |
[2025-10-12 18:52:19] [info] |-----|-----|
[2025-10-12 18:52:19] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:52:19] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:52:19] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] Summary
[2025-10-12 18:52:19] [info] -----
[2025-10-12 18:52:19] [info] Total Errors: 1120
[2025-10-12 18:52:19] [info] Total Warnings: 321
[2025-10-12 18:52:19] [info] DRC Status: FAILED
```

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```
[2025-10-12 18:53:16] [info] =====
[2025-10-12 18:53:16] [info] Design Rule Check Report
[2025-10-12 18:53:16] [info] =====

[2025-10-12 18:53:16] [info] Report Generated: 2025-10-12
[2025-10-12 18:53:16] [info] Run Start Time: 06:52:24 PM
[2025-10-12 18:53:16] [info] Run End Time: 06:53:16 PM
[2025-10-12 18:53:16] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 18:53:16] [info] Tool Version: LVR_2025.1
[2025-10-12 18:53:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:53:16] [info] User Name: ubuntu
[2025-10-12 18:53:16] [info] Cores Used: 8
[2025-10-12 18:53:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Top Cell Information
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Layout Top Cell: TOP
[2025-10-12 18:53:16] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:53:16] [info] Database Format: GDSII
[2025-10-12 18:53:16] [info] Verification Mode: Full Check

[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Type of Errors | Count
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] DB_WARN -> 1
[2025-10-12 18:53:16] [info] LRF_ERR001 -> 14
[2025-10-12 18:53:16] [info] DB_WARN002 -> 159
[2025-10-12 18:53:16] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:53:16] [info] DRC_ERR9 -> 89
[2025-10-12 18:53:16] [info] DRC_ERR -> 67
[2025-10-12 18:53:16] [info] DRC_ERR5 -> 408
[2025-10-12 18:53:16] [info] DRC_ERR4 -> 241
[2025-10-12 18:53:16] [info] DRC_ERR1 -> 301
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Rule Violation Summary
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:53:16] [info] |-----|-----|-----|
[2025-10-12 18:53:16] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:53:16] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:53:16] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:53:16] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:53:16] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:53:16] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:53:16] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:53:16] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:53:16] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:53:16] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:53:16] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:53:16] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:53:16] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:53:16] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:53:16] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Performance Metrics
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] | Stage | Time Taken |
[2025-10-12 18:53:16] [info] |-----|-----|
[2025-10-12 18:53:16] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:53:16] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 18:53:16] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Summary
[2025-10-12 18:53:16] [info] -----
[2025-10-12 18:53:16] [info] Total Errors: 1120
[2025-10-12 18:53:16] [info] Total Warnings: 321
[2025-10-12 18:53:16] [info] DRC Status: FAILED

```

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```

[2025-10-12 18:54:12] [info] =====
[2025-10-12 18:54:12] [info] Design Rule Check Report
[2025-10-12 18:54:12] [info] =====

[2025-10-12 18:54:12] [info] Report Generated: 2025-10-12
[2025-10-12 18:54:12] [info] Run Start Time: 06:53:22 PM
[2025-10-12 18:54:12] [info] Run End Time: 06:54:12 PM
[2025-10-12 18:54:12] [info] Time Taken: 0 minutes 49 seconds

[2025-10-12 18:54:12] [info] Tool Version: LVR_2025.1
[2025-10-12 18:54:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:54:12] [info] User Name: ubuntu
[2025-10-12 18:54:12] [info] Cores Used: 8
[2025-10-12 18:54:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Top Cell Information
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Layout Top Cell:      TOP
[2025-10-12 18:54:12] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:54:12] [info] Database Format:      GDSII
[2025-10-12 18:54:12] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Type of Errors | Count
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] DB_WARN -> 1
[2025-10-12 18:54:12] [info] LRF_ERR001 -> 14
[2025-10-12 18:54:12] [info] DB_WARN002 -> 159
[2025-10-12 18:54:12] [info] DB_WARN003 -> 161
[2025-10-12 18:54:12] [info] DRC_ERR9 -> 89
[2025-10-12 18:54:12] [info] DRC_ERR -> 67
[2025-10-12 18:54:12] [info] DRC_ERR5 -> 408
[2025-10-12 18:54:12] [info] DRC_ERR4 -> 241
[2025-10-12 18:54:12] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Rule Violation Summary
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:54:12] [info] |-----|-----|-----|
[2025-10-12 18:54:12] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:54:12] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:54:12] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:54:12] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:54:12] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:54:12] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:54:12] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:54:12] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:54:12] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:54:12] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:54:12] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:54:12] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:54:12] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:54:12] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:54:12] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:54:12] [info] |
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Performance Metrics
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] | Stage          | Time Taken |
[2025-10-12 18:54:12] [info] |-----|-----|
[2025-10-12 18:54:12] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:54:12] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 18:54:12] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Summary
```

```
[2025-10-12 18:54:12] [info] -----
[2025-10-12 18:54:12] [info] Total Errors:      1120
[2025-10-12 18:54:12] [info] Total Warnings:    321
[2025-10-12 18:54:12] [info] DRC Status:        FAILED
```

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```
[2025-10-12 18:55:10] [info] =====
[2025-10-12 18:55:10] [info]                      Design Rule Check Report
[2025-10-12 18:55:10] [info] =====

[2025-10-12 18:55:10] [info] Report Generated:      2025-10-12
[2025-10-12 18:55:10] [info] Run Start Time:       06:54:19 PM
[2025-10-12 18:55:10] [info] Run End Time:         06:55:10 PM
[2025-10-12 18:55:10] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 18:55:10] [info] Tool Version:         LVR_2025.1
[2025-10-12 18:55:10] [info] Host Name:            ip-172-31-37-173
[2025-10-12 18:55:10] [info] User Name:            ubuntu
[2025-10-12 18:55:10] [info] Cores Used:           8
[2025-10-12 18:55:10] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info]                      Top Cell Information
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info] Layout Top Cell:      TOP
[2025-10-12 18:55:10] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 18:55:10] [info] Database Format:        GDSII
[2025-10-12 18:55:10] [info] Verification Mode:     Full Check

[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info]                      Type of Errors | Count
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info] DB_WARN -> 1
[2025-10-12 18:55:10] [info] LRF_ERR001 -> 14
[2025-10-12 18:55:10] [info] DB_WARN002 -> 159
[2025-10-12 18:55:10] [info] DB_WARN003 -> 161
[2025-10-12 18:55:10] [info] DRC_ERR9 -> 89
[2025-10-12 18:55:10] [info] DRC_ERR -> 67
[2025-10-12 18:55:10] [info] DRC_ERR5 -> 408
[2025-10-12 18:55:10] [info] DRC_ERR4 -> 241
[2025-10-12 18:55:10] [info] DRC_ERR1 -> 301
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info]                      Rule Violation Summary
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:55:10] [info] |-----|-----|-----|
[2025-10-12 18:55:10] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:55:10] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:55:10] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:55:10] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:55:10] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:55:10] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 18:55:10] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:55:10] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:55:10] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:55:10] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:55:10] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:55:10] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:55:10] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:55:10] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:55:10] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:55:10] [info]
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info]                      Performance Metrics
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info] | Stage | Time Taken |
```

```
[2025-10-12 18:55:10] [info] |-----|-----|
[2025-10-12 18:55:10] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 18:55:10] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 18:55:10] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info]                Summary
[2025-10-12 18:55:10] [info] -----
[2025-10-12 18:55:10] [info] Total Errors:    1120
[2025-10-12 18:55:10] [info] Total Warnings:    321
[2025-10-12 18:55:10] [info] DRC Status:      FAILED
```

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```
[2025-10-12 18:56:06] [info] =====
[2025-10-12 18:56:06] [info]                Design Rule Check Report
[2025-10-12 18:56:06] [info] =====

[2025-10-12 18:56:06] [info] Report Generated:    2025-10-12
[2025-10-12 18:56:06] [info] Run Start Time:      06:55:16 PM
[2025-10-12 18:56:06] [info] Run End Time:        06:56:06 PM
[2025-10-12 18:56:06] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 18:56:06] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:56:06] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:56:06] [info] User Name:           ubuntu
[2025-10-12 18:56:06] [info] Cores Used:          8
[2025-10-12 18:56:06] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info]                Top Cell Information
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] Layout Top Cell:     TOP
[2025-10-12 18:56:06] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:56:06] [info] Database Format:       GDSII
[2025-10-12 18:56:06] [info] Verification Mode:    Full Check

[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info]                Type of Errors | Count
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] DB_WARN -> 1
[2025-10-12 18:56:06] [info] LRF_ERR001 -> 14
[2025-10-12 18:56:06] [info] DB_WARN002 -> 159
[2025-10-12 18:56:06] [info] DB_WARN003 -> 161
[2025-10-12 18:56:06] [info] DRC_ERR9 -> 89
[2025-10-12 18:56:06] [info] DRC_ERR -> 67
[2025-10-12 18:56:06] [info] DRC_ERR5 -> 408
[2025-10-12 18:56:06] [info] DRC_ERR4 -> 241
[2025-10-12 18:56:06] [info] DRC_ERR1 -> 301
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info]                Rule Violation Summary
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:56:06] [info] |-----|-----|-----|
[2025-10-12 18:56:06] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:56:06] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:56:06] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:56:06] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:56:06] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 18:56:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:56:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:56:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:56:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:56:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:56:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:56:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:56:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:56:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:56:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:56:06] [info]
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] Performance Metrics
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] | Stage | Time Taken |
[2025-10-12 18:56:06] [info] |-----|-----|
[2025-10-12 18:56:06] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:56:06] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:56:06] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] Summary
[2025-10-12 18:56:06] [info] -----
[2025-10-12 18:56:06] [info] Total Errors: 1120
[2025-10-12 18:56:06] [info] Total Warnings: 321
[2025-10-12 18:56:06] [info] DRC Status: FAILED
```

lvr_summary_518.rpt

```
[2025-10-12 18:57:03] [info] =====
[2025-10-12 18:57:03] [info] Design Rule Check Report
[2025-10-12 18:57:03] [info] =====

[2025-10-12 18:57:03] [info] Report Generated: 2025-10-12
[2025-10-12 18:57:03] [info] Run Start Time: 06:56:12 PM
[2025-10-12 18:57:03] [info] Run End Time: 06:57:03 PM
[2025-10-12 18:57:03] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:57:03] [info] Tool Version: LVR_2025.1
[2025-10-12 18:57:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:57:03] [info] User Name: ubuntu
[2025-10-12 18:57:03] [info] Cores Used: 8
[2025-10-12 18:57:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Top Cell Information
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Layout Top Cell: TOP
[2025-10-12 18:57:03] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:57:03] [info] Database Format: GDSII
[2025-10-12 18:57:03] [info] Verification Mode: Full Check

[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Type of Errors | Count
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] DB_WARN -> 1
[2025-10-12 18:57:03] [info] LRF_ERR001 -> 14
[2025-10-12 18:57:03] [info] DB_WARN002 -> 159
[2025-10-12 18:57:03] [info] DB_WARN003 -> 161
```

```

[2025-10-12 18:57:03] [info] DRC_ERR9 -> 89
[2025-10-12 18:57:03] [info] DRC_ERR -> 67
[2025-10-12 18:57:03] [info] DRC_ERR5 -> 408
[2025-10-12 18:57:03] [info] DRC_ERR4 -> 241
[2025-10-12 18:57:03] [info] DRC_ERR1 -> 301
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Rule Violation Summary
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 18:57:03] [info] |-----|-----|-----|
[2025-10-12 18:57:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 18:57:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 18:57:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 18:57:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 18:57:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 18:57:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:57:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:57:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:57:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:57:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:57:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:57:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:57:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:57:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:57:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Performance Metrics
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] | Stage | Time Taken |
[2025-10-12 18:57:03] [info] |-----|-----|
[2025-10-12 18:57:03] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:57:03] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 18:57:03] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Summary
[2025-10-12 18:57:03] [info] -----
[2025-10-12 18:57:03] [info] Total Errors: 1120
[2025-10-12 18:57:03] [info] Total Warnings: 321
[2025-10-12 18:57:03] [info] DRC Status: FAILED

```

lvr_summary_519.rpt

```

[2025-10-12 18:58:01] [info] =====
[2025-10-12 18:58:01] [info] Design Rule Check Report
[2025-10-12 18:58:01] [info] =====

[2025-10-12 18:58:01] [info] Report Generated: 2025-10-12
[2025-10-12 18:58:01] [info] Run Start Time: 06:57:09 PM
[2025-10-12 18:58:01] [info] Run End Time: 06:58:01 PM
[2025-10-12 18:58:01] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:58:01] [info] Tool Version: LVR_2025.1
[2025-10-12 18:58:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:58:01] [info] User Name: ubuntu
[2025-10-12 18:58:01] [info] Cores Used: 8
[2025-10-12 18:58:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Top Cell Information
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Layout Top Cell:      TOP
[2025-10-12 18:58:01] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:58:01] [info] Database Format:      GDSII
[2025-10-12 18:58:01] [info] Verification Mode:    Full Check
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Type of Errors | Count
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] DB_WARN -> 1
[2025-10-12 18:58:01] [info] LRF_ERR001 -> 14
[2025-10-12 18:58:01] [info] DB_WARN002 -> 159
[2025-10-12 18:58:01] [info] DB_WARN003 -> 161
[2025-10-12 18:58:01] [info] DRC_ERR9 -> 89
[2025-10-12 18:58:01] [info] DRC_ERR -> 67
[2025-10-12 18:58:01] [info] DRC_ERR5 -> 408
[2025-10-12 18:58:01] [info] DRC_ERR4 -> 241
[2025-10-12 18:58:01] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Rule Violation Summary
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:58:01] [info] |-----|-----|-----|
[2025-10-12 18:58:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:58:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:58:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:58:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:58:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:58:01] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 18:58:01] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:58:01] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:58:01] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:58:01] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:58:01] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:58:01] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:58:01] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:58:01] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:58:01] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:58:01] [info] |
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Performance Metrics
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] | Stage          | Time Taken |
[2025-10-12 18:58:01] [info] |-----|-----|
[2025-10-12 18:58:01] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:58:01] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 18:58:01] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Summary
```

```
[2025-10-12 18:58:01] [info] -----
[2025-10-12 18:58:01] [info] Total Errors:      1120
[2025-10-12 18:58:01] [info] Total Warnings:    321
[2025-10-12 18:58:01] [info] DRC Status:        FAILED
```

lvr_summary_52.rpt

```
[2025-10-12 11:30:41] [info] =====
[2025-10-12 11:30:41] [info]                      Design Rule Check Report
[2025-10-12 11:30:41] [info] =====

[2025-10-12 11:30:41] [info] Report Generated:    2025-10-12
[2025-10-12 11:30:41] [info] Run Start Time:      11:29:50 AM
[2025-10-12 11:30:41] [info] Run End Time:        11:30:41 AM
[2025-10-12 11:30:41] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 11:30:41] [info] Tool Version:       LVR_2025.1
[2025-10-12 11:30:41] [info] Host Name:          ip-172-31-37-173
[2025-10-12 11:30:41] [info] User Name:          ubuntu
[2025-10-12 11:30:41] [info] Cores Used:         8
[2025-10-12 11:30:41] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info]                      Top Cell Information
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info] Layout Top Cell:    TOP
[2025-10-12 11:30:41] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 11:30:41] [info] Database Format:      GDSII
[2025-10-12 11:30:41] [info] Verification Mode:   Full Check

[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info]                      Type of Errors | Count
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info] DB_WARN -> 1
[2025-10-12 11:30:41] [info] LRF_ERR001 -> 14
[2025-10-12 11:30:41] [info] DB_WARN002 -> 159
[2025-10-12 11:30:41] [info] DB_WARN003 -> 161
[2025-10-12 11:30:41] [info] DRC_ERR9 -> 89
[2025-10-12 11:30:41] [info] DRC_ERR -> 67
[2025-10-12 11:30:41] [info] DRC_ERR5 -> 408
[2025-10-12 11:30:41] [info] DRC_ERR4 -> 241
[2025-10-12 11:30:41] [info] DRC_ERR1 -> 301
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info]                      Rule Violation Summary
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:30:41] [info] |-----|-----|-----|
[2025-10-12 11:30:41] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:30:41] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:30:41] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:30:41] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:30:41] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:30:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:30:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:30:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:30:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:30:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:30:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:30:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:30:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:30:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:30:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:30:41] [info]
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info]                      Performance Metrics
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:30:41] [info] |-----|-----|
[2025-10-12 11:30:41] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:30:41] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 11:30:41] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info]                Summary
[2025-10-12 11:30:41] [info] -----
[2025-10-12 11:30:41] [info] Total Errors:    1120
[2025-10-12 11:30:41] [info] Total Warnings:    321
[2025-10-12 11:30:41] [info] DRC Status:      FAILED
```

lvr_summary_520.rpt

```
[2025-10-12 18:58:55] [info] =====
[2025-10-12 18:58:55] [info]                Design Rule Check Report
[2025-10-12 18:58:55] [info] =====

[2025-10-12 18:58:55] [info] Report Generated:    2025-10-12
[2025-10-12 18:58:55] [info] Run Start Time:      06:58:05 PM
[2025-10-12 18:58:55] [info] Run End Time:        06:58:55 PM
[2025-10-12 18:58:55] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 18:58:55] [info] Tool Version:        LVR_2025.1
[2025-10-12 18:58:55] [info] Host Name:           ip-172-31-37-173
[2025-10-12 18:58:55] [info] User Name:           ubuntu
[2025-10-12 18:58:55] [info] Cores Used:          8
[2025-10-12 18:58:55] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info]                Top Cell Information
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] Layout Top Cell:     TOP
[2025-10-12 18:58:55] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 18:58:55] [info] Database Format:       GDSII
[2025-10-12 18:58:55] [info] Verification Mode:    Full Check

[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info]                Type of Errors | Count
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] DB_WARN -> 1
[2025-10-12 18:58:55] [info] LRF_ERR001 -> 14
[2025-10-12 18:58:55] [info] DB_WARN002 -> 159
[2025-10-12 18:58:55] [info] DB_WARN003 -> 161
[2025-10-12 18:58:55] [info] DRC_ERR9 -> 89
[2025-10-12 18:58:55] [info] DRC_ERR -> 67
[2025-10-12 18:58:55] [info] DRC_ERR5 -> 408
[2025-10-12 18:58:55] [info] DRC_ERR4 -> 241
[2025-10-12 18:58:55] [info] DRC_ERR1 -> 301
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info]                Rule Violation Summary
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 18:58:55] [info] |-----|-----|-----|
[2025-10-12 18:58:55] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:58:55] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:58:55] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:58:55] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:58:55] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 18:58:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 18:58:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 18:58:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 18:58:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 18:58:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 18:58:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 18:58:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 18:58:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 18:58:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 18:58:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 18:58:55] [info]
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] Performance Metrics
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] | Stage | Time Taken |
[2025-10-12 18:58:55] [info] |-----|-----|
[2025-10-12 18:58:55] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 18:58:55] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 18:58:55] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] Summary
[2025-10-12 18:58:55] [info] -----
[2025-10-12 18:58:55] [info] Total Errors: 1120
[2025-10-12 18:58:55] [info] Total Warnings: 321
[2025-10-12 18:58:55] [info] DRC Status: FAILED
```

lvr_summary_521.rpt

```
[2025-10-12 18:59:53] [info] =====
[2025-10-12 18:59:53] [info] Design Rule Check Report
[2025-10-12 18:59:53] [info] =====

[2025-10-12 18:59:53] [info] Report Generated: 2025-10-12
[2025-10-12 18:59:53] [info] Run Start Time: 06:59:02 PM
[2025-10-12 18:59:53] [info] Run End Time: 06:59:53 PM
[2025-10-12 18:59:53] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 18:59:53] [info] Tool Version: LVR_2025.1
[2025-10-12 18:59:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 18:59:53] [info] User Name: ubuntu
[2025-10-12 18:59:53] [info] Cores Used: 8
[2025-10-12 18:59:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Top Cell Information
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Layout Top Cell: TOP
[2025-10-12 18:59:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 18:59:53] [info] Database Format: GDSII
[2025-10-12 18:59:53] [info] Verification Mode: Full Check

[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Type of Errors | Count
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] DB_WARN -> 1
[2025-10-12 18:59:53] [info] LRF_ERR001 -> 14
[2025-10-12 18:59:53] [info] DB_WARN002 -> 159
[2025-10-12 18:59:53] [info] DB_WARN003 -> 161
```

```
[2025-10-12 18:59:53] [info] DRC_ERR9 -> 89
[2025-10-12 18:59:53] [info] DRC_ERR -> 67
[2025-10-12 18:59:53] [info] DRC_ERR5 -> 408
[2025-10-12 18:59:53] [info] DRC_ERR4 -> 241
[2025-10-12 18:59:53] [info] DRC_ERR1 -> 301
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Rule Violation Summary
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 18:59:53] [info] |-----|-----|-----|
[2025-10-12 18:59:53] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 18:59:53] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 18:59:53] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 18:59:53] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 18:59:53] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 18:59:53] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 18:59:53] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 18:59:53] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 18:59:53] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 18:59:53] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 18:59:53] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 18:59:53] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 18:59:53] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 18:59:53] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 18:59:53] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 18:59:53] [info] |
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Performance Metrics
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] | Stage          | Time Taken |
[2025-10-12 18:59:53] [info] |-----|-----|
[2025-10-12 18:59:53] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 18:59:53] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 18:59:53] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 18:59:53] [info] |
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Summary
[2025-10-12 18:59:53] [info] -----
[2025-10-12 18:59:53] [info] Total Errors: 1120
[2025-10-12 18:59:53] [info] Total Warnings: 321
[2025-10-12 18:59:53] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:00:52] [info] =====
[2025-10-12 19:00:52] [info] Design Rule Check Report
[2025-10-12 19:00:52] [info] =====

[2025-10-12 19:00:52] [info] Report Generated: 2025-10-12
[2025-10-12 19:00:52] [info] Run Start Time: 07:00:00 PM
[2025-10-12 19:00:52] [info] Run End Time: 07:00:52 PM
[2025-10-12 19:00:52] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:00:52] [info] Tool Version: LVR_2025.1
[2025-10-12 19:00:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:00:52] [info] User Name: ubuntu
[2025-10-12 19:00:52] [info] Cores Used: 8
[2025-10-12 19:00:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Top Cell Information
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Layout Top Cell:      TOP
[2025-10-12 19:00:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:00:52] [info] Database Format:      GDSII
[2025-10-12 19:00:52] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Type of Errors | Count
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] DB_WARN -> 1
[2025-10-12 19:00:52] [info] LRF_ERR001 -> 14
[2025-10-12 19:00:52] [info] DB_WARN002 -> 159
[2025-10-12 19:00:52] [info] DB_WARN003 -> 161
[2025-10-12 19:00:52] [info] DRC_ERR9 -> 89
[2025-10-12 19:00:52] [info] DRC_ERR -> 67
[2025-10-12 19:00:52] [info] DRC_ERR5 -> 408
[2025-10-12 19:00:52] [info] DRC_ERR4 -> 241
[2025-10-12 19:00:52] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Rule Violation Summary
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:00:52] [info] |-----|-----|-----|
[2025-10-12 19:00:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:00:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:00:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:00:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:00:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:00:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:00:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:00:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:00:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:00:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:00:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:00:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:00:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:00:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:00:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:00:52] [info] |
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Performance Metrics
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] | Stage          | Time Taken |
[2025-10-12 19:00:52] [info] |-----|-----|
[2025-10-12 19:00:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:00:52] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:00:52] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Summary
```

```
[2025-10-12 19:00:52] [info] -----
[2025-10-12 19:00:52] [info] Total Errors:      1120
[2025-10-12 19:00:52] [info] Total Warnings:    321
[2025-10-12 19:00:52] [info] DRC Status:        FAILED
```


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```
[2025-10-12 19:01:49] [info] =====
[2025-10-12 19:01:49] [info] Design Rule Check Report
[2025-10-12 19:01:49] [info] =====

[2025-10-12 19:01:49] [info] Report Generated: 2025-10-12
[2025-10-12 19:01:49] [info] Run Start Time: 07:00:57 PM
[2025-10-12 19:01:49] [info] Run End Time: 07:01:49 PM
[2025-10-12 19:01:49] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:01:49] [info] Tool Version: LVR_2025.1
[2025-10-12 19:01:49] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:01:49] [info] User Name: ubuntu
[2025-10-12 19:01:49] [info] Cores Used: 8
[2025-10-12 19:01:49] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] Top Cell Information
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] Layout Top Cell: TOP
[2025-10-12 19:01:49] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:01:49] [info] Database Format: GDSII
[2025-10-12 19:01:49] [info] Verification Mode: Full Check

[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] Type of Errors | Count
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] DB_WARN -> 1
[2025-10-12 19:01:49] [info] LRF_ERR001 -> 14
[2025-10-12 19:01:49] [info] DB_WARN002 -> 159
[2025-10-12 19:01:49] [info] DB_WARN003 -> 161
[2025-10-12 19:01:49] [info] DRC_ERR9 -> 89
[2025-10-12 19:01:49] [info] DRC_ERR -> 67
[2025-10-12 19:01:49] [info] DRC_ERR5 -> 408
[2025-10-12 19:01:49] [info] DRC_ERR4 -> 241
[2025-10-12 19:01:49] [info] DRC_ERR1 -> 301
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] Rule Violation Summary
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:01:49] [info] |-----|-----|-----|
[2025-10-12 19:01:49] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:01:49] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:01:49] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:01:49] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:01:49] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:01:49] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:01:49] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:01:49] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:01:49] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:01:49] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:01:49] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:01:49] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:01:49] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:01:49] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:01:49] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:01:49] [info]
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] Performance Metrics
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:01:49] [info] |-----|-----|
[2025-10-12 19:01:49] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:01:49] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 19:01:49] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info]                Summary
[2025-10-12 19:01:49] [info] -----
[2025-10-12 19:01:49] [info] Total Errors:    1120
[2025-10-12 19:01:49] [info] Total Warnings:    321
[2025-10-12 19:01:49] [info] DRC Status:      FAILED
```

lvr_summary_524.rpt

```
[2025-10-12 19:02:46] [info] =====
[2025-10-12 19:02:46] [info]                Design Rule Check Report
[2025-10-12 19:02:46] [info] =====

[2025-10-12 19:02:46] [info] Report Generated:    2025-10-12
[2025-10-12 19:02:46] [info] Run Start Time:      07:01:53 PM
[2025-10-12 19:02:46] [info] Run End Time:        07:02:46 PM
[2025-10-12 19:02:46] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 19:02:46] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:02:46] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:02:46] [info] User Name:           ubuntu
[2025-10-12 19:02:46] [info] Cores Used:          8
[2025-10-12 19:02:46] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info]                Top Cell Information
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] Layout Top Cell:     TOP
[2025-10-12 19:02:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:02:46] [info] Database Format:       GDSII
[2025-10-12 19:02:46] [info] Verification Mode:    Full Check

[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info]                Type of Errors | Count
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] DB_WARN -> 1
[2025-10-12 19:02:46] [info] LRF_ERR001 -> 14
[2025-10-12 19:02:46] [info] DB_WARN002 -> 159
[2025-10-12 19:02:46] [info] DB_WARN003 -> 161
[2025-10-12 19:02:46] [info] DRC_ERR9 -> 89
[2025-10-12 19:02:46] [info] DRC_ERR -> 67
[2025-10-12 19:02:46] [info] DRC_ERR5 -> 408
[2025-10-12 19:02:46] [info] DRC_ERR4 -> 241
[2025-10-12 19:02:46] [info] DRC_ERR1 -> 301
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info]                Rule Violation Summary
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:02:46] [info] |-----|-----|-----|
[2025-10-12 19:02:46] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:02:46] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:02:46] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:02:46] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:02:46] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:02:46] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:02:46] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:02:46] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:02:46] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:02:46] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:02:46] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:02:46] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:02:46] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:02:46] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:02:46] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:02:46] [info]
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] Performance Metrics
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] | Stage | Time Taken |
[2025-10-12 19:02:46] [info] |-----|-----|
[2025-10-12 19:02:46] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:02:46] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:02:46] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] Summary
[2025-10-12 19:02:46] [info] -----
[2025-10-12 19:02:46] [info] Total Errors: 1120
[2025-10-12 19:02:46] [info] Total Warnings: 321
[2025-10-12 19:02:46] [info] DRC Status: FAILED
```

lvr_summary_525.rpt

```
[2025-10-12 19:03:42] [info] =====
[2025-10-12 19:03:42] [info] Design Rule Check Report
[2025-10-12 19:03:42] [info] =====

[2025-10-12 19:03:42] [info] Report Generated: 2025-10-12
[2025-10-12 19:03:42] [info] Run Start Time: 07:02:51 PM
[2025-10-12 19:03:42] [info] Run End Time: 07:03:42 PM
[2025-10-12 19:03:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:03:42] [info] Tool Version: LVR_2025.1
[2025-10-12 19:03:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:03:42] [info] User Name: ubuntu
[2025-10-12 19:03:42] [info] Cores Used: 8
[2025-10-12 19:03:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Top Cell Information
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Layout Top Cell: TOP
[2025-10-12 19:03:42] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:03:42] [info] Database Format: GDSII
[2025-10-12 19:03:42] [info] Verification Mode: Full Check

[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Type of Errors | Count
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] DB_WARN -> 1
[2025-10-12 19:03:42] [info] LRF_ERR001 -> 14
[2025-10-12 19:03:42] [info] DB_WARN002 -> 159
[2025-10-12 19:03:42] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:03:42] [info] DRC_ERR9 -> 89
[2025-10-12 19:03:42] [info] DRC_ERR -> 67
[2025-10-12 19:03:42] [info] DRC_ERR5 -> 408
[2025-10-12 19:03:42] [info] DRC_ERR4 -> 241
[2025-10-12 19:03:42] [info] DRC_ERR1 -> 301
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Rule Violation Summary
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:03:42] [info] |-----|-----|-----|
[2025-10-12 19:03:42] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:03:42] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:03:42] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:03:42] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:03:42] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:03:42] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 19:03:42] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:03:42] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:03:42] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:03:42] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:03:42] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:03:42] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:03:42] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:03:42] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:03:42] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Performance Metrics
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] | Stage          | Time Taken |
[2025-10-12 19:03:42] [info] |-----|-----|
[2025-10-12 19:03:42] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:03:42] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:03:42] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Summary
[2025-10-12 19:03:42] [info] -----
[2025-10-12 19:03:42] [info] Total Errors: 1120
[2025-10-12 19:03:42] [info] Total Warnings: 321
[2025-10-12 19:03:42] [info] DRC Status: FAILED
```

lvr_summary_526.rpt

```
[2025-10-12 19:04:39] [info] =====
[2025-10-12 19:04:39] [info] Design Rule Check Report
[2025-10-12 19:04:39] [info] =====

[2025-10-12 19:04:39] [info] Report Generated: 2025-10-12
[2025-10-12 19:04:39] [info] Run Start Time: 07:03:47 PM
[2025-10-12 19:04:39] [info] Run End Time: 07:04:39 PM
[2025-10-12 19:04:39] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:04:39] [info] Tool Version: LVR_2025.1
[2025-10-12 19:04:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:04:39] [info] User Name: ubuntu
[2025-10-12 19:04:39] [info] Cores Used: 8
[2025-10-12 19:04:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Top Cell Information
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Layout Top Cell:      TOP
[2025-10-12 19:04:39] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:04:39] [info] Database Format:      GDSII
[2025-10-12 19:04:39] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Type of Errors | Count
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] DB_WARN -> 1
[2025-10-12 19:04:39] [info] LRF_ERR001 -> 14
[2025-10-12 19:04:39] [info] DB_WARN002 -> 159
[2025-10-12 19:04:39] [info] DB_WARN003 -> 161
[2025-10-12 19:04:39] [info] DRC_ERR9 -> 89
[2025-10-12 19:04:39] [info] DRC_ERR -> 67
[2025-10-12 19:04:39] [info] DRC_ERR5 -> 408
[2025-10-12 19:04:39] [info] DRC_ERR4 -> 241
[2025-10-12 19:04:39] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Rule Violation Summary
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:04:39] [info] |-----|-----|-----|
[2025-10-12 19:04:39] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:04:39] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:04:39] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:04:39] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:04:39] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:04:39] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:04:39] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:04:39] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:04:39] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:04:39] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:04:39] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:04:39] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:04:39] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:04:39] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:04:39] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:04:39] [info] |
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Performance Metrics
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] | Stage          | Time Taken |
[2025-10-12 19:04:39] [info] |-----|-----|
[2025-10-12 19:04:39] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:04:39] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 19:04:39] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Summary
```

```
[2025-10-12 19:04:39] [info] -----
[2025-10-12 19:04:39] [info] Total Errors:      1120
[2025-10-12 19:04:39] [info] Total Warnings:    321
[2025-10-12 19:04:39] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:05:37] [info] =====
[2025-10-12 19:05:37] [info]                      Design Rule Check Report
[2025-10-12 19:05:37] [info] =====

[2025-10-12 19:05:37] [info] Report Generated:      2025-10-12
[2025-10-12 19:05:37] [info] Run Start Time:       07:04:44 PM
[2025-10-12 19:05:37] [info] Run End Time:         07:05:37 PM
[2025-10-12 19:05:37] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 19:05:37] [info] Tool Version:         LVR_2025.1
[2025-10-12 19:05:37] [info] Host Name:            ip-172-31-37-173
[2025-10-12 19:05:37] [info] User Name:            ubuntu
[2025-10-12 19:05:37] [info] Cores Used:           8
[2025-10-12 19:05:37] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info]                      Top Cell Information
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info] Layout Top Cell:      TOP
[2025-10-12 19:05:37] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 19:05:37] [info] Database Format:        GDSII
[2025-10-12 19:05:37] [info] Verification Mode:     Full Check

[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info]                      Type of Errors | Count
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info] DB_WARN -> 1
[2025-10-12 19:05:37] [info] LRF_ERR001 -> 14
[2025-10-12 19:05:37] [info] DB_WARN002 -> 159
[2025-10-12 19:05:37] [info] DB_WARN003 -> 161
[2025-10-12 19:05:37] [info] DRC_ERR9 -> 89
[2025-10-12 19:05:37] [info] DRC_ERR -> 67
[2025-10-12 19:05:37] [info] DRC_ERR5 -> 408
[2025-10-12 19:05:37] [info] DRC_ERR4 -> 241
[2025-10-12 19:05:37] [info] DRC_ERR1 -> 301
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info]                      Rule Violation Summary
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:05:37] [info] |-----|-----|-----|
[2025-10-12 19:05:37] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:05:37] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:05:37] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:05:37] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:05:37] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:05:37] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:05:37] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:05:37] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:05:37] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:05:37] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:05:37] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:05:37] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:05:37] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:05:37] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:05:37] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:05:37] [info]
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info]                      Performance Metrics
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:05:37] [info] |-----|-----|
[2025-10-12 19:05:37] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:05:37] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:05:37] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info] Summary
[2025-10-12 19:05:37] [info] -----
[2025-10-12 19:05:37] [info] Total Errors: 1120
[2025-10-12 19:05:37] [info] Total Warnings: 321
[2025-10-12 19:05:37] [info] DRC Status: FAILED
```

lvr_summary_528.rpt

```
[2025-10-12 19:06:34] [info] =====
[2025-10-12 19:06:34] [info] Design Rule Check Report
[2025-10-12 19:06:34] [info] =====

[2025-10-12 19:06:34] [info] Report Generated: 2025-10-12
[2025-10-12 19:06:34] [info] Run Start Time: 07:05:41 PM
[2025-10-12 19:06:34] [info] Run End Time: 07:06:34 PM
[2025-10-12 19:06:34] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:06:34] [info] Tool Version: LVR_2025.1
[2025-10-12 19:06:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:06:34] [info] User Name: ubuntu
[2025-10-12 19:06:34] [info] Cores Used: 8
[2025-10-12 19:06:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Top Cell Information
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Layout Top Cell: TOP
[2025-10-12 19:06:34] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:06:34] [info] Database Format: GDSII
[2025-10-12 19:06:34] [info] Verification Mode: Full Check

[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Type of Errors | Count
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] DB_WARN -> 1
[2025-10-12 19:06:34] [info] LRF_ERR001 -> 14
[2025-10-12 19:06:34] [info] DB_WARN002 -> 159
[2025-10-12 19:06:34] [info] DB_WARN003 -> 161
[2025-10-12 19:06:34] [info] DRC_ERR9 -> 89
[2025-10-12 19:06:34] [info] DRC_ERR -> 67
[2025-10-12 19:06:34] [info] DRC_ERR5 -> 408
[2025-10-12 19:06:34] [info] DRC_ERR4 -> 241
[2025-10-12 19:06:34] [info] DRC_ERR1 -> 301
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Rule Violation Summary
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:06:34] [info] |-----|-----|-----|
[2025-10-12 19:06:34] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:06:34] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:06:34] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:06:34] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:06:34] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 19:06:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:06:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:06:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:06:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:06:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:06:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:06:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:06:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:06:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:06:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:06:34] [info]
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Performance Metrics
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] | Stage | Time Taken |
[2025-10-12 19:06:34] [info] |-----|-----|
[2025-10-12 19:06:34] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:06:34] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:06:34] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Summary
[2025-10-12 19:06:34] [info] -----
[2025-10-12 19:06:34] [info] Total Errors: 1120
[2025-10-12 19:06:34] [info] Total Warnings: 321
[2025-10-12 19:06:34] [info] DRC Status: FAILED
```

lvr_summary_529.rpt

```
[2025-10-12 19:07:32] [info] =====
[2025-10-12 19:07:32] [info] Design Rule Check Report
[2025-10-12 19:07:32] [info] =====

[2025-10-12 19:07:32] [info] Report Generated: 2025-10-12
[2025-10-12 19:07:32] [info] Run Start Time: 07:06:40 PM
[2025-10-12 19:07:32] [info] Run End Time: 07:07:32 PM
[2025-10-12 19:07:32] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:07:32] [info] Tool Version: LVR_2025.1
[2025-10-12 19:07:32] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:07:32] [info] User Name: ubuntu
[2025-10-12 19:07:32] [info] Cores Used: 8
[2025-10-12 19:07:32] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Top Cell Information
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Layout Top Cell: TOP
[2025-10-12 19:07:32] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:07:32] [info] Database Format: GDSII
[2025-10-12 19:07:32] [info] Verification Mode: Full Check

[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Type of Errors | Count
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] DB_WARN -> 1
[2025-10-12 19:07:32] [info] LRF_ERR001 -> 14
[2025-10-12 19:07:32] [info] DB_WARN002 -> 159
[2025-10-12 19:07:32] [info] DB_WARN003 -> 161
```



```

[2025-10-12 19:07:32] [info] DRC_ERR9 -> 89
[2025-10-12 19:07:32] [info] DRC_ERR -> 67
[2025-10-12 19:07:32] [info] DRC_ERR5 -> 408
[2025-10-12 19:07:32] [info] DRC_ERR4 -> 241
[2025-10-12 19:07:32] [info] DRC_ERR1 -> 301
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Rule Violation Summary
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:07:32] [info] |-----|-----|-----|
[2025-10-12 19:07:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:07:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:07:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:07:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:07:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:07:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:07:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:07:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:07:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:07:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:07:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:07:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:07:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:07:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:07:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Performance Metrics
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] | Stage | Time Taken |
[2025-10-12 19:07:32] [info] |-----|-----|
[2025-10-12 19:07:32] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:07:32] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:07:32] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Summary
[2025-10-12 19:07:32] [info] -----
[2025-10-12 19:07:32] [info] Total Errors: 1120
[2025-10-12 19:07:32] [info] Total Warnings: 321
[2025-10-12 19:07:32] [info] DRC Status: FAILED

```

lvr_summary_53.rpt

```

[2025-10-12 11:31:37] [info] =====
[2025-10-12 11:31:37] [info] Design Rule Check Report
[2025-10-12 11:31:37] [info] =====

[2025-10-12 11:31:37] [info] Report Generated: 2025-10-12
[2025-10-12 11:31:37] [info] Run Start Time: 11:30:46 AM
[2025-10-12 11:31:37] [info] Run End Time: 11:31:37 AM
[2025-10-12 11:31:37] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:31:37] [info] Tool Version: LVR_2025.1
[2025-10-12 11:31:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:31:37] [info] User Name: ubuntu
[2025-10-12 11:31:37] [info] Cores Used: 8
[2025-10-12 11:31:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Top Cell Information
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Layout Top Cell:      TOP
[2025-10-12 11:31:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:31:37] [info] Database Format:      GDSII
[2025-10-12 11:31:37] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Type of Errors | Count
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] DB_WARN -> 1
[2025-10-12 11:31:37] [info] LRF_ERR001 -> 14
[2025-10-12 11:31:37] [info] DB_WARN002 -> 159
[2025-10-12 11:31:37] [info] DB_WARN003 -> 161
[2025-10-12 11:31:37] [info] DRC_ERR9 -> 89
[2025-10-12 11:31:37] [info] DRC_ERR -> 67
[2025-10-12 11:31:37] [info] DRC_ERR5 -> 408
[2025-10-12 11:31:37] [info] DRC_ERR4 -> 241
[2025-10-12 11:31:37] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Rule Violation Summary
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:31:37] [info] |-----|-----|-----|
[2025-10-12 11:31:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:31:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:31:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:31:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:31:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:31:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:31:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:31:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:31:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:31:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:31:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:31:37] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:31:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:31:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:31:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Performance Metrics
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] | Stage          | Time Taken |
[2025-10-12 11:31:37] [info] |-----|-----|
[2025-10-12 11:31:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:31:37] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 11:31:37] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Summary
[2025-10-12 11:31:37] [info] -----
[2025-10-12 11:31:37] [info] Total Errors:      1120
[2025-10-12 11:31:37] [info] Total Warnings:    321
[2025-10-12 11:31:37] [info] DRC Status:        FAILED
```

lvr_summary_530.rpt

```
[2025-10-12 19:08:27] [info] =====
[2025-10-12 19:08:27] [info]                Design Rule Check Report
[2025-10-12 19:08:27] [info] =====

[2025-10-12 19:08:27] [info] Report Generated:    2025-10-12
[2025-10-12 19:08:27] [info] Run Start Time:     07:07:36 PM
[2025-10-12 19:08:27] [info] Run End Time:       07:08:27 PM
[2025-10-12 19:08:27] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 19:08:27] [info] Tool Version:       LVR_2025.1
[2025-10-12 19:08:27] [info] Host Name:          ip-172-31-37-173
[2025-10-12 19:08:27] [info] User Name:          ubuntu
[2025-10-12 19:08:27] [info] Cores Used:         8
[2025-10-12 19:08:27] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info]                Top Cell Information
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info] Layout Top Cell:    TOP
[2025-10-12 19:08:27] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 19:08:27] [info] Database Format:      GDSII
[2025-10-12 19:08:27] [info] Verification Mode:   Full Check

[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info]                Type of Errors | Count
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info] DB_WARN -> 1
[2025-10-12 19:08:27] [info] LRF_ERR001 -> 14
[2025-10-12 19:08:27] [info] DB_WARN002 -> 159
[2025-10-12 19:08:27] [info] DB_WARN003 -> 161
[2025-10-12 19:08:27] [info] DRC_ERR9 -> 89
[2025-10-12 19:08:27] [info] DRC_ERR -> 67
[2025-10-12 19:08:27] [info] DRC_ERR5 -> 408
[2025-10-12 19:08:27] [info] DRC_ERR4 -> 241
[2025-10-12 19:08:27] [info] DRC_ERR1 -> 301
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info]                Rule Violation Summary
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:08:27] [info] |-----|-----|-----|
[2025-10-12 19:08:27] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:08:27] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:08:27] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:08:27] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:08:27] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:08:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:08:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:08:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:08:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:08:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:08:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:08:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:08:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:08:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:08:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:08:27] [info]
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info]                Performance Metrics
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:08:27] [info] |-----|-----|
[2025-10-12 19:08:27] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:08:27] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 19:08:27] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info] Summary
[2025-10-12 19:08:27] [info] -----
[2025-10-12 19:08:27] [info] Total Errors: 1120
[2025-10-12 19:08:27] [info] Total Warnings: 321
[2025-10-12 19:08:27] [info] DRC Status: FAILED
```

lvr_summary_531.rpt

```
[2025-10-12 19:09:24] [info] =====
[2025-10-12 19:09:24] [info] Design Rule Check Report
[2025-10-12 19:09:24] [info] =====

[2025-10-12 19:09:24] [info] Report Generated: 2025-10-12
[2025-10-12 19:09:24] [info] Run Start Time: 07:08:33 PM
[2025-10-12 19:09:24] [info] Run End Time: 07:09:24 PM
[2025-10-12 19:09:24] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:09:24] [info] Tool Version: LVR_2025.1
[2025-10-12 19:09:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:09:24] [info] User Name: ubuntu
[2025-10-12 19:09:24] [info] Cores Used: 8
[2025-10-12 19:09:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Top Cell Information
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Layout Top Cell: TOP
[2025-10-12 19:09:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:09:24] [info] Database Format: GDSII
[2025-10-12 19:09:24] [info] Verification Mode: Full Check

[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Type of Errors | Count
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] DB_WARN -> 1
[2025-10-12 19:09:24] [info] LRF_ERR001 -> 14
[2025-10-12 19:09:24] [info] DB_WARN002 -> 159
[2025-10-12 19:09:24] [info] DB_WARN003 -> 161
[2025-10-12 19:09:24] [info] DRC_ERR9 -> 89
[2025-10-12 19:09:24] [info] DRC_ERR -> 67
[2025-10-12 19:09:24] [info] DRC_ERR5 -> 408
[2025-10-12 19:09:24] [info] DRC_ERR4 -> 241
[2025-10-12 19:09:24] [info] DRC_ERR1 -> 301
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Rule Violation Summary
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:09:24] [info] |-----|-----|-----|
[2025-10-12 19:09:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:09:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:09:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:09:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:09:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 19:09:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:09:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:09:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:09:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:09:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:09:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:09:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:09:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:09:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:09:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:09:24] [info]
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Performance Metrics
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] | Stage | Time Taken |
[2025-10-12 19:09:24] [info] |-----|-----|
[2025-10-12 19:09:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:09:24] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:09:24] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Summary
[2025-10-12 19:09:24] [info] -----
[2025-10-12 19:09:24] [info] Total Errors: 1120
[2025-10-12 19:09:24] [info] Total Warnings: 321
[2025-10-12 19:09:24] [info] DRC Status: FAILED
```

lvr_summary_532.rpt

```
[2025-10-12 19:10:21] [info] =====
[2025-10-12 19:10:21] [info] Design Rule Check Report
[2025-10-12 19:10:21] [info] =====

[2025-10-12 19:10:21] [info] Report Generated: 2025-10-12
[2025-10-12 19:10:21] [info] Run Start Time: 07:09:29 PM
[2025-10-12 19:10:21] [info] Run End Time: 07:10:21 PM
[2025-10-12 19:10:21] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:10:21] [info] Tool Version: LVR_2025.1
[2025-10-12 19:10:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:10:21] [info] User Name: ubuntu
[2025-10-12 19:10:21] [info] Cores Used: 8
[2025-10-12 19:10:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Top Cell Information
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Layout Top Cell: TOP
[2025-10-12 19:10:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:10:21] [info] Database Format: GDSII
[2025-10-12 19:10:21] [info] Verification Mode: Full Check

[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Type of Errors | Count
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] DB_WARN -> 1
[2025-10-12 19:10:21] [info] LRF_ERR001 -> 14
[2025-10-12 19:10:21] [info] DB_WARN002 -> 159
[2025-10-12 19:10:21] [info] DB_WARN003 -> 161
```

```

[2025-10-12 19:10:21] [info] DRC_ERR9 -> 89
[2025-10-12 19:10:21] [info] DRC_ERR -> 67
[2025-10-12 19:10:21] [info] DRC_ERR5 -> 408
[2025-10-12 19:10:21] [info] DRC_ERR4 -> 241
[2025-10-12 19:10:21] [info] DRC_ERR1 -> 301
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Rule Violation Summary
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:10:21] [info] |-----|-----|-----|
[2025-10-12 19:10:21] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:10:21] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:10:21] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:10:21] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:10:21] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:10:21] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 19:10:21] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:10:21] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:10:21] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:10:21] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:10:21] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:10:21] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:10:21] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:10:21] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:10:21] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Performance Metrics
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] | Stage          | Time Taken |
[2025-10-12 19:10:21] [info] |-----|-----|
[2025-10-12 19:10:21] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:10:21] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:10:21] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Summary
[2025-10-12 19:10:21] [info] -----
[2025-10-12 19:10:21] [info] Total Errors: 1120
[2025-10-12 19:10:21] [info] Total Warnings: 321
[2025-10-12 19:10:21] [info] DRC Status: FAILED

```

lvr_summary_533.rpt

```

[2025-10-12 19:11:18] [info] =====
[2025-10-12 19:11:18] [info] Design Rule Check Report
[2025-10-12 19:11:18] [info] =====

[2025-10-12 19:11:18] [info] Report Generated: 2025-10-12
[2025-10-12 19:11:18] [info] Run Start Time: 07:10:27 PM
[2025-10-12 19:11:18] [info] Run End Time: 07:11:18 PM
[2025-10-12 19:11:18] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:11:18] [info] Tool Version: LVR_2025.1
[2025-10-12 19:11:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:11:18] [info] User Name: ubuntu
[2025-10-12 19:11:18] [info] Cores Used: 8
[2025-10-12 19:11:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Top Cell Information
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Layout Top Cell:      TOP
[2025-10-12 19:11:18] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:11:18] [info] Database Format:      GDSII
[2025-10-12 19:11:18] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Type of Errors | Count
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] DB_WARN -> 1
[2025-10-12 19:11:18] [info] LRF_ERR001 -> 14
[2025-10-12 19:11:18] [info] DB_WARN002 -> 159
[2025-10-12 19:11:18] [info] DB_WARN003 -> 161
[2025-10-12 19:11:18] [info] DRC_ERR9 -> 89
[2025-10-12 19:11:18] [info] DRC_ERR -> 67
[2025-10-12 19:11:18] [info] DRC_ERR5 -> 408
[2025-10-12 19:11:18] [info] DRC_ERR4 -> 241
[2025-10-12 19:11:18] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Rule Violation Summary
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:11:18] [info] |-----|-----|-----|
[2025-10-12 19:11:18] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:11:18] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:11:18] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:11:18] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:11:18] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:11:18] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:11:18] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:11:18] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:11:18] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:11:18] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:11:18] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:11:18] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:11:18] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:11:18] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:11:18] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:11:18] [info] |
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Performance Metrics
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] | Stage          | Time Taken |
[2025-10-12 19:11:18] [info] |-----|-----|
[2025-10-12 19:11:18] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:11:18] [info] | Rule Checking   | 0 minutes 40 seconds |
[2025-10-12 19:11:18] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Summary
```

```
[2025-10-12 19:11:18] [info] -----
[2025-10-12 19:11:18] [info] Total Errors:      1120
[2025-10-12 19:11:18] [info] Total Warnings:    321
[2025-10-12 19:11:18] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:12:16] [info] =====
[2025-10-12 19:12:16] [info]                      Design Rule Check Report
[2025-10-12 19:12:16] [info] =====

[2025-10-12 19:12:16] [info] Report Generated:      2025-10-12
[2025-10-12 19:12:16] [info] Run Start Time:       07:11:23 PM
[2025-10-12 19:12:16] [info] Run End Time:         07:12:16 PM
[2025-10-12 19:12:16] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 19:12:16] [info] Tool Version:          LVR_2025.1
[2025-10-12 19:12:16] [info] Host Name:             ip-172-31-37-173
[2025-10-12 19:12:16] [info] User Name:             ubuntu
[2025-10-12 19:12:16] [info] Cores Used:            8
[2025-10-12 19:12:16] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info]                      Top Cell Information
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info] Layout Top Cell:      TOP
[2025-10-12 19:12:16] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 19:12:16] [info] Database Format:       GDSII
[2025-10-12 19:12:16] [info] Verification Mode:     Full Check

[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info]                      Type of Errors | Count
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info] DB_WARN -> 1
[2025-10-12 19:12:16] [info] LRF_ERR001 -> 14
[2025-10-12 19:12:16] [info] DB_WARN002 -> 159
[2025-10-12 19:12:16] [info] DB_WARN003 -> 161
[2025-10-12 19:12:16] [info] DRC_ERR9 -> 89
[2025-10-12 19:12:16] [info] DRC_ERR -> 67
[2025-10-12 19:12:16] [info] DRC_ERR5 -> 408
[2025-10-12 19:12:16] [info] DRC_ERR4 -> 241
[2025-10-12 19:12:16] [info] DRC_ERR1 -> 301
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info]                      Rule Violation Summary
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:12:16] [info] |-----|-----|-----|
[2025-10-12 19:12:16] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:12:16] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:12:16] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:12:16] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:12:16] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:12:16] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:12:16] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:12:16] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:12:16] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:12:16] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:12:16] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:12:16] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:12:16] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:12:16] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:12:16] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:12:16] [info]
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info]                      Performance Metrics
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info] | Stage | Time Taken |
```



```
[2025-10-12 19:12:16] [info] |-----|-----|
[2025-10-12 19:12:16] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:12:16] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:12:16] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info] Summary
[2025-10-12 19:12:16] [info] -----
[2025-10-12 19:12:16] [info] Total Errors: 1120
[2025-10-12 19:12:16] [info] Total Warnings: 321
[2025-10-12 19:12:16] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:13:12] [info] =====
[2025-10-12 19:13:12] [info] Design Rule Check Report
[2025-10-12 19:13:12] [info] =====

[2025-10-12 19:13:12] [info] Report Generated: 2025-10-12
[2025-10-12 19:13:12] [info] Run Start Time: 07:12:21 PM
[2025-10-12 19:13:12] [info] Run End Time: 07:13:12 PM
[2025-10-12 19:13:12] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:13:12] [info] Tool Version: LVR_2025.1
[2025-10-12 19:13:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:13:12] [info] User Name: ubuntu
[2025-10-12 19:13:12] [info] Cores Used: 8
[2025-10-12 19:13:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Top Cell Information
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Layout Top Cell: TOP
[2025-10-12 19:13:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:13:12] [info] Database Format: GDSII
[2025-10-12 19:13:12] [info] Verification Mode: Full Check

[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Type of Errors | Count
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] DB_WARN -> 1
[2025-10-12 19:13:12] [info] LRF_ERR001 -> 14
[2025-10-12 19:13:12] [info] DB_WARN002 -> 159
[2025-10-12 19:13:12] [info] DB_WARN003 -> 161
[2025-10-12 19:13:12] [info] DRC_ERR9 -> 89
[2025-10-12 19:13:12] [info] DRC_ERR -> 67
[2025-10-12 19:13:12] [info] DRC_ERR5 -> 408
[2025-10-12 19:13:12] [info] DRC_ERR4 -> 241
[2025-10-12 19:13:12] [info] DRC_ERR1 -> 301
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Rule Violation Summary
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:13:12] [info] |-----|-----|-----|
[2025-10-12 19:13:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:13:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:13:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:13:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:13:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 19:13:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:13:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:13:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:13:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:13:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:13:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:13:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:13:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:13:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:13:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:13:12] [info]
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Performance Metrics
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] | Stage | Time Taken |
[2025-10-12 19:13:12] [info] |-----|-----|
[2025-10-12 19:13:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:13:12] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 19:13:12] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Summary
[2025-10-12 19:13:12] [info] -----
[2025-10-12 19:13:12] [info] Total Errors: 1120
[2025-10-12 19:13:12] [info] Total Warnings: 321
[2025-10-12 19:13:12] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:14:09] [info] =====
[2025-10-12 19:14:09] [info] Design Rule Check Report
[2025-10-12 19:14:09] [info] =====

[2025-10-12 19:14:09] [info] Report Generated: 2025-10-12
[2025-10-12 19:14:09] [info] Run Start Time: 07:13:17 PM
[2025-10-12 19:14:09] [info] Run End Time: 07:14:09 PM
[2025-10-12 19:14:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:14:09] [info] Tool Version: LVR_2025.1
[2025-10-12 19:14:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:14:09] [info] User Name: ubuntu
[2025-10-12 19:14:09] [info] Cores Used: 8
[2025-10-12 19:14:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Top Cell Information
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Layout Top Cell: TOP
[2025-10-12 19:14:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:14:09] [info] Database Format: GDSII
[2025-10-12 19:14:09] [info] Verification Mode: Full Check

[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Type of Errors | Count
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] DB_WARN -> 1
[2025-10-12 19:14:09] [info] LRF_ERR001 -> 14
[2025-10-12 19:14:09] [info] DB_WARN002 -> 159
[2025-10-12 19:14:09] [info] DB_WARN003 -> 161
```

```

[2025-10-12 19:14:09] [info] DRC_ERR9 -> 89
[2025-10-12 19:14:09] [info] DRC_ERR -> 67
[2025-10-12 19:14:09] [info] DRC_ERR5 -> 408
[2025-10-12 19:14:09] [info] DRC_ERR4 -> 241
[2025-10-12 19:14:09] [info] DRC_ERR1 -> 301
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Rule Violation Summary
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:14:09] [info] |-----|-----|-----|
[2025-10-12 19:14:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:14:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:14:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:14:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:14:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:14:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:14:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:14:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:14:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:14:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:14:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:14:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:14:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:14:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:14:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Performance Metrics
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] | Stage | Time Taken |
[2025-10-12 19:14:09] [info] |-----|-----|
[2025-10-12 19:14:09] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:14:09] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:14:09] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Summary
[2025-10-12 19:14:09] [info] -----
[2025-10-12 19:14:09] [info] Total Errors: 1120
[2025-10-12 19:14:09] [info] Total Warnings: 321
[2025-10-12 19:14:09] [info] DRC Status: FAILED

```

lvr_summary_537.rpt

```

[2025-10-12 19:15:06] [info] =====
[2025-10-12 19:15:06] [info] Design Rule Check Report
[2025-10-12 19:15:06] [info] =====

[2025-10-12 19:15:06] [info] Report Generated: 2025-10-12
[2025-10-12 19:15:06] [info] Run Start Time: 07:14:15 PM
[2025-10-12 19:15:06] [info] Run End Time: 07:15:06 PM
[2025-10-12 19:15:06] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:15:06] [info] Tool Version: LVR_2025.1
[2025-10-12 19:15:06] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:15:06] [info] User Name: ubuntu
[2025-10-12 19:15:06] [info] Cores Used: 8
[2025-10-12 19:15:06] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Top Cell Information
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Layout Top Cell:      TOP
[2025-10-12 19:15:06] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:15:06] [info] Database Format:      GDSII
[2025-10-12 19:15:06] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Type of Errors | Count
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] DB_WARN -> 1
[2025-10-12 19:15:06] [info] LRF_ERR001 -> 14
[2025-10-12 19:15:06] [info] DB_WARN002 -> 159
[2025-10-12 19:15:06] [info] DB_WARN003 -> 161
[2025-10-12 19:15:06] [info] DRC_ERR9 -> 89
[2025-10-12 19:15:06] [info] DRC_ERR -> 67
[2025-10-12 19:15:06] [info] DRC_ERR5 -> 408
[2025-10-12 19:15:06] [info] DRC_ERR4 -> 241
[2025-10-12 19:15:06] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Rule Violation Summary
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:15:06] [info] |-----|-----|-----|
[2025-10-12 19:15:06] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:15:06] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:15:06] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:15:06] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:15:06] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:15:06] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:15:06] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:15:06] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:15:06] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:15:06] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:15:06] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:15:06] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:15:06] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:15:06] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:15:06] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:15:06] [info] |
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Performance Metrics
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] | Stage          | Time Taken |
[2025-10-12 19:15:06] [info] |-----|-----|
[2025-10-12 19:15:06] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:15:06] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:15:06] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Summary
```

```
[2025-10-12 19:15:06] [info] -----
[2025-10-12 19:15:06] [info] Total Errors:      1120
[2025-10-12 19:15:06] [info] Total Warnings:    321
[2025-10-12 19:15:06] [info] DRC Status:        FAILED
```

lvr_summary_538.rpt

```
[2025-10-12 19:16:04] [info] =====
[2025-10-12 19:16:04] [info] Design Rule Check Report
[2025-10-12 19:16:04] [info] =====

[2025-10-12 19:16:04] [info] Report Generated: 2025-10-12
[2025-10-12 19:16:04] [info] Run Start Time: 07:15:12 PM
[2025-10-12 19:16:04] [info] Run End Time: 07:16:04 PM
[2025-10-12 19:16:04] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:16:04] [info] Tool Version: LVR_2025.1
[2025-10-12 19:16:04] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:16:04] [info] User Name: ubuntu
[2025-10-12 19:16:04] [info] Cores Used: 8
[2025-10-12 19:16:04] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] Top Cell Information
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] Layout Top Cell: TOP
[2025-10-12 19:16:04] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:16:04] [info] Database Format: GDSII
[2025-10-12 19:16:04] [info] Verification Mode: Full Check

[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] Type of Errors | Count
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] DB_WARN -> 1
[2025-10-12 19:16:04] [info] LRF_ERR001 -> 14
[2025-10-12 19:16:04] [info] DB_WARN002 -> 159
[2025-10-12 19:16:04] [info] DB_WARN003 -> 161
[2025-10-12 19:16:04] [info] DRC_ERR9 -> 89
[2025-10-12 19:16:04] [info] DRC_ERR -> 67
[2025-10-12 19:16:04] [info] DRC_ERR5 -> 408
[2025-10-12 19:16:04] [info] DRC_ERR4 -> 241
[2025-10-12 19:16:04] [info] DRC_ERR1 -> 301
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] Rule Violation Summary
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:16:04] [info] |-----|-----|-----|
[2025-10-12 19:16:04] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:16:04] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:16:04] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:16:04] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:16:04] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:16:04] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:16:04] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:16:04] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:16:04] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:16:04] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:16:04] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:16:04] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:16:04] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:16:04] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:16:04] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:16:04] [info] |
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] Performance Metrics
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:16:04] [info] |-----|-----|
[2025-10-12 19:16:04] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:16:04] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:16:04] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info]                Summary
[2025-10-12 19:16:04] [info] -----
[2025-10-12 19:16:04] [info] Total Errors:    1120
[2025-10-12 19:16:04] [info] Total Warnings:    321
[2025-10-12 19:16:04] [info] DRC Status:      FAILED
```

lvr_summary_539.rpt

```
[2025-10-12 19:17:00] [info] =====
[2025-10-12 19:17:00] [info]                Design Rule Check Report
[2025-10-12 19:17:00] [info] =====

[2025-10-12 19:17:00] [info] Report Generated:    2025-10-12
[2025-10-12 19:17:00] [info] Run Start Time:      07:16:08 PM
[2025-10-12 19:17:00] [info] Run End Time:        07:17:00 PM
[2025-10-12 19:17:00] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:17:00] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:17:00] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:17:00] [info] User Name:           ubuntu
[2025-10-12 19:17:00] [info] Cores Used:          8
[2025-10-12 19:17:00] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info]                Top Cell Information
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] Layout Top Cell:     TOP
[2025-10-12 19:17:00] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:17:00] [info] Database Format:       GDSII
[2025-10-12 19:17:00] [info] Verification Mode:    Full Check

[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info]                Type of Errors | Count
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] DB_WARN -> 1
[2025-10-12 19:17:00] [info] LRF_ERR001 -> 14
[2025-10-12 19:17:00] [info] DB_WARN002 -> 159
[2025-10-12 19:17:00] [info] DB_WARN003 -> 161
[2025-10-12 19:17:00] [info] DRC_ERR9 -> 89
[2025-10-12 19:17:00] [info] DRC_ERR -> 67
[2025-10-12 19:17:00] [info] DRC_ERR5 -> 408
[2025-10-12 19:17:00] [info] DRC_ERR4 -> 241
[2025-10-12 19:17:00] [info] DRC_ERR1 -> 301
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info]                Rule Violation Summary
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:17:00] [info] |-----|-----|-----|
[2025-10-12 19:17:00] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:17:00] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:17:00] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:17:00] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:17:00] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:17:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:17:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:17:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:17:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:17:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:17:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:17:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:17:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:17:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:17:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:17:00] [info]
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] Performance Metrics
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] | Stage | Time Taken |
[2025-10-12 19:17:00] [info] |-----|-----|
[2025-10-12 19:17:00] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:17:00] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:17:00] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] Summary
[2025-10-12 19:17:00] [info] -----
[2025-10-12 19:17:00] [info] Total Errors: 1120
[2025-10-12 19:17:00] [info] Total Warnings: 321
[2025-10-12 19:17:00] [info] DRC Status: FAILED
```

lvr_summary_54.rpt

```
[2025-10-12 11:32:34] [info] =====
[2025-10-12 11:32:34] [info] Design Rule Check Report
[2025-10-12 11:32:34] [info] =====

[2025-10-12 11:32:34] [info] Report Generated: 2025-10-12
[2025-10-12 11:32:34] [info] Run Start Time: 11:31:43 AM
[2025-10-12 11:32:34] [info] Run End Time: 11:32:34 AM
[2025-10-12 11:32:34] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:32:34] [info] Tool Version: LVR_2025.1
[2025-10-12 11:32:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:32:34] [info] User Name: ubuntu
[2025-10-12 11:32:34] [info] Cores Used: 8
[2025-10-12 11:32:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Top Cell Information
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Layout Top Cell: TOP
[2025-10-12 11:32:34] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:32:34] [info] Database Format: GDSII
[2025-10-12 11:32:34] [info] Verification Mode: Full Check

[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Type of Errors | Count
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] DB_WARN -> 1
[2025-10-12 11:32:34] [info] LRF_ERR001 -> 14
[2025-10-12 11:32:34] [info] DB_WARN002 -> 159
[2025-10-12 11:32:34] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:32:34] [info] DRC_ERR9 -> 89
[2025-10-12 11:32:34] [info] DRC_ERR -> 67
[2025-10-12 11:32:34] [info] DRC_ERR5 -> 408
[2025-10-12 11:32:34] [info] DRC_ERR4 -> 241
[2025-10-12 11:32:34] [info] DRC_ERR1 -> 301
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Rule Violation Summary
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:32:34] [info] |-----|-----|-----|
[2025-10-12 11:32:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:32:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:32:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:32:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:32:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:32:34] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 11:32:34] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:32:34] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:32:34] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:32:34] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:32:34] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:32:34] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:32:34] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:32:34] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:32:34] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Performance Metrics
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] | Stage          | Time Taken |
[2025-10-12 11:32:34] [info] |-----|-----|
[2025-10-12 11:32:34] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:32:34] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 11:32:34] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Summary
[2025-10-12 11:32:34] [info] -----
[2025-10-12 11:32:34] [info] Total Errors: 1120
[2025-10-12 11:32:34] [info] Total Warnings: 321
[2025-10-12 11:32:34] [info] DRC Status: FAILED
```

lvr_summary_540.rpt

```
[2025-10-12 19:17:57] [info] =====
[2025-10-12 19:17:57] [info] Design Rule Check Report
[2025-10-12 19:17:57] [info] =====

[2025-10-12 19:17:57] [info] Report Generated: 2025-10-12
[2025-10-12 19:17:57] [info] Run Start Time: 07:17:05 PM
[2025-10-12 19:17:57] [info] Run End Time: 07:17:57 PM
[2025-10-12 19:17:57] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:17:57] [info] Tool Version: LVR_2025.1
[2025-10-12 19:17:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:17:57] [info] User Name: ubuntu
[2025-10-12 19:17:57] [info] Cores Used: 8
[2025-10-12 19:17:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] Top Cell Information
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] Layout Top Cell:      TOP
[2025-10-12 19:17:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:17:57] [info] Database Format:      GDSII
[2025-10-12 19:17:57] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] Type of Errors | Count
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] DB_WARN -> 1
[2025-10-12 19:17:57] [info] LRF_ERR001 -> 14
[2025-10-12 19:17:57] [info] DB_WARN002 -> 159
[2025-10-12 19:17:57] [info] DB_WARN003 -> 161
[2025-10-12 19:17:57] [info] DRC_ERR9 -> 89
[2025-10-12 19:17:57] [info] DRC_ERR -> 67
[2025-10-12 19:17:57] [info] DRC_ERR5 -> 408
[2025-10-12 19:17:57] [info] DRC_ERR4 -> 241
[2025-10-12 19:17:57] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] Rule Violation Summary
```

```
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:17:57] [info] |-----|-----|-----|
[2025-10-12 19:17:57] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:17:57] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:17:57] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:17:57] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:17:57] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:17:57] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:17:57] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:17:57] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:17:57] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:17:57] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:17:57] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:17:57] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:17:57] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:17:57] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:17:57] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:17:57] [info] -----
```

```
[2025-10-12 19:17:57] [info] Performance Metrics
```

```
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] | Stage          | Time Taken |
[2025-10-12 19:17:57] [info] |-----|-----|
[2025-10-12 19:17:57] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:17:57] [info] | Rule Checking   | 0 minutes 40 seconds |
[2025-10-12 19:17:57] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] Summary
[2025-10-12 19:17:57] [info] -----
[2025-10-12 19:17:57] [info] Total Errors:      1120
[2025-10-12 19:17:57] [info] Total Warnings:    321
[2025-10-12 19:17:57] [info] DRC Status:        FAILED
```

lvr_summary_541.rpt

```
[2025-10-12 19:18:55] [info] =====
[2025-10-12 19:18:55] [info] Design Rule Check Report
[2025-10-12 19:18:55] [info] =====

[2025-10-12 19:18:55] [info] Report Generated: 2025-10-12
[2025-10-12 19:18:55] [info] Run Start Time: 07:18:03 PM
[2025-10-12 19:18:55] [info] Run End Time: 07:18:55 PM
[2025-10-12 19:18:55] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:18:55] [info] Tool Version: LVR_2025.1
[2025-10-12 19:18:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:18:55] [info] User Name: ubuntu
[2025-10-12 19:18:55] [info] Cores Used: 8
[2025-10-12 19:18:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] Top Cell Information
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] Layout Top Cell: TOP
[2025-10-12 19:18:55] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:18:55] [info] Database Format: GDSII
[2025-10-12 19:18:55] [info] Verification Mode: Full Check

[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] Type of Errors | Count
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] DB_WARN -> 1
[2025-10-12 19:18:55] [info] LRF_ERR001 -> 14
[2025-10-12 19:18:55] [info] DB_WARN002 -> 159
[2025-10-12 19:18:55] [info] DB_WARN003 -> 161
[2025-10-12 19:18:55] [info] DRC_ERR9 -> 89
[2025-10-12 19:18:55] [info] DRC_ERR -> 67
[2025-10-12 19:18:55] [info] DRC_ERR5 -> 408
[2025-10-12 19:18:55] [info] DRC_ERR4 -> 241
[2025-10-12 19:18:55] [info] DRC_ERR1 -> 301
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] Rule Violation Summary
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:18:55] [info] |-----|-----|-----|
[2025-10-12 19:18:55] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:18:55] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:18:55] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:18:55] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:18:55] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:18:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:18:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:18:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:18:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:18:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:18:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:18:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:18:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:18:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:18:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:18:55] [info]
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] Performance Metrics
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:18:55] [info] |-----|-----|
[2025-10-12 19:18:55] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:18:55] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 19:18:55] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info]                Summary
[2025-10-12 19:18:55] [info] -----
[2025-10-12 19:18:55] [info] Total Errors:    1120
[2025-10-12 19:18:55] [info] Total Warnings:    321
[2025-10-12 19:18:55] [info] DRC Status:      FAILED
```

lvr_summary_542.rpt

```
[2025-10-12 19:19:52] [info] =====
[2025-10-12 19:19:52] [info]                Design Rule Check Report
[2025-10-12 19:19:52] [info] =====

[2025-10-12 19:19:52] [info] Report Generated:    2025-10-12
[2025-10-12 19:19:52] [info] Run Start Time:      07:19:00 PM
[2025-10-12 19:19:52] [info] Run End Time:        07:19:52 PM
[2025-10-12 19:19:52] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 19:19:52] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:19:52] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:19:52] [info] User Name:           ubuntu
[2025-10-12 19:19:52] [info] Cores Used:          8
[2025-10-12 19:19:52] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info]                Top Cell Information
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] Layout Top Cell:     TOP
[2025-10-12 19:19:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:19:52] [info] Database Format:       GDSII
[2025-10-12 19:19:52] [info] Verification Mode:    Full Check

[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info]                Type of Errors | Count
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] DB_WARN -> 1
[2025-10-12 19:19:52] [info] LRF_ERR001 -> 14
[2025-10-12 19:19:52] [info] DB_WARN002 -> 159
[2025-10-12 19:19:52] [info] DB_WARN003 -> 161
[2025-10-12 19:19:52] [info] DRC_ERR9 -> 89
[2025-10-12 19:19:52] [info] DRC_ERR -> 67
[2025-10-12 19:19:52] [info] DRC_ERR5 -> 408
[2025-10-12 19:19:52] [info] DRC_ERR4 -> 241
[2025-10-12 19:19:52] [info] DRC_ERR1 -> 301
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info]                Rule Violation Summary
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:19:52] [info] |-----|-----|-----|
[2025-10-12 19:19:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:19:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:19:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:19:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:19:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:19:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 19:19:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:19:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:19:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:19:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:19:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:19:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:19:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:19:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:19:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:19:52] [info]
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] Performance Metrics
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] | Stage | Time Taken |
[2025-10-12 19:19:52] [info] |-----|-----|
[2025-10-12 19:19:52] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:19:52] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:19:52] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] Summary
[2025-10-12 19:19:52] [info] -----
[2025-10-12 19:19:52] [info] Total Errors: 1120
[2025-10-12 19:19:52] [info] Total Warnings: 321
[2025-10-12 19:19:52] [info] DRC Status: FAILED
```

lvr_summary_543.rpt

```
[2025-10-12 19:20:50] [info] =====
[2025-10-12 19:20:50] [info] Design Rule Check Report
[2025-10-12 19:20:50] [info] =====

[2025-10-12 19:20:50] [info] Report Generated: 2025-10-12
[2025-10-12 19:20:50] [info] Run Start Time: 07:19:58 PM
[2025-10-12 19:20:50] [info] Run End Time: 07:20:50 PM
[2025-10-12 19:20:50] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:20:50] [info] Tool Version: LVR_2025.1
[2025-10-12 19:20:50] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:20:50] [info] User Name: ubuntu
[2025-10-12 19:20:50] [info] Cores Used: 8
[2025-10-12 19:20:50] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Top Cell Information
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Layout Top Cell: TOP
[2025-10-12 19:20:50] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:20:50] [info] Database Format: GDSII
[2025-10-12 19:20:50] [info] Verification Mode: Full Check

[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Type of Errors | Count
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] DB_WARN -> 1
[2025-10-12 19:20:50] [info] LRF_ERR001 -> 14
[2025-10-12 19:20:50] [info] DB_WARN002 -> 159
[2025-10-12 19:20:50] [info] DB_WARN003 -> 161
```

```

[2025-10-12 19:20:50] [info] DRC_ERR9 -> 89
[2025-10-12 19:20:50] [info] DRC_ERR -> 67
[2025-10-12 19:20:50] [info] DRC_ERR5 -> 408
[2025-10-12 19:20:50] [info] DRC_ERR4 -> 241
[2025-10-12 19:20:50] [info] DRC_ERR1 -> 301
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Rule Violation Summary
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:20:50] [info] |-----|-----|-----|
[2025-10-12 19:20:50] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:20:50] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:20:50] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:20:50] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:20:50] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:20:50] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:20:50] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:20:50] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:20:50] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:20:50] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:20:50] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:20:50] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:20:50] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:20:50] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:20:50] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Performance Metrics
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] | Stage | Time Taken |
[2025-10-12 19:20:50] [info] |-----|-----|
[2025-10-12 19:20:50] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:20:50] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:20:50] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Summary
[2025-10-12 19:20:50] [info] -----
[2025-10-12 19:20:50] [info] Total Errors: 1120
[2025-10-12 19:20:50] [info] Total Warnings: 321
[2025-10-12 19:20:50] [info] DRC Status: FAILED

```

lvr_summary_544.rpt

```

[2025-10-12 19:21:46] [info] =====
[2025-10-12 19:21:46] [info] Design Rule Check Report
[2025-10-12 19:21:46] [info] =====

[2025-10-12 19:21:46] [info] Report Generated: 2025-10-12
[2025-10-12 19:21:46] [info] Run Start Time: 07:20:55 PM
[2025-10-12 19:21:46] [info] Run End Time: 07:21:46 PM
[2025-10-12 19:21:46] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:21:46] [info] Tool Version: LVR_2025.1
[2025-10-12 19:21:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:21:46] [info] User Name: ubuntu
[2025-10-12 19:21:46] [info] Cores Used: 8
[2025-10-12 19:21:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] Top Cell Information
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] Layout Top Cell:      TOP
[2025-10-12 19:21:46] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:21:46] [info] Database Format:      GDSII
[2025-10-12 19:21:46] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] Type of Errors | Count
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] DB_WARN -> 1
[2025-10-12 19:21:46] [info] LRF_ERR001 -> 14
[2025-10-12 19:21:46] [info] DB_WARN002 -> 159
[2025-10-12 19:21:46] [info] DB_WARN003 -> 161
[2025-10-12 19:21:46] [info] DRC_ERR9 -> 89
[2025-10-12 19:21:46] [info] DRC_ERR -> 67
[2025-10-12 19:21:46] [info] DRC_ERR5 -> 408
[2025-10-12 19:21:46] [info] DRC_ERR4 -> 241
[2025-10-12 19:21:46] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] Rule Violation Summary
```

```
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:21:46] [info] |-----|-----|-----|
[2025-10-12 19:21:46] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:21:46] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:21:46] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:21:46] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:21:46] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:21:46] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:21:46] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:21:46] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:21:46] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:21:46] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:21:46] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:21:46] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:21:46] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:21:46] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:21:46] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:21:46] [info] -----
```

```
[2025-10-12 19:21:46] [info] Performance Metrics
```

```
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] | Stage          | Time Taken |
[2025-10-12 19:21:46] [info] |-----|-----|
[2025-10-12 19:21:46] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:21:46] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:21:46] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:21:46] [info] -----
```

```
[2025-10-12 19:21:46] [info] Summary
```

```
[2025-10-12 19:21:46] [info] -----
[2025-10-12 19:21:46] [info] Total Errors:      1120
[2025-10-12 19:21:46] [info] Total Warnings:    321
[2025-10-12 19:21:46] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:22:42] [info] =====
[2025-10-12 19:22:42] [info]                      Design Rule Check Report
[2025-10-12 19:22:42] [info] =====

[2025-10-12 19:22:42] [info] Report Generated:      2025-10-12
[2025-10-12 19:22:42] [info] Run Start Time:       07:21:52 PM
[2025-10-12 19:22:42] [info] Run End Time:         07:22:42 PM
[2025-10-12 19:22:42] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 19:22:42] [info] Tool Version:          LVR_2025.1
[2025-10-12 19:22:42] [info] Host Name:             ip-172-31-37-173
[2025-10-12 19:22:42] [info] User Name:             ubuntu
[2025-10-12 19:22:42] [info] Cores Used:            8
[2025-10-12 19:22:42] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info]                      Top Cell Information
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info] Layout Top Cell:      TOP
[2025-10-12 19:22:42] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 19:22:42] [info] Database Format:        GDSII
[2025-10-12 19:22:42] [info] Verification Mode:     Full Check

[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info]                      Type of Errors | Count
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info] DB_WARN -> 1
[2025-10-12 19:22:42] [info] LRF_ERR001 -> 14
[2025-10-12 19:22:42] [info] DB_WARN002 -> 159
[2025-10-12 19:22:42] [info] DB_WARN003 -> 161
[2025-10-12 19:22:42] [info] DRC_ERR9 -> 89
[2025-10-12 19:22:42] [info] DRC_ERR -> 67
[2025-10-12 19:22:42] [info] DRC_ERR5 -> 408
[2025-10-12 19:22:42] [info] DRC_ERR4 -> 241
[2025-10-12 19:22:42] [info] DRC_ERR1 -> 301
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info]                      Rule Violation Summary
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:22:42] [info] |-----|-----|-----|
[2025-10-12 19:22:42] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:22:42] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:22:42] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:22:42] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:22:42] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:22:42] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:22:42] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:22:42] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:22:42] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:22:42] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:22:42] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:22:42] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:22:42] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:22:42] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:22:42] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:22:42] [info]
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info]                      Performance Metrics
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:22:42] [info] |-----|-----|
[2025-10-12 19:22:42] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 19:22:42] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 19:22:42] [info] | Report Generation   | 0 minutes 0 seconds |

[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info]                Summary
[2025-10-12 19:22:42] [info] -----
[2025-10-12 19:22:42] [info] Total Errors:      1120
[2025-10-12 19:22:42] [info] Total Warnings:    321
[2025-10-12 19:22:42] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:23:41] [info] =====
[2025-10-12 19:23:41] [info]                Design Rule Check Report
[2025-10-12 19:23:41] [info] =====

[2025-10-12 19:23:41] [info] Report Generated:   2025-10-12
[2025-10-12 19:23:41] [info] Run Start Time:     07:22:49 PM
[2025-10-12 19:23:41] [info] Run End Time:       07:23:41 PM
[2025-10-12 19:23:41] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 19:23:41] [info] Tool Version:       LVR_2025.1
[2025-10-12 19:23:41] [info] Host Name:          ip-172-31-37-173
[2025-10-12 19:23:41] [info] User Name:          ubuntu
[2025-10-12 19:23:41] [info] Cores Used:         8
[2025-10-12 19:23:41] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info]                Top Cell Information
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] Layout Top Cell:    TOP
[2025-10-12 19:23:41] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 19:23:41] [info] Database Format:      GDSII
[2025-10-12 19:23:41] [info] Verification Mode:   Full Check

[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info]                Type of Errors | Count
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] DB_WARN -> 1
[2025-10-12 19:23:41] [info] LRF_ERR001 -> 14
[2025-10-12 19:23:41] [info] DB_WARN002 -> 159
[2025-10-12 19:23:41] [info] DB_WARN003 -> 161
[2025-10-12 19:23:41] [info] DRC_ERR9 -> 89
[2025-10-12 19:23:41] [info] DRC_ERR -> 67
[2025-10-12 19:23:41] [info] DRC_ERR5 -> 408
[2025-10-12 19:23:41] [info] DRC_ERR4 -> 241
[2025-10-12 19:23:41] [info] DRC_ERR1 -> 301
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info]                Rule Violation Summary
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:23:41] [info] |-----|-----|-----|
[2025-10-12 19:23:41] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:23:41] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:23:41] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:23:41] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:23:41] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 19:23:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:23:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:23:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:23:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:23:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:23:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:23:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:23:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:23:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:23:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:23:41] [info]
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] Performance Metrics
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] | Stage | Time Taken |
[2025-10-12 19:23:41] [info] |-----|-----|
[2025-10-12 19:23:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:23:41] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:23:41] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] Summary
[2025-10-12 19:23:41] [info] -----
[2025-10-12 19:23:41] [info] Total Errors: 1120
[2025-10-12 19:23:41] [info] Total Warnings: 321
[2025-10-12 19:23:41] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:24:38] [info] =====
[2025-10-12 19:24:38] [info] Design Rule Check Report
[2025-10-12 19:24:38] [info] =====

[2025-10-12 19:24:38] [info] Report Generated: 2025-10-12
[2025-10-12 19:24:38] [info] Run Start Time: 07:23:46 PM
[2025-10-12 19:24:38] [info] Run End Time: 07:24:38 PM
[2025-10-12 19:24:38] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:24:38] [info] Tool Version: LVR_2025.1
[2025-10-12 19:24:38] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:24:38] [info] User Name: ubuntu
[2025-10-12 19:24:38] [info] Cores Used: 8
[2025-10-12 19:24:38] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Top Cell Information
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Layout Top Cell: TOP
[2025-10-12 19:24:38] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:24:38] [info] Database Format: GDSII
[2025-10-12 19:24:38] [info] Verification Mode: Full Check

[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Type of Errors | Count
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] DB_WARN -> 1
[2025-10-12 19:24:38] [info] LRF_ERR001 -> 14
[2025-10-12 19:24:38] [info] DB_WARN002 -> 159
[2025-10-12 19:24:38] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:24:38] [info] DRC_ERR9 -> 89
[2025-10-12 19:24:38] [info] DRC_ERR -> 67
[2025-10-12 19:24:38] [info] DRC_ERR5 -> 408
[2025-10-12 19:24:38] [info] DRC_ERR4 -> 241
[2025-10-12 19:24:38] [info] DRC_ERR1 -> 301
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Rule Violation Summary
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:24:38] [info] |-----|-----|-----|
[2025-10-12 19:24:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:24:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:24:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:24:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:24:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:24:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:24:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:24:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:24:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:24:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:24:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:24:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:24:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:24:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:24:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Performance Metrics
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] | Stage | Time Taken |
[2025-10-12 19:24:38] [info] |-----|-----|
[2025-10-12 19:24:38] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:24:38] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:24:38] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Summary
[2025-10-12 19:24:38] [info] -----
[2025-10-12 19:24:38] [info] Total Errors: 1120
[2025-10-12 19:24:38] [info] Total Warnings: 321
[2025-10-12 19:24:38] [info] DRC Status: FAILED
```

lvr_summary_548.rpt

```
[2025-10-12 19:25:35] [info] =====
[2025-10-12 19:25:35] [info] Design Rule Check Report
[2025-10-12 19:25:35] [info] =====

[2025-10-12 19:25:35] [info] Report Generated: 2025-10-12
[2025-10-12 19:25:35] [info] Run Start Time: 07:24:43 PM
[2025-10-12 19:25:35] [info] Run End Time: 07:25:35 PM
[2025-10-12 19:25:35] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:25:35] [info] Tool Version: LVR_2025.1
[2025-10-12 19:25:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:25:35] [info] User Name: ubuntu
[2025-10-12 19:25:35] [info] Cores Used: 8
[2025-10-12 19:25:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Top Cell Information
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Layout Top Cell:      TOP
[2025-10-12 19:25:35] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:25:35] [info] Database Format:      GDSII
[2025-10-12 19:25:35] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Type of Errors | Count
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] DB_WARN -> 1
[2025-10-12 19:25:35] [info] LRF_ERR001 -> 14
[2025-10-12 19:25:35] [info] DB_WARN002 -> 159
[2025-10-12 19:25:35] [info] DB_WARN003 -> 161
[2025-10-12 19:25:35] [info] DRC_ERR9 -> 89
[2025-10-12 19:25:35] [info] DRC_ERR -> 67
[2025-10-12 19:25:35] [info] DRC_ERR5 -> 408
[2025-10-12 19:25:35] [info] DRC_ERR4 -> 241
[2025-10-12 19:25:35] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Rule Violation Summary
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:25:35] [info] |-----|-----|-----|
[2025-10-12 19:25:35] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:25:35] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:25:35] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:25:35] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:25:35] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:25:35] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:25:35] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:25:35] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:25:35] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:25:35] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:25:35] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:25:35] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:25:35] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:25:35] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:25:35] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:25:35] [info] |
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Performance Metrics
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] | Stage          | Time Taken |
[2025-10-12 19:25:35] [info] |-----|-----|
[2025-10-12 19:25:35] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:25:35] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:25:35] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Summary
```

```
[2025-10-12 19:25:35] [info] -----
[2025-10-12 19:25:35] [info] Total Errors:      1120
[2025-10-12 19:25:35] [info] Total Warnings:    321
[2025-10-12 19:25:35] [info] DRC Status:        FAILED
```

lvr_summary_549.rpt

```
[2025-10-12 19:26:32] [info] =====
[2025-10-12 19:26:32] [info]                      Design Rule Check Report
[2025-10-12 19:26:32] [info] =====

[2025-10-12 19:26:32] [info] Report Generated:    2025-10-12
[2025-10-12 19:26:32] [info] Run Start Time:      07:25:40 PM
[2025-10-12 19:26:32] [info] Run End Time:        07:26:32 PM
[2025-10-12 19:26:32] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:26:32] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:26:32] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:26:32] [info] User Name:           ubuntu
[2025-10-12 19:26:32] [info] Cores Used:           8
[2025-10-12 19:26:32] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info]                      Top Cell Information
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info] Layout Top Cell:     TOP
[2025-10-12 19:26:32] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:26:32] [info] Database Format:       GDSII
[2025-10-12 19:26:32] [info] Verification Mode:    Full Check

[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info]                      Type of Errors | Count
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info] DB_WARN -> 1
[2025-10-12 19:26:32] [info] LRF_ERR001 -> 14
[2025-10-12 19:26:32] [info] DB_WARN002 -> 159
[2025-10-12 19:26:32] [info] DB_WARN003 -> 161
[2025-10-12 19:26:32] [info] DRC_ERR9 -> 89
[2025-10-12 19:26:32] [info] DRC_ERR -> 67
[2025-10-12 19:26:32] [info] DRC_ERR5 -> 408
[2025-10-12 19:26:32] [info] DRC_ERR4 -> 241
[2025-10-12 19:26:32] [info] DRC_ERR1 -> 301
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info]                      Rule Violation Summary
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:26:32] [info] |-----|-----|-----|
[2025-10-12 19:26:32] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:26:32] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:26:32] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:26:32] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:26:32] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:26:32] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:26:32] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:26:32] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:26:32] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:26:32] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:26:32] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:26:32] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:26:32] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:26:32] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:26:32] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:26:32] [info]
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info]                      Performance Metrics
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:26:32] [info] |-----|-----|
[2025-10-12 19:26:32] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:26:32] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 19:26:32] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info]                Summary
[2025-10-12 19:26:32] [info] -----
[2025-10-12 19:26:32] [info] Total Errors:    1120
[2025-10-12 19:26:32] [info] Total Warnings:    321
[2025-10-12 19:26:32] [info] DRC Status:      FAILED
```

lvr_summary_55.rpt

```
[2025-10-12 11:33:31] [info] =====
[2025-10-12 11:33:31] [info]                Design Rule Check Report
[2025-10-12 11:33:31] [info] =====

[2025-10-12 11:33:31] [info] Report Generated:    2025-10-12
[2025-10-12 11:33:31] [info] Run Start Time:      11:32:40 AM
[2025-10-12 11:33:31] [info] Run End Time:        11:33:31 AM
[2025-10-12 11:33:31] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 11:33:31] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:33:31] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:33:31] [info] User Name:           ubuntu
[2025-10-12 11:33:31] [info] Cores Used:          8
[2025-10-12 11:33:31] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info]                Top Cell Information
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] Layout Top Cell:     TOP
[2025-10-12 11:33:31] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:33:31] [info] Database Format:      GDSII
[2025-10-12 11:33:31] [info] Verification Mode:    Full Check

[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info]                Type of Errors | Count
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] DB_WARN -> 1
[2025-10-12 11:33:31] [info] LRF_ERR001 -> 14
[2025-10-12 11:33:31] [info] DB_WARN002 -> 159
[2025-10-12 11:33:31] [info] DB_WARN003 -> 161
[2025-10-12 11:33:31] [info] DRC_ERR9 -> 89
[2025-10-12 11:33:31] [info] DRC_ERR -> 67
[2025-10-12 11:33:31] [info] DRC_ERR5 -> 408
[2025-10-12 11:33:31] [info] DRC_ERR4 -> 241
[2025-10-12 11:33:31] [info] DRC_ERR1 -> 301
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info]                Rule Violation Summary
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:33:31] [info] |-----|-----|-----|
[2025-10-12 11:33:31] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:33:31] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:33:31] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:33:31] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:33:31] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:33:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:33:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:33:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:33:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:33:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:33:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:33:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:33:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:33:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:33:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:33:31] [info]
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] Performance Metrics
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] | Stage | Time Taken |
[2025-10-12 11:33:31] [info] |-----|-----|
[2025-10-12 11:33:31] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:33:31] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:33:31] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] Summary
[2025-10-12 11:33:31] [info] -----
[2025-10-12 11:33:31] [info] Total Errors: 1120
[2025-10-12 11:33:31] [info] Total Warnings: 321
[2025-10-12 11:33:31] [info] DRC Status: FAILED
```

lvr_summary_550.rpt

```
[2025-10-12 19:27:28] [info] =====
[2025-10-12 19:27:28] [info] Design Rule Check Report
[2025-10-12 19:27:28] [info] =====

[2025-10-12 19:27:28] [info] Report Generated: 2025-10-12
[2025-10-12 19:27:28] [info] Run Start Time: 07:26:37 PM
[2025-10-12 19:27:28] [info] Run End Time: 07:27:28 PM
[2025-10-12 19:27:28] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:27:28] [info] Tool Version: LVR_2025.1
[2025-10-12 19:27:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:27:28] [info] User Name: ubuntu
[2025-10-12 19:27:28] [info] Cores Used: 8
[2025-10-12 19:27:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Top Cell Information
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Layout Top Cell: TOP
[2025-10-12 19:27:28] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:27:28] [info] Database Format: GDSII
[2025-10-12 19:27:28] [info] Verification Mode: Full Check

[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Type of Errors | Count
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] DB_WARN -> 1
[2025-10-12 19:27:28] [info] LRF_ERR001 -> 14
[2025-10-12 19:27:28] [info] DB_WARN002 -> 159
[2025-10-12 19:27:28] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:27:28] [info] DRC_ERR9 -> 89
[2025-10-12 19:27:28] [info] DRC_ERR -> 67
[2025-10-12 19:27:28] [info] DRC_ERR5 -> 408
[2025-10-12 19:27:28] [info] DRC_ERR4 -> 241
[2025-10-12 19:27:28] [info] DRC_ERR1 -> 301
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Rule Violation Summary
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:27:28] [info] |-----|-----|-----|
[2025-10-12 19:27:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:27:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:27:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:27:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:27:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:27:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:27:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:27:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:27:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:27:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:27:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:27:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:27:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:27:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:27:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Performance Metrics
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] | Stage | Time Taken |
[2025-10-12 19:27:28] [info] |-----|-----|
[2025-10-12 19:27:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:27:28] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:27:28] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Summary
[2025-10-12 19:27:28] [info] -----
[2025-10-12 19:27:28] [info] Total Errors: 1120
[2025-10-12 19:27:28] [info] Total Warnings: 321
[2025-10-12 19:27:28] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:28:24] [info] =====
[2025-10-12 19:28:24] [info] Design Rule Check Report
[2025-10-12 19:28:24] [info] =====

[2025-10-12 19:28:24] [info] Report Generated: 2025-10-12
[2025-10-12 19:28:24] [info] Run Start Time: 07:27:33 PM
[2025-10-12 19:28:24] [info] Run End Time: 07:28:24 PM
[2025-10-12 19:28:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:28:24] [info] Tool Version: LVR_2025.1
[2025-10-12 19:28:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:28:24] [info] User Name: ubuntu
[2025-10-12 19:28:24] [info] Cores Used: 8
[2025-10-12 19:28:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Top Cell Information
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Layout Top Cell:      TOP
[2025-10-12 19:28:24] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:28:24] [info] Database Format:      GDSII
[2025-10-12 19:28:24] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Type of Errors | Count
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] DB_WARN -> 1
[2025-10-12 19:28:24] [info] LRF_ERR001 -> 14
[2025-10-12 19:28:24] [info] DB_WARN002 -> 159
[2025-10-12 19:28:24] [info] DB_WARN003 -> 161
[2025-10-12 19:28:24] [info] DRC_ERR9 -> 89
[2025-10-12 19:28:24] [info] DRC_ERR -> 67
[2025-10-12 19:28:24] [info] DRC_ERR5 -> 408
[2025-10-12 19:28:24] [info] DRC_ERR4 -> 241
[2025-10-12 19:28:24] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Rule Violation Summary
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:28:24] [info] |-----|-----|-----|
[2025-10-12 19:28:24] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:28:24] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:28:24] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:28:24] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:28:24] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:28:24] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:28:24] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:28:24] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:28:24] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:28:24] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:28:24] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:28:24] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:28:24] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:28:24] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:28:24] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:28:24] [info] |
```

```
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Performance Metrics
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] | Stage          | Time Taken |
[2025-10-12 19:28:24] [info] |-----|-----|
[2025-10-12 19:28:24] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:28:24] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 19:28:24] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Summary
[2025-10-12 19:28:24] [info] -----
[2025-10-12 19:28:24] [info] Total Errors:      1120
[2025-10-12 19:28:24] [info] Total Warnings:    321
[2025-10-12 19:28:24] [info] DRC Status:        FAILED
```


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```
[2025-10-12 19:29:22] [info] =====
[2025-10-12 19:29:22] [info]                      Design Rule Check Report
[2025-10-12 19:29:22] [info] =====

[2025-10-12 19:29:22] [info] Report Generated:    2025-10-12
[2025-10-12 19:29:22] [info] Run Start Time:      07:28:31 PM
[2025-10-12 19:29:22] [info] Run End Time:        07:29:22 PM
[2025-10-12 19:29:22] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:29:22] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:29:22] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:29:22] [info] User Name:           ubuntu
[2025-10-12 19:29:22] [info] Cores Used:          8
[2025-10-12 19:29:22] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info]                      Top Cell Information
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info] Layout Top Cell:     TOP
[2025-10-12 19:29:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:29:22] [info] Database Format:      GDSII
[2025-10-12 19:29:22] [info] Verification Mode:    Full Check

[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info]                      Type of Errors | Count
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info] DB_WARN -> 1
[2025-10-12 19:29:22] [info] LRF_ERR001 -> 14
[2025-10-12 19:29:22] [info] DB_WARN002 -> 159
[2025-10-12 19:29:22] [info] DB_WARN003 -> 161
[2025-10-12 19:29:22] [info] DRC_ERR9 -> 89
[2025-10-12 19:29:22] [info] DRC_ERR -> 67
[2025-10-12 19:29:22] [info] DRC_ERR5 -> 408
[2025-10-12 19:29:22] [info] DRC_ERR4 -> 241
[2025-10-12 19:29:22] [info] DRC_ERR1 -> 301
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info]                      Rule Violation Summary
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:29:22] [info] |-----|-----|-----|
[2025-10-12 19:29:22] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:29:22] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:29:22] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:29:22] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:29:22] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:29:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:29:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:29:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:29:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:29:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:29:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:29:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:29:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:29:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:29:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:29:22] [info]
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info]                      Performance Metrics
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:29:22] [info] |-----|-----|
[2025-10-12 19:29:22] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 19:29:22] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 19:29:22] [info] | Report Generation    | 0 minutes 0 seconds  |

[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info]                Summary
[2025-10-12 19:29:22] [info] -----
[2025-10-12 19:29:22] [info] Total Errors:      1120
[2025-10-12 19:29:22] [info] Total Warnings:    321
[2025-10-12 19:29:22] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:30:18] [info] =====
[2025-10-12 19:30:18] [info]                Design Rule Check Report
[2025-10-12 19:30:18] [info] =====

[2025-10-12 19:30:18] [info] Report Generated:   2025-10-12
[2025-10-12 19:30:18] [info] Run Start Time:     07:29:27 PM
[2025-10-12 19:30:18] [info] Run End Time:       07:30:18 PM
[2025-10-12 19:30:18] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 19:30:18] [info] Tool Version:       LVR_2025.1
[2025-10-12 19:30:18] [info] Host Name:          ip-172-31-37-173
[2025-10-12 19:30:18] [info] User Name:          ubuntu
[2025-10-12 19:30:18] [info] Cores Used:         8
[2025-10-12 19:30:18] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info]                Top Cell Information
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] Layout Top Cell:    TOP
[2025-10-12 19:30:18] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 19:30:18] [info] Database Format:      GDSII
[2025-10-12 19:30:18] [info] Verification Mode:   Full Check

[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info]                Type of Errors | Count
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] DB_WARN -> 1
[2025-10-12 19:30:18] [info] LRF_ERR001 -> 14
[2025-10-12 19:30:18] [info] DB_WARN002 -> 159
[2025-10-12 19:30:18] [info] DB_WARN003 -> 161
[2025-10-12 19:30:18] [info] DRC_ERR9 -> 89
[2025-10-12 19:30:18] [info] DRC_ERR -> 67
[2025-10-12 19:30:18] [info] DRC_ERR5 -> 408
[2025-10-12 19:30:18] [info] DRC_ERR4 -> 241
[2025-10-12 19:30:18] [info] DRC_ERR1 -> 301
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info]                Rule Violation Summary
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:30:18] [info] |-----|-----|-----|
[2025-10-12 19:30:18] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:30:18] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:30:18] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:30:18] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:30:18] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:30:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:30:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:30:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:30:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:30:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:30:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:30:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:30:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:30:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:30:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:30:18] [info]
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] Performance Metrics
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] | Stage | Time Taken |
[2025-10-12 19:30:18] [info] |-----|-----|
[2025-10-12 19:30:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:30:18] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:30:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] Summary
[2025-10-12 19:30:18] [info] -----
[2025-10-12 19:30:18] [info] Total Errors: 1120
[2025-10-12 19:30:18] [info] Total Warnings: 321
[2025-10-12 19:30:18] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:31:16] [info] =====
[2025-10-12 19:31:16] [info] Design Rule Check Report
[2025-10-12 19:31:16] [info] =====

[2025-10-12 19:31:16] [info] Report Generated: 2025-10-12
[2025-10-12 19:31:16] [info] Run Start Time: 07:30:24 PM
[2025-10-12 19:31:16] [info] Run End Time: 07:31:16 PM
[2025-10-12 19:31:16] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:31:16] [info] Tool Version: LVR_2025.1
[2025-10-12 19:31:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:31:16] [info] User Name: ubuntu
[2025-10-12 19:31:16] [info] Cores Used: 8
[2025-10-12 19:31:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Top Cell Information
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Layout Top Cell: TOP
[2025-10-12 19:31:16] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:31:16] [info] Database Format: GDSII
[2025-10-12 19:31:16] [info] Verification Mode: Full Check

[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Type of Errors | Count
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] DB_WARN -> 1
[2025-10-12 19:31:16] [info] LRF_ERR001 -> 14
[2025-10-12 19:31:16] [info] DB_WARN002 -> 159
[2025-10-12 19:31:16] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:31:16] [info] DRC_ERR9 -> 89
[2025-10-12 19:31:16] [info] DRC_ERR -> 67
[2025-10-12 19:31:16] [info] DRC_ERR5 -> 408
[2025-10-12 19:31:16] [info] DRC_ERR4 -> 241
[2025-10-12 19:31:16] [info] DRC_ERR1 -> 301
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Rule Violation Summary
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:31:16] [info] |-----|-----|-----|
[2025-10-12 19:31:16] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:31:16] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:31:16] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:31:16] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:31:16] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:31:16] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:31:16] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:31:16] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:31:16] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:31:16] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:31:16] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:31:16] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:31:16] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:31:16] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:31:16] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Performance Metrics
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] | Stage | Time Taken |
[2025-10-12 19:31:16] [info] |-----|-----|
[2025-10-12 19:31:16] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:31:16] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:31:16] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Summary
[2025-10-12 19:31:16] [info] -----
[2025-10-12 19:31:16] [info] Total Errors: 1120
[2025-10-12 19:31:16] [info] Total Warnings: 321
[2025-10-12 19:31:16] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:32:12] [info] =====
[2025-10-12 19:32:12] [info] Design Rule Check Report
[2025-10-12 19:32:12] [info] =====

[2025-10-12 19:32:12] [info] Report Generated: 2025-10-12
[2025-10-12 19:32:12] [info] Run Start Time: 07:31:21 PM
[2025-10-12 19:32:12] [info] Run End Time: 07:32:12 PM
[2025-10-12 19:32:12] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:32:12] [info] Tool Version: LVR_2025.1
[2025-10-12 19:32:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:32:12] [info] User Name: ubuntu
[2025-10-12 19:32:12] [info] Cores Used: 8
[2025-10-12 19:32:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] Top Cell Information
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] Layout Top Cell:      TOP
[2025-10-12 19:32:12] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:32:12] [info] Database Format:      GDSII
[2025-10-12 19:32:12] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] Type of Errors | Count
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] DB_WARN -> 1
[2025-10-12 19:32:12] [info] LRF_ERR001 -> 14
[2025-10-12 19:32:12] [info] DB_WARN002 -> 159
[2025-10-12 19:32:12] [info] DB_WARN003 -> 161
[2025-10-12 19:32:12] [info] DRC_ERR9 -> 89
[2025-10-12 19:32:12] [info] DRC_ERR -> 67
[2025-10-12 19:32:12] [info] DRC_ERR5 -> 408
[2025-10-12 19:32:12] [info] DRC_ERR4 -> 241
[2025-10-12 19:32:12] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] Rule Violation Summary
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:32:12] [info] |-----|-----|-----|
[2025-10-12 19:32:12] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:32:12] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:32:12] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:32:12] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:32:12] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:32:12] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:32:12] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:32:12] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:32:12] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:32:12] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:32:12] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:32:12] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:32:12] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:32:12] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:32:12] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:32:12] [info] -----
```

```
[2025-10-12 19:32:12] [info] Performance Metrics
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] | Stage          | Time Taken |
[2025-10-12 19:32:12] [info] |-----|-----|
[2025-10-12 19:32:12] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:32:12] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:32:12] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] Summary
```

```
[2025-10-12 19:32:12] [info] -----
[2025-10-12 19:32:12] [info] Total Errors:      1120
[2025-10-12 19:32:12] [info] Total Warnings:    321
[2025-10-12 19:32:12] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:33:09] [info] =====
[2025-10-12 19:33:09] [info] Design Rule Check Report
[2025-10-12 19:33:09] [info] =====

[2025-10-12 19:33:09] [info] Report Generated: 2025-10-12
[2025-10-12 19:33:09] [info] Run Start Time: 07:32:17 PM
[2025-10-12 19:33:09] [info] Run End Time: 07:33:09 PM
[2025-10-12 19:33:09] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:33:09] [info] Tool Version: LVR_2025.1
[2025-10-12 19:33:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:33:09] [info] User Name: ubuntu
[2025-10-12 19:33:09] [info] Cores Used: 8
[2025-10-12 19:33:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Top Cell Information
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Layout Top Cell: TOP
[2025-10-12 19:33:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:33:09] [info] Database Format: GDSII
[2025-10-12 19:33:09] [info] Verification Mode: Full Check

[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Type of Errors | Count
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] DB_WARN -> 1
[2025-10-12 19:33:09] [info] LRF_ERR001 -> 14
[2025-10-12 19:33:09] [info] DB_WARN002 -> 159
[2025-10-12 19:33:09] [info] DB_WARN003 -> 161
[2025-10-12 19:33:09] [info] DRC_ERR9 -> 89
[2025-10-12 19:33:09] [info] DRC_ERR -> 67
[2025-10-12 19:33:09] [info] DRC_ERR5 -> 408
[2025-10-12 19:33:09] [info] DRC_ERR4 -> 241
[2025-10-12 19:33:09] [info] DRC_ERR1 -> 301
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Rule Violation Summary
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:33:09] [info] |-----|-----|-----|
[2025-10-12 19:33:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:33:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:33:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:33:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:33:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:33:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:33:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:33:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:33:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:33:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:33:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:33:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:33:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:33:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:33:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:33:09] [info]
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Performance Metrics
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:33:09] [info] |-----|-----|
[2025-10-12 19:33:09] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:33:09] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:33:09] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Summary
[2025-10-12 19:33:09] [info] -----
[2025-10-12 19:33:09] [info] Total Errors: 1120
[2025-10-12 19:33:09] [info] Total Warnings: 321
[2025-10-12 19:33:09] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:34:05] [info] =====
[2025-10-12 19:34:05] [info] Design Rule Check Report
[2025-10-12 19:34:05] [info] =====

[2025-10-12 19:34:05] [info] Report Generated: 2025-10-12
[2025-10-12 19:34:05] [info] Run Start Time: 07:33:14 PM
[2025-10-12 19:34:05] [info] Run End Time: 07:34:05 PM
[2025-10-12 19:34:05] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:34:05] [info] Tool Version: LVR_2025.1
[2025-10-12 19:34:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:34:05] [info] User Name: ubuntu
[2025-10-12 19:34:05] [info] Cores Used: 8
[2025-10-12 19:34:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Top Cell Information
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Layout Top Cell: TOP
[2025-10-12 19:34:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:34:05] [info] Database Format: GDSII
[2025-10-12 19:34:05] [info] Verification Mode: Full Check

[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Type of Errors | Count
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] DB_WARN -> 1
[2025-10-12 19:34:05] [info] LRF_ERR001 -> 14
[2025-10-12 19:34:05] [info] DB_WARN002 -> 159
[2025-10-12 19:34:05] [info] DB_WARN003 -> 161
[2025-10-12 19:34:05] [info] DRC_ERR9 -> 89
[2025-10-12 19:34:05] [info] DRC_ERR -> 67
[2025-10-12 19:34:05] [info] DRC_ERR5 -> 408
[2025-10-12 19:34:05] [info] DRC_ERR4 -> 241
[2025-10-12 19:34:05] [info] DRC_ERR1 -> 301
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Rule Violation Summary
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:34:05] [info] |-----|-----|-----|
[2025-10-12 19:34:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:34:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:34:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:34:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:34:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 19:34:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:34:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:34:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:34:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:34:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:34:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:34:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:34:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:34:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:34:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:34:05] [info]
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Performance Metrics
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] | Stage | Time Taken |
[2025-10-12 19:34:05] [info] |-----|-----|
[2025-10-12 19:34:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:34:05] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:34:05] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Summary
[2025-10-12 19:34:05] [info] -----
[2025-10-12 19:34:05] [info] Total Errors: 1120
[2025-10-12 19:34:05] [info] Total Warnings: 321
[2025-10-12 19:34:05] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:35:02] [info] =====
[2025-10-12 19:35:02] [info] Design Rule Check Report
[2025-10-12 19:35:02] [info] =====

[2025-10-12 19:35:02] [info] Report Generated: 2025-10-12
[2025-10-12 19:35:02] [info] Run Start Time: 07:34:11 PM
[2025-10-12 19:35:02] [info] Run End Time: 07:35:02 PM
[2025-10-12 19:35:02] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:35:02] [info] Tool Version: LVR_2025.1
[2025-10-12 19:35:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:35:02] [info] User Name: ubuntu
[2025-10-12 19:35:02] [info] Cores Used: 8
[2025-10-12 19:35:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Top Cell Information
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Layout Top Cell: TOP
[2025-10-12 19:35:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:35:02] [info] Database Format: GDSII
[2025-10-12 19:35:02] [info] Verification Mode: Full Check

[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Type of Errors | Count
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] DB_WARN -> 1
[2025-10-12 19:35:02] [info] LRF_ERR001 -> 14
[2025-10-12 19:35:02] [info] DB_WARN002 -> 159
[2025-10-12 19:35:02] [info] DB_WARN003 -> 161
```



```
[2025-10-12 19:35:02] [info] DRC_ERR9 -> 89
[2025-10-12 19:35:02] [info] DRC_ERR -> 67
[2025-10-12 19:35:02] [info] DRC_ERR5 -> 408
[2025-10-12 19:35:02] [info] DRC_ERR4 -> 241
[2025-10-12 19:35:02] [info] DRC_ERR1 -> 301
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Rule Violation Summary
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:35:02] [info] |-----|-----|-----|
[2025-10-12 19:35:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:35:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:35:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:35:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:35:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:35:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:35:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:35:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:35:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:35:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:35:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:35:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:35:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:35:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:35:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Performance Metrics
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] | Stage | Time Taken |
[2025-10-12 19:35:02] [info] |-----|-----|
[2025-10-12 19:35:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:35:02] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:35:02] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Summary
[2025-10-12 19:35:02] [info] -----
[2025-10-12 19:35:02] [info] Total Errors: 1120
[2025-10-12 19:35:02] [info] Total Warnings: 321
[2025-10-12 19:35:02] [info] DRC Status: FAILED
```

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```
[2025-10-12 19:35:58] [info] =====
[2025-10-12 19:35:58] [info] Design Rule Check Report
[2025-10-12 19:35:58] [info] =====

[2025-10-12 19:35:58] [info] Report Generated: 2025-10-12
[2025-10-12 19:35:58] [info] Run Start Time: 07:35:08 PM
[2025-10-12 19:35:58] [info] Run End Time: 07:35:58 PM
[2025-10-12 19:35:58] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:35:58] [info] Tool Version: LVR_2025.1
[2025-10-12 19:35:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:35:58] [info] User Name: ubuntu
[2025-10-12 19:35:58] [info] Cores Used: 8
[2025-10-12 19:35:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Top Cell Information
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Layout Top Cell:      TOP
[2025-10-12 19:35:58] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:35:58] [info] Database Format:      GDSII
[2025-10-12 19:35:58] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Type of Errors | Count
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] DB_WARN -> 1
[2025-10-12 19:35:58] [info] LRF_ERR001 -> 14
[2025-10-12 19:35:58] [info] DB_WARN002 -> 159
[2025-10-12 19:35:58] [info] DB_WARN003 -> 161
[2025-10-12 19:35:58] [info] DRC_ERR9 -> 89
[2025-10-12 19:35:58] [info] DRC_ERR -> 67
[2025-10-12 19:35:58] [info] DRC_ERR5 -> 408
[2025-10-12 19:35:58] [info] DRC_ERR4 -> 241
[2025-10-12 19:35:58] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Rule Violation Summary
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:35:58] [info] |-----|-----|-----|
[2025-10-12 19:35:58] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:35:58] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:35:58] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:35:58] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:35:58] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:35:58] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:35:58] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:35:58] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:35:58] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:35:58] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:35:58] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:35:58] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:35:58] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:35:58] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:35:58] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:35:58] [info] |
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Performance Metrics
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] | Stage          | Time Taken |
[2025-10-12 19:35:58] [info] |-----|-----|
[2025-10-12 19:35:58] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:35:58] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:35:58] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Summary
```

```
[2025-10-12 19:35:58] [info] -----
[2025-10-12 19:35:58] [info] Total Errors:      1120
[2025-10-12 19:35:58] [info] Total Warnings:    321
[2025-10-12 19:35:58] [info] DRC Status:        FAILED
```

lvr_summary_56.rpt

```
[2025-10-12 11:34:29] [info] =====
[2025-10-12 11:34:29] [info]                      Design Rule Check Report
[2025-10-12 11:34:29] [info] =====

[2025-10-12 11:34:29] [info] Report Generated:    2025-10-12
[2025-10-12 11:34:29] [info] Run Start Time:      11:33:37 AM
[2025-10-12 11:34:29] [info] Run End Time:        11:34:29 AM
[2025-10-12 11:34:29] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:34:29] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:34:29] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:34:29] [info] User Name:           ubuntu
[2025-10-12 11:34:29] [info] Cores Used:          8
[2025-10-12 11:34:29] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info]                      Top Cell Information
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info] Layout Top Cell:     TOP
[2025-10-12 11:34:29] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:34:29] [info] Database Format:      GDSII
[2025-10-12 11:34:29] [info] Verification Mode:    Full Check

[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info]                      Type of Errors | Count
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info] DB_WARN -> 1
[2025-10-12 11:34:29] [info] LRF_ERR001 -> 14
[2025-10-12 11:34:29] [info] DB_WARN002 -> 159
[2025-10-12 11:34:29] [info] DB_WARN003 -> 161
[2025-10-12 11:34:29] [info] DRC_ERR9 -> 89
[2025-10-12 11:34:29] [info] DRC_ERR -> 67
[2025-10-12 11:34:29] [info] DRC_ERR5 -> 408
[2025-10-12 11:34:29] [info] DRC_ERR4 -> 241
[2025-10-12 11:34:29] [info] DRC_ERR1 -> 301
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info]                      Rule Violation Summary
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:34:29] [info] |-----|-----|-----|
[2025-10-12 11:34:29] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:34:29] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:34:29] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:34:29] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:34:29] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:34:29] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:34:29] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:34:29] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:34:29] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:34:29] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:34:29] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:34:29] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:34:29] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:34:29] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:34:29] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info]                      Performance Metrics
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:34:29] [info] |-----|-----|
[2025-10-12 11:34:29] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:34:29] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 11:34:29] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info]                Summary
[2025-10-12 11:34:29] [info] -----
[2025-10-12 11:34:29] [info] Total Errors:    1120
[2025-10-12 11:34:29] [info] Total Warnings:    321
[2025-10-12 11:34:29] [info] DRC Status:      FAILED
```

lvr_summary_560.rpt

```
[2025-10-12 19:36:55] [info] =====
[2025-10-12 19:36:55] [info]                Design Rule Check Report
[2025-10-12 19:36:55] [info] =====

[2025-10-12 19:36:55] [info] Report Generated:    2025-10-12
[2025-10-12 19:36:55] [info] Run Start Time:      07:36:05 PM
[2025-10-12 19:36:55] [info] Run End Time:        07:36:55 PM
[2025-10-12 19:36:55] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 19:36:55] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:36:55] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:36:55] [info] User Name:           ubuntu
[2025-10-12 19:36:55] [info] Cores Used:          8
[2025-10-12 19:36:55] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info]                Top Cell Information
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] Layout Top Cell:     TOP
[2025-10-12 19:36:55] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:36:55] [info] Database Format:       GDSII
[2025-10-12 19:36:55] [info] Verification Mode:    Full Check

[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info]                Type of Errors | Count
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] DB_WARN -> 1
[2025-10-12 19:36:55] [info] LRF_ERR001 -> 14
[2025-10-12 19:36:55] [info] DB_WARN002 -> 159
[2025-10-12 19:36:55] [info] DB_WARN003 -> 161
[2025-10-12 19:36:55] [info] DRC_ERR9 -> 89
[2025-10-12 19:36:55] [info] DRC_ERR -> 67
[2025-10-12 19:36:55] [info] DRC_ERR5 -> 408
[2025-10-12 19:36:55] [info] DRC_ERR4 -> 241
[2025-10-12 19:36:55] [info] DRC_ERR1 -> 301
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info]                Rule Violation Summary
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:36:55] [info] |-----|-----|-----|
[2025-10-12 19:36:55] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:36:55] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:36:55] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:36:55] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:36:55] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:36:55] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:36:55] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:36:55] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:36:55] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:36:55] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:36:55] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:36:55] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:36:55] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:36:55] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:36:55] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:36:55] [info]
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] Performance Metrics
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] | Stage | Time Taken |
[2025-10-12 19:36:55] [info] |-----|-----|
[2025-10-12 19:36:55] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:36:55] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 19:36:55] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] Summary
[2025-10-12 19:36:55] [info] -----
[2025-10-12 19:36:55] [info] Total Errors: 1120
[2025-10-12 19:36:55] [info] Total Warnings: 321
[2025-10-12 19:36:55] [info] DRC Status: FAILED
```

lvr_summary_561.rpt

```
[2025-10-12 19:37:52] [info] =====
[2025-10-12 19:37:52] [info] Design Rule Check Report
[2025-10-12 19:37:52] [info] =====

[2025-10-12 19:37:52] [info] Report Generated: 2025-10-12
[2025-10-12 19:37:52] [info] Run Start Time: 07:37:01 PM
[2025-10-12 19:37:52] [info] Run End Time: 07:37:52 PM
[2025-10-12 19:37:52] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:37:52] [info] Tool Version: LVR_2025.1
[2025-10-12 19:37:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:37:52] [info] User Name: ubuntu
[2025-10-12 19:37:52] [info] Cores Used: 8
[2025-10-12 19:37:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Top Cell Information
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Layout Top Cell: TOP
[2025-10-12 19:37:52] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:37:52] [info] Database Format: GDSII
[2025-10-12 19:37:52] [info] Verification Mode: Full Check

[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Type of Errors | Count
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] DB_WARN -> 1
[2025-10-12 19:37:52] [info] LRF_ERR001 -> 14
[2025-10-12 19:37:52] [info] DB_WARN002 -> 159
[2025-10-12 19:37:52] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:37:52] [info] DRC_ERR9 -> 89
[2025-10-12 19:37:52] [info] DRC_ERR -> 67
[2025-10-12 19:37:52] [info] DRC_ERR5 -> 408
[2025-10-12 19:37:52] [info] DRC_ERR4 -> 241
[2025-10-12 19:37:52] [info] DRC_ERR1 -> 301
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Rule Violation Summary
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:37:52] [info] |-----|-----|-----|
[2025-10-12 19:37:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:37:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:37:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:37:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:37:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:37:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 19:37:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:37:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:37:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:37:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:37:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:37:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:37:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:37:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:37:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Performance Metrics
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] | Stage          | Time Taken |
[2025-10-12 19:37:52] [info] |-----|-----|
[2025-10-12 19:37:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:37:52] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:37:52] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Summary
[2025-10-12 19:37:52] [info] -----
[2025-10-12 19:37:52] [info] Total Errors: 1120
[2025-10-12 19:37:52] [info] Total Warnings: 321
[2025-10-12 19:37:52] [info] DRC Status: FAILED
```

lvr_summary_562.rpt

```
[2025-10-12 19:38:49] [info] =====
[2025-10-12 19:38:49] [info] Design Rule Check Report
[2025-10-12 19:38:49] [info] =====

[2025-10-12 19:38:49] [info] Report Generated: 2025-10-12
[2025-10-12 19:38:49] [info] Run Start Time: 07:37:58 PM
[2025-10-12 19:38:49] [info] Run End Time: 07:38:49 PM
[2025-10-12 19:38:49] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:38:49] [info] Tool Version: LVR_2025.1
[2025-10-12 19:38:49] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:38:49] [info] User Name: ubuntu
[2025-10-12 19:38:49] [info] Cores Used: 8
[2025-10-12 19:38:49] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Top Cell Information
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Layout Top Cell:      TOP
[2025-10-12 19:38:49] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:38:49] [info] Database Format:      GDSII
[2025-10-12 19:38:49] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Type of Errors | Count
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] DB_WARN -> 1
[2025-10-12 19:38:49] [info] LRF_ERR001 -> 14
[2025-10-12 19:38:49] [info] DB_WARN002 -> 159
[2025-10-12 19:38:49] [info] DB_WARN003 -> 161
[2025-10-12 19:38:49] [info] DRC_ERR9 -> 89
[2025-10-12 19:38:49] [info] DRC_ERR -> 67
[2025-10-12 19:38:49] [info] DRC_ERR5 -> 408
[2025-10-12 19:38:49] [info] DRC_ERR4 -> 241
[2025-10-12 19:38:49] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Rule Violation Summary
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:38:49] [info] |-----|-----|-----|
[2025-10-12 19:38:49] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:38:49] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:38:49] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:38:49] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:38:49] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:38:49] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:38:49] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:38:49] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:38:49] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:38:49] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:38:49] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:38:49] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:38:49] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:38:49] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:38:49] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:38:49] [info] |
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Performance Metrics
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] | Stage          | Time Taken |
[2025-10-12 19:38:49] [info] |-----|-----|
[2025-10-12 19:38:49] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:38:49] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 19:38:49] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Summary
```

```
[2025-10-12 19:38:49] [info] -----
[2025-10-12 19:38:49] [info] Total Errors:      1120
[2025-10-12 19:38:49] [info] Total Warnings:    321
[2025-10-12 19:38:49] [info] DRC Status:        FAILED
```

lvr_summary_563.rpt

```
[2025-10-12 19:39:46] [info] =====
[2025-10-12 19:39:46] [info] Design Rule Check Report
[2025-10-12 19:39:46] [info] =====

[2025-10-12 19:39:46] [info] Report Generated: 2025-10-12
[2025-10-12 19:39:46] [info] Run Start Time: 07:38:55 PM
[2025-10-12 19:39:46] [info] Run End Time: 07:39:46 PM
[2025-10-12 19:39:46] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:39:46] [info] Tool Version: LVR_2025.1
[2025-10-12 19:39:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:39:46] [info] User Name: ubuntu
[2025-10-12 19:39:46] [info] Cores Used: 8
[2025-10-12 19:39:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] Top Cell Information
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] Layout Top Cell: TOP
[2025-10-12 19:39:46] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:39:46] [info] Database Format: GDSII
[2025-10-12 19:39:46] [info] Verification Mode: Full Check

[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] Type of Errors | Count
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] DB_WARN -> 1
[2025-10-12 19:39:46] [info] LRF_ERR001 -> 14
[2025-10-12 19:39:46] [info] DB_WARN002 -> 159
[2025-10-12 19:39:46] [info] DB_WARN003 -> 161
[2025-10-12 19:39:46] [info] DRC_ERR9 -> 89
[2025-10-12 19:39:46] [info] DRC_ERR -> 67
[2025-10-12 19:39:46] [info] DRC_ERR5 -> 408
[2025-10-12 19:39:46] [info] DRC_ERR4 -> 241
[2025-10-12 19:39:46] [info] DRC_ERR1 -> 301
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] Rule Violation Summary
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:39:46] [info] |-----|-----|-----|
[2025-10-12 19:39:46] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:39:46] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:39:46] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:39:46] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:39:46] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:39:46] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:39:46] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:39:46] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:39:46] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:39:46] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:39:46] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:39:46] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:39:46] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:39:46] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:39:46] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:39:46] [info]
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] Performance Metrics
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] | Stage | Time Taken |
```



```
[2025-10-12 19:39:46] [info] |-----|-----|
[2025-10-12 19:39:46] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:39:46] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:39:46] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info]                Summary
[2025-10-12 19:39:46] [info] -----
[2025-10-12 19:39:46] [info] Total Errors:    1120
[2025-10-12 19:39:46] [info] Total Warnings:    321
[2025-10-12 19:39:46] [info] DRC Status:      FAILED
```

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```
[2025-10-12 19:40:43] [info] =====
[2025-10-12 19:40:43] [info]                Design Rule Check Report
[2025-10-12 19:40:43] [info] =====

[2025-10-12 19:40:43] [info] Report Generated:    2025-10-12
[2025-10-12 19:40:43] [info] Run Start Time:      07:39:51 PM
[2025-10-12 19:40:43] [info] Run End Time:        07:40:43 PM
[2025-10-12 19:40:43] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:40:43] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:40:43] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:40:43] [info] User Name:           ubuntu
[2025-10-12 19:40:43] [info] Cores Used:          8
[2025-10-12 19:40:43] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info]                Top Cell Information
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] Layout Top Cell:     TOP
[2025-10-12 19:40:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:40:43] [info] Database Format:       GDSII
[2025-10-12 19:40:43] [info] Verification Mode:    Full Check

[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info]                Type of Errors | Count
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] DB_WARN -> 1
[2025-10-12 19:40:43] [info] LRF_ERR001 -> 14
[2025-10-12 19:40:43] [info] DB_WARN002 -> 159
[2025-10-12 19:40:43] [info] DB_WARN003 -> 161
[2025-10-12 19:40:43] [info] DRC_ERR9 -> 89
[2025-10-12 19:40:43] [info] DRC_ERR -> 67
[2025-10-12 19:40:43] [info] DRC_ERR5 -> 408
[2025-10-12 19:40:43] [info] DRC_ERR4 -> 241
[2025-10-12 19:40:43] [info] DRC_ERR1 -> 301
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info]                Rule Violation Summary
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:40:43] [info] |-----|-----|-----|
[2025-10-12 19:40:43] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:40:43] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:40:43] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:40:43] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:40:43] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:40:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:40:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:40:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:40:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:40:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:40:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:40:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:40:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:40:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:40:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:40:43] [info]
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] Performance Metrics
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] | Stage | Time Taken |
[2025-10-12 19:40:43] [info] |-----|-----|
[2025-10-12 19:40:43] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:40:43] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:40:43] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] Summary
[2025-10-12 19:40:43] [info] -----
[2025-10-12 19:40:43] [info] Total Errors: 1120
[2025-10-12 19:40:43] [info] Total Warnings: 321
[2025-10-12 19:40:43] [info] DRC Status: FAILED
```

lvr_summary_565.rpt

```
[2025-10-12 19:41:39] [info] =====
[2025-10-12 19:41:39] [info] Design Rule Check Report
[2025-10-12 19:41:39] [info] =====

[2025-10-12 19:41:39] [info] Report Generated: 2025-10-12
[2025-10-12 19:41:39] [info] Run Start Time: 07:40:48 PM
[2025-10-12 19:41:39] [info] Run End Time: 07:41:39 PM
[2025-10-12 19:41:39] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:41:39] [info] Tool Version: LVR_2025.1
[2025-10-12 19:41:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:41:39] [info] User Name: ubuntu
[2025-10-12 19:41:39] [info] Cores Used: 8
[2025-10-12 19:41:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Top Cell Information
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Layout Top Cell: TOP
[2025-10-12 19:41:39] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:41:39] [info] Database Format: GDSII
[2025-10-12 19:41:39] [info] Verification Mode: Full Check

[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Type of Errors | Count
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] DB_WARN -> 1
[2025-10-12 19:41:39] [info] LRF_ERR001 -> 14
[2025-10-12 19:41:39] [info] DB_WARN002 -> 159
[2025-10-12 19:41:39] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:41:39] [info] DRC_ERR9 -> 89
[2025-10-12 19:41:39] [info] DRC_ERR -> 67
[2025-10-12 19:41:39] [info] DRC_ERR5 -> 408
[2025-10-12 19:41:39] [info] DRC_ERR4 -> 241
[2025-10-12 19:41:39] [info] DRC_ERR1 -> 301
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Rule Violation Summary
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:41:39] [info] |-----|-----|-----|
[2025-10-12 19:41:39] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:41:39] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:41:39] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:41:39] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:41:39] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:41:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:41:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:41:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:41:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:41:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:41:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:41:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:41:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:41:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:41:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Performance Metrics
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] | Stage | Time Taken |
[2025-10-12 19:41:39] [info] |-----|-----|
[2025-10-12 19:41:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:41:39] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:41:39] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Summary
[2025-10-12 19:41:39] [info] -----
[2025-10-12 19:41:39] [info] Total Errors: 1120
[2025-10-12 19:41:39] [info] Total Warnings: 321
[2025-10-12 19:41:39] [info] DRC Status: FAILED
```

lvr_summary_566.rpt

```
[2025-10-12 19:42:37] [info] =====
[2025-10-12 19:42:37] [info] Design Rule Check Report
[2025-10-12 19:42:37] [info] =====

[2025-10-12 19:42:37] [info] Report Generated: 2025-10-12
[2025-10-12 19:42:37] [info] Run Start Time: 07:41:45 PM
[2025-10-12 19:42:37] [info] Run End Time: 07:42:37 PM
[2025-10-12 19:42:37] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:42:37] [info] Tool Version: LVR_2025.1
[2025-10-12 19:42:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:42:37] [info] User Name: ubuntu
[2025-10-12 19:42:37] [info] Cores Used: 8
[2025-10-12 19:42:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Top Cell Information
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Layout Top Cell:      TOP
[2025-10-12 19:42:37] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:42:37] [info] Database Format:      GDSII
[2025-10-12 19:42:37] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Type of Errors | Count
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] DB_WARN -> 1
[2025-10-12 19:42:37] [info] LRF_ERR001 -> 14
[2025-10-12 19:42:37] [info] DB_WARN002 -> 159
[2025-10-12 19:42:37] [info] DB_WARN003 -> 161
[2025-10-12 19:42:37] [info] DRC_ERR9 -> 89
[2025-10-12 19:42:37] [info] DRC_ERR -> 67
[2025-10-12 19:42:37] [info] DRC_ERR5 -> 408
[2025-10-12 19:42:37] [info] DRC_ERR4 -> 241
[2025-10-12 19:42:37] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Rule Violation Summary
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:42:37] [info] |-----|-----|-----|
[2025-10-12 19:42:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:42:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:42:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:42:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:42:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:42:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:42:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:42:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:42:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:42:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:42:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:42:37] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:42:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:42:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:42:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:42:37] [info] |
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Performance Metrics
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] | Stage          | Time Taken |
[2025-10-12 19:42:37] [info] |-----|-----|
[2025-10-12 19:42:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:42:37] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:42:37] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Summary
```

```
[2025-10-12 19:42:37] [info] -----
[2025-10-12 19:42:37] [info] Total Errors:      1120
[2025-10-12 19:42:37] [info] Total Warnings:    321
[2025-10-12 19:42:37] [info] DRC Status:        FAILED
```

lvr_summary_567.rpt

```
[2025-10-12 19:43:34] [info] =====
[2025-10-12 19:43:34] [info] Design Rule Check Report
[2025-10-12 19:43:34] [info] =====

[2025-10-12 19:43:34] [info] Report Generated: 2025-10-12
[2025-10-12 19:43:34] [info] Run Start Time: 07:42:42 PM
[2025-10-12 19:43:34] [info] Run End Time: 07:43:34 PM
[2025-10-12 19:43:34] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:43:34] [info] Tool Version: LVR_2025.1
[2025-10-12 19:43:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:43:34] [info] User Name: ubuntu
[2025-10-12 19:43:34] [info] Cores Used: 8
[2025-10-12 19:43:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] Top Cell Information
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] Layout Top Cell: TOP
[2025-10-12 19:43:34] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:43:34] [info] Database Format: GDSII
[2025-10-12 19:43:34] [info] Verification Mode: Full Check

[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] Type of Errors | Count
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] DB_WARN -> 1
[2025-10-12 19:43:34] [info] LRF_ERR001 -> 14
[2025-10-12 19:43:34] [info] DB_WARN002 -> 159
[2025-10-12 19:43:34] [info] DB_WARN003 -> 161
[2025-10-12 19:43:34] [info] DRC_ERR9 -> 89
[2025-10-12 19:43:34] [info] DRC_ERR -> 67
[2025-10-12 19:43:34] [info] DRC_ERR5 -> 408
[2025-10-12 19:43:34] [info] DRC_ERR4 -> 241
[2025-10-12 19:43:34] [info] DRC_ERR1 -> 301
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] Rule Violation Summary
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:43:34] [info] |-----|-----|-----|
[2025-10-12 19:43:34] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:43:34] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:43:34] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:43:34] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:43:34] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:43:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:43:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:43:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:43:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:43:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:43:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:43:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:43:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:43:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:43:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:43:34] [info]
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] Performance Metrics
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:43:34] [info] |-----|-----|
[2025-10-12 19:43:34] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:43:34] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:43:34] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info]                Summary
[2025-10-12 19:43:34] [info] -----
[2025-10-12 19:43:34] [info] Total Errors:    1120
[2025-10-12 19:43:34] [info] Total Warnings:    321
[2025-10-12 19:43:34] [info] DRC Status:      FAILED
```

lvr_summary_568.rpt

```
[2025-10-12 19:44:30] [info] =====
[2025-10-12 19:44:30] [info]                Design Rule Check Report
[2025-10-12 19:44:30] [info] =====

[2025-10-12 19:44:30] [info] Report Generated:    2025-10-12
[2025-10-12 19:44:30] [info] Run Start Time:      07:43:38 PM
[2025-10-12 19:44:30] [info] Run End Time:        07:44:30 PM
[2025-10-12 19:44:30] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:44:30] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:44:30] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:44:30] [info] User Name:           ubuntu
[2025-10-12 19:44:30] [info] Cores Used:          8
[2025-10-12 19:44:30] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info]                Top Cell Information
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] Layout Top Cell:     TOP
[2025-10-12 19:44:30] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:44:30] [info] Database Format:       GDSII
[2025-10-12 19:44:30] [info] Verification Mode:    Full Check

[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info]                Type of Errors | Count
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] DB_WARN -> 1
[2025-10-12 19:44:30] [info] LRF_ERR001 -> 14
[2025-10-12 19:44:30] [info] DB_WARN002 -> 159
[2025-10-12 19:44:30] [info] DB_WARN003 -> 161
[2025-10-12 19:44:30] [info] DRC_ERR9 -> 89
[2025-10-12 19:44:30] [info] DRC_ERR -> 67
[2025-10-12 19:44:30] [info] DRC_ERR5 -> 408
[2025-10-12 19:44:30] [info] DRC_ERR4 -> 241
[2025-10-12 19:44:30] [info] DRC_ERR1 -> 301
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info]                Rule Violation Summary
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:44:30] [info] |-----|-----|-----|
[2025-10-12 19:44:30] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:44:30] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:44:30] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:44:30] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:44:30] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:44:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:44:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:44:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:44:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:44:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:44:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:44:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:44:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:44:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:44:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:44:30] [info]
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] Performance Metrics
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] | Stage | Time Taken |
[2025-10-12 19:44:30] [info] |-----|-----|
[2025-10-12 19:44:30] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:44:30] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:44:30] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] Summary
[2025-10-12 19:44:30] [info] -----
[2025-10-12 19:44:30] [info] Total Errors: 1120
[2025-10-12 19:44:30] [info] Total Warnings: 321
[2025-10-12 19:44:30] [info] DRC Status: FAILED
```

lvr_summary_569.rpt

```
[2025-10-12 19:45:26] [info] =====
[2025-10-12 19:45:26] [info] Design Rule Check Report
[2025-10-12 19:45:26] [info] =====

[2025-10-12 19:45:26] [info] Report Generated: 2025-10-12
[2025-10-12 19:45:26] [info] Run Start Time: 07:44:36 PM
[2025-10-12 19:45:26] [info] Run End Time: 07:45:26 PM
[2025-10-12 19:45:26] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:45:26] [info] Tool Version: LVR_2025.1
[2025-10-12 19:45:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:45:26] [info] User Name: ubuntu
[2025-10-12 19:45:26] [info] Cores Used: 8
[2025-10-12 19:45:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Top Cell Information
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Layout Top Cell: TOP
[2025-10-12 19:45:26] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:45:26] [info] Database Format: GDSII
[2025-10-12 19:45:26] [info] Verification Mode: Full Check

[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Type of Errors | Count
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] DB_WARN -> 1
[2025-10-12 19:45:26] [info] LRF_ERR001 -> 14
[2025-10-12 19:45:26] [info] DB_WARN002 -> 159
[2025-10-12 19:45:26] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:45:26] [info] DRC_ERR9 -> 89
[2025-10-12 19:45:26] [info] DRC_ERR -> 67
[2025-10-12 19:45:26] [info] DRC_ERR5 -> 408
[2025-10-12 19:45:26] [info] DRC_ERR4 -> 241
[2025-10-12 19:45:26] [info] DRC_ERR1 -> 301
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Rule Violation Summary
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:45:26] [info] |-----|-----|-----|
[2025-10-12 19:45:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:45:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:45:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:45:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:45:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:45:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:45:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:45:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:45:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:45:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:45:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:45:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:45:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:45:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:45:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Performance Metrics
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] | Stage | Time Taken |
[2025-10-12 19:45:26] [info] |-----|-----|
[2025-10-12 19:45:26] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:45:26] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 19:45:26] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Summary
[2025-10-12 19:45:26] [info] -----
[2025-10-12 19:45:26] [info] Total Errors: 1120
[2025-10-12 19:45:26] [info] Total Warnings: 321
[2025-10-12 19:45:26] [info] DRC Status: FAILED
```

lvr_summary_57.rpt

```
[2025-10-12 11:35:25] [info] =====
[2025-10-12 11:35:25] [info] Design Rule Check Report
[2025-10-12 11:35:25] [info] =====

[2025-10-12 11:35:25] [info] Report Generated: 2025-10-12
[2025-10-12 11:35:25] [info] Run Start Time: 11:34:34 AM
[2025-10-12 11:35:25] [info] Run End Time: 11:35:25 AM
[2025-10-12 11:35:25] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:35:25] [info] Tool Version: LVR_2025.1
[2025-10-12 11:35:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:35:25] [info] User Name: ubuntu
[2025-10-12 11:35:25] [info] Cores Used: 8
[2025-10-12 11:35:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 11:35:25] [info] -----
[2025-10-12 11:35:25] [info] Top Cell Information
[2025-10-12 11:35:25] [info] -----
[2025-10-12 11:35:25] [info] Layout Top Cell: TOP
[2025-10-12 11:35:25] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:35:25] [info] Database Format: GDSII
[2025-10-12 11:35:25] [info] Verification Mode: Full Check
```

```
[2025-10-12 11:35:25] [info] -----
[2025-10-12 11:35:25] [info] Type of Errors | Count
[2025-10-12 11:35:25] [info] -----
[2025-10-12 11:35:25] [info] DB_WARN -> 1
[2025-10-12 11:35:25] [info] LRF_ERR001 -> 14
[2025-10-12 11:35:25] [info] DB_WARN002 -> 159
[2025-10-12 11:35:25] [info] DB_WARN003 -> 161
[2025-10-12 11:35:25] [info] DRC_ERR9 -> 89
[2025-10-12 11:35:25] [info] DRC_ERR -> 67
[2025-10-12 11:35:25] [info] DRC_ERR5 -> 408
[2025-10-12 11:35:25] [info] DRC_ERR4 -> 241
[2025-10-12 11:35:25] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:35:25] [info] -----
[2025-10-12 11:35:25] [info] Rule Violation Summary
```

```
[2025-10-12 11:35:25] [info] -----
[2025-10-12 11:35:25] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:35:25] [info] |-----|-----|-----|
[2025-10-12 11:35:25] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:35:25] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:35:25] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:35:25] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:35:25] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:35:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:35:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:35:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:35:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:35:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:35:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:35:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:35:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:35:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:35:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 11:35:25] [info] -----
```

```
[2025-10-12 11:35:25] [info] Performance Metrics
[2025-10-12 11:35:25] [info] -----
```

```
[2025-10-12 11:35:25] [info] | Stage | Time Taken |
[2025-10-12 11:35:25] [info] |-----|-----|
[2025-10-12 11:35:25] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:35:25] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:35:25] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:35:25] [info] -----
```

```
[2025-10-12 11:35:25] [info] Summary
```

```
[2025-10-12 11:35:25] [info] -----
```

```
[2025-10-12 11:35:25] [info] Total Errors: 1120
[2025-10-12 11:35:25] [info] Total Warnings: 321
[2025-10-12 11:35:25] [info] DRC Status: FAILED
```

lvr_summary_570.rpt

```
[2025-10-12 19:46:25] [info] =====
[2025-10-12 19:46:25] [info] Design Rule Check Report
[2025-10-12 19:46:25] [info] =====

[2025-10-12 19:46:25] [info] Report Generated: 2025-10-12
[2025-10-12 19:46:25] [info] Run Start Time: 07:45:32 PM
[2025-10-12 19:46:25] [info] Run End Time: 07:46:25 PM
[2025-10-12 19:46:25] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:46:25] [info] Tool Version: LVR_2025.1
[2025-10-12 19:46:25] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:46:25] [info] User Name: ubuntu
[2025-10-12 19:46:25] [info] Cores Used: 8
[2025-10-12 19:46:25] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] Top Cell Information
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] Layout Top Cell: TOP
[2025-10-12 19:46:25] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:46:25] [info] Database Format: GDSII
[2025-10-12 19:46:25] [info] Verification Mode: Full Check

[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] Type of Errors | Count
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] DB_WARN -> 1
[2025-10-12 19:46:25] [info] LRF_ERR001 -> 14
[2025-10-12 19:46:25] [info] DB_WARN002 -> 159
[2025-10-12 19:46:25] [info] DB_WARN003 -> 161
[2025-10-12 19:46:25] [info] DRC_ERR9 -> 89
[2025-10-12 19:46:25] [info] DRC_ERR -> 67
[2025-10-12 19:46:25] [info] DRC_ERR5 -> 408
[2025-10-12 19:46:25] [info] DRC_ERR4 -> 241
[2025-10-12 19:46:25] [info] DRC_ERR1 -> 301
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] Rule Violation Summary
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:46:25] [info] |-----|-----|-----|
[2025-10-12 19:46:25] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:46:25] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:46:25] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:46:25] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:46:25] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:46:25] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:46:25] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:46:25] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:46:25] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:46:25] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:46:25] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:46:25] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:46:25] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:46:25] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:46:25] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:46:25] [info]
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] Performance Metrics
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:46:25] [info] |-----|-----|
[2025-10-12 19:46:25] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:46:25] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:46:25] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info]                Summary
[2025-10-12 19:46:25] [info] -----
[2025-10-12 19:46:25] [info] Total Errors:    1120
[2025-10-12 19:46:25] [info] Total Warnings:    321
[2025-10-12 19:46:25] [info] DRC Status:      FAILED
```

lvr_summary_571.rpt

```
[2025-10-12 19:47:22] [info] =====
[2025-10-12 19:47:22] [info]                Design Rule Check Report
[2025-10-12 19:47:22] [info] =====

[2025-10-12 19:47:22] [info] Report Generated:    2025-10-12
[2025-10-12 19:47:22] [info] Run Start Time:      07:46:30 PM
[2025-10-12 19:47:22] [info] Run End Time:        07:47:22 PM
[2025-10-12 19:47:22] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:47:22] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:47:22] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:47:22] [info] User Name:           ubuntu
[2025-10-12 19:47:22] [info] Cores Used:          8
[2025-10-12 19:47:22] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info]                Top Cell Information
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] Layout Top Cell:     TOP
[2025-10-12 19:47:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:47:22] [info] Database Format:       GDSII
[2025-10-12 19:47:22] [info] Verification Mode:    Full Check

[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info]                Type of Errors | Count
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] DB_WARN -> 1
[2025-10-12 19:47:22] [info] LRF_ERR001 -> 14
[2025-10-12 19:47:22] [info] DB_WARN002 -> 159
[2025-10-12 19:47:22] [info] DB_WARN003 -> 161
[2025-10-12 19:47:22] [info] DRC_ERR9 -> 89
[2025-10-12 19:47:22] [info] DRC_ERR -> 67
[2025-10-12 19:47:22] [info] DRC_ERR5 -> 408
[2025-10-12 19:47:22] [info] DRC_ERR4 -> 241
[2025-10-12 19:47:22] [info] DRC_ERR1 -> 301
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info]                Rule Violation Summary
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:47:22] [info] |-----|-----|-----|
[2025-10-12 19:47:22] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:47:22] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:47:22] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:47:22] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:47:22] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:47:22] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:47:22] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:47:22] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:47:22] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:47:22] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:47:22] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:47:22] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:47:22] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:47:22] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:47:22] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:47:22] [info]
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] Performance Metrics
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] | Stage | Time Taken |
[2025-10-12 19:47:22] [info] |-----|-----|
[2025-10-12 19:47:22] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:47:22] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:47:22] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] Summary
[2025-10-12 19:47:22] [info] -----
[2025-10-12 19:47:22] [info] Total Errors: 1120
[2025-10-12 19:47:22] [info] Total Warnings: 321
[2025-10-12 19:47:22] [info] DRC Status: FAILED
```

lvr_summary_572.rpt

```
[2025-10-12 19:48:17] [info] =====
[2025-10-12 19:48:17] [info] Design Rule Check Report
[2025-10-12 19:48:17] [info] =====

[2025-10-12 19:48:17] [info] Report Generated: 2025-10-12
[2025-10-12 19:48:17] [info] Run Start Time: 07:47:26 PM
[2025-10-12 19:48:17] [info] Run End Time: 07:48:17 PM
[2025-10-12 19:48:17] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:48:17] [info] Tool Version: LVR_2025.1
[2025-10-12 19:48:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:48:17] [info] User Name: ubuntu
[2025-10-12 19:48:17] [info] Cores Used: 8
[2025-10-12 19:48:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Top Cell Information
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Layout Top Cell: TOP
[2025-10-12 19:48:17] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:48:17] [info] Database Format: GDSII
[2025-10-12 19:48:17] [info] Verification Mode: Full Check

[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Type of Errors | Count
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] DB_WARN -> 1
[2025-10-12 19:48:17] [info] LRF_ERR001 -> 14
[2025-10-12 19:48:17] [info] DB_WARN002 -> 159
[2025-10-12 19:48:17] [info] DB_WARN003 -> 161
```

```
[2025-10-12 19:48:17] [info] DRC_ERR9 -> 89
[2025-10-12 19:48:17] [info] DRC_ERR -> 67
[2025-10-12 19:48:17] [info] DRC_ERR5 -> 408
[2025-10-12 19:48:17] [info] DRC_ERR4 -> 241
[2025-10-12 19:48:17] [info] DRC_ERR1 -> 301
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Rule Violation Summary
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:48:17] [info] |-----|-----|-----|
[2025-10-12 19:48:17] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:48:17] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:48:17] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:48:17] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:48:17] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:48:17] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 19:48:17] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:48:17] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:48:17] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:48:17] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:48:17] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:48:17] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:48:17] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:48:17] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:48:17] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:48:17] [info] |
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Performance Metrics
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] | Stage          | Time Taken |
[2025-10-12 19:48:17] [info] |-----|-----|
[2025-10-12 19:48:17] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:48:17] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:48:17] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 19:48:17] [info] |
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Summary
[2025-10-12 19:48:17] [info] -----
[2025-10-12 19:48:17] [info] Total Errors: 1120
[2025-10-12 19:48:17] [info] Total Warnings: 321
[2025-10-12 19:48:17] [info] DRC Status: FAILED
```

lvr_summary_573.rpt

```
[2025-10-12 19:49:16] [info] =====
[2025-10-12 19:49:16] [info] Design Rule Check Report
[2025-10-12 19:49:16] [info] =====

[2025-10-12 19:49:16] [info] Report Generated: 2025-10-12
[2025-10-12 19:49:16] [info] Run Start Time: 07:48:24 PM
[2025-10-12 19:49:16] [info] Run End Time: 07:49:16 PM
[2025-10-12 19:49:16] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:49:16] [info] Tool Version: LVR_2025.1
[2025-10-12 19:49:16] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:49:16] [info] User Name: ubuntu
[2025-10-12 19:49:16] [info] Cores Used: 8
[2025-10-12 19:49:16] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Top Cell Information
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Layout Top Cell:      TOP
[2025-10-12 19:49:16] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:49:16] [info] Database Format:      GDSII
[2025-10-12 19:49:16] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Type of Errors | Count
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] DB_WARN -> 1
[2025-10-12 19:49:16] [info] LRF_ERR001 -> 14
[2025-10-12 19:49:16] [info] DB_WARN002 -> 159
[2025-10-12 19:49:16] [info] DB_WARN003 -> 161
[2025-10-12 19:49:16] [info] DRC_ERR9 -> 89
[2025-10-12 19:49:16] [info] DRC_ERR -> 67
[2025-10-12 19:49:16] [info] DRC_ERR5 -> 408
[2025-10-12 19:49:16] [info] DRC_ERR4 -> 241
[2025-10-12 19:49:16] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Rule Violation Summary
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:49:16] [info] |-----|-----|-----|
[2025-10-12 19:49:16] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:49:16] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:49:16] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:49:16] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:49:16] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:49:16] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:49:16] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:49:16] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:49:16] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:49:16] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:49:16] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:49:16] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:49:16] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:49:16] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:49:16] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:49:16] [info] |
```

```
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Performance Metrics
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] | Stage          | Time Taken |
[2025-10-12 19:49:16] [info] |-----|-----|
[2025-10-12 19:49:16] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:49:16] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 19:49:16] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Summary
[2025-10-12 19:49:16] [info] -----
[2025-10-12 19:49:16] [info] Total Errors:      1120
[2025-10-12 19:49:16] [info] Total Warnings:    321
[2025-10-12 19:49:16] [info] DRC Status:        FAILED
```

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```
[2025-10-12 19:50:12] [info] =====
[2025-10-12 19:50:12] [info] Design Rule Check Report
[2025-10-12 19:50:12] [info] =====

[2025-10-12 19:50:12] [info] Report Generated: 2025-10-12
[2025-10-12 19:50:12] [info] Run Start Time: 07:49:20 PM
[2025-10-12 19:50:12] [info] Run End Time: 07:50:12 PM
[2025-10-12 19:50:12] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 19:50:12] [info] Tool Version: LVR_2025.1
[2025-10-12 19:50:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:50:12] [info] User Name: ubuntu
[2025-10-12 19:50:12] [info] Cores Used: 8
[2025-10-12 19:50:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] Top Cell Information
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] Layout Top Cell: TOP
[2025-10-12 19:50:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:50:12] [info] Database Format: GDSII
[2025-10-12 19:50:12] [info] Verification Mode: Full Check

[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] Type of Errors | Count
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] DB_WARN -> 1
[2025-10-12 19:50:12] [info] LRF_ERR001 -> 14
[2025-10-12 19:50:12] [info] DB_WARN002 -> 159
[2025-10-12 19:50:12] [info] DB_WARN003 -> 161
[2025-10-12 19:50:12] [info] DRC_ERR9 -> 89
[2025-10-12 19:50:12] [info] DRC_ERR -> 67
[2025-10-12 19:50:12] [info] DRC_ERR5 -> 408
[2025-10-12 19:50:12] [info] DRC_ERR4 -> 241
[2025-10-12 19:50:12] [info] DRC_ERR1 -> 301
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] Rule Violation Summary
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:50:12] [info] |-----|-----|-----|
[2025-10-12 19:50:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:50:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:50:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:50:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:50:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:50:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:50:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:50:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:50:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:50:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:50:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:50:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:50:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:50:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:50:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:50:12] [info]
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] Performance Metrics
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:50:12] [info] |-----|-----|
[2025-10-12 19:50:12] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:50:12] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 19:50:12] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info]                Summary
[2025-10-12 19:50:12] [info] -----
[2025-10-12 19:50:12] [info] Total Errors:    1120
[2025-10-12 19:50:12] [info] Total Warnings:    321
[2025-10-12 19:50:12] [info] DRC Status:      FAILED
```

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```
[2025-10-12 19:51:08] [info] =====
[2025-10-12 19:51:08] [info]                Design Rule Check Report
[2025-10-12 19:51:08] [info] =====

[2025-10-12 19:51:08] [info] Report Generated:    2025-10-12
[2025-10-12 19:51:08] [info] Run Start Time:      07:50:17 PM
[2025-10-12 19:51:08] [info] Run End Time:        07:51:08 PM
[2025-10-12 19:51:08] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 19:51:08] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:51:08] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:51:08] [info] User Name:           ubuntu
[2025-10-12 19:51:08] [info] Cores Used:          8
[2025-10-12 19:51:08] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info]                Top Cell Information
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] Layout Top Cell:     TOP
[2025-10-12 19:51:08] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:51:08] [info] Database Format:       GDSII
[2025-10-12 19:51:08] [info] Verification Mode:    Full Check

[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info]                Type of Errors | Count
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] DB_WARN -> 1
[2025-10-12 19:51:08] [info] LRF_ERR001 -> 14
[2025-10-12 19:51:08] [info] DB_WARN002 -> 159
[2025-10-12 19:51:08] [info] DB_WARN003 -> 161
[2025-10-12 19:51:08] [info] DRC_ERR9 -> 89
[2025-10-12 19:51:08] [info] DRC_ERR -> 67
[2025-10-12 19:51:08] [info] DRC_ERR5 -> 408
[2025-10-12 19:51:08] [info] DRC_ERR4 -> 241
[2025-10-12 19:51:08] [info] DRC_ERR1 -> 301
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info]                Rule Violation Summary
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:51:08] [info] |-----|-----|-----|
[2025-10-12 19:51:08] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:51:08] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:51:08] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:51:08] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:51:08] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 19:51:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:51:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:51:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:51:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:51:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:51:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:51:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:51:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:51:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:51:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:51:08] [info]
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] Performance Metrics
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] | Stage | Time Taken |
[2025-10-12 19:51:08] [info] |-----|-----|
[2025-10-12 19:51:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:51:08] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 19:51:08] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] Summary
[2025-10-12 19:51:08] [info] -----
[2025-10-12 19:51:08] [info] Total Errors: 1120
[2025-10-12 19:51:08] [info] Total Warnings: 321
[2025-10-12 19:51:08] [info] DRC Status: FAILED
```

lvr_summary_576.rpt

```
[2025-10-12 19:52:05] [info] =====
[2025-10-12 19:52:05] [info] Design Rule Check Report
[2025-10-12 19:52:05] [info] =====

[2025-10-12 19:52:05] [info] Report Generated: 2025-10-12
[2025-10-12 19:52:05] [info] Run Start Time: 07:51:14 PM
[2025-10-12 19:52:05] [info] Run End Time: 07:52:05 PM
[2025-10-12 19:52:05] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:52:05] [info] Tool Version: LVR_2025.1
[2025-10-12 19:52:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:52:05] [info] User Name: ubuntu
[2025-10-12 19:52:05] [info] Cores Used: 8
[2025-10-12 19:52:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Top Cell Information
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Layout Top Cell: TOP
[2025-10-12 19:52:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:52:05] [info] Database Format: GDSII
[2025-10-12 19:52:05] [info] Verification Mode: Full Check

[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Type of Errors | Count
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] DB_WARN -> 1
[2025-10-12 19:52:05] [info] LRF_ERR001 -> 14
[2025-10-12 19:52:05] [info] DB_WARN002 -> 159
[2025-10-12 19:52:05] [info] DB_WARN003 -> 161
```

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[2025-10-12 19:52:05] [info] DRC_ERR9 -> 89
[2025-10-12 19:52:05] [info] DRC_ERR -> 67
[2025-10-12 19:52:05] [info] DRC_ERR5 -> 408
[2025-10-12 19:52:05] [info] DRC_ERR4 -> 241
[2025-10-12 19:52:05] [info] DRC_ERR1 -> 301
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Rule Violation Summary
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:52:05] [info] |-----|-----|-----|
[2025-10-12 19:52:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:52:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:52:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:52:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:52:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:52:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:52:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:52:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:52:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:52:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:52:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:52:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:52:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:52:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:52:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Performance Metrics
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] | Stage | Time Taken |
[2025-10-12 19:52:05] [info] |-----|-----|
[2025-10-12 19:52:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:52:05] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 19:52:05] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Summary
[2025-10-12 19:52:05] [info] -----
[2025-10-12 19:52:05] [info] Total Errors: 1120
[2025-10-12 19:52:05] [info] Total Warnings: 321
[2025-10-12 19:52:05] [info] DRC Status: FAILED

```

lvr_summary_577.rpt

```

[2025-10-12 19:53:02] [info] =====
[2025-10-12 19:53:02] [info] Design Rule Check Report
[2025-10-12 19:53:02] [info] =====

[2025-10-12 19:53:02] [info] Report Generated: 2025-10-12
[2025-10-12 19:53:02] [info] Run Start Time: 07:52:11 PM
[2025-10-12 19:53:02] [info] Run End Time: 07:53:02 PM
[2025-10-12 19:53:02] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:53:02] [info] Tool Version: LVR_2025.1
[2025-10-12 19:53:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:53:02] [info] User Name: ubuntu
[2025-10-12 19:53:02] [info] Cores Used: 8
[2025-10-12 19:53:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] Top Cell Information
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] Layout Top Cell:      TOP
[2025-10-12 19:53:02] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:53:02] [info] Database Format:      GDSII
[2025-10-12 19:53:02] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] Type of Errors | Count
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] DB_WARN -> 1
[2025-10-12 19:53:02] [info] LRF_ERR001 -> 14
[2025-10-12 19:53:02] [info] DB_WARN002 -> 159
[2025-10-12 19:53:02] [info] DB_WARN003 -> 161
[2025-10-12 19:53:02] [info] DRC_ERR9 -> 89
[2025-10-12 19:53:02] [info] DRC_ERR -> 67
[2025-10-12 19:53:02] [info] DRC_ERR5 -> 408
[2025-10-12 19:53:02] [info] DRC_ERR4 -> 241
[2025-10-12 19:53:02] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] Rule Violation Summary
```

```
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:53:02] [info] |-----|-----|-----|
[2025-10-12 19:53:02] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:53:02] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:53:02] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:53:02] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:53:02] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:53:02] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:53:02] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:53:02] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:53:02] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:53:02] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:53:02] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:53:02] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:53:02] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:53:02] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:53:02] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:53:02] [info] -----
```

```
[2025-10-12 19:53:02] [info] Performance Metrics
```

```
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] | Stage          | Time Taken |
[2025-10-12 19:53:02] [info] |-----|-----|
[2025-10-12 19:53:02] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:53:02] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 19:53:02] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:53:02] [info] -----
```

```
[2025-10-12 19:53:02] [info] Summary
```

```
[2025-10-12 19:53:02] [info] -----
[2025-10-12 19:53:02] [info] Total Errors:      1120
[2025-10-12 19:53:02] [info] Total Warnings:    321
[2025-10-12 19:53:02] [info] DRC Status:        FAILED
```

lvr_summary_578.rpt

```
[2025-10-12 19:54:01] [info] =====
[2025-10-12 19:54:01] [info] Design Rule Check Report
[2025-10-12 19:54:01] [info] =====

[2025-10-12 19:54:01] [info] Report Generated: 2025-10-12
[2025-10-12 19:54:01] [info] Run Start Time: 07:53:10 PM
[2025-10-12 19:54:01] [info] Run End Time: 07:54:01 PM
[2025-10-12 19:54:01] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:54:01] [info] Tool Version: LVR_2025.1
[2025-10-12 19:54:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:54:01] [info] User Name: ubuntu
[2025-10-12 19:54:01] [info] Cores Used: 8
[2025-10-12 19:54:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Top Cell Information
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Layout Top Cell: TOP
[2025-10-12 19:54:01] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:54:01] [info] Database Format: GDSII
[2025-10-12 19:54:01] [info] Verification Mode: Full Check

[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Type of Errors | Count
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] DB_WARN -> 1
[2025-10-12 19:54:01] [info] LRF_ERR001 -> 14
[2025-10-12 19:54:01] [info] DB_WARN002 -> 159
[2025-10-12 19:54:01] [info] DB_WARN003 -> 161
[2025-10-12 19:54:01] [info] DRC_ERR9 -> 89
[2025-10-12 19:54:01] [info] DRC_ERR -> 67
[2025-10-12 19:54:01] [info] DRC_ERR5 -> 408
[2025-10-12 19:54:01] [info] DRC_ERR4 -> 241
[2025-10-12 19:54:01] [info] DRC_ERR1 -> 301
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Rule Violation Summary
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:54:01] [info] |-----|-----|-----|
[2025-10-12 19:54:01] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:54:01] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:54:01] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:54:01] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:54:01] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:54:01] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:54:01] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:54:01] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:54:01] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:54:01] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:54:01] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:54:01] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:54:01] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:54:01] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:54:01] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:54:01] [info]
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Performance Metrics
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:54:01] [info] |-----|-----|
[2025-10-12 19:54:01] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:54:01] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 19:54:01] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Summary
[2025-10-12 19:54:01] [info] -----
[2025-10-12 19:54:01] [info] Total Errors: 1120
[2025-10-12 19:54:01] [info] Total Warnings: 321
[2025-10-12 19:54:01] [info] DRC Status: FAILED
```

lvr_summary_579.rpt

```
[2025-10-12 19:54:59] [info] =====
[2025-10-12 19:54:59] [info] Design Rule Check Report
[2025-10-12 19:54:59] [info] =====

[2025-10-12 19:54:59] [info] Report Generated: 2025-10-12
[2025-10-12 19:54:59] [info] Run Start Time: 07:54:08 PM
[2025-10-12 19:54:59] [info] Run End Time: 07:54:59 PM
[2025-10-12 19:54:59] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:54:59] [info] Tool Version: LVR_2025.1
[2025-10-12 19:54:59] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:54:59] [info] User Name: ubuntu
[2025-10-12 19:54:59] [info] Cores Used: 8
[2025-10-12 19:54:59] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Top Cell Information
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Layout Top Cell: TOP
[2025-10-12 19:54:59] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:54:59] [info] Database Format: GDSII
[2025-10-12 19:54:59] [info] Verification Mode: Full Check

[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Type of Errors | Count
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] DB_WARN -> 1
[2025-10-12 19:54:59] [info] LRF_ERR001 -> 14
[2025-10-12 19:54:59] [info] DB_WARN002 -> 159
[2025-10-12 19:54:59] [info] DB_WARN003 -> 161
[2025-10-12 19:54:59] [info] DRC_ERR9 -> 89
[2025-10-12 19:54:59] [info] DRC_ERR -> 67
[2025-10-12 19:54:59] [info] DRC_ERR5 -> 408
[2025-10-12 19:54:59] [info] DRC_ERR4 -> 241
[2025-10-12 19:54:59] [info] DRC_ERR1 -> 301
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Rule Violation Summary
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:54:59] [info] |-----|-----|-----|
[2025-10-12 19:54:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:54:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:54:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:54:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:54:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 19:54:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:54:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:54:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:54:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:54:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:54:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:54:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:54:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:54:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:54:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:54:59] [info]
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Performance Metrics
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] | Stage | Time Taken |
[2025-10-12 19:54:59] [info] |-----|-----|
[2025-10-12 19:54:59] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:54:59] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:54:59] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Summary
[2025-10-12 19:54:59] [info] -----
[2025-10-12 19:54:59] [info] Total Errors: 1120
[2025-10-12 19:54:59] [info] Total Warnings: 321
[2025-10-12 19:54:59] [info] DRC Status: FAILED
```

lvr_summary_58.rpt

```
[2025-10-12 11:36:23] [info] =====
[2025-10-12 11:36:23] [info] Design Rule Check Report
[2025-10-12 11:36:23] [info] =====

[2025-10-12 11:36:23] [info] Report Generated: 2025-10-12
[2025-10-12 11:36:23] [info] Run Start Time: 11:35:32 AM
[2025-10-12 11:36:23] [info] Run End Time: 11:36:23 AM
[2025-10-12 11:36:23] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:36:23] [info] Tool Version: LVR_2025.1
[2025-10-12 11:36:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:36:23] [info] User Name: ubuntu
[2025-10-12 11:36:23] [info] Cores Used: 8
[2025-10-12 11:36:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Top Cell Information
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Layout Top Cell: TOP
[2025-10-12 11:36:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:36:23] [info] Database Format: GDSII
[2025-10-12 11:36:23] [info] Verification Mode: Full Check

[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Type of Errors | Count
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] DB_WARN -> 1
[2025-10-12 11:36:23] [info] LRF_ERR001 -> 14
[2025-10-12 11:36:23] [info] DB_WARN002 -> 159
[2025-10-12 11:36:23] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:36:23] [info] DRC_ERR9 -> 89
[2025-10-12 11:36:23] [info] DRC_ERR -> 67
[2025-10-12 11:36:23] [info] DRC_ERR5 -> 408
[2025-10-12 11:36:23] [info] DRC_ERR4 -> 241
[2025-10-12 11:36:23] [info] DRC_ERR1 -> 301
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Rule Violation Summary
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:36:23] [info] |-----|-----|-----|
[2025-10-12 11:36:23] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:36:23] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:36:23] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:36:23] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:36:23] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:36:23] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:36:23] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:36:23] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:36:23] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:36:23] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:36:23] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:36:23] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:36:23] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:36:23] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:36:23] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Performance Metrics
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] | Stage | Time Taken |
[2025-10-12 11:36:23] [info] |-----|-----|
[2025-10-12 11:36:23] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:36:23] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:36:23] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Summary
[2025-10-12 11:36:23] [info] -----
[2025-10-12 11:36:23] [info] Total Errors: 1120
[2025-10-12 11:36:23] [info] Total Warnings: 321
[2025-10-12 11:36:23] [info] DRC Status: FAILED
```

lvr_summary_580.rpt

```
[2025-10-12 19:55:55] [info] =====
[2025-10-12 19:55:55] [info] Design Rule Check Report
[2025-10-12 19:55:55] [info] =====

[2025-10-12 19:55:55] [info] Report Generated: 2025-10-12
[2025-10-12 19:55:55] [info] Run Start Time: 07:55:04 PM
[2025-10-12 19:55:55] [info] Run End Time: 07:55:55 PM
[2025-10-12 19:55:55] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:55:55] [info] Tool Version: LVR_2025.1
[2025-10-12 19:55:55] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:55:55] [info] User Name: ubuntu
[2025-10-12 19:55:55] [info] Cores Used: 8
[2025-10-12 19:55:55] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Top Cell Information
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Layout Top Cell:      TOP
[2025-10-12 19:55:55] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:55:55] [info] Database Format:      GDSII
[2025-10-12 19:55:55] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Type of Errors | Count
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] DB_WARN -> 1
[2025-10-12 19:55:55] [info] LRF_ERR001 -> 14
[2025-10-12 19:55:55] [info] DB_WARN002 -> 159
[2025-10-12 19:55:55] [info] DB_WARN003 -> 161
[2025-10-12 19:55:55] [info] DRC_ERR9 -> 89
[2025-10-12 19:55:55] [info] DRC_ERR -> 67
[2025-10-12 19:55:55] [info] DRC_ERR5 -> 408
[2025-10-12 19:55:55] [info] DRC_ERR4 -> 241
[2025-10-12 19:55:55] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Rule Violation Summary
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:55:55] [info] |-----|-----|-----|
[2025-10-12 19:55:55] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:55:55] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:55:55] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:55:55] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:55:55] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:55:55] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:55:55] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:55:55] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:55:55] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:55:55] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:55:55] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:55:55] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:55:55] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:55:55] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:55:55] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Performance Metrics
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] | Stage          | Time Taken |
[2025-10-12 19:55:55] [info] |-----|-----|
[2025-10-12 19:55:55] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:55:55] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:55:55] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Summary
[2025-10-12 19:55:55] [info] -----
[2025-10-12 19:55:55] [info] Total Errors:      1120
[2025-10-12 19:55:55] [info] Total Warnings:    321
[2025-10-12 19:55:55] [info] DRC Status:        FAILED
```


lvr_summary_581.rpt

```
[2025-10-12 19:56:52] [info] =====
[2025-10-12 19:56:52] [info]                      Design Rule Check Report
[2025-10-12 19:56:52] [info] =====

[2025-10-12 19:56:52] [info] Report Generated:    2025-10-12
[2025-10-12 19:56:52] [info] Run Start Time:      07:56:01 PM
[2025-10-12 19:56:52] [info] Run End Time:        07:56:52 PM
[2025-10-12 19:56:52] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 19:56:52] [info] Tool Version:       LVR_2025.1
[2025-10-12 19:56:52] [info] Host Name:          ip-172-31-37-173
[2025-10-12 19:56:52] [info] User Name:          ubuntu
[2025-10-12 19:56:52] [info] Cores Used:         8
[2025-10-12 19:56:52] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info]                      Top Cell Information
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info] Layout Top Cell:    TOP
[2025-10-12 19:56:52] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 19:56:52] [info] Database Format:     GDSII
[2025-10-12 19:56:52] [info] Verification Mode:   Full Check

[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info]                      Type of Errors | Count
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info] DB_WARN -> 1
[2025-10-12 19:56:52] [info] LRF_ERR001 -> 14
[2025-10-12 19:56:52] [info] DB_WARN002 -> 159
[2025-10-12 19:56:52] [info] DB_WARN003 -> 161
[2025-10-12 19:56:52] [info] DRC_ERR9 -> 89
[2025-10-12 19:56:52] [info] DRC_ERR -> 67
[2025-10-12 19:56:52] [info] DRC_ERR5 -> 408
[2025-10-12 19:56:52] [info] DRC_ERR4 -> 241
[2025-10-12 19:56:52] [info] DRC_ERR1 -> 301
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info]                      Rule Violation Summary
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:56:52] [info] |-----|-----|-----|
[2025-10-12 19:56:52] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:56:52] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:56:52] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:56:52] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:56:52] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:56:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 19:56:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:56:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:56:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:56:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:56:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:56:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:56:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:56:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:56:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:56:52] [info]
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info]                      Performance Metrics
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info] | Stage | Time Taken |
```

```
[2025-10-12 19:56:52] [info] |-----|-----|
[2025-10-12 19:56:52] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 19:56:52] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 19:56:52] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info]                Summary
[2025-10-12 19:56:52] [info] -----
[2025-10-12 19:56:52] [info] Total Errors:    1120
[2025-10-12 19:56:52] [info] Total Warnings:    321
[2025-10-12 19:56:52] [info] DRC Status:      FAILED
```

lvr_summary_582.rpt

```
[2025-10-12 19:57:49] [info] =====
[2025-10-12 19:57:49] [info]                Design Rule Check Report
[2025-10-12 19:57:49] [info] =====

[2025-10-12 19:57:49] [info] Report Generated:    2025-10-12
[2025-10-12 19:57:49] [info] Run Start Time:      07:56:57 PM
[2025-10-12 19:57:49] [info] Run End Time:        07:57:49 PM
[2025-10-12 19:57:49] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 19:57:49] [info] Tool Version:        LVR_2025.1
[2025-10-12 19:57:49] [info] Host Name:           ip-172-31-37-173
[2025-10-12 19:57:49] [info] User Name:           ubuntu
[2025-10-12 19:57:49] [info] Cores Used:          8
[2025-10-12 19:57:49] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info]                Top Cell Information
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] Layout Top Cell:     TOP
[2025-10-12 19:57:49] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:57:49] [info] Database Format:       GDSII
[2025-10-12 19:57:49] [info] Verification Mode:    Full Check

[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info]                Type of Errors | Count
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] DB_WARN -> 1
[2025-10-12 19:57:49] [info] LRF_ERR001 -> 14
[2025-10-12 19:57:49] [info] DB_WARN002 -> 159
[2025-10-12 19:57:49] [info] DB_WARN003 -> 161
[2025-10-12 19:57:49] [info] DRC_ERR9 -> 89
[2025-10-12 19:57:49] [info] DRC_ERR -> 67
[2025-10-12 19:57:49] [info] DRC_ERR5 -> 408
[2025-10-12 19:57:49] [info] DRC_ERR4 -> 241
[2025-10-12 19:57:49] [info] DRC_ERR1 -> 301
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info]                Rule Violation Summary
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 19:57:49] [info] |-----|-----|-----|
[2025-10-12 19:57:49] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:57:49] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:57:49] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:57:49] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:57:49] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 19:57:49] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:57:49] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:57:49] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:57:49] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:57:49] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:57:49] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:57:49] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:57:49] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:57:49] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:57:49] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:57:49] [info]
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] Performance Metrics
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] | Stage | Time Taken |
[2025-10-12 19:57:49] [info] |-----|-----|
[2025-10-12 19:57:49] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:57:49] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 19:57:49] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] Summary
[2025-10-12 19:57:49] [info] -----
[2025-10-12 19:57:49] [info] Total Errors: 1120
[2025-10-12 19:57:49] [info] Total Warnings: 321
[2025-10-12 19:57:49] [info] DRC Status: FAILED
```

lvr_summary_583.rpt

```
[2025-10-12 19:58:45] [info] =====
[2025-10-12 19:58:45] [info] Design Rule Check Report
[2025-10-12 19:58:45] [info] =====

[2025-10-12 19:58:45] [info] Report Generated: 2025-10-12
[2025-10-12 19:58:45] [info] Run Start Time: 07:57:54 PM
[2025-10-12 19:58:45] [info] Run End Time: 07:58:45 PM
[2025-10-12 19:58:45] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 19:58:45] [info] Tool Version: LVR_2025.1
[2025-10-12 19:58:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:58:45] [info] User Name: ubuntu
[2025-10-12 19:58:45] [info] Cores Used: 8
[2025-10-12 19:58:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Top Cell Information
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Layout Top Cell: TOP
[2025-10-12 19:58:45] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 19:58:45] [info] Database Format: GDSII
[2025-10-12 19:58:45] [info] Verification Mode: Full Check

[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Type of Errors | Count
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] DB_WARN -> 1
[2025-10-12 19:58:45] [info] LRF_ERR001 -> 14
[2025-10-12 19:58:45] [info] DB_WARN002 -> 159
[2025-10-12 19:58:45] [info] DB_WARN003 -> 161
```

```

[2025-10-12 19:58:45] [info] DRC_ERR9 -> 89
[2025-10-12 19:58:45] [info] DRC_ERR -> 67
[2025-10-12 19:58:45] [info] DRC_ERR5 -> 408
[2025-10-12 19:58:45] [info] DRC_ERR4 -> 241
[2025-10-12 19:58:45] [info] DRC_ERR1 -> 301
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Rule Violation Summary
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] | Rule ID | Description | Error Count |
[2025-10-12 19:58:45] [info] |-----|-----|-----|
[2025-10-12 19:58:45] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 19:58:45] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 19:58:45] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 19:58:45] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 19:58:45] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 19:58:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 19:58:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 19:58:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 19:58:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 19:58:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 19:58:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 19:58:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 19:58:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 19:58:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 19:58:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Performance Metrics
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] | Stage | Time Taken |
[2025-10-12 19:58:45] [info] |-----|-----|
[2025-10-12 19:58:45] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 19:58:45] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 19:58:45] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Summary
[2025-10-12 19:58:45] [info] -----
[2025-10-12 19:58:45] [info] Total Errors: 1120
[2025-10-12 19:58:45] [info] Total Warnings: 321
[2025-10-12 19:58:45] [info] DRC Status: FAILED

```

lvr_summary_584.rpt

```

[2025-10-12 19:59:42] [info] =====
[2025-10-12 19:59:42] [info] Design Rule Check Report
[2025-10-12 19:59:42] [info] =====

[2025-10-12 19:59:42] [info] Report Generated: 2025-10-12
[2025-10-12 19:59:42] [info] Run Start Time: 07:58:51 PM
[2025-10-12 19:59:42] [info] Run End Time: 07:59:42 PM
[2025-10-12 19:59:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 19:59:42] [info] Tool Version: LVR_2025.1
[2025-10-12 19:59:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 19:59:42] [info] User Name: ubuntu
[2025-10-12 19:59:42] [info] Cores Used: 8
[2025-10-12 19:59:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] Top Cell Information
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] Layout Top Cell:      TOP
[2025-10-12 19:59:42] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 19:59:42] [info] Database Format:      GDSII
[2025-10-12 19:59:42] [info] Verification Mode:    Full Check
```

```
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] Type of Errors | Count
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] DB_WARN -> 1
[2025-10-12 19:59:42] [info] LRF_ERR001 -> 14
[2025-10-12 19:59:42] [info] DB_WARN002 -> 159
[2025-10-12 19:59:42] [info] DB_WARN003 -> 161
[2025-10-12 19:59:42] [info] DRC_ERR9 -> 89
[2025-10-12 19:59:42] [info] DRC_ERR -> 67
[2025-10-12 19:59:42] [info] DRC_ERR5 -> 408
[2025-10-12 19:59:42] [info] DRC_ERR4 -> 241
[2025-10-12 19:59:42] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] Rule Violation Summary
```

```
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 19:59:42] [info] |-----|-----|-----|
[2025-10-12 19:59:42] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 19:59:42] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 19:59:42] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 19:59:42] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 19:59:42] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 19:59:42] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 19:59:42] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 19:59:42] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 19:59:42] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 19:59:42] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 19:59:42] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 19:59:42] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 19:59:42] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 19:59:42] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 19:59:42] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 19:59:42] [info] -----
```

```
[2025-10-12 19:59:42] [info] Performance Metrics
```

```
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] | Stage          | Time Taken |
[2025-10-12 19:59:42] [info] |-----|-----|
[2025-10-12 19:59:42] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 19:59:42] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 19:59:42] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 19:59:42] [info] -----
```

```
[2025-10-12 19:59:42] [info] Summary
```

```
[2025-10-12 19:59:42] [info] -----
[2025-10-12 19:59:42] [info] Total Errors:      1120
[2025-10-12 19:59:42] [info] Total Warnings:    321
[2025-10-12 19:59:42] [info] DRC Status:        FAILED
```

lvr_summary_585.rpt

```
[2025-10-12 20:00:40] [info] =====
[2025-10-12 20:00:40] [info]                      Design Rule Check Report
[2025-10-12 20:00:40] [info] =====

[2025-10-12 20:00:40] [info] Report Generated:      2025-10-12
[2025-10-12 20:00:40] [info] Run Start Time:       07:59:48 PM
[2025-10-12 20:00:40] [info] Run End Time:         08:00:40 PM
[2025-10-12 20:00:40] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 20:00:40] [info] Tool Version:         LVR_2025.1
[2025-10-12 20:00:40] [info] Host Name:            ip-172-31-37-173
[2025-10-12 20:00:40] [info] User Name:            ubuntu
[2025-10-12 20:00:40] [info] Cores Used:           8
[2025-10-12 20:00:40] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info]                      Top Cell Information
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info] Layout Top Cell:      TOP
[2025-10-12 20:00:40] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:00:40] [info] Database Format:        GDSII
[2025-10-12 20:00:40] [info] Verification Mode:     Full Check

[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info]                      Type of Errors | Count
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info] DB_WARN -> 1
[2025-10-12 20:00:40] [info] LRF_ERR001 -> 14
[2025-10-12 20:00:40] [info] DB_WARN002 -> 159
[2025-10-12 20:00:40] [info] DB_WARN003 -> 161
[2025-10-12 20:00:40] [info] DRC_ERR9 -> 89
[2025-10-12 20:00:40] [info] DRC_ERR -> 67
[2025-10-12 20:00:40] [info] DRC_ERR5 -> 408
[2025-10-12 20:00:40] [info] DRC_ERR4 -> 241
[2025-10-12 20:00:40] [info] DRC_ERR1 -> 301
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info]                      Rule Violation Summary
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:00:40] [info] |-----|-----|-----|
[2025-10-12 20:00:40] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:00:40] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:00:40] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:00:40] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:00:40] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:00:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:00:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:00:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:00:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:00:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:00:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:00:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:00:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:00:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:00:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:00:40] [info]
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info]                      Performance Metrics
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:00:40] [info] |-----|-----|
[2025-10-12 20:00:40] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:00:40] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:00:40] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info] Summary
[2025-10-12 20:00:40] [info] -----
[2025-10-12 20:00:40] [info] Total Errors: 1120
[2025-10-12 20:00:40] [info] Total Warnings: 321
[2025-10-12 20:00:40] [info] DRC Status: FAILED
```

lvr_summary_586.rpt

```
[2025-10-12 20:01:36] [info] =====
[2025-10-12 20:01:36] [info] Design Rule Check Report
[2025-10-12 20:01:36] [info] =====

[2025-10-12 20:01:36] [info] Report Generated: 2025-10-12
[2025-10-12 20:01:36] [info] Run Start Time: 08:00:44 PM
[2025-10-12 20:01:36] [info] Run End Time: 08:01:36 PM
[2025-10-12 20:01:36] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 20:01:36] [info] Tool Version: LVR_2025.1
[2025-10-12 20:01:36] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:01:36] [info] User Name: ubuntu
[2025-10-12 20:01:36] [info] Cores Used: 8
[2025-10-12 20:01:36] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Top Cell Information
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Layout Top Cell: TOP
[2025-10-12 20:01:36] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:01:36] [info] Database Format: GDSII
[2025-10-12 20:01:36] [info] Verification Mode: Full Check

[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Type of Errors | Count
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] DB_WARN -> 1
[2025-10-12 20:01:36] [info] LRF_ERR001 -> 14
[2025-10-12 20:01:36] [info] DB_WARN002 -> 159
[2025-10-12 20:01:36] [info] DB_WARN003 -> 161
[2025-10-12 20:01:36] [info] DRC_ERR9 -> 89
[2025-10-12 20:01:36] [info] DRC_ERR -> 67
[2025-10-12 20:01:36] [info] DRC_ERR5 -> 408
[2025-10-12 20:01:36] [info] DRC_ERR4 -> 241
[2025-10-12 20:01:36] [info] DRC_ERR1 -> 301
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Rule Violation Summary
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:01:36] [info] |-----|-----|-----|
[2025-10-12 20:01:36] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:01:36] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:01:36] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:01:36] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:01:36] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 20:01:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:01:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:01:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:01:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:01:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:01:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:01:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:01:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:01:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:01:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:01:36] [info]
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Performance Metrics
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] | Stage | Time Taken |
[2025-10-12 20:01:36] [info] |-----|-----|
[2025-10-12 20:01:36] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:01:36] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:01:36] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Summary
[2025-10-12 20:01:36] [info] -----
[2025-10-12 20:01:36] [info] Total Errors: 1120
[2025-10-12 20:01:36] [info] Total Warnings: 321
[2025-10-12 20:01:36] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:02:32] [info] =====
[2025-10-12 20:02:32] [info] Design Rule Check Report
[2025-10-12 20:02:32] [info] =====

[2025-10-12 20:02:32] [info] Report Generated: 2025-10-12
[2025-10-12 20:02:32] [info] Run Start Time: 08:01:41 PM
[2025-10-12 20:02:32] [info] Run End Time: 08:02:32 PM
[2025-10-12 20:02:32] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:02:32] [info] Tool Version: LVR_2025.1
[2025-10-12 20:02:32] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:02:32] [info] User Name: ubuntu
[2025-10-12 20:02:32] [info] Cores Used: 8
[2025-10-12 20:02:32] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Top Cell Information
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Layout Top Cell: TOP
[2025-10-12 20:02:32] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:02:32] [info] Database Format: GDSII
[2025-10-12 20:02:32] [info] Verification Mode: Full Check

[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Type of Errors | Count
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] DB_WARN -> 1
[2025-10-12 20:02:32] [info] LRF_ERR001 -> 14
[2025-10-12 20:02:32] [info] DB_WARN002 -> 159
[2025-10-12 20:02:32] [info] DB_WARN003 -> 161
```



```
[2025-10-12 20:02:32] [info] DRC_ERR9 -> 89
[2025-10-12 20:02:32] [info] DRC_ERR -> 67
[2025-10-12 20:02:32] [info] DRC_ERR5 -> 408
[2025-10-12 20:02:32] [info] DRC_ERR4 -> 241
[2025-10-12 20:02:32] [info] DRC_ERR1 -> 301
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Rule Violation Summary
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:02:32] [info] |-----|-----|-----|
[2025-10-12 20:02:32] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:02:32] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:02:32] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:02:32] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:02:32] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:02:32] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:02:32] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:02:32] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:02:32] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:02:32] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:02:32] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:02:32] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:02:32] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:02:32] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:02:32] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Performance Metrics
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] | Stage          | Time Taken |
[2025-10-12 20:02:32] [info] |-----|-----|
[2025-10-12 20:02:32] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:02:32] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:02:32] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Summary
[2025-10-12 20:02:32] [info] -----
[2025-10-12 20:02:32] [info] Total Errors: 1120
[2025-10-12 20:02:32] [info] Total Warnings: 321
[2025-10-12 20:02:32] [info] DRC Status: FAILED
```

lvr_summary_588.rpt

```
[2025-10-12 20:03:28] [info] =====
[2025-10-12 20:03:28] [info] Design Rule Check Report
[2025-10-12 20:03:28] [info] =====

[2025-10-12 20:03:28] [info] Report Generated: 2025-10-12
[2025-10-12 20:03:28] [info] Run Start Time: 08:02:37 PM
[2025-10-12 20:03:28] [info] Run End Time: 08:03:28 PM
[2025-10-12 20:03:28] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:03:28] [info] Tool Version: LVR_2025.1
[2025-10-12 20:03:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:03:28] [info] User Name: ubuntu
[2025-10-12 20:03:28] [info] Cores Used: 8
[2025-10-12 20:03:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] Top Cell Information
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] Layout Top Cell:      TOP
[2025-10-12 20:03:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:03:28] [info] Database Format:      GDSII
[2025-10-12 20:03:28] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] Type of Errors | Count
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] DB_WARN -> 1
[2025-10-12 20:03:28] [info] LRF_ERR001 -> 14
[2025-10-12 20:03:28] [info] DB_WARN002 -> 159
[2025-10-12 20:03:28] [info] DB_WARN003 -> 161
[2025-10-12 20:03:28] [info] DRC_ERR9 -> 89
[2025-10-12 20:03:28] [info] DRC_ERR -> 67
[2025-10-12 20:03:28] [info] DRC_ERR5 -> 408
[2025-10-12 20:03:28] [info] DRC_ERR4 -> 241
[2025-10-12 20:03:28] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] Rule Violation Summary
```

```
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:03:28] [info] |-----|-----|-----|
[2025-10-12 20:03:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:03:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:03:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:03:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:03:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:03:28] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:03:28] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:03:28] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:03:28] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:03:28] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:03:28] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:03:28] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:03:28] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:03:28] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:03:28] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:03:28] [info] -----
```

```
[2025-10-12 20:03:28] [info] Performance Metrics
```

```
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] | Stage          | Time Taken |
[2025-10-12 20:03:28] [info] |-----|-----|
[2025-10-12 20:03:28] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:03:28] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:03:28] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:03:28] [info] -----
```

```
[2025-10-12 20:03:28] [info] Summary
```

```
[2025-10-12 20:03:28] [info] -----
[2025-10-12 20:03:28] [info] Total Errors:      1120
[2025-10-12 20:03:28] [info] Total Warnings:    321
[2025-10-12 20:03:28] [info] DRC Status:        FAILED
```

lvr_summary_589.rpt

```
[2025-10-12 20:04:27] [info] =====
[2025-10-12 20:04:27] [info]                      Design Rule Check Report
[2025-10-12 20:04:27] [info] =====

[2025-10-12 20:04:27] [info] Report Generated:      2025-10-12
[2025-10-12 20:04:27] [info] Run Start Time:       08:03:35 PM
[2025-10-12 20:04:27] [info] Run End Time:         08:04:27 PM
[2025-10-12 20:04:27] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 20:04:27] [info] Tool Version:         LVR_2025.1
[2025-10-12 20:04:27] [info] Host Name:            ip-172-31-37-173
[2025-10-12 20:04:27] [info] User Name:            ubuntu
[2025-10-12 20:04:27] [info] Cores Used:           8
[2025-10-12 20:04:27] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info]                      Top Cell Information
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info] Layout Top Cell:      TOP
[2025-10-12 20:04:27] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:04:27] [info] Database Format:        GDSII
[2025-10-12 20:04:27] [info] Verification Mode:     Full Check

[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info]                      Type of Errors | Count
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info] DB_WARN -> 1
[2025-10-12 20:04:27] [info] LRF_ERR001 -> 14
[2025-10-12 20:04:27] [info] DB_WARN002 -> 159
[2025-10-12 20:04:27] [info] DB_WARN003 -> 161
[2025-10-12 20:04:27] [info] DRC_ERR9 -> 89
[2025-10-12 20:04:27] [info] DRC_ERR -> 67
[2025-10-12 20:04:27] [info] DRC_ERR5 -> 408
[2025-10-12 20:04:27] [info] DRC_ERR4 -> 241
[2025-10-12 20:04:27] [info] DRC_ERR1 -> 301
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info]                      Rule Violation Summary
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:04:27] [info] |-----|-----|-----|
[2025-10-12 20:04:27] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:04:27] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:04:27] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:04:27] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:04:27] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:04:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:04:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:04:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:04:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:04:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:04:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:04:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:04:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:04:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:04:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:04:27] [info]
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info]                      Performance Metrics
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:04:27] [info] |-----|-----|
[2025-10-12 20:04:27] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:04:27] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:04:27] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info]                Summary
[2025-10-12 20:04:27] [info] -----
[2025-10-12 20:04:27] [info] Total Errors:    1120
[2025-10-12 20:04:27] [info] Total Warnings:    321
[2025-10-12 20:04:27] [info] DRC Status:      FAILED
```

lvr_summary_59.rpt

```
[2025-10-12 11:37:20] [info] =====
[2025-10-12 11:37:20] [info]                Design Rule Check Report
[2025-10-12 11:37:20] [info] =====

[2025-10-12 11:37:20] [info] Report Generated:    2025-10-12
[2025-10-12 11:37:20] [info] Run Start Time:      11:36:28 AM
[2025-10-12 11:37:20] [info] Run End Time:        11:37:20 AM
[2025-10-12 11:37:20] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:37:20] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:37:20] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:37:20] [info] User Name:           ubuntu
[2025-10-12 11:37:20] [info] Cores Used:          8
[2025-10-12 11:37:20] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info]                Top Cell Information
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] Layout Top Cell:     TOP
[2025-10-12 11:37:20] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:37:20] [info] Database Format:       GDSII
[2025-10-12 11:37:20] [info] Verification Mode:    Full Check

[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info]                Type of Errors | Count
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] DB_WARN -> 1
[2025-10-12 11:37:20] [info] LRF_ERR001 -> 14
[2025-10-12 11:37:20] [info] DB_WARN002 -> 159
[2025-10-12 11:37:20] [info] DB_WARN003 -> 161
[2025-10-12 11:37:20] [info] DRC_ERR9 -> 89
[2025-10-12 11:37:20] [info] DRC_ERR -> 67
[2025-10-12 11:37:20] [info] DRC_ERR5 -> 408
[2025-10-12 11:37:20] [info] DRC_ERR4 -> 241
[2025-10-12 11:37:20] [info] DRC_ERR1 -> 301
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info]                Rule Violation Summary
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:37:20] [info] |-----|-----|-----|
[2025-10-12 11:37:20] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:37:20] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:37:20] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:37:20] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:37:20] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:37:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:37:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:37:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:37:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:37:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:37:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:37:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:37:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:37:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:37:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:37:20] [info]
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] Performance Metrics
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] | Stage | Time Taken |
[2025-10-12 11:37:20] [info] |-----|-----|
[2025-10-12 11:37:20] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:37:20] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:37:20] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] Summary
[2025-10-12 11:37:20] [info] -----
[2025-10-12 11:37:20] [info] Total Errors: 1120
[2025-10-12 11:37:20] [info] Total Warnings: 321
[2025-10-12 11:37:20] [info] DRC Status: FAILED
```

lvr_summary_590.rpt

```
[2025-10-12 20:05:23] [info] =====
[2025-10-12 20:05:23] [info] Design Rule Check Report
[2025-10-12 20:05:23] [info] =====

[2025-10-12 20:05:23] [info] Report Generated: 2025-10-12
[2025-10-12 20:05:23] [info] Run Start Time: 08:04:31 PM
[2025-10-12 20:05:23] [info] Run End Time: 08:05:23 PM
[2025-10-12 20:05:23] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:05:23] [info] Tool Version: LVR_2025.1
[2025-10-12 20:05:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:05:23] [info] User Name: ubuntu
[2025-10-12 20:05:23] [info] Cores Used: 8
[2025-10-12 20:05:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Top Cell Information
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Layout Top Cell: TOP
[2025-10-12 20:05:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:05:23] [info] Database Format: GDSII
[2025-10-12 20:05:23] [info] Verification Mode: Full Check

[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Type of Errors | Count
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] DB_WARN -> 1
[2025-10-12 20:05:23] [info] LRF_ERR001 -> 14
[2025-10-12 20:05:23] [info] DB_WARN002 -> 159
[2025-10-12 20:05:23] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:05:23] [info] DRC_ERR9 -> 89
[2025-10-12 20:05:23] [info] DRC_ERR -> 67
[2025-10-12 20:05:23] [info] DRC_ERR5 -> 408
[2025-10-12 20:05:23] [info] DRC_ERR4 -> 241
[2025-10-12 20:05:23] [info] DRC_ERR1 -> 301
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Rule Violation Summary
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:05:23] [info] |-----|-----|-----|
[2025-10-12 20:05:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:05:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:05:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:05:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:05:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:05:23] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:05:23] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:05:23] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:05:23] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:05:23] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:05:23] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:05:23] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:05:23] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:05:23] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:05:23] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Performance Metrics
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] | Stage          | Time Taken |
[2025-10-12 20:05:23] [info] |-----|-----|
[2025-10-12 20:05:23] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:05:23] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:05:23] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Summary
[2025-10-12 20:05:23] [info] -----
[2025-10-12 20:05:23] [info] Total Errors: 1120
[2025-10-12 20:05:23] [info] Total Warnings: 321
[2025-10-12 20:05:23] [info] DRC Status: FAILED
```

lvr_summary_591.rpt

```
[2025-10-12 20:06:19] [info] =====
[2025-10-12 20:06:19] [info] Design Rule Check Report
[2025-10-12 20:06:19] [info] =====

[2025-10-12 20:06:19] [info] Report Generated: 2025-10-12
[2025-10-12 20:06:19] [info] Run Start Time: 08:05:28 PM
[2025-10-12 20:06:19] [info] Run End Time: 08:06:19 PM
[2025-10-12 20:06:19] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:06:19] [info] Tool Version: LVR_2025.1
[2025-10-12 20:06:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:06:19] [info] User Name: ubuntu
[2025-10-12 20:06:19] [info] Cores Used: 8
[2025-10-12 20:06:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] Top Cell Information
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] Layout Top Cell:      TOP
[2025-10-12 20:06:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:06:19] [info] Database Format:      GDSII
[2025-10-12 20:06:19] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] Type of Errors | Count
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] DB_WARN -> 1
[2025-10-12 20:06:19] [info] LRF_ERR001 -> 14
[2025-10-12 20:06:19] [info] DB_WARN002 -> 159
[2025-10-12 20:06:19] [info] DB_WARN003 -> 161
[2025-10-12 20:06:19] [info] DRC_ERR9 -> 89
[2025-10-12 20:06:19] [info] DRC_ERR -> 67
[2025-10-12 20:06:19] [info] DRC_ERR5 -> 408
[2025-10-12 20:06:19] [info] DRC_ERR4 -> 241
[2025-10-12 20:06:19] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] Rule Violation Summary
```

```
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:06:19] [info] |-----|-----|-----|
[2025-10-12 20:06:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:06:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:06:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:06:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:06:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:06:19] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:06:19] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:06:19] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:06:19] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:06:19] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:06:19] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:06:19] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:06:19] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:06:19] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:06:19] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:06:19] [info] -----
```

```
[2025-10-12 20:06:19] [info] Performance Metrics
```

```
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] | Stage          | Time Taken |
[2025-10-12 20:06:19] [info] |-----|-----|
[2025-10-12 20:06:19] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:06:19] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:06:19] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:06:19] [info] -----
```

```
[2025-10-12 20:06:19] [info] Summary
```

```
[2025-10-12 20:06:19] [info] -----
[2025-10-12 20:06:19] [info] Total Errors:      1120
[2025-10-12 20:06:19] [info] Total Warnings:    321
[2025-10-12 20:06:19] [info] DRC Status:        FAILED
```

lvr_summary_592.rpt

```
[2025-10-12 20:07:15] [info] =====
[2025-10-12 20:07:15] [info] Design Rule Check Report
[2025-10-12 20:07:15] [info] =====

[2025-10-12 20:07:15] [info] Report Generated: 2025-10-12
[2025-10-12 20:07:15] [info] Run Start Time: 08:06:24 PM
[2025-10-12 20:07:15] [info] Run End Time: 08:07:15 PM
[2025-10-12 20:07:15] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:07:15] [info] Tool Version: LVR_2025.1
[2025-10-12 20:07:15] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:07:15] [info] User Name: ubuntu
[2025-10-12 20:07:15] [info] Cores Used: 8
[2025-10-12 20:07:15] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] Top Cell Information
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] Layout Top Cell: TOP
[2025-10-12 20:07:15] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:07:15] [info] Database Format: GDSII
[2025-10-12 20:07:15] [info] Verification Mode: Full Check

[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] Type of Errors | Count
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] DB_WARN -> 1
[2025-10-12 20:07:15] [info] LRF_ERR001 -> 14
[2025-10-12 20:07:15] [info] DB_WARN002 -> 159
[2025-10-12 20:07:15] [info] DB_WARN003 -> 161
[2025-10-12 20:07:15] [info] DRC_ERR9 -> 89
[2025-10-12 20:07:15] [info] DRC_ERR -> 67
[2025-10-12 20:07:15] [info] DRC_ERR5 -> 408
[2025-10-12 20:07:15] [info] DRC_ERR4 -> 241
[2025-10-12 20:07:15] [info] DRC_ERR1 -> 301
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] Rule Violation Summary
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:07:15] [info] |-----|-----|-----|
[2025-10-12 20:07:15] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:07:15] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:07:15] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:07:15] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:07:15] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:07:15] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:07:15] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:07:15] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:07:15] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:07:15] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:07:15] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:07:15] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:07:15] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:07:15] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:07:15] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:07:15] [info]
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] Performance Metrics
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] | Stage | Time Taken |
```



```
[2025-10-12 20:07:15] [info] |-----|-----|
[2025-10-12 20:07:15] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:07:15] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 20:07:15] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info]                Summary
[2025-10-12 20:07:15] [info] -----
[2025-10-12 20:07:15] [info] Total Errors:    1120
[2025-10-12 20:07:15] [info] Total Warnings:    321
[2025-10-12 20:07:15] [info] DRC Status:      FAILED
```

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```
[2025-10-12 20:08:12] [info] =====
[2025-10-12 20:08:12] [info]                Design Rule Check Report
[2025-10-12 20:08:12] [info] =====

[2025-10-12 20:08:12] [info] Report Generated:    2025-10-12
[2025-10-12 20:08:12] [info] Run Start Time:      08:07:22 PM
[2025-10-12 20:08:12] [info] Run End Time:        08:08:12 PM
[2025-10-12 20:08:12] [info] Time Taken:          0 minutes 49 seconds

[2025-10-12 20:08:12] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:08:12] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:08:12] [info] User Name:           ubuntu
[2025-10-12 20:08:12] [info] Cores Used:          8
[2025-10-12 20:08:12] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info]                Top Cell Information
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] Layout Top Cell:     TOP
[2025-10-12 20:08:12] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:08:12] [info] Database Format:       GDSII
[2025-10-12 20:08:12] [info] Verification Mode:    Full Check

[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info]                Type of Errors | Count
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] DB_WARN -> 1
[2025-10-12 20:08:12] [info] LRF_ERR001 -> 14
[2025-10-12 20:08:12] [info] DB_WARN002 -> 159
[2025-10-12 20:08:12] [info] DB_WARN003 -> 161
[2025-10-12 20:08:12] [info] DRC_ERR9 -> 89
[2025-10-12 20:08:12] [info] DRC_ERR -> 67
[2025-10-12 20:08:12] [info] DRC_ERR5 -> 408
[2025-10-12 20:08:12] [info] DRC_ERR4 -> 241
[2025-10-12 20:08:12] [info] DRC_ERR1 -> 301
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info]                Rule Violation Summary
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:08:12] [info] |-----|-----|-----|
[2025-10-12 20:08:12] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:08:12] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:08:12] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:08:12] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:08:12] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:08:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:08:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:08:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:08:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:08:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:08:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:08:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:08:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:08:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:08:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:08:12] [info]
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] Performance Metrics
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] | Stage | Time Taken |
[2025-10-12 20:08:12] [info] |-----|-----|
[2025-10-12 20:08:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:08:12] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 20:08:12] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] Summary
[2025-10-12 20:08:12] [info] -----
[2025-10-12 20:08:12] [info] Total Errors: 1120
[2025-10-12 20:08:12] [info] Total Warnings: 321
[2025-10-12 20:08:12] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:09:10] [info] =====
[2025-10-12 20:09:10] [info] Design Rule Check Report
[2025-10-12 20:09:10] [info] =====

[2025-10-12 20:09:10] [info] Report Generated: 2025-10-12
[2025-10-12 20:09:10] [info] Run Start Time: 08:08:18 PM
[2025-10-12 20:09:10] [info] Run End Time: 08:09:10 PM
[2025-10-12 20:09:10] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:09:10] [info] Tool Version: LVR_2025.1
[2025-10-12 20:09:10] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:09:10] [info] User Name: ubuntu
[2025-10-12 20:09:10] [info] Cores Used: 8
[2025-10-12 20:09:10] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Top Cell Information
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Layout Top Cell: TOP
[2025-10-12 20:09:10] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:09:10] [info] Database Format: GDSII
[2025-10-12 20:09:10] [info] Verification Mode: Full Check

[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Type of Errors | Count
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] DB_WARN -> 1
[2025-10-12 20:09:10] [info] LRF_ERR001 -> 14
[2025-10-12 20:09:10] [info] DB_WARN002 -> 159
[2025-10-12 20:09:10] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:09:10] [info] DRC_ERR9 -> 89
[2025-10-12 20:09:10] [info] DRC_ERR -> 67
[2025-10-12 20:09:10] [info] DRC_ERR5 -> 408
[2025-10-12 20:09:10] [info] DRC_ERR4 -> 241
[2025-10-12 20:09:10] [info] DRC_ERR1 -> 301
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Rule Violation Summary
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:09:10] [info] |-----|-----|-----|
[2025-10-12 20:09:10] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:09:10] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:09:10] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:09:10] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:09:10] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:09:10] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:09:10] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:09:10] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:09:10] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:09:10] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:09:10] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:09:10] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:09:10] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:09:10] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:09:10] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Performance Metrics
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] | Stage | Time Taken |
[2025-10-12 20:09:10] [info] |-----|-----|
[2025-10-12 20:09:10] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:09:10] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:09:10] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Summary
[2025-10-12 20:09:10] [info] -----
[2025-10-12 20:09:10] [info] Total Errors: 1120
[2025-10-12 20:09:10] [info] Total Warnings: 321
[2025-10-12 20:09:10] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:10:07] [info] =====
[2025-10-12 20:10:07] [info] Design Rule Check Report
[2025-10-12 20:10:07] [info] =====

[2025-10-12 20:10:07] [info] Report Generated: 2025-10-12
[2025-10-12 20:10:07] [info] Run Start Time: 08:09:15 PM
[2025-10-12 20:10:07] [info] Run End Time: 08:10:07 PM
[2025-10-12 20:10:07] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:10:07] [info] Tool Version: LVR_2025.1
[2025-10-12 20:10:07] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:10:07] [info] User Name: ubuntu
[2025-10-12 20:10:07] [info] Cores Used: 8
[2025-10-12 20:10:07] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] Top Cell Information
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] Layout Top Cell:      TOP
[2025-10-12 20:10:07] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:10:07] [info] Database Format:       GDSII
[2025-10-12 20:10:07] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] Type of Errors | Count
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] DB_WARN -> 1
[2025-10-12 20:10:07] [info] LRF_ERR001 -> 14
[2025-10-12 20:10:07] [info] DB_WARN002 -> 159
[2025-10-12 20:10:07] [info] DB_WARN003 -> 161
[2025-10-12 20:10:07] [info] DRC_ERR9 -> 89
[2025-10-12 20:10:07] [info] DRC_ERR -> 67
[2025-10-12 20:10:07] [info] DRC_ERR5 -> 408
[2025-10-12 20:10:07] [info] DRC_ERR4 -> 241
[2025-10-12 20:10:07] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] Rule Violation Summary
```

```
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:10:07] [info] |-----|-----|-----|
[2025-10-12 20:10:07] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:10:07] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:10:07] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:10:07] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:10:07] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:10:07] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:10:07] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:10:07] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:10:07] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:10:07] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:10:07] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:10:07] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:10:07] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:10:07] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:10:07] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:10:07] [info] -----
```

```
[2025-10-12 20:10:07] [info] Performance Metrics
```

```
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] | Stage          | Time Taken |
[2025-10-12 20:10:07] [info] |-----|-----|
[2025-10-12 20:10:07] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:10:07] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 20:10:07] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:10:07] [info] -----
```

```
[2025-10-12 20:10:07] [info] Summary
```

```
[2025-10-12 20:10:07] [info] -----
[2025-10-12 20:10:07] [info] Total Errors:      1120
[2025-10-12 20:10:07] [info] Total Warnings:    321
[2025-10-12 20:10:07] [info] DRC Status:        FAILED
```

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```
[2025-10-12 20:11:03] [info] =====
[2025-10-12 20:11:03] [info] Design Rule Check Report
[2025-10-12 20:11:03] [info] =====

[2025-10-12 20:11:03] [info] Report Generated: 2025-10-12
[2025-10-12 20:11:03] [info] Run Start Time: 08:10:11 PM
[2025-10-12 20:11:03] [info] Run End Time: 08:11:03 PM
[2025-10-12 20:11:03] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:11:03] [info] Tool Version: LVR_2025.1
[2025-10-12 20:11:03] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:11:03] [info] User Name: ubuntu
[2025-10-12 20:11:03] [info] Cores Used: 8
[2025-10-12 20:11:03] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] Top Cell Information
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] Layout Top Cell: TOP
[2025-10-12 20:11:03] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:11:03] [info] Database Format: GDSII
[2025-10-12 20:11:03] [info] Verification Mode: Full Check

[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] Type of Errors | Count
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] DB_WARN -> 1
[2025-10-12 20:11:03] [info] LRF_ERR001 -> 14
[2025-10-12 20:11:03] [info] DB_WARN002 -> 159
[2025-10-12 20:11:03] [info] DB_WARN003 -> 161
[2025-10-12 20:11:03] [info] DRC_ERR9 -> 89
[2025-10-12 20:11:03] [info] DRC_ERR -> 67
[2025-10-12 20:11:03] [info] DRC_ERR5 -> 408
[2025-10-12 20:11:03] [info] DRC_ERR4 -> 241
[2025-10-12 20:11:03] [info] DRC_ERR1 -> 301
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] Rule Violation Summary
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:11:03] [info] |-----|-----|-----|
[2025-10-12 20:11:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:11:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:11:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:11:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:11:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:11:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:11:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:11:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:11:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:11:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:11:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:11:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:11:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:11:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:11:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:11:03] [info]
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] Performance Metrics
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:11:03] [info] |-----|-----|
[2025-10-12 20:11:03] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 20:11:03] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 20:11:03] [info] | Report Generation   | 0 minutes 0 seconds  |

[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info]                Summary
[2025-10-12 20:11:03] [info] -----
[2025-10-12 20:11:03] [info] Total Errors:      1120
[2025-10-12 20:11:03] [info] Total Warnings:    321
[2025-10-12 20:11:03] [info] DRC Status:        FAILED
```

lvr_summary_597.rpt

```
[2025-10-12 20:12:00] [info] =====
[2025-10-12 20:12:00] [info]                Design Rule Check Report
[2025-10-12 20:12:00] [info] =====

[2025-10-12 20:12:00] [info] Report Generated:   2025-10-12
[2025-10-12 20:12:00] [info] Run Start Time:     08:11:08 PM
[2025-10-12 20:12:00] [info] Run End Time:       08:12:00 PM
[2025-10-12 20:12:00] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 20:12:00] [info] Tool Version:       LVR_2025.1
[2025-10-12 20:12:00] [info] Host Name:          ip-172-31-37-173
[2025-10-12 20:12:00] [info] User Name:          ubuntu
[2025-10-12 20:12:00] [info] Cores Used:         8
[2025-10-12 20:12:00] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info]                Top Cell Information
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] Layout Top Cell:    TOP
[2025-10-12 20:12:00] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 20:12:00] [info] Database Format:      GDSII
[2025-10-12 20:12:00] [info] Verification Mode:   Full Check

[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info]                Type of Errors | Count
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] DB_WARN -> 1
[2025-10-12 20:12:00] [info] LRF_ERR001 -> 14
[2025-10-12 20:12:00] [info] DB_WARN002 -> 159
[2025-10-12 20:12:00] [info] DB_WARN003 -> 161
[2025-10-12 20:12:00] [info] DRC_ERR9 -> 89
[2025-10-12 20:12:00] [info] DRC_ERR -> 67
[2025-10-12 20:12:00] [info] DRC_ERR5 -> 408
[2025-10-12 20:12:00] [info] DRC_ERR4 -> 241
[2025-10-12 20:12:00] [info] DRC_ERR1 -> 301
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info]                Rule Violation Summary
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:12:00] [info] |-----|-----|-----|
[2025-10-12 20:12:00] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:12:00] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:12:00] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:12:00] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:12:00] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:12:00] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:12:00] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:12:00] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:12:00] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:12:00] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:12:00] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:12:00] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:12:00] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:12:00] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:12:00] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:12:00] [info]
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] Performance Metrics
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] | Stage | Time Taken |
[2025-10-12 20:12:00] [info] |-----|-----|
[2025-10-12 20:12:00] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:12:00] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:12:00] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] Summary
[2025-10-12 20:12:00] [info] -----
[2025-10-12 20:12:00] [info] Total Errors: 1120
[2025-10-12 20:12:00] [info] Total Warnings: 321
[2025-10-12 20:12:00] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:12:56] [info] =====
[2025-10-12 20:12:56] [info] Design Rule Check Report
[2025-10-12 20:12:56] [info] =====

[2025-10-12 20:12:56] [info] Report Generated: 2025-10-12
[2025-10-12 20:12:56] [info] Run Start Time: 08:12:05 PM
[2025-10-12 20:12:56] [info] Run End Time: 08:12:56 PM
[2025-10-12 20:12:56] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:12:56] [info] Tool Version: LVR_2025.1
[2025-10-12 20:12:56] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:12:56] [info] User Name: ubuntu
[2025-10-12 20:12:56] [info] Cores Used: 8
[2025-10-12 20:12:56] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Top Cell Information
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Layout Top Cell: TOP
[2025-10-12 20:12:56] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:12:56] [info] Database Format: GDSII
[2025-10-12 20:12:56] [info] Verification Mode: Full Check

[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Type of Errors | Count
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] DB_WARN -> 1
[2025-10-12 20:12:56] [info] LRF_ERR001 -> 14
[2025-10-12 20:12:56] [info] DB_WARN002 -> 159
[2025-10-12 20:12:56] [info] DB_WARN003 -> 161
```

```

[2025-10-12 20:12:56] [info] DRC_ERR9 -> 89
[2025-10-12 20:12:56] [info] DRC_ERR -> 67
[2025-10-12 20:12:56] [info] DRC_ERR5 -> 408
[2025-10-12 20:12:56] [info] DRC_ERR4 -> 241
[2025-10-12 20:12:56] [info] DRC_ERR1 -> 301
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Rule Violation Summary
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:12:56] [info] |-----|-----|-----|
[2025-10-12 20:12:56] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:12:56] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:12:56] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:12:56] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:12:56] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:12:56] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:12:56] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:12:56] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:12:56] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:12:56] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:12:56] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:12:56] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:12:56] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:12:56] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:12:56] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Performance Metrics
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] | Stage          | Time Taken |
[2025-10-12 20:12:56] [info] |-----|-----|
[2025-10-12 20:12:56] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:12:56] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:12:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Summary
[2025-10-12 20:12:56] [info] -----
[2025-10-12 20:12:56] [info] Total Errors: 1120
[2025-10-12 20:12:56] [info] Total Warnings: 321
[2025-10-12 20:12:56] [info] DRC Status: FAILED

```

lvr_summary_599.rpt

```

[2025-10-12 20:13:52] [info] =====
[2025-10-12 20:13:52] [info] Design Rule Check Report
[2025-10-12 20:13:52] [info] =====

[2025-10-12 20:13:52] [info] Report Generated: 2025-10-12
[2025-10-12 20:13:52] [info] Run Start Time: 08:13:02 PM
[2025-10-12 20:13:52] [info] Run End Time: 08:13:52 PM
[2025-10-12 20:13:52] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:13:52] [info] Tool Version: LVR_2025.1
[2025-10-12 20:13:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:13:52] [info] User Name: ubuntu
[2025-10-12 20:13:52] [info] Cores Used: 8
[2025-10-12 20:13:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Top Cell Information
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Layout Top Cell:      TOP
[2025-10-12 20:13:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:13:52] [info] Database Format:      GDSII
[2025-10-12 20:13:52] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Type of Errors | Count
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] DB_WARN -> 1
[2025-10-12 20:13:52] [info] LRF_ERR001 -> 14
[2025-10-12 20:13:52] [info] DB_WARN002 -> 159
[2025-10-12 20:13:52] [info] DB_WARN003 -> 161
[2025-10-12 20:13:52] [info] DRC_ERR9 -> 89
[2025-10-12 20:13:52] [info] DRC_ERR -> 67
[2025-10-12 20:13:52] [info] DRC_ERR5 -> 408
[2025-10-12 20:13:52] [info] DRC_ERR4 -> 241
[2025-10-12 20:13:52] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Rule Violation Summary
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:13:52] [info] |-----|-----|-----|
[2025-10-12 20:13:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:13:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:13:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:13:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:13:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:13:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:13:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:13:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:13:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:13:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:13:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:13:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:13:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:13:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:13:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:13:52] [info] |
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Performance Metrics
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] | Stage          | Time Taken |
[2025-10-12 20:13:52] [info] |-----|-----|
[2025-10-12 20:13:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:13:52] [info] | Rule Checking   | 0 minutes 39 seconds |
[2025-10-12 20:13:52] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Summary
```

```
[2025-10-12 20:13:52] [info] -----
[2025-10-12 20:13:52] [info] Total Errors:      1120
[2025-10-12 20:13:52] [info] Total Warnings:    321
[2025-10-12 20:13:52] [info] DRC Status:        FAILED
```

lvr_summary_6.rpt

```
[2025-10-12 10:47:08] [info] =====
[2025-10-12 10:47:08] [info] Design Rule Check Report
[2025-10-12 10:47:08] [info] =====

[2025-10-12 10:47:08] [info] Report Generated: 2025-10-12
[2025-10-12 10:47:08] [info] Run Start Time: 10:46:17 AM
[2025-10-12 10:47:08] [info] Run End Time: 10:47:08 AM
[2025-10-12 10:47:08] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:47:08] [info] Tool Version: LVR_2025.1
[2025-10-12 10:47:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:47:08] [info] User Name: ubuntu
[2025-10-12 10:47:08] [info] Cores Used: 8
[2025-10-12 10:47:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] Top Cell Information
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] Layout Top Cell: TOP
[2025-10-12 10:47:08] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:47:08] [info] Database Format: GDSII
[2025-10-12 10:47:08] [info] Verification Mode: Full Check

[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] Type of Errors | Count
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] DB_WARN -> 1
[2025-10-12 10:47:08] [info] LRF_ERR001 -> 14
[2025-10-12 10:47:08] [info] DB_WARN002 -> 159
[2025-10-12 10:47:08] [info] DB_WARN003 -> 161
[2025-10-12 10:47:08] [info] DRC_ERR9 -> 89
[2025-10-12 10:47:08] [info] DRC_ERR -> 67
[2025-10-12 10:47:08] [info] DRC_ERR5 -> 408
[2025-10-12 10:47:08] [info] DRC_ERR4 -> 241
[2025-10-12 10:47:08] [info] DRC_ERR1 -> 301
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] Rule Violation Summary
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:47:08] [info] |-----|-----|-----|
[2025-10-12 10:47:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:47:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:47:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:47:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:47:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:47:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 10:47:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:47:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:47:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:47:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:47:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:47:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:47:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:47:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:47:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:47:08] [info]
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] Performance Metrics
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] | Stage | Time Taken |
```

```
[2025-10-12 10:47:08] [info] |-----|-----|
[2025-10-12 10:47:08] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 10:47:08] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 10:47:08] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info]                Summary
[2025-10-12 10:47:08] [info] -----
[2025-10-12 10:47:08] [info] Total Errors:    1120
[2025-10-12 10:47:08] [info] Total Warnings:    321
[2025-10-12 10:47:08] [info] DRC Status:      FAILED
```

lvr_summary_60.rpt

```
[2025-10-12 11:38:17] [info] =====
[2025-10-12 11:38:17] [info]                Design Rule Check Report
[2025-10-12 11:38:17] [info] =====

[2025-10-12 11:38:17] [info] Report Generated:    2025-10-12
[2025-10-12 11:38:17] [info] Run Start Time:      11:37:26 AM
[2025-10-12 11:38:17] [info] Run End Time:        11:38:17 AM
[2025-10-12 11:38:17] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:38:17] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:38:17] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:38:17] [info] User Name:           ubuntu
[2025-10-12 11:38:17] [info] Cores Used:          8
[2025-10-12 11:38:17] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info]                Top Cell Information
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] Layout Top Cell:     TOP
[2025-10-12 11:38:17] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:38:17] [info] Database Format:       GDSII
[2025-10-12 11:38:17] [info] Verification Mode:    Full Check

[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info]                Type of Errors | Count
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] DB_WARN -> 1
[2025-10-12 11:38:17] [info] LRF_ERR001 -> 14
[2025-10-12 11:38:17] [info] DB_WARN002 -> 159
[2025-10-12 11:38:17] [info] DB_WARN003 -> 161
[2025-10-12 11:38:17] [info] DRC_ERR9 -> 89
[2025-10-12 11:38:17] [info] DRC_ERR -> 67
[2025-10-12 11:38:17] [info] DRC_ERR5 -> 408
[2025-10-12 11:38:17] [info] DRC_ERR4 -> 241
[2025-10-12 11:38:17] [info] DRC_ERR1 -> 301
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info]                Rule Violation Summary
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:38:17] [info] |-----|-----|-----|
[2025-10-12 11:38:17] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:38:17] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:38:17] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:38:17] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:38:17] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:38:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:38:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:38:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:38:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:38:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:38:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:38:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:38:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:38:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:38:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:38:17] [info]
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] Performance Metrics
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] | Stage | Time Taken |
[2025-10-12 11:38:17] [info] |-----|-----|
[2025-10-12 11:38:17] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:38:17] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:38:17] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] Summary
[2025-10-12 11:38:17] [info] -----
[2025-10-12 11:38:17] [info] Total Errors: 1120
[2025-10-12 11:38:17] [info] Total Warnings: 321
[2025-10-12 11:38:17] [info] DRC Status: FAILED
```

lvr_summary_600.rpt

```
[2025-10-12 20:14:49] [info] =====
[2025-10-12 20:14:49] [info] Design Rule Check Report
[2025-10-12 20:14:49] [info] =====

[2025-10-12 20:14:49] [info] Report Generated: 2025-10-12
[2025-10-12 20:14:49] [info] Run Start Time: 08:13:58 PM
[2025-10-12 20:14:49] [info] Run End Time: 08:14:49 PM
[2025-10-12 20:14:49] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:14:49] [info] Tool Version: LVR_2025.1
[2025-10-12 20:14:49] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:14:49] [info] User Name: ubuntu
[2025-10-12 20:14:49] [info] Cores Used: 8
[2025-10-12 20:14:49] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Top Cell Information
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Layout Top Cell: TOP
[2025-10-12 20:14:49] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:14:49] [info] Database Format: GDSII
[2025-10-12 20:14:49] [info] Verification Mode: Full Check

[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Type of Errors | Count
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] DB_WARN -> 1
[2025-10-12 20:14:49] [info] LRF_ERR001 -> 14
[2025-10-12 20:14:49] [info] DB_WARN002 -> 159
[2025-10-12 20:14:49] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:14:49] [info] DRC_ERR9 -> 89
[2025-10-12 20:14:49] [info] DRC_ERR -> 67
[2025-10-12 20:14:49] [info] DRC_ERR5 -> 408
[2025-10-12 20:14:49] [info] DRC_ERR4 -> 241
[2025-10-12 20:14:49] [info] DRC_ERR1 -> 301
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Rule Violation Summary
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:14:49] [info] |-----|-----|-----|
[2025-10-12 20:14:49] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:14:49] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:14:49] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:14:49] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:14:49] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:14:49] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:14:49] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:14:49] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:14:49] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:14:49] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:14:49] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:14:49] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:14:49] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:14:49] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:14:49] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Performance Metrics
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] | Stage          | Time Taken |
[2025-10-12 20:14:49] [info] |-----|-----|
[2025-10-12 20:14:49] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:14:49] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:14:49] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Summary
[2025-10-12 20:14:49] [info] -----
[2025-10-12 20:14:49] [info] Total Errors: 1120
[2025-10-12 20:14:49] [info] Total Warnings: 321
[2025-10-12 20:14:49] [info] DRC Status: FAILED
```

lvr_summary_601.rpt

```
[2025-10-12 20:15:47] [info] =====
[2025-10-12 20:15:47] [info] Design Rule Check Report
[2025-10-12 20:15:47] [info] =====

[2025-10-12 20:15:47] [info] Report Generated: 2025-10-12
[2025-10-12 20:15:47] [info] Run Start Time: 08:14:55 PM
[2025-10-12 20:15:47] [info] Run End Time: 08:15:47 PM
[2025-10-12 20:15:47] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:15:47] [info] Tool Version: LVR_2025.1
[2025-10-12 20:15:47] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:15:47] [info] User Name: ubuntu
[2025-10-12 20:15:47] [info] Cores Used: 8
[2025-10-12 20:15:47] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] Top Cell Information
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] Layout Top Cell:      TOP
[2025-10-12 20:15:47] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:15:47] [info] Database Format:      GDSII
[2025-10-12 20:15:47] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] Type of Errors | Count
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] DB_WARN -> 1
[2025-10-12 20:15:47] [info] LRF_ERR001 -> 14
[2025-10-12 20:15:47] [info] DB_WARN002 -> 159
[2025-10-12 20:15:47] [info] DB_WARN003 -> 161
[2025-10-12 20:15:47] [info] DRC_ERR9 -> 89
[2025-10-12 20:15:47] [info] DRC_ERR -> 67
[2025-10-12 20:15:47] [info] DRC_ERR5 -> 408
[2025-10-12 20:15:47] [info] DRC_ERR4 -> 241
[2025-10-12 20:15:47] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] Rule Violation Summary
[2025-10-12 20:15:47] [info] -----
```

[2025-10-12 20:15:47] [info]	Rule ID	Description	Error Count
[2025-10-12 20:15:47] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 20:15:47] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 20:15:47] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 20:15:47] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 20:15:47] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 20:15:47] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 20:15:47] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 20:15:47] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 20:15:47] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 20:15:47] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 20:15:47] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 20:15:47] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" 15	
[2025-10-12 20:15:47] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 20:15:47] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 20:15:47] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

```
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] Performance Metrics
[2025-10-12 20:15:47] [info] -----
```

[2025-10-12 20:15:47] [info]	Stage	Time Taken
[2025-10-12 20:15:47] [info]	GDS Parsing	0 minutes 9 seconds
[2025-10-12 20:15:47] [info]	Rule Checking	0 minutes 40 seconds
[2025-10-12 20:15:47] [info]	Report Generation	0 minutes 0 seconds

```
[2025-10-12 20:15:47] [info] -----
[2025-10-12 20:15:47] [info] Summary
[2025-10-12 20:15:47] [info] -----
```

```
[2025-10-12 20:15:47] [info] Total Errors:      1120
[2025-10-12 20:15:47] [info] Total Warnings:    321
[2025-10-12 20:15:47] [info] DRC Status:        FAILED
```

lvr_summary_602.rpt

```
[2025-10-12 20:16:43] [info] =====
[2025-10-12 20:16:43] [info]                      Design Rule Check Report
[2025-10-12 20:16:43] [info] =====

[2025-10-12 20:16:43] [info] Report Generated:    2025-10-12
[2025-10-12 20:16:43] [info] Run Start Time:      08:15:52 PM
[2025-10-12 20:16:43] [info] Run End Time:        08:16:43 PM
[2025-10-12 20:16:43] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:16:43] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:16:43] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:16:43] [info] User Name:           ubuntu
[2025-10-12 20:16:43] [info] Cores Used:          8
[2025-10-12 20:16:43] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info]                      Top Cell Information
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info] Layout Top Cell:     TOP
[2025-10-12 20:16:43] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:16:43] [info] Database Format:      GDSII
[2025-10-12 20:16:43] [info] Verification Mode:    Full Check

[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info]                      Type of Errors | Count
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info] DB_WARN -> 1
[2025-10-12 20:16:43] [info] LRF_ERR001 -> 14
[2025-10-12 20:16:43] [info] DB_WARN002 -> 159
[2025-10-12 20:16:43] [info] DB_WARN003 -> 161
[2025-10-12 20:16:43] [info] DRC_ERR9 -> 89
[2025-10-12 20:16:43] [info] DRC_ERR -> 67
[2025-10-12 20:16:43] [info] DRC_ERR5 -> 408
[2025-10-12 20:16:43] [info] DRC_ERR4 -> 241
[2025-10-12 20:16:43] [info] DRC_ERR1 -> 301
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info]                      Rule Violation Summary
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:16:43] [info] |-----|-----|-----|
[2025-10-12 20:16:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:16:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:16:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:16:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:16:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:16:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:16:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:16:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:16:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:16:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:16:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:16:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:16:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:16:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:16:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:16:43] [info]
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info]                      Performance Metrics
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:16:43] [info] |-----|-----|
[2025-10-12 20:16:43] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:16:43] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 20:16:43] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info]                Summary
[2025-10-12 20:16:43] [info] -----
[2025-10-12 20:16:43] [info] Total Errors:    1120
[2025-10-12 20:16:43] [info] Total Warnings:    321
[2025-10-12 20:16:43] [info] DRC Status:      FAILED
```

lvr_summary_603.rpt

```
[2025-10-12 20:17:41] [info] =====
[2025-10-12 20:17:41] [info]                Design Rule Check Report
[2025-10-12 20:17:41] [info] =====

[2025-10-12 20:17:41] [info] Report Generated:    2025-10-12
[2025-10-12 20:17:41] [info] Run Start Time:      08:16:49 PM
[2025-10-12 20:17:41] [info] Run End Time:        08:17:41 PM
[2025-10-12 20:17:41] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 20:17:41] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:17:41] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:17:41] [info] User Name:           ubuntu
[2025-10-12 20:17:41] [info] Cores Used:          8
[2025-10-12 20:17:41] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info]                Top Cell Information
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] Layout Top Cell:     TOP
[2025-10-12 20:17:41] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:17:41] [info] Database Format:       GDSII
[2025-10-12 20:17:41] [info] Verification Mode:    Full Check

[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info]                Type of Errors | Count
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] DB_WARN -> 1
[2025-10-12 20:17:41] [info] LRF_ERR001 -> 14
[2025-10-12 20:17:41] [info] DB_WARN002 -> 159
[2025-10-12 20:17:41] [info] DB_WARN003 -> 161
[2025-10-12 20:17:41] [info] DRC_ERR9 -> 89
[2025-10-12 20:17:41] [info] DRC_ERR -> 67
[2025-10-12 20:17:41] [info] DRC_ERR5 -> 408
[2025-10-12 20:17:41] [info] DRC_ERR4 -> 241
[2025-10-12 20:17:41] [info] DRC_ERR1 -> 301
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info]                Rule Violation Summary
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:17:41] [info] |-----|-----|-----|
[2025-10-12 20:17:41] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:17:41] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:17:41] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:17:41] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:17:41] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 20:17:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:17:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:17:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:17:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:17:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:17:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:17:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:17:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:17:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:17:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:17:41] [info]
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] Performance Metrics
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] | Stage | Time Taken |
[2025-10-12 20:17:41] [info] |-----|-----|
[2025-10-12 20:17:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:17:41] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:17:41] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] Summary
[2025-10-12 20:17:41] [info] -----
[2025-10-12 20:17:41] [info] Total Errors: 1120
[2025-10-12 20:17:41] [info] Total Warnings: 321
[2025-10-12 20:17:41] [info] DRC Status: FAILED
```

lvr_summary_604.rpt

```
[2025-10-12 20:18:37] [info] =====
[2025-10-12 20:18:37] [info] Design Rule Check Report
[2025-10-12 20:18:37] [info] =====

[2025-10-12 20:18:37] [info] Report Generated: 2025-10-12
[2025-10-12 20:18:37] [info] Run Start Time: 08:17:46 PM
[2025-10-12 20:18:37] [info] Run End Time: 08:18:37 PM
[2025-10-12 20:18:37] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:18:37] [info] Tool Version: LVR_2025.1
[2025-10-12 20:18:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:18:37] [info] User Name: ubuntu
[2025-10-12 20:18:37] [info] Cores Used: 8
[2025-10-12 20:18:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Top Cell Information
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Layout Top Cell: TOP
[2025-10-12 20:18:37] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:18:37] [info] Database Format: GDSII
[2025-10-12 20:18:37] [info] Verification Mode: Full Check

[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Type of Errors | Count
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] DB_WARN -> 1
[2025-10-12 20:18:37] [info] LRF_ERR001 -> 14
[2025-10-12 20:18:37] [info] DB_WARN002 -> 159
[2025-10-12 20:18:37] [info] DB_WARN003 -> 161
```

```

[2025-10-12 20:18:37] [info] DRC_ERR9 -> 89
[2025-10-12 20:18:37] [info] DRC_ERR -> 67
[2025-10-12 20:18:37] [info] DRC_ERR5 -> 408
[2025-10-12 20:18:37] [info] DRC_ERR4 -> 241
[2025-10-12 20:18:37] [info] DRC_ERR1 -> 301
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Rule Violation Summary
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:18:37] [info] |-----|-----|-----|
[2025-10-12 20:18:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:18:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:18:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:18:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:18:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:18:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:18:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:18:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:18:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:18:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:18:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:18:37] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:18:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:18:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:18:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Performance Metrics
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] | Stage          | Time Taken |
[2025-10-12 20:18:37] [info] |-----|-----|
[2025-10-12 20:18:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:18:37] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:18:37] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Summary
[2025-10-12 20:18:37] [info] -----
[2025-10-12 20:18:37] [info] Total Errors: 1120
[2025-10-12 20:18:37] [info] Total Warnings: 321
[2025-10-12 20:18:37] [info] DRC Status: FAILED

```

lvr_summary_605.rpt

```

[2025-10-12 20:19:33] [info] =====
[2025-10-12 20:19:33] [info] Design Rule Check Report
[2025-10-12 20:19:33] [info] =====

[2025-10-12 20:19:33] [info] Report Generated: 2025-10-12
[2025-10-12 20:19:33] [info] Run Start Time: 08:18:42 PM
[2025-10-12 20:19:33] [info] Run End Time: 08:19:33 PM
[2025-10-12 20:19:33] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:19:33] [info] Tool Version: LVR_2025.1
[2025-10-12 20:19:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:19:33] [info] User Name: ubuntu
[2025-10-12 20:19:33] [info] Cores Used: 8
[2025-10-12 20:19:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] Top Cell Information
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] Layout Top Cell:      TOP
[2025-10-12 20:19:33] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:19:33] [info] Database Format:       GDSII
[2025-10-12 20:19:33] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] Type of Errors | Count
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] DB_WARN -> 1
[2025-10-12 20:19:33] [info] LRF_ERR001 -> 14
[2025-10-12 20:19:33] [info] DB_WARN002 -> 159
[2025-10-12 20:19:33] [info] DB_WARN003 -> 161
[2025-10-12 20:19:33] [info] DRC_ERR9 -> 89
[2025-10-12 20:19:33] [info] DRC_ERR -> 67
[2025-10-12 20:19:33] [info] DRC_ERR5 -> 408
[2025-10-12 20:19:33] [info] DRC_ERR4 -> 241
[2025-10-12 20:19:33] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] Rule Violation Summary
```

```
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:19:33] [info] |-----|-----|-----|
[2025-10-12 20:19:33] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:19:33] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:19:33] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:19:33] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:19:33] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:19:33] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:19:33] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:19:33] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:19:33] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:19:33] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:19:33] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:19:33] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:19:33] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:19:33] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:19:33] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:19:33] [info] |-----|-----|-----|
```

```
[2025-10-12 20:19:33] [info] Performance Metrics
```

```
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] | Stage          | Time Taken |
[2025-10-12 20:19:33] [info] |-----|-----|
[2025-10-12 20:19:33] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:19:33] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:19:33] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:19:33] [info] -----
```

```
[2025-10-12 20:19:33] [info] Summary
```

```
[2025-10-12 20:19:33] [info] -----
[2025-10-12 20:19:33] [info] Total Errors:      1120
[2025-10-12 20:19:33] [info] Total Warnings:    321
[2025-10-12 20:19:33] [info] DRC Status:        FAILED
```

lvr_summary_606.rpt

```
[2025-10-12 20:20:30] [info] =====
[2025-10-12 20:20:30] [info]                      Design Rule Check Report
[2025-10-12 20:20:30] [info] =====

[2025-10-12 20:20:30] [info] Report Generated:      2025-10-12
[2025-10-12 20:20:30] [info] Run Start Time:       08:19:39 PM
[2025-10-12 20:20:30] [info] Run End Time:         08:20:30 PM
[2025-10-12 20:20:30] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 20:20:30] [info] Tool Version:         LVR_2025.1
[2025-10-12 20:20:30] [info] Host Name:            ip-172-31-37-173
[2025-10-12 20:20:30] [info] User Name:            ubuntu
[2025-10-12 20:20:30] [info] Cores Used:           8
[2025-10-12 20:20:30] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info]                      Top Cell Information
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info] Layout Top Cell:      TOP
[2025-10-12 20:20:30] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:20:30] [info] Database Format:        GDSII
[2025-10-12 20:20:30] [info] Verification Mode:     Full Check

[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info]                      Type of Errors | Count
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info] DB_WARN -> 1
[2025-10-12 20:20:30] [info] LRF_ERR001 -> 14
[2025-10-12 20:20:30] [info] DB_WARN002 -> 159
[2025-10-12 20:20:30] [info] DB_WARN003 -> 161
[2025-10-12 20:20:30] [info] DRC_ERR9 -> 89
[2025-10-12 20:20:30] [info] DRC_ERR -> 67
[2025-10-12 20:20:30] [info] DRC_ERR5 -> 408
[2025-10-12 20:20:30] [info] DRC_ERR4 -> 241
[2025-10-12 20:20:30] [info] DRC_ERR1 -> 301
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info]                      Rule Violation Summary
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:20:30] [info] |-----|-----|-----|
[2025-10-12 20:20:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:20:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:20:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:20:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:20:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:20:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:20:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:20:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:20:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:20:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:20:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:20:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:20:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:20:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:20:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:20:30] [info]
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info]                      Performance Metrics
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:20:30] [info] |-----|-----|
[2025-10-12 20:20:30] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:20:30] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:20:30] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info]                Summary
[2025-10-12 20:20:30] [info] -----
[2025-10-12 20:20:30] [info] Total Errors:    1120
[2025-10-12 20:20:30] [info] Total Warnings:    321
[2025-10-12 20:20:30] [info] DRC Status:      FAILED
```

lvr_summary_607.rpt

```
[2025-10-12 20:21:28] [info] =====
[2025-10-12 20:21:28] [info]                Design Rule Check Report
[2025-10-12 20:21:28] [info] =====

[2025-10-12 20:21:28] [info] Report Generated:    2025-10-12
[2025-10-12 20:21:28] [info] Run Start Time:      08:20:36 PM
[2025-10-12 20:21:28] [info] Run End Time:        08:21:28 PM
[2025-10-12 20:21:28] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:21:28] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:21:28] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:21:28] [info] User Name:           ubuntu
[2025-10-12 20:21:28] [info] Cores Used:          8
[2025-10-12 20:21:28] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info]                Top Cell Information
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] Layout Top Cell:     TOP
[2025-10-12 20:21:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:21:28] [info] Database Format:       GDSII
[2025-10-12 20:21:28] [info] Verification Mode:    Full Check

[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info]                Type of Errors | Count
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] DB_WARN -> 1
[2025-10-12 20:21:28] [info] LRF_ERR001 -> 14
[2025-10-12 20:21:28] [info] DB_WARN002 -> 159
[2025-10-12 20:21:28] [info] DB_WARN003 -> 161
[2025-10-12 20:21:28] [info] DRC_ERR9 -> 89
[2025-10-12 20:21:28] [info] DRC_ERR -> 67
[2025-10-12 20:21:28] [info] DRC_ERR5 -> 408
[2025-10-12 20:21:28] [info] DRC_ERR4 -> 241
[2025-10-12 20:21:28] [info] DRC_ERR1 -> 301
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info]                Rule Violation Summary
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:21:28] [info] |-----|-----|-----|
[2025-10-12 20:21:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:21:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:21:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:21:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:21:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:21:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:21:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:21:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:21:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:21:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:21:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:21:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:21:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:21:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:21:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:21:28] [info]
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] Performance Metrics
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] | Stage | Time Taken |
[2025-10-12 20:21:28] [info] |-----|-----|
[2025-10-12 20:21:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:21:28] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:21:28] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] Summary
[2025-10-12 20:21:28] [info] -----
[2025-10-12 20:21:28] [info] Total Errors: 1120
[2025-10-12 20:21:28] [info] Total Warnings: 321
[2025-10-12 20:21:28] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:22:23] [info] =====
[2025-10-12 20:22:23] [info] Design Rule Check Report
[2025-10-12 20:22:23] [info] =====

[2025-10-12 20:22:23] [info] Report Generated: 2025-10-12
[2025-10-12 20:22:23] [info] Run Start Time: 08:21:33 PM
[2025-10-12 20:22:23] [info] Run End Time: 08:22:23 PM
[2025-10-12 20:22:23] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:22:23] [info] Tool Version: LVR_2025.1
[2025-10-12 20:22:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:22:23] [info] User Name: ubuntu
[2025-10-12 20:22:23] [info] Cores Used: 8
[2025-10-12 20:22:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Top Cell Information
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Layout Top Cell: TOP
[2025-10-12 20:22:23] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:22:23] [info] Database Format: GDSII
[2025-10-12 20:22:23] [info] Verification Mode: Full Check

[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Type of Errors | Count
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] DB_WARN -> 1
[2025-10-12 20:22:23] [info] LRF_ERR001 -> 14
[2025-10-12 20:22:23] [info] DB_WARN002 -> 159
[2025-10-12 20:22:23] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:22:23] [info] DRC_ERR9 -> 89
[2025-10-12 20:22:23] [info] DRC_ERR -> 67
[2025-10-12 20:22:23] [info] DRC_ERR5 -> 408
[2025-10-12 20:22:23] [info] DRC_ERR4 -> 241
[2025-10-12 20:22:23] [info] DRC_ERR1 -> 301
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Rule Violation Summary
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:22:23] [info] |-----|-----|-----|
[2025-10-12 20:22:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:22:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:22:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:22:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:22:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:22:23] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:22:23] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:22:23] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:22:23] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:22:23] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:22:23] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:22:23] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:22:23] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:22:23] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:22:23] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Performance Metrics
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] | Stage          | Time Taken |
[2025-10-12 20:22:23] [info] |-----|-----|
[2025-10-12 20:22:23] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:22:23] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:22:23] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Summary
[2025-10-12 20:22:23] [info] -----
[2025-10-12 20:22:23] [info] Total Errors: 1120
[2025-10-12 20:22:23] [info] Total Warnings: 321
[2025-10-12 20:22:23] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:23:21] [info] =====
[2025-10-12 20:23:21] [info] Design Rule Check Report
[2025-10-12 20:23:21] [info] =====

[2025-10-12 20:23:21] [info] Report Generated: 2025-10-12
[2025-10-12 20:23:21] [info] Run Start Time: 08:22:29 PM
[2025-10-12 20:23:21] [info] Run End Time: 08:23:21 PM
[2025-10-12 20:23:21] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:23:21] [info] Tool Version: LVR_2025.1
[2025-10-12 20:23:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:23:21] [info] User Name: ubuntu
[2025-10-12 20:23:21] [info] Cores Used: 8
[2025-10-12 20:23:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] Top Cell Information
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] Layout Top Cell:      TOP
[2025-10-12 20:23:21] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:23:21] [info] Database Format:       GDSII
[2025-10-12 20:23:21] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] Type of Errors | Count
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] DB_WARN -> 1
[2025-10-12 20:23:21] [info] LRF_ERR001 -> 14
[2025-10-12 20:23:21] [info] DB_WARN002 -> 159
[2025-10-12 20:23:21] [info] DB_WARN003 -> 161
[2025-10-12 20:23:21] [info] DRC_ERR9 -> 89
[2025-10-12 20:23:21] [info] DRC_ERR -> 67
[2025-10-12 20:23:21] [info] DRC_ERR5 -> 408
[2025-10-12 20:23:21] [info] DRC_ERR4 -> 241
[2025-10-12 20:23:21] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] Rule Violation Summary
```

```
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:23:21] [info] |-----|-----|-----|
[2025-10-12 20:23:21] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:23:21] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:23:21] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:23:21] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:23:21] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:23:21] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:23:21] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:23:21] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:23:21] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:23:21] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:23:21] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:23:21] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:23:21] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:23:21] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:23:21] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:23:21] [info] -----
```

```
[2025-10-12 20:23:21] [info] Performance Metrics
```

```
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] | Stage          | Time Taken |
[2025-10-12 20:23:21] [info] |-----|-----|
[2025-10-12 20:23:21] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:23:21] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:23:21] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:23:21] [info] -----
```

```
[2025-10-12 20:23:21] [info] Summary
```

```
[2025-10-12 20:23:21] [info] -----
[2025-10-12 20:23:21] [info] Total Errors:      1120
[2025-10-12 20:23:21] [info] Total Warnings:    321
[2025-10-12 20:23:21] [info] DRC Status:        FAILED
```


lvr_summary_61.rpt

```
[2025-10-12 11:39:14] [info] =====
[2025-10-12 11:39:14] [info] Design Rule Check Report
[2025-10-12 11:39:14] [info] =====

[2025-10-12 11:39:14] [info] Report Generated: 2025-10-12
[2025-10-12 11:39:14] [info] Run Start Time: 11:38:22 AM
[2025-10-12 11:39:14] [info] Run End Time: 11:39:14 AM
[2025-10-12 11:39:14] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:39:14] [info] Tool Version: LVR_2025.1
[2025-10-12 11:39:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:39:14] [info] User Name: ubuntu
[2025-10-12 11:39:14] [info] Cores Used: 8
[2025-10-12 11:39:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] Top Cell Information
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] Layout Top Cell: TOP
[2025-10-12 11:39:14] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:39:14] [info] Database Format: GDSII
[2025-10-12 11:39:14] [info] Verification Mode: Full Check

[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] Type of Errors | Count
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] DB_WARN -> 1
[2025-10-12 11:39:14] [info] LRF_ERR001 -> 14
[2025-10-12 11:39:14] [info] DB_WARN002 -> 159
[2025-10-12 11:39:14] [info] DB_WARN003 -> 161
[2025-10-12 11:39:14] [info] DRC_ERR9 -> 89
[2025-10-12 11:39:14] [info] DRC_ERR -> 67
[2025-10-12 11:39:14] [info] DRC_ERR5 -> 408
[2025-10-12 11:39:14] [info] DRC_ERR4 -> 241
[2025-10-12 11:39:14] [info] DRC_ERR1 -> 301
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] Rule Violation Summary
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:39:14] [info] |-----|-----|-----|
[2025-10-12 11:39:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:39:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:39:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:39:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:39:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:39:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:39:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:39:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:39:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:39:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:39:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:39:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:39:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:39:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:39:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:39:14] [info]
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] Performance Metrics
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:39:14] [info] |-----|-----|
[2025-10-12 11:39:14] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:39:14] [info] | Rule Checking        | 0 minutes 40 seconds |
[2025-10-12 11:39:14] [info] | Report Generation     | 0 minutes 0 seconds  |

[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info]                Summary
[2025-10-12 11:39:14] [info] -----
[2025-10-12 11:39:14] [info] Total Errors:    1120
[2025-10-12 11:39:14] [info] Total Warnings:    321
[2025-10-12 11:39:14] [info] DRC Status:      FAILED
```

lvr_summary_610.rpt

```
[2025-10-12 20:24:18] [info] =====
[2025-10-12 20:24:18] [info]                Design Rule Check Report
[2025-10-12 20:24:18] [info] =====

[2025-10-12 20:24:18] [info] Report Generated:    2025-10-12
[2025-10-12 20:24:18] [info] Run Start Time:      08:23:26 PM
[2025-10-12 20:24:18] [info] Run End Time:        08:24:18 PM
[2025-10-12 20:24:18] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:24:18] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:24:18] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:24:18] [info] User Name:           ubuntu
[2025-10-12 20:24:18] [info] Cores Used:          8
[2025-10-12 20:24:18] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info]                Top Cell Information
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] Layout Top Cell:     TOP
[2025-10-12 20:24:18] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:24:18] [info] Database Format:       GDSII
[2025-10-12 20:24:18] [info] Verification Mode:    Full Check

[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info]                Type of Errors | Count
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] DB_WARN -> 1
[2025-10-12 20:24:18] [info] LRF_ERR001 -> 14
[2025-10-12 20:24:18] [info] DB_WARN002 -> 159
[2025-10-12 20:24:18] [info] DB_WARN003 -> 161
[2025-10-12 20:24:18] [info] DRC_ERR9 -> 89
[2025-10-12 20:24:18] [info] DRC_ERR -> 67
[2025-10-12 20:24:18] [info] DRC_ERR5 -> 408
[2025-10-12 20:24:18] [info] DRC_ERR4 -> 241
[2025-10-12 20:24:18] [info] DRC_ERR1 -> 301
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info]                Rule Violation Summary
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:24:18] [info] |-----|-----|-----|
[2025-10-12 20:24:18] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:24:18] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:24:18] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:24:18] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:24:18] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:24:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:24:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:24:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:24:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:24:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:24:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:24:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:24:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:24:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:24:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:24:18] [info]
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] Performance Metrics
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] | Stage | Time Taken |
[2025-10-12 20:24:18] [info] |-----|-----|
[2025-10-12 20:24:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:24:18] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:24:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] Summary
[2025-10-12 20:24:18] [info] -----
[2025-10-12 20:24:18] [info] Total Errors: 1120
[2025-10-12 20:24:18] [info] Total Warnings: 321
[2025-10-12 20:24:18] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:25:17] [info] =====
[2025-10-12 20:25:17] [info] Design Rule Check Report
[2025-10-12 20:25:17] [info] =====

[2025-10-12 20:25:17] [info] Report Generated: 2025-10-12
[2025-10-12 20:25:17] [info] Run Start Time: 08:24:25 PM
[2025-10-12 20:25:17] [info] Run End Time: 08:25:17 PM
[2025-10-12 20:25:17] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:25:17] [info] Tool Version: LVR_2025.1
[2025-10-12 20:25:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:25:17] [info] User Name: ubuntu
[2025-10-12 20:25:17] [info] Cores Used: 8
[2025-10-12 20:25:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Top Cell Information
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Layout Top Cell: TOP
[2025-10-12 20:25:17] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:25:17] [info] Database Format: GDSII
[2025-10-12 20:25:17] [info] Verification Mode: Full Check

[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Type of Errors | Count
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] DB_WARN -> 1
[2025-10-12 20:25:17] [info] LRF_ERR001 -> 14
[2025-10-12 20:25:17] [info] DB_WARN002 -> 159
[2025-10-12 20:25:17] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:25:17] [info] DRC_ERR9 -> 89
[2025-10-12 20:25:17] [info] DRC_ERR -> 67
[2025-10-12 20:25:17] [info] DRC_ERR5 -> 408
[2025-10-12 20:25:17] [info] DRC_ERR4 -> 241
[2025-10-12 20:25:17] [info] DRC_ERR1 -> 301
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Rule Violation Summary
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:25:17] [info] |-----|-----|-----|
[2025-10-12 20:25:17] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:25:17] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:25:17] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:25:17] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:25:17] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:25:17] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:25:17] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:25:17] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:25:17] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:25:17] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:25:17] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:25:17] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:25:17] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:25:17] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:25:17] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Performance Metrics
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] | Stage          | Time Taken |
[2025-10-12 20:25:17] [info] |-----|-----|
[2025-10-12 20:25:17] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:25:17] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:25:17] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Summary
[2025-10-12 20:25:17] [info] -----
[2025-10-12 20:25:17] [info] Total Errors: 1120
[2025-10-12 20:25:17] [info] Total Warnings: 321
[2025-10-12 20:25:17] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:26:14] [info] =====
[2025-10-12 20:26:14] [info] Design Rule Check Report
[2025-10-12 20:26:14] [info] =====

[2025-10-12 20:26:14] [info] Report Generated: 2025-10-12
[2025-10-12 20:26:14] [info] Run Start Time: 08:25:22 PM
[2025-10-12 20:26:14] [info] Run End Time: 08:26:14 PM
[2025-10-12 20:26:14] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:26:14] [info] Tool Version: LVR_2025.1
[2025-10-12 20:26:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:26:14] [info] User Name: ubuntu
[2025-10-12 20:26:14] [info] Cores Used: 8
[2025-10-12 20:26:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Top Cell Information
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Layout Top Cell:      TOP
[2025-10-12 20:26:14] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:26:14] [info] Database Format:      GDSII
[2025-10-12 20:26:14] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Type of Errors | Count
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] DB_WARN -> 1
[2025-10-12 20:26:14] [info] LRF_ERR001 -> 14
[2025-10-12 20:26:14] [info] DB_WARN002 -> 159
[2025-10-12 20:26:14] [info] DB_WARN003 -> 161
[2025-10-12 20:26:14] [info] DRC_ERR9 -> 89
[2025-10-12 20:26:14] [info] DRC_ERR -> 67
[2025-10-12 20:26:14] [info] DRC_ERR5 -> 408
[2025-10-12 20:26:14] [info] DRC_ERR4 -> 241
[2025-10-12 20:26:14] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Rule Violation Summary
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:26:14] [info] |-----|-----|-----|
[2025-10-12 20:26:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:26:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:26:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:26:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:26:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:26:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:26:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:26:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:26:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:26:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:26:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:26:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:26:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:26:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:26:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
```

```
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Performance Metrics
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] | Stage | Time Taken |
[2025-10-12 20:26:14] [info] |-----|-----|
[2025-10-12 20:26:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:26:14] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:26:14] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Summary
[2025-10-12 20:26:14] [info] -----
[2025-10-12 20:26:14] [info] Total Errors: 1120
[2025-10-12 20:26:14] [info] Total Warnings: 321
[2025-10-12 20:26:14] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:27:10] [info] =====
[2025-10-12 20:27:10] [info]                      Design Rule Check Report
[2025-10-12 20:27:10] [info] =====

[2025-10-12 20:27:10] [info] Report Generated:      2025-10-12
[2025-10-12 20:27:10] [info] Run Start Time:       08:26:19 PM
[2025-10-12 20:27:10] [info] Run End Time:         08:27:10 PM
[2025-10-12 20:27:10] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 20:27:10] [info] Tool Version:          LVR_2025.1
[2025-10-12 20:27:10] [info] Host Name:             ip-172-31-37-173
[2025-10-12 20:27:10] [info] User Name:             ubuntu
[2025-10-12 20:27:10] [info] Cores Used:            8
[2025-10-12 20:27:10] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info]                      Top Cell Information
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info] Layout Top Cell:      TOP
[2025-10-12 20:27:10] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:27:10] [info] Database Format:        GDSII
[2025-10-12 20:27:10] [info] Verification Mode:     Full Check

[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info]                      Type of Errors | Count
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info] DB_WARN -> 1
[2025-10-12 20:27:10] [info] LRF_ERR001 -> 14
[2025-10-12 20:27:10] [info] DB_WARN002 -> 159
[2025-10-12 20:27:10] [info] DB_WARN003 -> 161
[2025-10-12 20:27:10] [info] DRC_ERR9 -> 89
[2025-10-12 20:27:10] [info] DRC_ERR -> 67
[2025-10-12 20:27:10] [info] DRC_ERR5 -> 408
[2025-10-12 20:27:10] [info] DRC_ERR4 -> 241
[2025-10-12 20:27:10] [info] DRC_ERR1 -> 301
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info]                      Rule Violation Summary
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:27:10] [info] |-----|-----|-----|
[2025-10-12 20:27:10] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:27:10] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:27:10] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:27:10] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:27:10] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:27:10] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:27:10] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:27:10] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:27:10] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:27:10] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:27:10] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:27:10] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:27:10] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:27:10] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:27:10] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:27:10] [info]
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info]                      Performance Metrics
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:27:10] [info] |-----|-----|
[2025-10-12 20:27:10] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:27:10] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:27:10] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info]                Summary
[2025-10-12 20:27:10] [info] -----
[2025-10-12 20:27:10] [info] Total Errors:    1120
[2025-10-12 20:27:10] [info] Total Warnings:    321
[2025-10-12 20:27:10] [info] DRC Status:      FAILED
```

lvr_summary_614.rpt

```
[2025-10-12 20:28:08] [info] =====
[2025-10-12 20:28:08] [info]                Design Rule Check Report
[2025-10-12 20:28:08] [info] =====

[2025-10-12 20:28:08] [info] Report Generated:    2025-10-12
[2025-10-12 20:28:08] [info] Run Start Time:      08:27:16 PM
[2025-10-12 20:28:08] [info] Run End Time:        08:28:08 PM
[2025-10-12 20:28:08] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:28:08] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:28:08] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:28:08] [info] User Name:           ubuntu
[2025-10-12 20:28:08] [info] Cores Used:          8
[2025-10-12 20:28:08] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info]                Top Cell Information
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] Layout Top Cell:     TOP
[2025-10-12 20:28:08] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:28:08] [info] Database Format:       GDSII
[2025-10-12 20:28:08] [info] Verification Mode:    Full Check

[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info]                Type of Errors | Count
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] DB_WARN -> 1
[2025-10-12 20:28:08] [info] LRF_ERR001 -> 14
[2025-10-12 20:28:08] [info] DB_WARN002 -> 159
[2025-10-12 20:28:08] [info] DB_WARN003 -> 161
[2025-10-12 20:28:08] [info] DRC_ERR9 -> 89
[2025-10-12 20:28:08] [info] DRC_ERR -> 67
[2025-10-12 20:28:08] [info] DRC_ERR5 -> 408
[2025-10-12 20:28:08] [info] DRC_ERR4 -> 241
[2025-10-12 20:28:08] [info] DRC_ERR1 -> 301
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info]                Rule Violation Summary
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:28:08] [info] |-----|-----|-----|
[2025-10-12 20:28:08] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:28:08] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:28:08] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:28:08] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:28:08] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:28:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:28:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:28:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:28:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:28:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:28:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:28:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:28:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:28:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:28:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:28:08] [info]
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] Performance Metrics
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] | Stage | Time Taken |
[2025-10-12 20:28:08] [info] |-----|-----|
[2025-10-12 20:28:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:28:08] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:28:08] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] Summary
[2025-10-12 20:28:08] [info] -----
[2025-10-12 20:28:08] [info] Total Errors: 1120
[2025-10-12 20:28:08] [info] Total Warnings: 321
[2025-10-12 20:28:08] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:29:05] [info] =====
[2025-10-12 20:29:05] [info] Design Rule Check Report
[2025-10-12 20:29:05] [info] =====

[2025-10-12 20:29:05] [info] Report Generated: 2025-10-12
[2025-10-12 20:29:05] [info] Run Start Time: 08:28:13 PM
[2025-10-12 20:29:05] [info] Run End Time: 08:29:05 PM
[2025-10-12 20:29:05] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:29:05] [info] Tool Version: LVR_2025.1
[2025-10-12 20:29:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:29:05] [info] User Name: ubuntu
[2025-10-12 20:29:05] [info] Cores Used: 8
[2025-10-12 20:29:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Top Cell Information
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Layout Top Cell: TOP
[2025-10-12 20:29:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:29:05] [info] Database Format: GDSII
[2025-10-12 20:29:05] [info] Verification Mode: Full Check

[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Type of Errors | Count
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] DB_WARN -> 1
[2025-10-12 20:29:05] [info] LRF_ERR001 -> 14
[2025-10-12 20:29:05] [info] DB_WARN002 -> 159
[2025-10-12 20:29:05] [info] DB_WARN003 -> 161
```



```
[2025-10-12 20:29:05] [info] DRC_ERR9 -> 89
[2025-10-12 20:29:05] [info] DRC_ERR -> 67
[2025-10-12 20:29:05] [info] DRC_ERR5 -> 408
[2025-10-12 20:29:05] [info] DRC_ERR4 -> 241
[2025-10-12 20:29:05] [info] DRC_ERR1 -> 301
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Rule Violation Summary
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:29:05] [info] |-----|-----|-----|
[2025-10-12 20:29:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:29:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:29:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:29:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:29:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:29:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:29:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:29:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:29:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:29:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:29:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:29:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:29:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:29:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:29:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Performance Metrics
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] | Stage | Time Taken |
[2025-10-12 20:29:05] [info] |-----|-----|
[2025-10-12 20:29:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:29:05] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:29:05] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Summary
[2025-10-12 20:29:05] [info] -----
[2025-10-12 20:29:05] [info] Total Errors: 1120
[2025-10-12 20:29:05] [info] Total Warnings: 321
[2025-10-12 20:29:05] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:30:02] [info] =====
[2025-10-12 20:30:02] [info] Design Rule Check Report
[2025-10-12 20:30:02] [info] =====

[2025-10-12 20:30:02] [info] Report Generated: 2025-10-12
[2025-10-12 20:30:02] [info] Run Start Time: 08:29:11 PM
[2025-10-12 20:30:02] [info] Run End Time: 08:30:02 PM
[2025-10-12 20:30:02] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:30:02] [info] Tool Version: LVR_2025.1
[2025-10-12 20:30:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:30:02] [info] User Name: ubuntu
[2025-10-12 20:30:02] [info] Cores Used: 8
[2025-10-12 20:30:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] Top Cell Information
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] Layout Top Cell:      TOP
[2025-10-12 20:30:02] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:30:02] [info] Database Format:       GDSII
[2025-10-12 20:30:02] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] Type of Errors | Count
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] DB_WARN -> 1
[2025-10-12 20:30:02] [info] LRF_ERR001 -> 14
[2025-10-12 20:30:02] [info] DB_WARN002 -> 159
[2025-10-12 20:30:02] [info] DB_WARN003 -> 161
[2025-10-12 20:30:02] [info] DRC_ERR9 -> 89
[2025-10-12 20:30:02] [info] DRC_ERR -> 67
[2025-10-12 20:30:02] [info] DRC_ERR5 -> 408
[2025-10-12 20:30:02] [info] DRC_ERR4 -> 241
[2025-10-12 20:30:02] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] Rule Violation Summary
```

```
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:30:02] [info] |-----|-----|-----|
[2025-10-12 20:30:02] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:30:02] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:30:02] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:30:02] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:30:02] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:30:02] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:30:02] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:30:02] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:30:02] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:30:02] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:30:02] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:30:02] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:30:02] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:30:02] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:30:02] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:30:02] [info] -----
```

```
[2025-10-12 20:30:02] [info] Performance Metrics
```

```
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] | Stage          | Time Taken |
[2025-10-12 20:30:02] [info] |-----|-----|
[2025-10-12 20:30:02] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:30:02] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:30:02] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:30:02] [info] -----
```

```
[2025-10-12 20:30:02] [info] Summary
```

```
[2025-10-12 20:30:02] [info] -----
[2025-10-12 20:30:02] [info] Total Errors:      1120
[2025-10-12 20:30:02] [info] Total Warnings:    321
[2025-10-12 20:30:02] [info] DRC Status:        FAILED
```

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```
[2025-10-12 20:31:00] [info] =====
[2025-10-12 20:31:00] [info] Design Rule Check Report
[2025-10-12 20:31:00] [info] =====

[2025-10-12 20:31:00] [info] Report Generated: 2025-10-12
[2025-10-12 20:31:00] [info] Run Start Time: 08:30:09 PM
[2025-10-12 20:31:00] [info] Run End Time: 08:31:00 PM
[2025-10-12 20:31:00] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:31:00] [info] Tool Version: LVR_2025.1
[2025-10-12 20:31:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:31:00] [info] User Name: ubuntu
[2025-10-12 20:31:00] [info] Cores Used: 8
[2025-10-12 20:31:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:31:00] [info] -----
[2025-10-12 20:31:00] [info] Top Cell Information
[2025-10-12 20:31:00] [info] -----
[2025-10-12 20:31:00] [info] Layout Top Cell: TOP
[2025-10-12 20:31:00] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:31:00] [info] Database Format: GDSII
[2025-10-12 20:31:00] [info] Verification Mode: Full Check

[2025-10-12 20:31:00] [info] -----
[2025-10-12 20:31:00] [info] Type of Errors | Count
[2025-10-12 20:31:00] [info] -----
[2025-10-12 20:31:01] [info] DB_WARN -> 1
[2025-10-12 20:31:01] [info] LRF_ERR001 -> 14
[2025-10-12 20:31:01] [info] DB_WARN002 -> 159
[2025-10-12 20:31:01] [info] DB_WARN003 -> 161
[2025-10-12 20:31:01] [info] DRC_ERR9 -> 89
[2025-10-12 20:31:01] [info] DRC_ERR -> 67
[2025-10-12 20:31:01] [info] DRC_ERR5 -> 408
[2025-10-12 20:31:01] [info] DRC_ERR4 -> 241
[2025-10-12 20:31:01] [info] DRC_ERR1 -> 301
[2025-10-12 20:31:01] [info] -----
[2025-10-12 20:31:01] [info] Rule Violation Summary
[2025-10-12 20:31:01] [info] -----
[2025-10-12 20:31:01] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:31:01] [info] |-----|-----|-----|
[2025-10-12 20:31:01] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:31:01] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:31:01] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:31:01] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:31:01] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:31:01] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:31:01] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:31:01] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:31:01] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:31:01] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:31:01] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:31:01] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:31:01] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:31:01] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:31:01] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:31:01] [info]
[2025-10-12 20:31:01] [info] -----
[2025-10-12 20:31:01] [info] Performance Metrics
[2025-10-12 20:31:01] [info] -----
[2025-10-12 20:31:01] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:31:01] [info] |-----|-----|
[2025-10-12 20:31:01] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:31:01] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:31:01] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:31:01] [info] -----
[2025-10-12 20:31:01] [info] Summary
[2025-10-12 20:31:01] [info] -----
[2025-10-12 20:31:01] [info] Total Errors: 1120
[2025-10-12 20:31:01] [info] Total Warnings: 321
[2025-10-12 20:31:01] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:31:58] [info] =====
[2025-10-12 20:31:58] [info] Design Rule Check Report
[2025-10-12 20:31:58] [info] =====

[2025-10-12 20:31:58] [info] Report Generated: 2025-10-12
[2025-10-12 20:31:58] [info] Run Start Time: 08:31:06 PM
[2025-10-12 20:31:58] [info] Run End Time: 08:31:58 PM
[2025-10-12 20:31:58] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 20:31:58] [info] Tool Version: LVR_2025.1
[2025-10-12 20:31:58] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:31:58] [info] User Name: ubuntu
[2025-10-12 20:31:58] [info] Cores Used: 8
[2025-10-12 20:31:58] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Top Cell Information
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Layout Top Cell: TOP
[2025-10-12 20:31:58] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:31:58] [info] Database Format: GDSII
[2025-10-12 20:31:58] [info] Verification Mode: Full Check

[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Type of Errors | Count
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] DB_WARN -> 1
[2025-10-12 20:31:58] [info] LRF_ERR001 -> 14
[2025-10-12 20:31:58] [info] DB_WARN002 -> 159
[2025-10-12 20:31:58] [info] DB_WARN003 -> 161
[2025-10-12 20:31:58] [info] DRC_ERR9 -> 89
[2025-10-12 20:31:58] [info] DRC_ERR -> 67
[2025-10-12 20:31:58] [info] DRC_ERR5 -> 408
[2025-10-12 20:31:58] [info] DRC_ERR4 -> 241
[2025-10-12 20:31:58] [info] DRC_ERR1 -> 301
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Rule Violation Summary
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:31:58] [info] |-----|-----|-----|
[2025-10-12 20:31:58] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:31:58] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:31:58] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:31:58] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:31:58] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 20:31:58] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:31:58] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:31:58] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:31:58] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:31:58] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:31:58] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:31:58] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:31:58] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:31:58] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:31:58] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:31:58] [info]
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Performance Metrics
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] | Stage | Time Taken |
[2025-10-12 20:31:58] [info] |-----|-----|
[2025-10-12 20:31:58] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:31:58] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:31:58] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Summary
[2025-10-12 20:31:58] [info] -----
[2025-10-12 20:31:58] [info] Total Errors: 1120
[2025-10-12 20:31:58] [info] Total Warnings: 321
[2025-10-12 20:31:58] [info] DRC Status: FAILED
```

lvr_summary_619.rpt

```
[2025-10-12 20:32:54] [info] =====
[2025-10-12 20:32:54] [info] Design Rule Check Report
[2025-10-12 20:32:54] [info] =====

[2025-10-12 20:32:54] [info] Report Generated: 2025-10-12
[2025-10-12 20:32:54] [info] Run Start Time: 08:32:03 PM
[2025-10-12 20:32:54] [info] Run End Time: 08:32:54 PM
[2025-10-12 20:32:54] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:32:54] [info] Tool Version: LVR_2025.1
[2025-10-12 20:32:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:32:54] [info] User Name: ubuntu
[2025-10-12 20:32:54] [info] Cores Used: 8
[2025-10-12 20:32:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Top Cell Information
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Layout Top Cell: TOP
[2025-10-12 20:32:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:32:54] [info] Database Format: GDSII
[2025-10-12 20:32:54] [info] Verification Mode: Full Check

[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Type of Errors | Count
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] DB_WARN -> 1
[2025-10-12 20:32:54] [info] LRF_ERR001 -> 14
[2025-10-12 20:32:54] [info] DB_WARN002 -> 159
[2025-10-12 20:32:54] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:32:54] [info] DRC_ERR9 -> 89
[2025-10-12 20:32:54] [info] DRC_ERR -> 67
[2025-10-12 20:32:54] [info] DRC_ERR5 -> 408
[2025-10-12 20:32:54] [info] DRC_ERR4 -> 241
[2025-10-12 20:32:54] [info] DRC_ERR1 -> 301
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Rule Violation Summary
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:32:54] [info] |-----|-----|-----|
[2025-10-12 20:32:54] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:32:54] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:32:54] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:32:54] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:32:54] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:32:54] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:32:54] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:32:54] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:32:54] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:32:54] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:32:54] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:32:54] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:32:54] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:32:54] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:32:54] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Performance Metrics
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] | Stage          | Time Taken |
[2025-10-12 20:32:54] [info] |-----|-----|
[2025-10-12 20:32:54] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:32:54] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:32:54] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Summary
[2025-10-12 20:32:54] [info] -----
[2025-10-12 20:32:54] [info] Total Errors: 1120
[2025-10-12 20:32:54] [info] Total Warnings: 321
[2025-10-12 20:32:54] [info] DRC Status: FAILED
```

lvr_summary_62.rpt

```
[2025-10-12 11:40:12] [info] =====
[2025-10-12 11:40:12] [info] Design Rule Check Report
[2025-10-12 11:40:12] [info] =====

[2025-10-12 11:40:12] [info] Report Generated: 2025-10-12
[2025-10-12 11:40:12] [info] Run Start Time: 11:39:21 AM
[2025-10-12 11:40:12] [info] Run End Time: 11:40:12 AM
[2025-10-12 11:40:12] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:40:12] [info] Tool Version: LVR_2025.1
[2025-10-12 11:40:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:40:12] [info] User Name: ubuntu
[2025-10-12 11:40:12] [info] Cores Used: 8
[2025-10-12 11:40:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Top Cell Information
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Layout Top Cell:      TOP
[2025-10-12 11:40:12] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:40:12] [info] Database Format:      GDSII
[2025-10-12 11:40:12] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Type of Errors | Count
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] DB_WARN -> 1
[2025-10-12 11:40:12] [info] LRF_ERR001 -> 14
[2025-10-12 11:40:12] [info] DB_WARN002 -> 159
[2025-10-12 11:40:12] [info] DB_WARN003 -> 161
[2025-10-12 11:40:12] [info] DRC_ERR9 -> 89
[2025-10-12 11:40:12] [info] DRC_ERR -> 67
[2025-10-12 11:40:12] [info] DRC_ERR5 -> 408
[2025-10-12 11:40:12] [info] DRC_ERR4 -> 241
[2025-10-12 11:40:12] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Rule Violation Summary
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:40:12] [info] |-----|-----|-----|
[2025-10-12 11:40:12] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:40:12] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:40:12] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:40:12] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:40:12] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:40:12] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:40:12] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:40:12] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:40:12] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:40:12] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:40:12] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:40:12] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:40:12] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:40:12] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:40:12] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:40:12] [info] |
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Performance Metrics
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] | Stage          | Time Taken |
[2025-10-12 11:40:12] [info] |-----|-----|
[2025-10-12 11:40:12] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:40:12] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 11:40:12] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Summary
```

```
[2025-10-12 11:40:12] [info] -----
[2025-10-12 11:40:12] [info] Total Errors:      1120
[2025-10-12 11:40:12] [info] Total Warnings:    321
[2025-10-12 11:40:12] [info] DRC Status:        FAILED
```

lvr_summary_620.rpt

```
[2025-10-12 20:33:52] [info] =====
[2025-10-12 20:33:52] [info]                      Design Rule Check Report
[2025-10-12 20:33:52] [info] =====

[2025-10-12 20:33:52] [info] Report Generated:    2025-10-12
[2025-10-12 20:33:52] [info] Run Start Time:      08:33:01 PM
[2025-10-12 20:33:52] [info] Run End Time:        08:33:52 PM
[2025-10-12 20:33:52] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:33:52] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:33:52] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:33:52] [info] User Name:           ubuntu
[2025-10-12 20:33:52] [info] Cores Used:          8
[2025-10-12 20:33:52] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info]                      Top Cell Information
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info] Layout Top Cell:     TOP
[2025-10-12 20:33:52] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:33:52] [info] Database Format:      GDSII
[2025-10-12 20:33:52] [info] Verification Mode:    Full Check

[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info]                      Type of Errors | Count
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info] DB_WARN -> 1
[2025-10-12 20:33:52] [info] LRF_ERR001 -> 14
[2025-10-12 20:33:52] [info] DB_WARN002 -> 159
[2025-10-12 20:33:52] [info] DB_WARN003 -> 161
[2025-10-12 20:33:52] [info] DRC_ERR9 -> 89
[2025-10-12 20:33:52] [info] DRC_ERR -> 67
[2025-10-12 20:33:52] [info] DRC_ERR5 -> 408
[2025-10-12 20:33:52] [info] DRC_ERR4 -> 241
[2025-10-12 20:33:52] [info] DRC_ERR1 -> 301
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info]                      Rule Violation Summary
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:33:52] [info] |-----|-----|-----|
[2025-10-12 20:33:52] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:33:52] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:33:52] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:33:52] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:33:52] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:33:52] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:33:52] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:33:52] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:33:52] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:33:52] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:33:52] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:33:52] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:33:52] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:33:52] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:33:52] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:33:52] [info]
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info]                      Performance Metrics
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info] | Stage | Time Taken |
```



```
[2025-10-12 20:33:52] [info] |-----|-----|
[2025-10-12 20:33:52] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:33:52] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:33:52] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info]                Summary
[2025-10-12 20:33:52] [info] -----
[2025-10-12 20:33:52] [info] Total Errors:    1120
[2025-10-12 20:33:52] [info] Total Warnings:    321
[2025-10-12 20:33:52] [info] DRC Status:      FAILED
```

lvr_summary_621.rpt

```
[2025-10-12 20:34:48] [info] =====
[2025-10-12 20:34:48] [info]                Design Rule Check Report
[2025-10-12 20:34:48] [info] =====

[2025-10-12 20:34:48] [info] Report Generated:    2025-10-12
[2025-10-12 20:34:48] [info] Run Start Time:      08:33:57 PM
[2025-10-12 20:34:48] [info] Run End Time:        08:34:48 PM
[2025-10-12 20:34:48] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 20:34:48] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:34:48] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:34:48] [info] User Name:           ubuntu
[2025-10-12 20:34:48] [info] Cores Used:          8
[2025-10-12 20:34:48] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info]                Top Cell Information
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] Layout Top Cell:     TOP
[2025-10-12 20:34:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:34:48] [info] Database Format:       GDSII
[2025-10-12 20:34:48] [info] Verification Mode:    Full Check

[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info]                Type of Errors | Count
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] DB_WARN -> 1
[2025-10-12 20:34:48] [info] LRF_ERR001 -> 14
[2025-10-12 20:34:48] [info] DB_WARN002 -> 159
[2025-10-12 20:34:48] [info] DB_WARN003 -> 161
[2025-10-12 20:34:48] [info] DRC_ERR9 -> 89
[2025-10-12 20:34:48] [info] DRC_ERR -> 67
[2025-10-12 20:34:48] [info] DRC_ERR5 -> 408
[2025-10-12 20:34:48] [info] DRC_ERR4 -> 241
[2025-10-12 20:34:48] [info] DRC_ERR1 -> 301
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info]                Rule Violation Summary
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:34:48] [info] |-----|-----|-----|
[2025-10-12 20:34:48] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:34:48] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:34:48] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:34:48] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:34:48] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:34:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:34:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:34:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:34:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:34:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:34:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:34:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:34:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:34:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:34:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:34:48] [info]
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] Performance Metrics
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] | Stage | Time Taken |
[2025-10-12 20:34:48] [info] |-----|-----|
[2025-10-12 20:34:48] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:34:48] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 20:34:48] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] Summary
[2025-10-12 20:34:48] [info] -----
[2025-10-12 20:34:48] [info] Total Errors: 1120
[2025-10-12 20:34:48] [info] Total Warnings: 321
[2025-10-12 20:34:48] [info] DRC Status: FAILED
```

lvr_summary_622.rpt

```
[2025-10-12 20:35:46] [info] =====
[2025-10-12 20:35:46] [info] Design Rule Check Report
[2025-10-12 20:35:46] [info] =====

[2025-10-12 20:35:46] [info] Report Generated: 2025-10-12
[2025-10-12 20:35:46] [info] Run Start Time: 08:34:55 PM
[2025-10-12 20:35:46] [info] Run End Time: 08:35:46 PM
[2025-10-12 20:35:46] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:35:46] [info] Tool Version: LVR_2025.1
[2025-10-12 20:35:46] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:35:46] [info] User Name: ubuntu
[2025-10-12 20:35:46] [info] Cores Used: 8
[2025-10-12 20:35:46] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Top Cell Information
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Layout Top Cell: TOP
[2025-10-12 20:35:46] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:35:46] [info] Database Format: GDSII
[2025-10-12 20:35:46] [info] Verification Mode: Full Check

[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Type of Errors | Count
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] DB_WARN -> 1
[2025-10-12 20:35:46] [info] LRF_ERR001 -> 14
[2025-10-12 20:35:46] [info] DB_WARN002 -> 159
[2025-10-12 20:35:46] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:35:46] [info] DRC_ERR9 -> 89
[2025-10-12 20:35:46] [info] DRC_ERR -> 67
[2025-10-12 20:35:46] [info] DRC_ERR5 -> 408
[2025-10-12 20:35:46] [info] DRC_ERR4 -> 241
[2025-10-12 20:35:46] [info] DRC_ERR1 -> 301
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Rule Violation Summary
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:35:46] [info] |-----|-----|-----|
[2025-10-12 20:35:46] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:35:46] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:35:46] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:35:46] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:35:46] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:35:46] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:35:46] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:35:46] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:35:46] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:35:46] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:35:46] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:35:46] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:35:46] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:35:46] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:35:46] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Performance Metrics
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] | Stage          | Time Taken |
[2025-10-12 20:35:46] [info] |-----|-----|
[2025-10-12 20:35:46] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:35:46] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:35:46] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Summary
[2025-10-12 20:35:46] [info] -----
[2025-10-12 20:35:46] [info] Total Errors: 1120
[2025-10-12 20:35:46] [info] Total Warnings: 321
[2025-10-12 20:35:46] [info] DRC Status: FAILED
```

lvr_summary_623.rpt

```
[2025-10-12 20:36:42] [info] =====
[2025-10-12 20:36:42] [info] Design Rule Check Report
[2025-10-12 20:36:42] [info] =====

[2025-10-12 20:36:42] [info] Report Generated: 2025-10-12
[2025-10-12 20:36:42] [info] Run Start Time: 08:35:51 PM
[2025-10-12 20:36:42] [info] Run End Time: 08:36:42 PM
[2025-10-12 20:36:42] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:36:42] [info] Tool Version: LVR_2025.1
[2025-10-12 20:36:42] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:36:42] [info] User Name: ubuntu
[2025-10-12 20:36:42] [info] Cores Used: 8
[2025-10-12 20:36:42] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Top Cell Information
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Layout Top Cell:      TOP
[2025-10-12 20:36:42] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:36:42] [info] Database Format:      GDSII
[2025-10-12 20:36:42] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Type of Errors | Count
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] DB_WARN -> 1
[2025-10-12 20:36:42] [info] LRF_ERR001 -> 14
[2025-10-12 20:36:42] [info] DB_WARN002 -> 159
[2025-10-12 20:36:42] [info] DB_WARN003 -> 161
[2025-10-12 20:36:42] [info] DRC_ERR9 -> 89
[2025-10-12 20:36:42] [info] DRC_ERR -> 67
[2025-10-12 20:36:42] [info] DRC_ERR5 -> 408
[2025-10-12 20:36:42] [info] DRC_ERR4 -> 241
[2025-10-12 20:36:42] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Rule Violation Summary
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:36:42] [info] |-----|-----|-----|
[2025-10-12 20:36:42] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:36:42] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:36:42] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:36:42] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:36:42] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:36:42] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:36:42] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:36:42] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:36:42] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:36:42] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:36:42] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:36:42] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:36:42] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:36:42] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:36:42] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:36:42] [info] |
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Performance Metrics
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] | Stage          | Time Taken |
[2025-10-12 20:36:42] [info] |-----|-----|
[2025-10-12 20:36:42] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:36:42] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:36:42] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Summary
```

```
[2025-10-12 20:36:42] [info] -----
[2025-10-12 20:36:42] [info] Total Errors:      1120
[2025-10-12 20:36:42] [info] Total Warnings:    321
[2025-10-12 20:36:42] [info] DRC Status:        FAILED
```

lvr_summary_624.rpt

```
[2025-10-12 20:37:38] [info] =====
[2025-10-12 20:37:38] [info]                      Design Rule Check Report
[2025-10-12 20:37:38] [info] =====

[2025-10-12 20:37:38] [info] Report Generated:      2025-10-12
[2025-10-12 20:37:38] [info] Run Start Time:       08:36:48 PM
[2025-10-12 20:37:38] [info] Run End Time:         08:37:38 PM
[2025-10-12 20:37:38] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 20:37:38] [info] Tool Version:         LVR_2025.1
[2025-10-12 20:37:38] [info] Host Name:            ip-172-31-37-173
[2025-10-12 20:37:38] [info] User Name:            ubuntu
[2025-10-12 20:37:38] [info] Cores Used:           8
[2025-10-12 20:37:38] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info]                      Top Cell Information
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info] Layout Top Cell:      TOP
[2025-10-12 20:37:38] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:37:38] [info] Database Format:        GDSII
[2025-10-12 20:37:38] [info] Verification Mode:     Full Check

[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info]                      Type of Errors | Count
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info] DB_WARN -> 1
[2025-10-12 20:37:38] [info] LRF_ERR001 -> 14
[2025-10-12 20:37:38] [info] DB_WARN002 -> 159
[2025-10-12 20:37:38] [info] DB_WARN003 -> 161
[2025-10-12 20:37:38] [info] DRC_ERR9 -> 89
[2025-10-12 20:37:38] [info] DRC_ERR -> 67
[2025-10-12 20:37:38] [info] DRC_ERR5 -> 408
[2025-10-12 20:37:38] [info] DRC_ERR4 -> 241
[2025-10-12 20:37:38] [info] DRC_ERR1 -> 301
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info]                      Rule Violation Summary
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:37:38] [info] |-----|-----|-----|
[2025-10-12 20:37:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:37:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:37:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:37:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:37:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:37:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:37:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:37:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:37:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:37:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:37:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:37:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:37:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:37:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:37:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:37:38] [info]
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info]                      Performance Metrics
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:37:38] [info] |-----|-----|
[2025-10-12 20:37:38] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:37:38] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 20:37:38] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info]                Summary
[2025-10-12 20:37:38] [info] -----
[2025-10-12 20:37:38] [info] Total Errors:    1120
[2025-10-12 20:37:38] [info] Total Warnings:    321
[2025-10-12 20:37:38] [info] DRC Status:      FAILED
```

lvr_summary_625.rpt

```
[2025-10-12 20:38:36] [info] =====
[2025-10-12 20:38:36] [info]                Design Rule Check Report
[2025-10-12 20:38:36] [info] =====

[2025-10-12 20:38:36] [info] Report Generated:    2025-10-12
[2025-10-12 20:38:36] [info] Run Start Time:      08:37:45 PM
[2025-10-12 20:38:36] [info] Run End Time:        08:38:36 PM
[2025-10-12 20:38:36] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:38:36] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:38:36] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:38:36] [info] User Name:           ubuntu
[2025-10-12 20:38:36] [info] Cores Used:          8
[2025-10-12 20:38:36] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info]                Top Cell Information
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] Layout Top Cell:     TOP
[2025-10-12 20:38:36] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:38:36] [info] Database Format:       GDSII
[2025-10-12 20:38:36] [info] Verification Mode:    Full Check

[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info]                Type of Errors | Count
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] DB_WARN -> 1
[2025-10-12 20:38:36] [info] LRF_ERR001 -> 14
[2025-10-12 20:38:36] [info] DB_WARN002 -> 159
[2025-10-12 20:38:36] [info] DB_WARN003 -> 161
[2025-10-12 20:38:36] [info] DRC_ERR9 -> 89
[2025-10-12 20:38:36] [info] DRC_ERR -> 67
[2025-10-12 20:38:36] [info] DRC_ERR5 -> 408
[2025-10-12 20:38:36] [info] DRC_ERR4 -> 241
[2025-10-12 20:38:36] [info] DRC_ERR1 -> 301
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info]                Rule Violation Summary
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:38:36] [info] |-----|-----|-----|
[2025-10-12 20:38:36] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:38:36] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:38:36] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:38:36] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:38:36] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:38:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:38:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:38:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:38:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:38:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:38:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:38:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:38:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:38:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:38:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:38:36] [info]
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] Performance Metrics
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] | Stage | Time Taken |
[2025-10-12 20:38:36] [info] |-----|-----|
[2025-10-12 20:38:36] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:38:36] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 20:38:36] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] Summary
[2025-10-12 20:38:36] [info] -----
[2025-10-12 20:38:36] [info] Total Errors: 1120
[2025-10-12 20:38:36] [info] Total Warnings: 321
[2025-10-12 20:38:36] [info] DRC Status: FAILED
```

lvr_summary_626.rpt

```
[2025-10-12 20:39:34] [info] =====
[2025-10-12 20:39:34] [info] Design Rule Check Report
[2025-10-12 20:39:34] [info] =====

[2025-10-12 20:39:34] [info] Report Generated: 2025-10-12
[2025-10-12 20:39:34] [info] Run Start Time: 08:38:42 PM
[2025-10-12 20:39:34] [info] Run End Time: 08:39:34 PM
[2025-10-12 20:39:34] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:39:34] [info] Tool Version: LVR_2025.1
[2025-10-12 20:39:34] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:39:34] [info] User Name: ubuntu
[2025-10-12 20:39:34] [info] Cores Used: 8
[2025-10-12 20:39:34] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Top Cell Information
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Layout Top Cell: TOP
[2025-10-12 20:39:34] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:39:34] [info] Database Format: GDSII
[2025-10-12 20:39:34] [info] Verification Mode: Full Check

[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Type of Errors | Count
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] DB_WARN -> 1
[2025-10-12 20:39:34] [info] LRF_ERR001 -> 14
[2025-10-12 20:39:34] [info] DB_WARN002 -> 159
[2025-10-12 20:39:34] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:39:34] [info] DRC_ERR9 -> 89
[2025-10-12 20:39:34] [info] DRC_ERR -> 67
[2025-10-12 20:39:34] [info] DRC_ERR5 -> 408
[2025-10-12 20:39:34] [info] DRC_ERR4 -> 241
[2025-10-12 20:39:34] [info] DRC_ERR1 -> 301
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Rule Violation Summary
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:39:34] [info] |-----|-----|-----|
[2025-10-12 20:39:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:39:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:39:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:39:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:39:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:39:34] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:39:34] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:39:34] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:39:34] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:39:34] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:39:34] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:39:34] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:39:34] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:39:34] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:39:34] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Performance Metrics
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] | Stage          | Time Taken |
[2025-10-12 20:39:34] [info] |-----|-----|
[2025-10-12 20:39:34] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:39:34] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:39:34] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Summary
[2025-10-12 20:39:34] [info] -----
[2025-10-12 20:39:34] [info] Total Errors: 1120
[2025-10-12 20:39:34] [info] Total Warnings: 321
[2025-10-12 20:39:34] [info] DRC Status: FAILED
```

lvr_summary_627.rpt

```
[2025-10-12 20:40:30] [info] =====
[2025-10-12 20:40:30] [info] Design Rule Check Report
[2025-10-12 20:40:30] [info] =====

[2025-10-12 20:40:30] [info] Report Generated: 2025-10-12
[2025-10-12 20:40:30] [info] Run Start Time: 08:39:38 PM
[2025-10-12 20:40:30] [info] Run End Time: 08:40:30 PM
[2025-10-12 20:40:30] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:40:30] [info] Tool Version: LVR_2025.1
[2025-10-12 20:40:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:40:30] [info] User Name: ubuntu
[2025-10-12 20:40:30] [info] Cores Used: 8
[2025-10-12 20:40:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```



```
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] Top Cell Information
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] Layout Top Cell:      TOP
[2025-10-12 20:40:30] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:40:30] [info] Database Format:      GDSII
[2025-10-12 20:40:30] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] Type of Errors | Count
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] DB_WARN -> 1
[2025-10-12 20:40:30] [info] LRF_ERR001 -> 14
[2025-10-12 20:40:30] [info] DB_WARN002 -> 159
[2025-10-12 20:40:30] [info] DB_WARN003 -> 161
[2025-10-12 20:40:30] [info] DRC_ERR9 -> 89
[2025-10-12 20:40:30] [info] DRC_ERR -> 67
[2025-10-12 20:40:30] [info] DRC_ERR5 -> 408
[2025-10-12 20:40:30] [info] DRC_ERR4 -> 241
[2025-10-12 20:40:30] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] Rule Violation Summary
```

```
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:40:30] [info] |-----|-----|-----|
[2025-10-12 20:40:30] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:40:30] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:40:30] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:40:30] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:40:30] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:40:30] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:40:30] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:40:30] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:40:30] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:40:30] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:40:30] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:40:30] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:40:30] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:40:30] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:40:30] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:40:30] [info] -----
```

```
[2025-10-12 20:40:30] [info] Performance Metrics
```

```
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] | Stage          | Time Taken |
[2025-10-12 20:40:30] [info] |-----|-----|
[2025-10-12 20:40:30] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:40:30] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:40:30] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:40:30] [info] -----
```

```
[2025-10-12 20:40:30] [info] Summary
```

```
[2025-10-12 20:40:30] [info] -----
[2025-10-12 20:40:30] [info] Total Errors:      1120
[2025-10-12 20:40:30] [info] Total Warnings:    321
[2025-10-12 20:40:30] [info] DRC Status:        FAILED
```

lvr_summary_628.rpt

```
[2025-10-12 20:41:28] [info] =====
[2025-10-12 20:41:28] [info]                Design Rule Check Report
[2025-10-12 20:41:28] [info] =====

[2025-10-12 20:41:28] [info] Report Generated:    2025-10-12
[2025-10-12 20:41:28] [info] Run Start Time:      08:40:37 PM
[2025-10-12 20:41:28] [info] Run End Time:        08:41:28 PM
[2025-10-12 20:41:28] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 20:41:28] [info] Tool Version:       LVR_2025.1
[2025-10-12 20:41:28] [info] Host Name:          ip-172-31-37-173
[2025-10-12 20:41:28] [info] User Name:          ubuntu
[2025-10-12 20:41:28] [info] Cores Used:         8
[2025-10-12 20:41:28] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info]                Top Cell Information
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info] Layout Top Cell:    TOP
[2025-10-12 20:41:28] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 20:41:28] [info] Database Format:     GDSII
[2025-10-12 20:41:28] [info] Verification Mode:   Full Check

[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info]                Type of Errors | Count
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info] DB_WARN -> 1
[2025-10-12 20:41:28] [info] LRF_ERR001 -> 14
[2025-10-12 20:41:28] [info] DB_WARN002 -> 159
[2025-10-12 20:41:28] [info] DB_WARN003 -> 161
[2025-10-12 20:41:28] [info] DRC_ERR9 -> 89
[2025-10-12 20:41:28] [info] DRC_ERR -> 67
[2025-10-12 20:41:28] [info] DRC_ERR5 -> 408
[2025-10-12 20:41:28] [info] DRC_ERR4 -> 241
[2025-10-12 20:41:28] [info] DRC_ERR1 -> 301
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info]                Rule Violation Summary
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:41:28] [info] |-----|-----|-----|
[2025-10-12 20:41:28] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:41:28] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:41:28] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:41:28] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:41:28] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:41:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:41:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:41:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:41:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:41:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:41:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:41:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:41:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:41:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:41:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:41:28] [info]
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info]                Performance Metrics
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:41:28] [info] |-----|-----|
[2025-10-12 20:41:28] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:41:28] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:41:28] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info] Summary
[2025-10-12 20:41:28] [info] -----
[2025-10-12 20:41:28] [info] Total Errors: 1120
[2025-10-12 20:41:28] [info] Total Warnings: 321
[2025-10-12 20:41:28] [info] DRC Status: FAILED
```

lvr_summary_629.rpt

```
[2025-10-12 20:42:26] [info] =====
[2025-10-12 20:42:26] [info] Design Rule Check Report
[2025-10-12 20:42:26] [info] =====

[2025-10-12 20:42:26] [info] Report Generated: 2025-10-12
[2025-10-12 20:42:26] [info] Run Start Time: 08:41:34 PM
[2025-10-12 20:42:26] [info] Run End Time: 08:42:26 PM
[2025-10-12 20:42:26] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:42:26] [info] Tool Version: LVR_2025.1
[2025-10-12 20:42:26] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:42:26] [info] User Name: ubuntu
[2025-10-12 20:42:26] [info] Cores Used: 8
[2025-10-12 20:42:26] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Top Cell Information
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Layout Top Cell: TOP
[2025-10-12 20:42:26] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:42:26] [info] Database Format: GDSII
[2025-10-12 20:42:26] [info] Verification Mode: Full Check

[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Type of Errors | Count
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] DB_WARN -> 1
[2025-10-12 20:42:26] [info] LRF_ERR001 -> 14
[2025-10-12 20:42:26] [info] DB_WARN002 -> 159
[2025-10-12 20:42:26] [info] DB_WARN003 -> 161
[2025-10-12 20:42:26] [info] DRC_ERR9 -> 89
[2025-10-12 20:42:26] [info] DRC_ERR -> 67
[2025-10-12 20:42:26] [info] DRC_ERR5 -> 408
[2025-10-12 20:42:26] [info] DRC_ERR4 -> 241
[2025-10-12 20:42:26] [info] DRC_ERR1 -> 301
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Rule Violation Summary
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:42:26] [info] |-----|-----|-----|
[2025-10-12 20:42:26] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:42:26] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:42:26] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:42:26] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:42:26] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```

[2025-10-12 20:42:26] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 20:42:26] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:42:26] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:42:26] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:42:26] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:42:26] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:42:26] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:42:26] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:42:26] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:42:26] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:42:26] [info]
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Performance Metrics
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] | Stage | Time Taken |
[2025-10-12 20:42:26] [info] |-----|-----|
[2025-10-12 20:42:26] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:42:26] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:42:26] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Summary
[2025-10-12 20:42:26] [info] -----
[2025-10-12 20:42:26] [info] Total Errors: 1120
[2025-10-12 20:42:26] [info] Total Warnings: 321
[2025-10-12 20:42:26] [info] DRC Status: FAILED

```

lvr_summary_63.rpt

```

[2025-10-12 11:41:08] [info] =====
[2025-10-12 11:41:08] [info] Design Rule Check Report
[2025-10-12 11:41:08] [info] =====

[2025-10-12 11:41:08] [info] Report Generated: 2025-10-12
[2025-10-12 11:41:08] [info] Run Start Time: 11:40:18 AM
[2025-10-12 11:41:08] [info] Run End Time: 11:41:08 AM
[2025-10-12 11:41:08] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:41:08] [info] Tool Version: LVR_2025.1
[2025-10-12 11:41:08] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:41:08] [info] User Name: ubuntu
[2025-10-12 11:41:08] [info] Cores Used: 8
[2025-10-12 11:41:08] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Top Cell Information
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Layout Top Cell: TOP
[2025-10-12 11:41:08] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:41:08] [info] Database Format: GDSII
[2025-10-12 11:41:08] [info] Verification Mode: Full Check

[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Type of Errors | Count
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] DB_WARN -> 1
[2025-10-12 11:41:08] [info] LRF_ERR001 -> 14
[2025-10-12 11:41:08] [info] DB_WARN002 -> 159
[2025-10-12 11:41:08] [info] DB_WARN003 -> 161

```

```
[2025-10-12 11:41:08] [info] DRC_ERR9 -> 89
[2025-10-12 11:41:08] [info] DRC_ERR -> 67
[2025-10-12 11:41:08] [info] DRC_ERR5 -> 408
[2025-10-12 11:41:08] [info] DRC_ERR4 -> 241
[2025-10-12 11:41:08] [info] DRC_ERR1 -> 301
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Rule Violation Summary
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:41:08] [info] |-----|-----|-----|
[2025-10-12 11:41:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:41:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:41:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:41:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:41:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:41:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:41:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:41:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:41:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:41:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:41:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:41:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:41:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:41:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:41:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Performance Metrics
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] | Stage | Time Taken |
[2025-10-12 11:41:08] [info] |-----|-----|
[2025-10-12 11:41:08] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:41:08] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 11:41:08] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Summary
[2025-10-12 11:41:08] [info] -----
[2025-10-12 11:41:08] [info] Total Errors: 1120
[2025-10-12 11:41:08] [info] Total Warnings: 321
[2025-10-12 11:41:08] [info] DRC Status: FAILED
```

lvr_summary_630.rpt

```
[2025-10-12 20:43:23] [info] =====
[2025-10-12 20:43:23] [info] Design Rule Check Report
[2025-10-12 20:43:23] [info] =====

[2025-10-12 20:43:23] [info] Report Generated: 2025-10-12
[2025-10-12 20:43:23] [info] Run Start Time: 08:42:31 PM
[2025-10-12 20:43:23] [info] Run End Time: 08:43:23 PM
[2025-10-12 20:43:23] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:43:23] [info] Tool Version: LVR_2025.1
[2025-10-12 20:43:23] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:43:23] [info] User Name: ubuntu
[2025-10-12 20:43:23] [info] Cores Used: 8
[2025-10-12 20:43:23] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] Top Cell Information
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] Layout Top Cell:      TOP
[2025-10-12 20:43:23] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:43:23] [info] Database Format:      GDSII
[2025-10-12 20:43:23] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] Type of Errors | Count
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] DB_WARN -> 1
[2025-10-12 20:43:23] [info] LRF_ERR001 -> 14
[2025-10-12 20:43:23] [info] DB_WARN002 -> 159
[2025-10-12 20:43:23] [info] DB_WARN003 -> 161
[2025-10-12 20:43:23] [info] DRC_ERR9 -> 89
[2025-10-12 20:43:23] [info] DRC_ERR -> 67
[2025-10-12 20:43:23] [info] DRC_ERR5 -> 408
[2025-10-12 20:43:23] [info] DRC_ERR4 -> 241
[2025-10-12 20:43:23] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] Rule Violation Summary
```

```
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:43:23] [info] |-----|-----|-----|
[2025-10-12 20:43:23] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:43:23] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:43:23] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:43:23] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:43:23] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:43:23] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:43:23] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:43:23] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:43:23] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:43:23] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:43:23] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:43:23] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:43:23] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:43:23] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:43:23] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:43:23] [info] -----
```

```
[2025-10-12 20:43:23] [info] Performance Metrics
```

```
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] | Stage          | Time Taken |
[2025-10-12 20:43:23] [info] |-----|-----|
[2025-10-12 20:43:23] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:43:23] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:43:23] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:43:23] [info] -----
```

```
[2025-10-12 20:43:23] [info] Summary
```

```
[2025-10-12 20:43:23] [info] -----
[2025-10-12 20:43:23] [info] Total Errors:      1120
[2025-10-12 20:43:23] [info] Total Warnings:    321
[2025-10-12 20:43:23] [info] DRC Status:        FAILED
```

lvr_summary_631.rpt

```
[2025-10-12 20:44:19] [info] =====
[2025-10-12 20:44:19] [info] Design Rule Check Report
[2025-10-12 20:44:19] [info] =====

[2025-10-12 20:44:19] [info] Report Generated: 2025-10-12
[2025-10-12 20:44:19] [info] Run Start Time: 08:43:28 PM
[2025-10-12 20:44:19] [info] Run End Time: 08:44:19 PM
[2025-10-12 20:44:19] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:44:19] [info] Tool Version: LVR_2025.1
[2025-10-12 20:44:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:44:19] [info] User Name: ubuntu
[2025-10-12 20:44:19] [info] Cores Used: 8
[2025-10-12 20:44:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] Top Cell Information
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] Layout Top Cell: TOP
[2025-10-12 20:44:19] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:44:19] [info] Database Format: GDSII
[2025-10-12 20:44:19] [info] Verification Mode: Full Check

[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] Type of Errors | Count
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] DB_WARN -> 1
[2025-10-12 20:44:19] [info] LRF_ERR001 -> 14
[2025-10-12 20:44:19] [info] DB_WARN002 -> 159
[2025-10-12 20:44:19] [info] DB_WARN003 -> 161
[2025-10-12 20:44:19] [info] DRC_ERR9 -> 89
[2025-10-12 20:44:19] [info] DRC_ERR -> 67
[2025-10-12 20:44:19] [info] DRC_ERR5 -> 408
[2025-10-12 20:44:19] [info] DRC_ERR4 -> 241
[2025-10-12 20:44:19] [info] DRC_ERR1 -> 301
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] Rule Violation Summary
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:44:19] [info] |-----|-----|-----|
[2025-10-12 20:44:19] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:44:19] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:44:19] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:44:19] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:44:19] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:44:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:44:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:44:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:44:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:44:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:44:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:44:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:44:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:44:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:44:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:44:19] [info]
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] Performance Metrics
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:44:19] [info] |-----|
[2025-10-12 20:44:19] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:44:19] [info] | Rule Checking        | 0 minutes 41 seconds |
[2025-10-12 20:44:19] [info] | Report Generation     | 0 minutes 0 seconds  |

[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info]                Summary
[2025-10-12 20:44:19] [info] -----
[2025-10-12 20:44:19] [info] Total Errors:    1120
[2025-10-12 20:44:19] [info] Total Warnings:    321
[2025-10-12 20:44:19] [info] DRC Status:      FAILED
```

lvr_summary_632.rpt

```
[2025-10-12 20:45:15] [info] =====
[2025-10-12 20:45:15] [info]                Design Rule Check Report
[2025-10-12 20:45:15] [info] =====

[2025-10-12 20:45:15] [info] Report Generated:    2025-10-12
[2025-10-12 20:45:15] [info] Run Start Time:      08:44:24 PM
[2025-10-12 20:45:15] [info] Run End Time:        08:45:15 PM
[2025-10-12 20:45:15] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:45:15] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:45:15] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:45:15] [info] User Name:           ubuntu
[2025-10-12 20:45:15] [info] Cores Used:          8
[2025-10-12 20:45:15] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info]                Top Cell Information
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] Layout Top Cell:     TOP
[2025-10-12 20:45:15] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:45:15] [info] Database Format:       GDSII
[2025-10-12 20:45:15] [info] Verification Mode:    Full Check

[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info]                Type of Errors | Count
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] DB_WARN -> 1
[2025-10-12 20:45:15] [info] LRF_ERR001 -> 14
[2025-10-12 20:45:15] [info] DB_WARN002 -> 159
[2025-10-12 20:45:15] [info] DB_WARN003 -> 161
[2025-10-12 20:45:15] [info] DRC_ERR9 -> 89
[2025-10-12 20:45:15] [info] DRC_ERR -> 67
[2025-10-12 20:45:15] [info] DRC_ERR5 -> 408
[2025-10-12 20:45:15] [info] DRC_ERR4 -> 241
[2025-10-12 20:45:15] [info] DRC_ERR1 -> 301
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info]                Rule Violation Summary
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:45:15] [info] |-----|-----|-----|
[2025-10-12 20:45:15] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:45:15] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:45:15] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:45:15] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:45:15] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 20:45:15] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:45:15] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:45:15] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:45:15] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:45:15] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:45:15] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:45:15] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:45:15] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:45:15] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:45:15] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:45:15] [info]
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] Performance Metrics
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] | Stage | Time Taken |
[2025-10-12 20:45:15] [info] |-----|-----|
[2025-10-12 20:45:15] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:45:15] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 20:45:15] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] Summary
[2025-10-12 20:45:15] [info] -----
[2025-10-12 20:45:15] [info] Total Errors: 1120
[2025-10-12 20:45:15] [info] Total Warnings: 321
[2025-10-12 20:45:15] [info] DRC Status: FAILED
```

lvr_summary_633.rpt

```
[2025-10-12 20:46:14] [info] =====
[2025-10-12 20:46:14] [info] Design Rule Check Report
[2025-10-12 20:46:14] [info] =====

[2025-10-12 20:46:14] [info] Report Generated: 2025-10-12
[2025-10-12 20:46:14] [info] Run Start Time: 08:45:22 PM
[2025-10-12 20:46:14] [info] Run End Time: 08:46:14 PM
[2025-10-12 20:46:14] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:46:14] [info] Tool Version: LVR_2025.1
[2025-10-12 20:46:14] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:46:14] [info] User Name: ubuntu
[2025-10-12 20:46:14] [info] Cores Used: 8
[2025-10-12 20:46:14] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Top Cell Information
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Layout Top Cell: TOP
[2025-10-12 20:46:14] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:46:14] [info] Database Format: GDSII
[2025-10-12 20:46:14] [info] Verification Mode: Full Check

[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Type of Errors | Count
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] DB_WARN -> 1
[2025-10-12 20:46:14] [info] LRF_ERR001 -> 14
[2025-10-12 20:46:14] [info] DB_WARN002 -> 159
[2025-10-12 20:46:14] [info] DB_WARN003 -> 161
```

```

[2025-10-12 20:46:14] [info] DRC_ERR9 -> 89
[2025-10-12 20:46:14] [info] DRC_ERR -> 67
[2025-10-12 20:46:14] [info] DRC_ERR5 -> 408
[2025-10-12 20:46:14] [info] DRC_ERR4 -> 241
[2025-10-12 20:46:14] [info] DRC_ERR1 -> 301
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Rule Violation Summary
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:46:14] [info] |-----|-----|-----|
[2025-10-12 20:46:14] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:46:14] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:46:14] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:46:14] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:46:14] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:46:14] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:46:14] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:46:14] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:46:14] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:46:14] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:46:14] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:46:14] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:46:14] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:46:14] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:46:14] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Performance Metrics
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] | Stage | Time Taken |
[2025-10-12 20:46:14] [info] |-----|-----|
[2025-10-12 20:46:14] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:46:14] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:46:14] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Summary
[2025-10-12 20:46:14] [info] -----
[2025-10-12 20:46:14] [info] Total Errors: 1120
[2025-10-12 20:46:14] [info] Total Warnings: 321
[2025-10-12 20:46:14] [info] DRC Status: FAILED

```

lvr_summary_634.rpt

```

[2025-10-12 20:47:09] [info] =====
[2025-10-12 20:47:09] [info] Design Rule Check Report
[2025-10-12 20:47:09] [info] =====

[2025-10-12 20:47:09] [info] Report Generated: 2025-10-12
[2025-10-12 20:47:09] [info] Run Start Time: 08:46:19 PM
[2025-10-12 20:47:09] [info] Run End Time: 08:47:09 PM
[2025-10-12 20:47:09] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:47:09] [info] Tool Version: LVR_2025.1
[2025-10-12 20:47:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:47:09] [info] User Name: ubuntu
[2025-10-12 20:47:09] [info] Cores Used: 8
[2025-10-12 20:47:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] Top Cell Information
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] Layout Top Cell:      TOP
[2025-10-12 20:47:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:47:09] [info] Database Format:       GDSII
[2025-10-12 20:47:09] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] Type of Errors | Count
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] DB_WARN -> 1
[2025-10-12 20:47:09] [info] LRF_ERR001 -> 14
[2025-10-12 20:47:09] [info] DB_WARN002 -> 159
[2025-10-12 20:47:09] [info] DB_WARN003 -> 161
[2025-10-12 20:47:09] [info] DRC_ERR9 -> 89
[2025-10-12 20:47:09] [info] DRC_ERR -> 67
[2025-10-12 20:47:09] [info] DRC_ERR5 -> 408
[2025-10-12 20:47:09] [info] DRC_ERR4 -> 241
[2025-10-12 20:47:09] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] Rule Violation Summary
```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:47:09] [info] |-----|-----|-----|
[2025-10-12 20:47:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:47:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:47:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:47:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:47:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:47:09] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:47:09] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:47:09] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:47:09] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:47:09] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:47:09] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:47:09] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:47:09] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:47:09] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:47:09] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:47:09] [info] -----
```

```
[2025-10-12 20:47:09] [info] Performance Metrics
```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] | Stage          | Time Taken |
[2025-10-12 20:47:09] [info] |-----|-----|
[2025-10-12 20:47:09] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:47:09] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:47:09] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] Summary
```

```
[2025-10-12 20:47:09] [info] -----
[2025-10-12 20:47:09] [info] Total Errors:      1120
[2025-10-12 20:47:09] [info] Total Warnings:    321
[2025-10-12 20:47:09] [info] DRC Status:        FAILED
```

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```
[2025-10-12 20:48:07] [info] =====
[2025-10-12 20:48:07] [info] Design Rule Check Report
[2025-10-12 20:48:07] [info] =====

[2025-10-12 20:48:07] [info] Report Generated: 2025-10-12
[2025-10-12 20:48:07] [info] Run Start Time: 08:47:15 PM
[2025-10-12 20:48:07] [info] Run End Time: 08:48:07 PM
[2025-10-12 20:48:07] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:48:07] [info] Tool Version: LVR_2025.1
[2025-10-12 20:48:07] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:48:07] [info] User Name: ubuntu
[2025-10-12 20:48:07] [info] Cores Used: 8
[2025-10-12 20:48:07] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] Top Cell Information
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] Layout Top Cell: TOP
[2025-10-12 20:48:07] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:48:07] [info] Database Format: GDSII
[2025-10-12 20:48:07] [info] Verification Mode: Full Check

[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] Type of Errors | Count
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] DB_WARN -> 1
[2025-10-12 20:48:07] [info] LRF_ERR001 -> 14
[2025-10-12 20:48:07] [info] DB_WARN002 -> 159
[2025-10-12 20:48:07] [info] DB_WARN003 -> 161
[2025-10-12 20:48:07] [info] DRC_ERR9 -> 89
[2025-10-12 20:48:07] [info] DRC_ERR -> 67
[2025-10-12 20:48:07] [info] DRC_ERR5 -> 408
[2025-10-12 20:48:07] [info] DRC_ERR4 -> 241
[2025-10-12 20:48:07] [info] DRC_ERR1 -> 301
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] Rule Violation Summary
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:48:07] [info] |-----|-----|-----|
[2025-10-12 20:48:07] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:48:07] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:48:07] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:48:07] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:48:07] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:48:07] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:48:07] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:48:07] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:48:07] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:48:07] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:48:07] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:48:07] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:48:07] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:48:07] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:48:07] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:48:07] [info]
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] Performance Metrics
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:48:07] [info] |-----|-----|
[2025-10-12 20:48:07] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:48:07] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:48:07] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info]                Summary
[2025-10-12 20:48:07] [info] -----
[2025-10-12 20:48:07] [info] Total Errors:    1120
[2025-10-12 20:48:07] [info] Total Warnings:    321
[2025-10-12 20:48:07] [info] DRC Status:      FAILED
```

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```
[2025-10-12 20:49:04] [info] =====
[2025-10-12 20:49:04] [info]                Design Rule Check Report
[2025-10-12 20:49:04] [info] =====

[2025-10-12 20:49:04] [info] Report Generated:    2025-10-12
[2025-10-12 20:49:04] [info] Run Start Time:      08:48:12 PM
[2025-10-12 20:49:04] [info] Run End Time:        08:49:04 PM
[2025-10-12 20:49:04] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:49:04] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:49:04] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:49:04] [info] User Name:           ubuntu
[2025-10-12 20:49:04] [info] Cores Used:          8
[2025-10-12 20:49:04] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info]                Top Cell Information
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] Layout Top Cell:     TOP
[2025-10-12 20:49:04] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:49:04] [info] Database Format:       GDSII
[2025-10-12 20:49:04] [info] Verification Mode:    Full Check

[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info]                Type of Errors | Count
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] DB_WARN -> 1
[2025-10-12 20:49:04] [info] LRF_ERR001 -> 14
[2025-10-12 20:49:04] [info] DB_WARN002 -> 159
[2025-10-12 20:49:04] [info] DB_WARN003 -> 161
[2025-10-12 20:49:04] [info] DRC_ERR9 -> 89
[2025-10-12 20:49:04] [info] DRC_ERR -> 67
[2025-10-12 20:49:04] [info] DRC_ERR5 -> 408
[2025-10-12 20:49:04] [info] DRC_ERR4 -> 241
[2025-10-12 20:49:04] [info] DRC_ERR1 -> 301
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info]                Rule Violation Summary
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:49:04] [info] |-----|-----|-----|
[2025-10-12 20:49:04] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:49:04] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:49:04] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:49:04] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:49:04] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:49:04] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 20:49:04] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:49:04] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:49:04] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:49:04] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:49:04] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:49:04] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:49:04] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:49:04] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:49:04] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:49:04] [info]
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] Performance Metrics
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] | Stage | Time Taken |
[2025-10-12 20:49:04] [info] |-----|-----|
[2025-10-12 20:49:04] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:49:04] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 20:49:04] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] Summary
[2025-10-12 20:49:04] [info] -----
[2025-10-12 20:49:04] [info] Total Errors: 1120
[2025-10-12 20:49:04] [info] Total Warnings: 321
[2025-10-12 20:49:04] [info] DRC Status: FAILED
```

lvr_summary_637.rpt

```
[2025-10-12 20:49:59] [info] =====
[2025-10-12 20:49:59] [info] Design Rule Check Report
[2025-10-12 20:49:59] [info] =====

[2025-10-12 20:49:59] [info] Report Generated: 2025-10-12
[2025-10-12 20:49:59] [info] Run Start Time: 08:49:08 PM
[2025-10-12 20:49:59] [info] Run End Time: 08:49:59 PM
[2025-10-12 20:49:59] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:49:59] [info] Tool Version: LVR_2025.1
[2025-10-12 20:49:59] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:49:59] [info] User Name: ubuntu
[2025-10-12 20:49:59] [info] Cores Used: 8
[2025-10-12 20:49:59] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Top Cell Information
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Layout Top Cell: TOP
[2025-10-12 20:49:59] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:49:59] [info] Database Format: GDSII
[2025-10-12 20:49:59] [info] Verification Mode: Full Check

[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Type of Errors | Count
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] DB_WARN -> 1
[2025-10-12 20:49:59] [info] LRF_ERR001 -> 14
[2025-10-12 20:49:59] [info] DB_WARN002 -> 159
[2025-10-12 20:49:59] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:49:59] [info] DRC_ERR9 -> 89
[2025-10-12 20:49:59] [info] DRC_ERR -> 67
[2025-10-12 20:49:59] [info] DRC_ERR5 -> 408
[2025-10-12 20:49:59] [info] DRC_ERR4 -> 241
[2025-10-12 20:49:59] [info] DRC_ERR1 -> 301
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Rule Violation Summary
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:49:59] [info] |-----|-----|-----|
[2025-10-12 20:49:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:49:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:49:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:49:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:49:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:49:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:49:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:49:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:49:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:49:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:49:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:49:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:49:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:49:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:49:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Performance Metrics
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] | Stage | Time Taken |
[2025-10-12 20:49:59] [info] |-----|-----|
[2025-10-12 20:49:59] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:49:59] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 20:49:59] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Summary
[2025-10-12 20:49:59] [info] -----
[2025-10-12 20:49:59] [info] Total Errors: 1120
[2025-10-12 20:49:59] [info] Total Warnings: 321
[2025-10-12 20:49:59] [info] DRC Status: FAILED
```

lvr_summary_638.rpt

```
[2025-10-12 20:50:57] [info] =====
[2025-10-12 20:50:57] [info] Design Rule Check Report
[2025-10-12 20:50:57] [info] =====

[2025-10-12 20:50:57] [info] Report Generated: 2025-10-12
[2025-10-12 20:50:57] [info] Run Start Time: 08:50:05 PM
[2025-10-12 20:50:57] [info] Run End Time: 08:50:57 PM
[2025-10-12 20:50:57] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:50:57] [info] Tool Version: LVR_2025.1
[2025-10-12 20:50:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:50:57] [info] User Name: ubuntu
[2025-10-12 20:50:57] [info] Cores Used: 8
[2025-10-12 20:50:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] Top Cell Information
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] Layout Top Cell:      TOP
[2025-10-12 20:50:57] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:50:57] [info] Database Format:      GDSII
[2025-10-12 20:50:57] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] Type of Errors | Count
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] DB_WARN -> 1
[2025-10-12 20:50:57] [info] LRF_ERR001 -> 14
[2025-10-12 20:50:57] [info] DB_WARN002 -> 159
[2025-10-12 20:50:57] [info] DB_WARN003 -> 161
[2025-10-12 20:50:57] [info] DRC_ERR9 -> 89
[2025-10-12 20:50:57] [info] DRC_ERR -> 67
[2025-10-12 20:50:57] [info] DRC_ERR5 -> 408
[2025-10-12 20:50:57] [info] DRC_ERR4 -> 241
[2025-10-12 20:50:57] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] Rule Violation Summary
[2025-10-12 20:50:57] [info] -----
```

[2025-10-12 20:50:57] [info]	Rule ID	Description	Error Count
[2025-10-12 20:50:57] [info]	m5.1	"m5.1 : min. m5 width : 1.6um" 4	
[2025-10-12 20:50:57] [info]	li.5	"li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" 5	
[2025-10-12 20:50:57] [info]	poly.1a	"poly.1a : min. poly width : 0.15um" 19	
[2025-10-12 20:50:57] [info]	poly.7	"poly.7 : min. source/drain length : 0.25um" 8	
[2025-10-12 20:50:57] [info]	licon.15	"licon.15 : min. npc enclosure of poly-licon : 0.1um" 110	
[2025-10-12 20:50:57] [info]	m1.6	"m1.6 : min. m1 area : 0.083um ² " 59	
[2025-10-12 20:50:57] [info]	m4.5	"m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" 391	
[2025-10-12 20:50:57] [info]	m4.5ab	"m4.5ab : min. 3um.m4 spacing m4 : 0.4um" 301	
[2025-10-12 20:50:57] [info]	psdm.1	"psdm.1 : min. psdm width : 0.38um" 14	
[2025-10-12 20:50:57] [info]	licon.5	"licon.5 : min. diff enclosure of licon : 0.04um" 131	
[2025-10-12 20:50:57] [info]	li.1	"li.1 : min. li width : 0.17um" 17	
[2025-10-12 20:50:57] [info]	li.1a	"li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" 15	
[2025-10-12 20:50:57] [info]	m1.1	"m1.1 : min. m1 width : 0.14um" 3	
[2025-10-12 20:50:57] [info]	licon.8a	"licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" 12	
[2025-10-12 20:50:57] [info]	npc.1	"npc.1 : min. npc width : 0.27um" 17	

```
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] Performance Metrics
[2025-10-12 20:50:57] [info] -----
```

[2025-10-12 20:50:57] [info]	Stage	Time Taken
[2025-10-12 20:50:57] [info]	GDS Parsing	0 minutes 9 seconds
[2025-10-12 20:50:57] [info]	Rule Checking	0 minutes 40 seconds
[2025-10-12 20:50:57] [info]	Report Generation	0 minutes 0 seconds

```
[2025-10-12 20:50:57] [info] -----
[2025-10-12 20:50:57] [info] Summary
[2025-10-12 20:50:57] [info] -----
```

```
[2025-10-12 20:50:57] [info] Total Errors:      1120
[2025-10-12 20:50:57] [info] Total Warnings:    321
[2025-10-12 20:50:57] [info] DRC Status:        FAILED
```


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```
[2025-10-12 20:51:54] [info] =====
[2025-10-12 20:51:54] [info]                      Design Rule Check Report
[2025-10-12 20:51:54] [info] =====

[2025-10-12 20:51:54] [info] Report Generated:      2025-10-12
[2025-10-12 20:51:54] [info] Run Start Time:       08:51:02 PM
[2025-10-12 20:51:54] [info] Run End Time:         08:51:54 PM
[2025-10-12 20:51:54] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 20:51:54] [info] Tool Version:          LVR_2025.1
[2025-10-12 20:51:54] [info] Host Name:             ip-172-31-37-173
[2025-10-12 20:51:54] [info] User Name:             ubuntu
[2025-10-12 20:51:54] [info] Cores Used:            8
[2025-10-12 20:51:54] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info]                      Top Cell Information
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info] Layout Top Cell:      TOP
[2025-10-12 20:51:54] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:51:54] [info] Database Format:       GDSII
[2025-10-12 20:51:54] [info] Verification Mode:     Full Check

[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info]                      Type of Errors | Count
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info] DB_WARN -> 1
[2025-10-12 20:51:54] [info] LRF_ERR001 -> 14
[2025-10-12 20:51:54] [info] DB_WARN002 -> 159
[2025-10-12 20:51:54] [info] DB_WARN003 -> 161
[2025-10-12 20:51:54] [info] DRC_ERR9 -> 89
[2025-10-12 20:51:54] [info] DRC_ERR -> 67
[2025-10-12 20:51:54] [info] DRC_ERR5 -> 408
[2025-10-12 20:51:54] [info] DRC_ERR4 -> 241
[2025-10-12 20:51:54] [info] DRC_ERR1 -> 301
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info]                      Rule Violation Summary
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:51:54] [info] |-----|-----|-----|
[2025-10-12 20:51:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:51:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:51:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:51:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:51:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:51:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:51:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:51:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:51:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:51:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:51:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:51:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:51:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:51:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:51:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:51:54] [info]
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info]                      Performance Metrics
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:51:54] [info] |-----|-----|
[2025-10-12 20:51:54] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:51:54] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 20:51:54] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info] Summary
[2025-10-12 20:51:54] [info] -----
[2025-10-12 20:51:54] [info] Total Errors: 1120
[2025-10-12 20:51:54] [info] Total Warnings: 321
[2025-10-12 20:51:54] [info] DRC Status: FAILED
```

lvr_summary_64.rpt

```
[2025-10-12 11:42:06] [info] =====
[2025-10-12 11:42:06] [info] Design Rule Check Report
[2025-10-12 11:42:06] [info] =====

[2025-10-12 11:42:06] [info] Report Generated: 2025-10-12
[2025-10-12 11:42:06] [info] Run Start Time: 11:41:15 AM
[2025-10-12 11:42:06] [info] Run End Time: 11:42:06 AM
[2025-10-12 11:42:06] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:42:06] [info] Tool Version: LVR_2025.1
[2025-10-12 11:42:06] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:42:06] [info] User Name: ubuntu
[2025-10-12 11:42:06] [info] Cores Used: 8
[2025-10-12 11:42:06] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Top Cell Information
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Layout Top Cell: TOP
[2025-10-12 11:42:06] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:42:06] [info] Database Format: GDSII
[2025-10-12 11:42:06] [info] Verification Mode: Full Check

[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Type of Errors | Count
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] DB_WARN -> 1
[2025-10-12 11:42:06] [info] LRF_ERR001 -> 14
[2025-10-12 11:42:06] [info] DB_WARN002 -> 159
[2025-10-12 11:42:06] [info] DB_WARN003 -> 161
[2025-10-12 11:42:06] [info] DRC_ERR9 -> 89
[2025-10-12 11:42:06] [info] DRC_ERR -> 67
[2025-10-12 11:42:06] [info] DRC_ERR5 -> 408
[2025-10-12 11:42:06] [info] DRC_ERR4 -> 241
[2025-10-12 11:42:06] [info] DRC_ERR1 -> 301
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Rule Violation Summary
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:42:06] [info] |-----|-----|-----|
[2025-10-12 11:42:06] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:42:06] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:42:06] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:42:06] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:42:06] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 11:42:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:42:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:42:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:42:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:42:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:42:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:42:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:42:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:42:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:42:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:42:06] [info]
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Performance Metrics
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] | Stage | Time Taken |
[2025-10-12 11:42:06] [info] |-----|-----|
[2025-10-12 11:42:06] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:42:06] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:42:06] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Summary
[2025-10-12 11:42:06] [info] -----
[2025-10-12 11:42:06] [info] Total Errors: 1120
[2025-10-12 11:42:06] [info] Total Warnings: 321
[2025-10-12 11:42:06] [info] DRC Status: FAILED
```

lvr_summary_640.rpt

```
[2025-10-12 20:52:50] [info] =====
[2025-10-12 20:52:50] [info] Design Rule Check Report
[2025-10-12 20:52:50] [info] =====

[2025-10-12 20:52:50] [info] Report Generated: 2025-10-12
[2025-10-12 20:52:50] [info] Run Start Time: 08:51:59 PM
[2025-10-12 20:52:50] [info] Run End Time: 08:52:50 PM
[2025-10-12 20:52:50] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:52:50] [info] Tool Version: LVR_2025.1
[2025-10-12 20:52:50] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:52:50] [info] User Name: ubuntu
[2025-10-12 20:52:50] [info] Cores Used: 8
[2025-10-12 20:52:50] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Top Cell Information
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Layout Top Cell: TOP
[2025-10-12 20:52:50] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:52:50] [info] Database Format: GDSII
[2025-10-12 20:52:50] [info] Verification Mode: Full Check

[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Type of Errors | Count
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] DB_WARN -> 1
[2025-10-12 20:52:50] [info] LRF_ERR001 -> 14
[2025-10-12 20:52:50] [info] DB_WARN002 -> 159
[2025-10-12 20:52:50] [info] DB_WARN003 -> 161
```

```
[2025-10-12 20:52:50] [info] DRC_ERR9 -> 89
[2025-10-12 20:52:50] [info] DRC_ERR -> 67
[2025-10-12 20:52:50] [info] DRC_ERR5 -> 408
[2025-10-12 20:52:50] [info] DRC_ERR4 -> 241
[2025-10-12 20:52:50] [info] DRC_ERR1 -> 301
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Rule Violation Summary
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:52:50] [info] |-----|-----|-----|
[2025-10-12 20:52:50] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:52:50] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:52:50] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:52:50] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:52:50] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:52:50] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:52:50] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:52:50] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:52:50] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:52:50] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:52:50] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:52:50] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:52:50] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:52:50] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:52:50] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Performance Metrics
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] | Stage          | Time Taken |
[2025-10-12 20:52:50] [info] |-----|-----|
[2025-10-12 20:52:50] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:52:50] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:52:50] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Summary
[2025-10-12 20:52:50] [info] -----
[2025-10-12 20:52:50] [info] Total Errors: 1120
[2025-10-12 20:52:50] [info] Total Warnings: 321
[2025-10-12 20:52:50] [info] DRC Status: FAILED
```

lvr_summary_641.rpt

```
[2025-10-12 20:53:45] [info] =====
[2025-10-12 20:53:45] [info] Design Rule Check Report
[2025-10-12 20:53:45] [info] =====

[2025-10-12 20:53:45] [info] Report Generated: 2025-10-12
[2025-10-12 20:53:45] [info] Run Start Time: 08:52:55 PM
[2025-10-12 20:53:45] [info] Run End Time: 08:53:45 PM
[2025-10-12 20:53:45] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:53:45] [info] Tool Version: LVR_2025.1
[2025-10-12 20:53:45] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:53:45] [info] User Name: ubuntu
[2025-10-12 20:53:45] [info] Cores Used: 8
[2025-10-12 20:53:45] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Top Cell Information
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Layout Top Cell:      TOP
[2025-10-12 20:53:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:53:45] [info] Database Format:      GDSII
[2025-10-12 20:53:45] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Type of Errors | Count
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] DB_WARN -> 1
[2025-10-12 20:53:45] [info] LRF_ERR001 -> 14
[2025-10-12 20:53:45] [info] DB_WARN002 -> 159
[2025-10-12 20:53:45] [info] DB_WARN003 -> 161
[2025-10-12 20:53:45] [info] DRC_ERR9 -> 89
[2025-10-12 20:53:45] [info] DRC_ERR -> 67
[2025-10-12 20:53:45] [info] DRC_ERR5 -> 408
[2025-10-12 20:53:45] [info] DRC_ERR4 -> 241
[2025-10-12 20:53:45] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Rule Violation Summary
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:53:45] [info] |-----|-----|-----|
[2025-10-12 20:53:45] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:53:45] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:53:45] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:53:45] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:53:45] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:53:45] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:53:45] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:53:45] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:53:45] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:53:45] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:53:45] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:53:45] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:53:45] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:53:45] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:53:45] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:53:45] [info] |
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Performance Metrics
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] | Stage          | Time Taken |
[2025-10-12 20:53:45] [info] |-----|-----|
[2025-10-12 20:53:45] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:53:45] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 20:53:45] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Summary
```

```
[2025-10-12 20:53:45] [info] -----
[2025-10-12 20:53:45] [info] Total Errors:      1120
[2025-10-12 20:53:45] [info] Total Warnings:    321
[2025-10-12 20:53:45] [info] DRC Status:        FAILED
```

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```
[2025-10-12 20:54:43] [info] =====
[2025-10-12 20:54:43] [info]                      Design Rule Check Report
[2025-10-12 20:54:43] [info] =====

[2025-10-12 20:54:43] [info] Report Generated:      2025-10-12
[2025-10-12 20:54:43] [info] Run Start Time:       08:53:52 PM
[2025-10-12 20:54:43] [info] Run End Time:        08:54:43 PM
[2025-10-12 20:54:43] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 20:54:43] [info] Tool Version:         LVR_2025.1
[2025-10-12 20:54:43] [info] Host Name:            ip-172-31-37-173
[2025-10-12 20:54:43] [info] User Name:            ubuntu
[2025-10-12 20:54:43] [info] Cores Used:           8
[2025-10-12 20:54:43] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info]                      Top Cell Information
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info] Layout Top Cell:      TOP
[2025-10-12 20:54:43] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:54:43] [info] Database Format:        GDSII
[2025-10-12 20:54:43] [info] Verification Mode:     Full Check

[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info]                      Type of Errors | Count
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info] DB_WARN -> 1
[2025-10-12 20:54:43] [info] LRF_ERR001 -> 14
[2025-10-12 20:54:43] [info] DB_WARN002 -> 159
[2025-10-12 20:54:43] [info] DB_WARN003 -> 161
[2025-10-12 20:54:43] [info] DRC_ERR9 -> 89
[2025-10-12 20:54:43] [info] DRC_ERR -> 67
[2025-10-12 20:54:43] [info] DRC_ERR5 -> 408
[2025-10-12 20:54:43] [info] DRC_ERR4 -> 241
[2025-10-12 20:54:43] [info] DRC_ERR1 -> 301
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info]                      Rule Violation Summary
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:54:43] [info] |-----|-----|-----|
[2025-10-12 20:54:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:54:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:54:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:54:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:54:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:54:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:54:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:54:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:54:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:54:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:54:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:54:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:54:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:54:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:54:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:54:43] [info]
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info]                      Performance Metrics
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:54:43] [info] |-----|-----|
[2025-10-12 20:54:43] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:54:43] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 20:54:43] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info]                Summary
[2025-10-12 20:54:43] [info] -----
[2025-10-12 20:54:43] [info] Total Errors:    1120
[2025-10-12 20:54:43] [info] Total Warnings:    321
[2025-10-12 20:54:43] [info] DRC Status:      FAILED
```

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```
[2025-10-12 20:55:39] [info] =====
[2025-10-12 20:55:39] [info]                Design Rule Check Report
[2025-10-12 20:55:39] [info] =====

[2025-10-12 20:55:39] [info] Report Generated:    2025-10-12
[2025-10-12 20:55:39] [info] Run Start Time:      08:54:48 PM
[2025-10-12 20:55:39] [info] Run End Time:        08:55:39 PM
[2025-10-12 20:55:39] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 20:55:39] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:55:39] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:55:39] [info] User Name:           ubuntu
[2025-10-12 20:55:39] [info] Cores Used:          8
[2025-10-12 20:55:39] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info]                Top Cell Information
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] Layout Top Cell:     TOP
[2025-10-12 20:55:39] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:55:39] [info] Database Format:       GDSII
[2025-10-12 20:55:39] [info] Verification Mode:    Full Check

[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info]                Type of Errors | Count
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] DB_WARN -> 1
[2025-10-12 20:55:39] [info] LRF_ERR001 -> 14
[2025-10-12 20:55:39] [info] DB_WARN002 -> 159
[2025-10-12 20:55:39] [info] DB_WARN003 -> 161
[2025-10-12 20:55:39] [info] DRC_ERR9 -> 89
[2025-10-12 20:55:39] [info] DRC_ERR -> 67
[2025-10-12 20:55:39] [info] DRC_ERR5 -> 408
[2025-10-12 20:55:39] [info] DRC_ERR4 -> 241
[2025-10-12 20:55:39] [info] DRC_ERR1 -> 301
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info]                Rule Violation Summary
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:55:39] [info] |-----|-----|-----|
[2025-10-12 20:55:39] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:55:39] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:55:39] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:55:39] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:55:39] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:55:39] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:55:39] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:55:39] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:55:39] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:55:39] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:55:39] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:55:39] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:55:39] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:55:39] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:55:39] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:55:39] [info]
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] Performance Metrics
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] | Stage | Time Taken |
[2025-10-12 20:55:39] [info] |-----|-----|
[2025-10-12 20:55:39] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:55:39] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 20:55:39] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] Summary
[2025-10-12 20:55:39] [info] -----
[2025-10-12 20:55:39] [info] Total Errors: 1120
[2025-10-12 20:55:39] [info] Total Warnings: 321
[2025-10-12 20:55:39] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:56:37] [info] =====
[2025-10-12 20:56:37] [info] Design Rule Check Report
[2025-10-12 20:56:37] [info] =====

[2025-10-12 20:56:37] [info] Report Generated: 2025-10-12
[2025-10-12 20:56:37] [info] Run Start Time: 08:55:45 PM
[2025-10-12 20:56:37] [info] Run End Time: 08:56:37 PM
[2025-10-12 20:56:37] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 20:56:37] [info] Tool Version: LVR_2025.1
[2025-10-12 20:56:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:56:37] [info] User Name: ubuntu
[2025-10-12 20:56:37] [info] Cores Used: 8
[2025-10-12 20:56:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Top Cell Information
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Layout Top Cell: TOP
[2025-10-12 20:56:37] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 20:56:37] [info] Database Format: GDSII
[2025-10-12 20:56:37] [info] Verification Mode: Full Check

[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Type of Errors | Count
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] DB_WARN -> 1
[2025-10-12 20:56:37] [info] LRF_ERR001 -> 14
[2025-10-12 20:56:37] [info] DB_WARN002 -> 159
[2025-10-12 20:56:37] [info] DB_WARN003 -> 161
```



```
[2025-10-12 20:56:37] [info] DRC_ERR9 -> 89
[2025-10-12 20:56:37] [info] DRC_ERR -> 67
[2025-10-12 20:56:37] [info] DRC_ERR5 -> 408
[2025-10-12 20:56:37] [info] DRC_ERR4 -> 241
[2025-10-12 20:56:37] [info] DRC_ERR1 -> 301
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Rule Violation Summary
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:56:37] [info] |-----|-----|-----|
[2025-10-12 20:56:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:56:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:56:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:56:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:56:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:56:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 20:56:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:56:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:56:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:56:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:56:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:56:37] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:56:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:56:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:56:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Performance Metrics
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] | Stage          | Time Taken |
[2025-10-12 20:56:37] [info] |-----|-----|
[2025-10-12 20:56:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:56:37] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 20:56:37] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Summary
[2025-10-12 20:56:37] [info] -----
[2025-10-12 20:56:37] [info] Total Errors: 1120
[2025-10-12 20:56:37] [info] Total Warnings: 321
[2025-10-12 20:56:37] [info] DRC Status: FAILED
```

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```
[2025-10-12 20:57:33] [info] =====
[2025-10-12 20:57:33] [info] Design Rule Check Report
[2025-10-12 20:57:33] [info] =====

[2025-10-12 20:57:33] [info] Report Generated: 2025-10-12
[2025-10-12 20:57:33] [info] Run Start Time: 08:56:42 PM
[2025-10-12 20:57:33] [info] Run End Time: 08:57:33 PM
[2025-10-12 20:57:33] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 20:57:33] [info] Tool Version: LVR_2025.1
[2025-10-12 20:57:33] [info] Host Name: ip-172-31-37-173
[2025-10-12 20:57:33] [info] User Name: ubuntu
[2025-10-12 20:57:33] [info] Cores Used: 8
[2025-10-12 20:57:33] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Top Cell Information
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Layout Top Cell:      TOP
[2025-10-12 20:57:33] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:57:33] [info] Database Format:      GDSII
[2025-10-12 20:57:33] [info] Verification Mode:    Full Check
```

```
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Type of Errors | Count
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] DB_WARN -> 1
[2025-10-12 20:57:33] [info] LRF_ERR001 -> 14
[2025-10-12 20:57:33] [info] DB_WARN002 -> 159
[2025-10-12 20:57:33] [info] DB_WARN003 -> 161
[2025-10-12 20:57:33] [info] DRC_ERR9 -> 89
[2025-10-12 20:57:33] [info] DRC_ERR -> 67
[2025-10-12 20:57:33] [info] DRC_ERR5 -> 408
[2025-10-12 20:57:33] [info] DRC_ERR4 -> 241
[2025-10-12 20:57:33] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Rule Violation Summary
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 20:57:33] [info] |-----|-----|-----|
[2025-10-12 20:57:33] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:57:33] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:57:33] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:57:33] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:57:33] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 20:57:33] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 20:57:33] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 20:57:33] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 20:57:33] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 20:57:33] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 20:57:33] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 20:57:33] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 20:57:33] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 20:57:33] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 20:57:33] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Performance Metrics
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] | Stage          | Time Taken |
[2025-10-12 20:57:33] [info] |-----|-----|
[2025-10-12 20:57:33] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 20:57:33] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 20:57:33] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Summary
[2025-10-12 20:57:33] [info] -----
[2025-10-12 20:57:33] [info] Total Errors:      1120
[2025-10-12 20:57:33] [info] Total Warnings:    321
[2025-10-12 20:57:33] [info] DRC Status:        FAILED
```

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```
[2025-10-12 20:58:30] [info] =====
[2025-10-12 20:58:30] [info]                      Design Rule Check Report
[2025-10-12 20:58:30] [info] =====

[2025-10-12 20:58:30] [info] Report Generated:      2025-10-12
[2025-10-12 20:58:30] [info] Run Start Time:       08:57:39 PM
[2025-10-12 20:58:30] [info] Run End Time:         08:58:30 PM
[2025-10-12 20:58:30] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 20:58:30] [info] Tool Version:         LVR_2025.1
[2025-10-12 20:58:30] [info] Host Name:            ip-172-31-37-173
[2025-10-12 20:58:30] [info] User Name:            ubuntu
[2025-10-12 20:58:30] [info] Cores Used:           8
[2025-10-12 20:58:30] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info]                      Top Cell Information
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info] Layout Top Cell:      TOP
[2025-10-12 20:58:30] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 20:58:30] [info] Database Format:        GDSII
[2025-10-12 20:58:30] [info] Verification Mode:     Full Check

[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info]                      Type of Errors | Count
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info] DB_WARN -> 1
[2025-10-12 20:58:30] [info] LRF_ERR001 -> 14
[2025-10-12 20:58:30] [info] DB_WARN002 -> 159
[2025-10-12 20:58:30] [info] DB_WARN003 -> 161
[2025-10-12 20:58:30] [info] DRC_ERR9 -> 89
[2025-10-12 20:58:30] [info] DRC_ERR -> 67
[2025-10-12 20:58:30] [info] DRC_ERR5 -> 408
[2025-10-12 20:58:30] [info] DRC_ERR4 -> 241
[2025-10-12 20:58:30] [info] DRC_ERR1 -> 301
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info]                      Rule Violation Summary
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 20:58:30] [info] |-----|-----|-----|
[2025-10-12 20:58:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 20:58:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 20:58:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 20:58:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 20:58:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 20:58:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 20:58:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:58:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:58:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:58:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:58:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:58:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:58:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:58:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:58:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:58:30] [info]
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info]                      Performance Metrics
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info] | Stage | Time Taken |
```

```
[2025-10-12 20:58:30] [info] |-----|-----|
[2025-10-12 20:58:30] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 20:58:30] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 20:58:30] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info]                Summary
[2025-10-12 20:58:30] [info] -----
[2025-10-12 20:58:30] [info] Total Errors:    1120
[2025-10-12 20:58:30] [info] Total Warnings:    321
[2025-10-12 20:58:30] [info] DRC Status:      FAILED
```

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```
[2025-10-12 20:59:27] [info] =====
[2025-10-12 20:59:27] [info]                Design Rule Check Report
[2025-10-12 20:59:27] [info] =====

[2025-10-12 20:59:27] [info] Report Generated:    2025-10-12
[2025-10-12 20:59:27] [info] Run Start Time:      08:58:36 PM
[2025-10-12 20:59:27] [info] Run End Time:        08:59:27 PM
[2025-10-12 20:59:27] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 20:59:27] [info] Tool Version:        LVR_2025.1
[2025-10-12 20:59:27] [info] Host Name:           ip-172-31-37-173
[2025-10-12 20:59:27] [info] User Name:           ubuntu
[2025-10-12 20:59:27] [info] Cores Used:          8
[2025-10-12 20:59:27] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info]                Top Cell Information
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] Layout Top Cell:     TOP
[2025-10-12 20:59:27] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 20:59:27] [info] Database Format:       GDSII
[2025-10-12 20:59:27] [info] Verification Mode:    Full Check

[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info]                Type of Errors | Count
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] DB_WARN -> 1
[2025-10-12 20:59:27] [info] LRF_ERR001 -> 14
[2025-10-12 20:59:27] [info] DB_WARN002 -> 159
[2025-10-12 20:59:27] [info] DB_WARN003 -> 161
[2025-10-12 20:59:27] [info] DRC_ERR9 -> 89
[2025-10-12 20:59:27] [info] DRC_ERR -> 67
[2025-10-12 20:59:27] [info] DRC_ERR5 -> 408
[2025-10-12 20:59:27] [info] DRC_ERR4 -> 241
[2025-10-12 20:59:27] [info] DRC_ERR1 -> 301
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info]                Rule Violation Summary
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 20:59:27] [info] |-----|-----|-----|
[2025-10-12 20:59:27] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 20:59:27] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 20:59:27] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 20:59:27] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 20:59:27] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 20:59:27] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 20:59:27] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 20:59:27] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 20:59:27] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 20:59:27] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 20:59:27] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 20:59:27] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 20:59:27] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 20:59:27] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 20:59:27] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 20:59:27] [info]
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] Performance Metrics
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] | Stage | Time Taken |
[2025-10-12 20:59:27] [info] |-----|-----|
[2025-10-12 20:59:27] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 20:59:27] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 20:59:27] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] Summary
[2025-10-12 20:59:27] [info] -----
[2025-10-12 20:59:27] [info] Total Errors: 1120
[2025-10-12 20:59:27] [info] Total Warnings: 321
[2025-10-12 20:59:27] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:00:24] [info] =====
[2025-10-12 21:00:24] [info] Design Rule Check Report
[2025-10-12 21:00:24] [info] =====

[2025-10-12 21:00:24] [info] Report Generated: 2025-10-12
[2025-10-12 21:00:24] [info] Run Start Time: 08:59:33 PM
[2025-10-12 21:00:24] [info] Run End Time: 09:00:24 PM
[2025-10-12 21:00:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:00:24] [info] Tool Version: LVR_2025.1
[2025-10-12 21:00:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:00:24] [info] User Name: ubuntu
[2025-10-12 21:00:24] [info] Cores Used: 8
[2025-10-12 21:00:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Top Cell Information
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Layout Top Cell: TOP
[2025-10-12 21:00:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:00:24] [info] Database Format: GDSII
[2025-10-12 21:00:24] [info] Verification Mode: Full Check

[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Type of Errors | Count
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] DB_WARN -> 1
[2025-10-12 21:00:24] [info] LRF_ERR001 -> 14
[2025-10-12 21:00:24] [info] DB_WARN002 -> 159
[2025-10-12 21:00:24] [info] DB_WARN003 -> 161
```

```
[2025-10-12 21:00:24] [info] DRC_ERR9 -> 89
[2025-10-12 21:00:24] [info] DRC_ERR -> 67
[2025-10-12 21:00:24] [info] DRC_ERR5 -> 408
[2025-10-12 21:00:24] [info] DRC_ERR4 -> 241
[2025-10-12 21:00:24] [info] DRC_ERR1 -> 301
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Rule Violation Summary
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:00:24] [info] |-----|-----|-----|
[2025-10-12 21:00:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:00:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:00:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:00:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:00:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:00:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:00:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:00:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:00:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:00:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:00:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:00:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:00:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:00:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:00:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Performance Metrics
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] | Stage | Time Taken |
[2025-10-12 21:00:24] [info] |-----|-----|
[2025-10-12 21:00:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:00:24] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 21:00:24] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Summary
[2025-10-12 21:00:24] [info] -----
[2025-10-12 21:00:24] [info] Total Errors: 1120
[2025-10-12 21:00:24] [info] Total Warnings: 321
[2025-10-12 21:00:24] [info] DRC Status: FAILED
```

lvr_summary_649.rpt

```
[2025-10-12 21:01:22] [info] =====
[2025-10-12 21:01:22] [info] Design Rule Check Report
[2025-10-12 21:01:22] [info] =====

[2025-10-12 21:01:22] [info] Report Generated: 2025-10-12
[2025-10-12 21:01:22] [info] Run Start Time: 09:00:30 PM
[2025-10-12 21:01:22] [info] Run End Time: 09:01:22 PM
[2025-10-12 21:01:22] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:01:22] [info] Tool Version: LVR_2025.1
[2025-10-12 21:01:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:01:22] [info] User Name: ubuntu
[2025-10-12 21:01:22] [info] Cores Used: 8
[2025-10-12 21:01:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Top Cell Information
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Layout Top Cell:      TOP
[2025-10-12 21:01:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:01:22] [info] Database Format:       GDSII
[2025-10-12 21:01:22] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Type of Errors | Count
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] DB_WARN -> 1
[2025-10-12 21:01:22] [info] LRF_ERR001 -> 14
[2025-10-12 21:01:22] [info] DB_WARN002 -> 159
[2025-10-12 21:01:22] [info] DB_WARN003 -> 161
[2025-10-12 21:01:22] [info] DRC_ERR9 -> 89
[2025-10-12 21:01:22] [info] DRC_ERR -> 67
[2025-10-12 21:01:22] [info] DRC_ERR5 -> 408
[2025-10-12 21:01:22] [info] DRC_ERR4 -> 241
[2025-10-12 21:01:22] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Rule Violation Summary
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:01:22] [info] |-----|-----|-----|
[2025-10-12 21:01:22] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:01:22] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:01:22] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:01:22] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:01:22] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:01:22] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:01:22] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:01:22] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:01:22] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:01:22] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:01:22] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:01:22] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:01:22] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:01:22] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:01:22] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:01:22] [info] |
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Performance Metrics
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] | Stage          | Time Taken |
[2025-10-12 21:01:22] [info] |-----|-----|
[2025-10-12 21:01:22] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:01:22] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 21:01:22] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Summary
```

```
[2025-10-12 21:01:22] [info] -----
[2025-10-12 21:01:22] [info] Total Errors:      1120
[2025-10-12 21:01:22] [info] Total Warnings:    321
[2025-10-12 21:01:22] [info] DRC Status:        FAILED
```

lvr_summary_65.rpt

```
[2025-10-12 11:43:03] [info] =====
[2025-10-12 11:43:03] [info]                      Design Rule Check Report
[2025-10-12 11:43:03] [info] =====

[2025-10-12 11:43:03] [info] Report Generated:      2025-10-12
[2025-10-12 11:43:03] [info] Run Start Time:       11:42:12 AM
[2025-10-12 11:43:03] [info] Run End Time:         11:43:03 AM
[2025-10-12 11:43:03] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 11:43:03] [info] Tool Version:          LVR_2025.1
[2025-10-12 11:43:03] [info] Host Name:             ip-172-31-37-173
[2025-10-12 11:43:03] [info] User Name:             ubuntu
[2025-10-12 11:43:03] [info] Cores Used:            8
[2025-10-12 11:43:03] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info]                      Top Cell Information
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info] Layout Top Cell:      TOP
[2025-10-12 11:43:03] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 11:43:03] [info] Database Format:       GDSII
[2025-10-12 11:43:03] [info] Verification Mode:     Full Check

[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info]                      Type of Errors | Count
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info] DB_WARN -> 1
[2025-10-12 11:43:03] [info] LRF_ERR001 -> 14
[2025-10-12 11:43:03] [info] DB_WARN002 -> 159
[2025-10-12 11:43:03] [info] DB_WARN003 -> 161
[2025-10-12 11:43:03] [info] DRC_ERR9 -> 89
[2025-10-12 11:43:03] [info] DRC_ERR -> 67
[2025-10-12 11:43:03] [info] DRC_ERR5 -> 408
[2025-10-12 11:43:03] [info] DRC_ERR4 -> 241
[2025-10-12 11:43:03] [info] DRC_ERR1 -> 301
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info]                      Rule Violation Summary
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:43:03] [info] |-----|-----|-----|
[2025-10-12 11:43:03] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:43:03] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:43:03] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:43:03] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:43:03] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:43:03] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:43:03] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:43:03] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:43:03] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:43:03] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:43:03] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:43:03] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:43:03] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:43:03] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:43:03] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:43:03] [info]
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info]                      Performance Metrics
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info] | Stage | Time Taken |
```



```
[2025-10-12 11:43:03] [info] |-----|-----|
[2025-10-12 11:43:03] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:43:03] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 11:43:03] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info] Summary
[2025-10-12 11:43:03] [info] -----
[2025-10-12 11:43:03] [info] Total Errors: 1120
[2025-10-12 11:43:03] [info] Total Warnings: 321
[2025-10-12 11:43:03] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:02:18] [info] =====
[2025-10-12 21:02:18] [info] Design Rule Check Report
[2025-10-12 21:02:18] [info] =====

[2025-10-12 21:02:18] [info] Report Generated: 2025-10-12
[2025-10-12 21:02:18] [info] Run Start Time: 09:01:27 PM
[2025-10-12 21:02:18] [info] Run End Time: 09:02:18 PM
[2025-10-12 21:02:18] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:02:18] [info] Tool Version: LVR_2025.1
[2025-10-12 21:02:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:02:18] [info] User Name: ubuntu
[2025-10-12 21:02:18] [info] Cores Used: 8
[2025-10-12 21:02:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Top Cell Information
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Layout Top Cell: TOP
[2025-10-12 21:02:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:02:18] [info] Database Format: GDSII
[2025-10-12 21:02:18] [info] Verification Mode: Full Check

[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Type of Errors | Count
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] DB_WARN -> 1
[2025-10-12 21:02:18] [info] LRF_ERR001 -> 14
[2025-10-12 21:02:18] [info] DB_WARN002 -> 159
[2025-10-12 21:02:18] [info] DB_WARN003 -> 161
[2025-10-12 21:02:18] [info] DRC_ERR9 -> 89
[2025-10-12 21:02:18] [info] DRC_ERR -> 67
[2025-10-12 21:02:18] [info] DRC_ERR5 -> 408
[2025-10-12 21:02:18] [info] DRC_ERR4 -> 241
[2025-10-12 21:02:18] [info] DRC_ERR1 -> 301
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Rule Violation Summary
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:02:18] [info] |-----|-----|-----|
[2025-10-12 21:02:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:02:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:02:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:02:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:02:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 21:02:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:02:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:02:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:02:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:02:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:02:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:02:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:02:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:02:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:02:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:02:18] [info]
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Performance Metrics
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] | Stage | Time Taken |
[2025-10-12 21:02:18] [info] |-----|-----|
[2025-10-12 21:02:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:02:18] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 21:02:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Summary
[2025-10-12 21:02:18] [info] -----
[2025-10-12 21:02:18] [info] Total Errors: 1120
[2025-10-12 21:02:18] [info] Total Warnings: 321
[2025-10-12 21:02:18] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:03:15] [info] =====
[2025-10-12 21:03:15] [info] Design Rule Check Report
[2025-10-12 21:03:15] [info] =====

[2025-10-12 21:03:15] [info] Report Generated: 2025-10-12
[2025-10-12 21:03:15] [info] Run Start Time: 09:02:24 PM
[2025-10-12 21:03:15] [info] Run End Time: 09:03:15 PM
[2025-10-12 21:03:15] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:03:15] [info] Tool Version: LVR_2025.1
[2025-10-12 21:03:15] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:03:15] [info] User Name: ubuntu
[2025-10-12 21:03:15] [info] Cores Used: 8
[2025-10-12 21:03:15] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Top Cell Information
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Layout Top Cell: TOP
[2025-10-12 21:03:15] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:03:15] [info] Database Format: GDSII
[2025-10-12 21:03:15] [info] Verification Mode: Full Check

[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Type of Errors | Count
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] DB_WARN -> 1
[2025-10-12 21:03:15] [info] LRF_ERR001 -> 14
[2025-10-12 21:03:15] [info] DB_WARN002 -> 159
[2025-10-12 21:03:15] [info] DB_WARN003 -> 161
```

```

[2025-10-12 21:03:15] [info] DRC_ERR9 -> 89
[2025-10-12 21:03:15] [info] DRC_ERR -> 67
[2025-10-12 21:03:15] [info] DRC_ERR5 -> 408
[2025-10-12 21:03:15] [info] DRC_ERR4 -> 241
[2025-10-12 21:03:15] [info] DRC_ERR1 -> 301
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Rule Violation Summary
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] | Rule ID          | Description                                     | Error Count |
[2025-10-12 21:03:15] [info] |-----|-----|-----|
[2025-10-12 21:03:15] [info] | m5.1            | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:03:15] [info] | li.5            | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:03:15] [info] | poly.1a         | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:03:15] [info] | poly.7          | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:03:15] [info] | licon.15        | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:03:15] [info] | m1.6            | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 21:03:15] [info] | m4.5            | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:03:15] [info] | m4.5ab          | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:03:15] [info] | psdm.1          | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:03:15] [info] | licon.5         | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:03:15] [info] | li.1            | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:03:15] [info] | li.1a           | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:03:15] [info] | m1.1            | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:03:15] [info] | licon.8a        | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:03:15] [info] | npc.1           | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Performance Metrics
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] | Stage              | Time Taken |
[2025-10-12 21:03:15] [info] |-----|-----|
[2025-10-12 21:03:15] [info] | GDS Parsing        | 0 minutes 9 seconds |
[2025-10-12 21:03:15] [info] | Rule Checking       | 0 minutes 39 seconds |
[2025-10-12 21:03:15] [info] | Report Generation   | 0 minutes 0 seconds |
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Summary
[2025-10-12 21:03:15] [info] -----
[2025-10-12 21:03:15] [info] Total Errors:      1120
[2025-10-12 21:03:15] [info] Total Warnings:    321
[2025-10-12 21:03:15] [info] DRC Status:        FAILED

```

lvr_summary_652.rpt

```

[2025-10-12 21:04:11] [info] =====
[2025-10-12 21:04:11] [info] Design Rule Check Report
[2025-10-12 21:04:11] [info] =====

[2025-10-12 21:04:11] [info] Report Generated:    2025-10-12
[2025-10-12 21:04:11] [info] Run Start Time:     09:03:20 PM
[2025-10-12 21:04:11] [info] Run End Time:       09:04:11 PM
[2025-10-12 21:04:11] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 21:04:11] [info] Tool Version:       LVR_2025.1
[2025-10-12 21:04:11] [info] Host Name:          ip-172-31-37-173
[2025-10-12 21:04:11] [info] User Name:          ubuntu
[2025-10-12 21:04:11] [info] Cores Used:         8
[2025-10-12 21:04:11] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Top Cell Information
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Layout Top Cell:      TOP
[2025-10-12 21:04:11] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:04:11] [info] Database Format:       GDSII
[2025-10-12 21:04:11] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Type of Errors | Count
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] DB_WARN -> 1
[2025-10-12 21:04:11] [info] LRF_ERR001 -> 14
[2025-10-12 21:04:11] [info] DB_WARN002 -> 159
[2025-10-12 21:04:11] [info] DB_WARN003 -> 161
[2025-10-12 21:04:11] [info] DRC_ERR9 -> 89
[2025-10-12 21:04:11] [info] DRC_ERR -> 67
[2025-10-12 21:04:11] [info] DRC_ERR5 -> 408
[2025-10-12 21:04:11] [info] DRC_ERR4 -> 241
[2025-10-12 21:04:11] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Rule Violation Summary
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:04:11] [info] |-----|-----|-----|
[2025-10-12 21:04:11] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:04:11] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:04:11] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:04:11] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:04:11] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:04:11] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:04:11] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:04:11] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:04:11] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:04:11] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:04:11] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:04:11] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:04:11] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:04:11] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:04:11] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:04:11] [info] |
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Performance Metrics
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] | Stage          | Time Taken |
[2025-10-12 21:04:11] [info] |-----|-----|
[2025-10-12 21:04:11] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:04:11] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 21:04:11] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Summary
```

```
[2025-10-12 21:04:11] [info] -----
[2025-10-12 21:04:11] [info] Total Errors:      1120
[2025-10-12 21:04:11] [info] Total Warnings:    321
[2025-10-12 21:04:11] [info] DRC Status:        FAILED
```

lvr_summary_653.rpt

```
[2025-10-12 21:05:09] [info] =====
[2025-10-12 21:05:09] [info]                      Design Rule Check Report
[2025-10-12 21:05:09] [info] =====

[2025-10-12 21:05:09] [info] Report Generated:      2025-10-12
[2025-10-12 21:05:09] [info] Run Start Time:       09:04:18 PM
[2025-10-12 21:05:09] [info] Run End Time:         09:05:09 PM
[2025-10-12 21:05:09] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 21:05:09] [info] Tool Version:         LVR_2025.1
[2025-10-12 21:05:09] [info] Host Name:            ip-172-31-37-173
[2025-10-12 21:05:09] [info] User Name:            ubuntu
[2025-10-12 21:05:09] [info] Cores Used:           8
[2025-10-12 21:05:09] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info]                      Top Cell Information
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info] Layout Top Cell:      TOP
[2025-10-12 21:05:09] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 21:05:09] [info] Database Format:        GDSII
[2025-10-12 21:05:09] [info] Verification Mode:     Full Check

[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info]                      Type of Errors | Count
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info] DB_WARN -> 1
[2025-10-12 21:05:09] [info] LRF_ERR001 -> 14
[2025-10-12 21:05:09] [info] DB_WARN002 -> 159
[2025-10-12 21:05:09] [info] DB_WARN003 -> 161
[2025-10-12 21:05:09] [info] DRC_ERR9 -> 89
[2025-10-12 21:05:09] [info] DRC_ERR -> 67
[2025-10-12 21:05:09] [info] DRC_ERR5 -> 408
[2025-10-12 21:05:09] [info] DRC_ERR4 -> 241
[2025-10-12 21:05:09] [info] DRC_ERR1 -> 301
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info]                      Rule Violation Summary
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:05:09] [info] |-----|-----|-----|
[2025-10-12 21:05:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:05:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:05:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:05:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:05:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:05:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 21:05:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:05:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:05:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:05:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:05:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:05:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:05:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:05:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:05:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:05:09] [info]
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info]                      Performance Metrics
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:05:09] [info] |-----|-----|
[2025-10-12 21:05:09] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:05:09] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 21:05:09] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info]                Summary
[2025-10-12 21:05:09] [info] -----
[2025-10-12 21:05:09] [info] Total Errors:    1120
[2025-10-12 21:05:09] [info] Total Warnings:    321
[2025-10-12 21:05:09] [info] DRC Status:      FAILED
```

lvr_summary_654.rpt

```
[2025-10-12 21:06:06] [info] =====
[2025-10-12 21:06:06] [info]                Design Rule Check Report
[2025-10-12 21:06:06] [info] =====

[2025-10-12 21:06:06] [info] Report Generated:    2025-10-12
[2025-10-12 21:06:06] [info] Run Start Time:      09:05:15 PM
[2025-10-12 21:06:06] [info] Run End Time:        09:06:06 PM
[2025-10-12 21:06:06] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 21:06:06] [info] Tool Version:        LVR_2025.1
[2025-10-12 21:06:06] [info] Host Name:           ip-172-31-37-173
[2025-10-12 21:06:06] [info] User Name:           ubuntu
[2025-10-12 21:06:06] [info] Cores Used:          8
[2025-10-12 21:06:06] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info]                Top Cell Information
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] Layout Top Cell:     TOP
[2025-10-12 21:06:06] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:06:06] [info] Database Format:       GDSII
[2025-10-12 21:06:06] [info] Verification Mode:    Full Check

[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info]                Type of Errors | Count
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] DB_WARN -> 1
[2025-10-12 21:06:06] [info] LRF_ERR001 -> 14
[2025-10-12 21:06:06] [info] DB_WARN002 -> 159
[2025-10-12 21:06:06] [info] DB_WARN003 -> 161
[2025-10-12 21:06:06] [info] DRC_ERR9 -> 89
[2025-10-12 21:06:06] [info] DRC_ERR -> 67
[2025-10-12 21:06:06] [info] DRC_ERR5 -> 408
[2025-10-12 21:06:06] [info] DRC_ERR4 -> 241
[2025-10-12 21:06:06] [info] DRC_ERR1 -> 301
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info]                Rule Violation Summary
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 21:06:06] [info] |-----|-----|-----|
[2025-10-12 21:06:06] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:06:06] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:06:06] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:06:06] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:06:06] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 21:06:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:06:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:06:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:06:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:06:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:06:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:06:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:06:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:06:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:06:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:06:06] [info]
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] Performance Metrics
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] | Stage | Time Taken |
[2025-10-12 21:06:06] [info] |-----|-----|
[2025-10-12 21:06:06] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:06:06] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 21:06:06] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] Summary
[2025-10-12 21:06:06] [info] -----
[2025-10-12 21:06:06] [info] Total Errors: 1120
[2025-10-12 21:06:06] [info] Total Warnings: 321
[2025-10-12 21:06:06] [info] DRC Status: FAILED
```

lvr_summary_655.rpt

```
[2025-10-12 21:07:02] [info] =====
[2025-10-12 21:07:02] [info] Design Rule Check Report
[2025-10-12 21:07:02] [info] =====

[2025-10-12 21:07:02] [info] Report Generated: 2025-10-12
[2025-10-12 21:07:02] [info] Run Start Time: 09:06:12 PM
[2025-10-12 21:07:02] [info] Run End Time: 09:07:02 PM
[2025-10-12 21:07:02] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:07:02] [info] Tool Version: LVR_2025.1
[2025-10-12 21:07:02] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:07:02] [info] User Name: ubuntu
[2025-10-12 21:07:02] [info] Cores Used: 8
[2025-10-12 21:07:02] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Top Cell Information
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Layout Top Cell: TOP
[2025-10-12 21:07:02] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:07:02] [info] Database Format: GDSII
[2025-10-12 21:07:02] [info] Verification Mode: Full Check

[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Type of Errors | Count
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] DB_WARN -> 1
[2025-10-12 21:07:02] [info] LRF_ERR001 -> 14
[2025-10-12 21:07:02] [info] DB_WARN002 -> 159
[2025-10-12 21:07:02] [info] DB_WARN003 -> 161
```

```

[2025-10-12 21:07:02] [info] DRC_ERR9 -> 89
[2025-10-12 21:07:02] [info] DRC_ERR -> 67
[2025-10-12 21:07:02] [info] DRC_ERR5 -> 408
[2025-10-12 21:07:02] [info] DRC_ERR4 -> 241
[2025-10-12 21:07:02] [info] DRC_ERR1 -> 301
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Rule Violation Summary
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:07:02] [info] |-----|-----|-----|
[2025-10-12 21:07:02] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:07:02] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:07:02] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:07:02] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:07:02] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:07:02] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:07:02] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:07:02] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:07:02] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:07:02] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:07:02] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:07:02] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:07:02] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:07:02] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:07:02] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Performance Metrics
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] | Stage | Time Taken |
[2025-10-12 21:07:02] [info] |-----|-----|
[2025-10-12 21:07:02] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:07:02] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 21:07:02] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Summary
[2025-10-12 21:07:02] [info] -----
[2025-10-12 21:07:02] [info] Total Errors: 1120
[2025-10-12 21:07:02] [info] Total Warnings: 321
[2025-10-12 21:07:02] [info] DRC Status: FAILED

```

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```

[2025-10-12 21:08:00] [info] =====
[2025-10-12 21:08:00] [info] Design Rule Check Report
[2025-10-12 21:08:00] [info] =====

[2025-10-12 21:08:00] [info] Report Generated: 2025-10-12
[2025-10-12 21:08:00] [info] Run Start Time: 09:07:08 PM
[2025-10-12 21:08:00] [info] Run End Time: 09:08:00 PM
[2025-10-12 21:08:00] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:08:00] [info] Tool Version: LVR_2025.1
[2025-10-12 21:08:00] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:08:00] [info] User Name: ubuntu
[2025-10-12 21:08:00] [info] Cores Used: 8
[2025-10-12 21:08:00] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Top Cell Information
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Layout Top Cell:      TOP
[2025-10-12 21:08:00] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:08:00] [info] Database Format:       GDSII
[2025-10-12 21:08:00] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Type of Errors | Count
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] DB_WARN -> 1
[2025-10-12 21:08:00] [info] LRF_ERR001 -> 14
[2025-10-12 21:08:00] [info] DB_WARN002 -> 159
[2025-10-12 21:08:00] [info] DB_WARN003 -> 161
[2025-10-12 21:08:00] [info] DRC_ERR9 -> 89
[2025-10-12 21:08:00] [info] DRC_ERR -> 67
[2025-10-12 21:08:00] [info] DRC_ERR5 -> 408
[2025-10-12 21:08:00] [info] DRC_ERR4 -> 241
[2025-10-12 21:08:00] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Rule Violation Summary
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:08:00] [info] |-----|-----|-----|
[2025-10-12 21:08:00] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:08:00] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:08:00] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:08:00] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:08:00] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:08:00] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:08:00] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:08:00] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:08:00] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:08:00] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:08:00] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:08:00] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:08:00] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:08:00] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:08:00] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:08:00] [info] |
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Performance Metrics
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] | Stage          | Time Taken |
[2025-10-12 21:08:00] [info] |-----|-----|
[2025-10-12 21:08:00] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:08:00] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 21:08:00] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Summary
```

```
[2025-10-12 21:08:00] [info] -----
[2025-10-12 21:08:00] [info] Total Errors:      1120
[2025-10-12 21:08:00] [info] Total Warnings:    321
[2025-10-12 21:08:00] [info] DRC Status:        FAILED
```

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```
[2025-10-12 21:08:56] [info] =====
[2025-10-12 21:08:56] [info]                      Design Rule Check Report
[2025-10-12 21:08:56] [info] =====

[2025-10-12 21:08:56] [info] Report Generated:      2025-10-12
[2025-10-12 21:08:56] [info] Run Start Time:       09:08:05 PM
[2025-10-12 21:08:56] [info] Run End Time:         09:08:56 PM
[2025-10-12 21:08:56] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 21:08:56] [info] Tool Version:          LVR_2025.1
[2025-10-12 21:08:56] [info] Host Name:             ip-172-31-37-173
[2025-10-12 21:08:56] [info] User Name:             ubuntu
[2025-10-12 21:08:56] [info] Cores Used:            8
[2025-10-12 21:08:56] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info]                      Top Cell Information
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info] Layout Top Cell:      TOP
[2025-10-12 21:08:56] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 21:08:56] [info] Database Format:        GDSII
[2025-10-12 21:08:56] [info] Verification Mode:     Full Check

[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info]                      Type of Errors | Count
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info] DB_WARN -> 1
[2025-10-12 21:08:56] [info] LRF_ERR001 -> 14
[2025-10-12 21:08:56] [info] DB_WARN002 -> 159
[2025-10-12 21:08:56] [info] DB_WARN003 -> 161
[2025-10-12 21:08:56] [info] DRC_ERR9 -> 89
[2025-10-12 21:08:56] [info] DRC_ERR -> 67
[2025-10-12 21:08:56] [info] DRC_ERR5 -> 408
[2025-10-12 21:08:56] [info] DRC_ERR4 -> 241
[2025-10-12 21:08:56] [info] DRC_ERR1 -> 301
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info]                      Rule Violation Summary
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:08:56] [info] |-----|-----|-----|
[2025-10-12 21:08:56] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:08:56] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:08:56] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:08:56] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:08:56] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:08:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 21:08:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:08:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:08:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:08:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:08:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:08:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:08:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:08:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:08:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:08:56] [info]
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info]                      Performance Metrics
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:08:56] [info] |-----|-----|
[2025-10-12 21:08:56] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:08:56] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 21:08:56] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info] Summary
[2025-10-12 21:08:56] [info] -----
[2025-10-12 21:08:56] [info] Total Errors: 1120
[2025-10-12 21:08:56] [info] Total Warnings: 321
[2025-10-12 21:08:56] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:09:53] [info] =====
[2025-10-12 21:09:53] [info] Design Rule Check Report
[2025-10-12 21:09:53] [info] =====

[2025-10-12 21:09:53] [info] Report Generated: 2025-10-12
[2025-10-12 21:09:53] [info] Run Start Time: 09:09:01 PM
[2025-10-12 21:09:53] [info] Run End Time: 09:09:53 PM
[2025-10-12 21:09:53] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:09:53] [info] Tool Version: LVR_2025.1
[2025-10-12 21:09:53] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:09:53] [info] User Name: ubuntu
[2025-10-12 21:09:53] [info] Cores Used: 8
[2025-10-12 21:09:53] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Top Cell Information
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Layout Top Cell: TOP
[2025-10-12 21:09:53] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:09:53] [info] Database Format: GDSII
[2025-10-12 21:09:53] [info] Verification Mode: Full Check

[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Type of Errors | Count
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] DB_WARN -> 1
[2025-10-12 21:09:53] [info] LRF_ERR001 -> 14
[2025-10-12 21:09:53] [info] DB_WARN002 -> 159
[2025-10-12 21:09:53] [info] DB_WARN003 -> 161
[2025-10-12 21:09:53] [info] DRC_ERR9 -> 89
[2025-10-12 21:09:53] [info] DRC_ERR -> 67
[2025-10-12 21:09:53] [info] DRC_ERR5 -> 408
[2025-10-12 21:09:53] [info] DRC_ERR4 -> 241
[2025-10-12 21:09:53] [info] DRC_ERR1 -> 301
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Rule Violation Summary
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:09:53] [info] |-----|-----|-----|
[2025-10-12 21:09:53] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:09:53] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:09:53] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:09:53] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:09:53] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 21:09:53] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:09:53] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:09:53] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:09:53] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:09:53] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:09:53] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:09:53] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:09:53] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:09:53] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:09:53] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:09:53] [info]
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Performance Metrics
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] | Stage | Time Taken |
[2025-10-12 21:09:53] [info] |-----|-----|
[2025-10-12 21:09:53] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:09:53] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 21:09:53] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Summary
[2025-10-12 21:09:53] [info] -----
[2025-10-12 21:09:53] [info] Total Errors: 1120
[2025-10-12 21:09:53] [info] Total Warnings: 321
[2025-10-12 21:09:53] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:10:52] [info] =====
[2025-10-12 21:10:52] [info] Design Rule Check Report
[2025-10-12 21:10:52] [info] =====

[2025-10-12 21:10:52] [info] Report Generated: 2025-10-12
[2025-10-12 21:10:52] [info] Run Start Time: 09:10:00 PM
[2025-10-12 21:10:52] [info] Run End Time: 09:10:52 PM
[2025-10-12 21:10:52] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:10:52] [info] Tool Version: LVR_2025.1
[2025-10-12 21:10:52] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:10:52] [info] User Name: ubuntu
[2025-10-12 21:10:52] [info] Cores Used: 8
[2025-10-12 21:10:52] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Top Cell Information
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Layout Top Cell: TOP
[2025-10-12 21:10:52] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:10:52] [info] Database Format: GDSII
[2025-10-12 21:10:52] [info] Verification Mode: Full Check

[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Type of Errors | Count
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] DB_WARN -> 1
[2025-10-12 21:10:52] [info] LRF_ERR001 -> 14
[2025-10-12 21:10:52] [info] DB_WARN002 -> 159
[2025-10-12 21:10:52] [info] DB_WARN003 -> 161
```

```
[2025-10-12 21:10:52] [info] DRC_ERR9 -> 89
[2025-10-12 21:10:52] [info] DRC_ERR -> 67
[2025-10-12 21:10:52] [info] DRC_ERR5 -> 408
[2025-10-12 21:10:52] [info] DRC_ERR4 -> 241
[2025-10-12 21:10:52] [info] DRC_ERR1 -> 301
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Rule Violation Summary
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:10:52] [info] |-----|-----|-----|
[2025-10-12 21:10:52] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:10:52] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:10:52] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:10:52] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:10:52] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:10:52] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 21:10:52] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:10:52] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:10:52] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:10:52] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:10:52] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:10:52] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:10:52] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:10:52] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:10:52] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Performance Metrics
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] | Stage          | Time Taken |
[2025-10-12 21:10:52] [info] |-----|-----|
[2025-10-12 21:10:52] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:10:52] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 21:10:52] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Summary
[2025-10-12 21:10:52] [info] -----
[2025-10-12 21:10:52] [info] Total Errors: 1120
[2025-10-12 21:10:52] [info] Total Warnings: 321
[2025-10-12 21:10:52] [info] DRC Status: FAILED
```

lvr_summary_66.rpt

```
[2025-10-12 11:44:01] [info] =====
[2025-10-12 11:44:01] [info] Design Rule Check Report
[2025-10-12 11:44:01] [info] =====

[2025-10-12 11:44:01] [info] Report Generated: 2025-10-12
[2025-10-12 11:44:01] [info] Run Start Time: 11:43:09 AM
[2025-10-12 11:44:01] [info] Run End Time: 11:44:01 AM
[2025-10-12 11:44:01] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:44:01] [info] Tool Version: LVR_2025.1
[2025-10-12 11:44:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:44:01] [info] User Name: ubuntu
[2025-10-12 11:44:01] [info] Cores Used: 8
[2025-10-12 11:44:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Top Cell Information
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Layout Top Cell:      TOP
[2025-10-12 11:44:01] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:44:01] [info] Database Format:       GDSII
[2025-10-12 11:44:01] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Type of Errors | Count
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] DB_WARN -> 1
[2025-10-12 11:44:01] [info] LRF_ERR001 -> 14
[2025-10-12 11:44:01] [info] DB_WARN002 -> 159
[2025-10-12 11:44:01] [info] DB_WARN003 -> 161
[2025-10-12 11:44:01] [info] DRC_ERR9 -> 89
[2025-10-12 11:44:01] [info] DRC_ERR -> 67
[2025-10-12 11:44:01] [info] DRC_ERR5 -> 408
[2025-10-12 11:44:01] [info] DRC_ERR4 -> 241
[2025-10-12 11:44:01] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Rule Violation Summary
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:44:01] [info] |-----|-----|-----|
[2025-10-12 11:44:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:44:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:44:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:44:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:44:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:44:01] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:44:01] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:44:01] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:44:01] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:44:01] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:44:01] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:44:01] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:44:01] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:44:01] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:44:01] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:44:01] [info] |
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Performance Metrics
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] | Stage          | Time Taken |
[2025-10-12 11:44:01] [info] |-----|-----|
[2025-10-12 11:44:01] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:44:01] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:44:01] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Summary
```

```
[2025-10-12 11:44:01] [info] -----
[2025-10-12 11:44:01] [info] Total Errors:      1120
[2025-10-12 11:44:01] [info] Total Warnings:    321
[2025-10-12 11:44:01] [info] DRC Status:        FAILED
```

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```
[2025-10-12 21:11:48] [info] =====
[2025-10-12 21:11:48] [info]                      Design Rule Check Report
[2025-10-12 21:11:48] [info] =====

[2025-10-12 21:11:48] [info] Report Generated:      2025-10-12
[2025-10-12 21:11:48] [info] Run Start Time:       09:10:56 PM
[2025-10-12 21:11:48] [info] Run End Time:         09:11:48 PM
[2025-10-12 21:11:48] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 21:11:48] [info] Tool Version:          LVR_2025.1
[2025-10-12 21:11:48] [info] Host Name:             ip-172-31-37-173
[2025-10-12 21:11:48] [info] User Name:             ubuntu
[2025-10-12 21:11:48] [info] Cores Used:            8
[2025-10-12 21:11:48] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info]                      Top Cell Information
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info] Layout Top Cell:      TOP
[2025-10-12 21:11:48] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 21:11:48] [info] Database Format:        GDSII
[2025-10-12 21:11:48] [info] Verification Mode:     Full Check

[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info]                      Type of Errors | Count
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info] DB_WARN -> 1
[2025-10-12 21:11:48] [info] LRF_ERR001 -> 14
[2025-10-12 21:11:48] [info] DB_WARN002 -> 159
[2025-10-12 21:11:48] [info] DB_WARN003 -> 161
[2025-10-12 21:11:48] [info] DRC_ERR9 -> 89
[2025-10-12 21:11:48] [info] DRC_ERR -> 67
[2025-10-12 21:11:48] [info] DRC_ERR5 -> 408
[2025-10-12 21:11:48] [info] DRC_ERR4 -> 241
[2025-10-12 21:11:48] [info] DRC_ERR1 -> 301
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info]                      Rule Violation Summary
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:11:48] [info] |-----|-----|-----|
[2025-10-12 21:11:48] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:11:48] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:11:48] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:11:48] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:11:48] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:11:48] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 21:11:48] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:11:48] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:11:48] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:11:48] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:11:48] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:11:48] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:11:48] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:11:48] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:11:48] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:11:48] [info]
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info]                      Performance Metrics
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:11:48] [info] |-----|-----|
[2025-10-12 21:11:48] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:11:48] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 21:11:48] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info] Summary
[2025-10-12 21:11:48] [info] -----
[2025-10-12 21:11:48] [info] Total Errors: 1120
[2025-10-12 21:11:48] [info] Total Warnings: 321
[2025-10-12 21:11:48] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:12:44] [info] =====
[2025-10-12 21:12:44] [info] Design Rule Check Report
[2025-10-12 21:12:44] [info] =====

[2025-10-12 21:12:44] [info] Report Generated: 2025-10-12
[2025-10-12 21:12:44] [info] Run Start Time: 09:11:54 PM
[2025-10-12 21:12:44] [info] Run End Time: 09:12:44 PM
[2025-10-12 21:12:44] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:12:44] [info] Tool Version: LVR_2025.1
[2025-10-12 21:12:44] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:12:44] [info] User Name: ubuntu
[2025-10-12 21:12:44] [info] Cores Used: 8
[2025-10-12 21:12:44] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Top Cell Information
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Layout Top Cell: TOP
[2025-10-12 21:12:44] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:12:44] [info] Database Format: GDSII
[2025-10-12 21:12:44] [info] Verification Mode: Full Check

[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Type of Errors | Count
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] DB_WARN -> 1
[2025-10-12 21:12:44] [info] LRF_ERR001 -> 14
[2025-10-12 21:12:44] [info] DB_WARN002 -> 159
[2025-10-12 21:12:44] [info] DB_WARN003 -> 161
[2025-10-12 21:12:44] [info] DRC_ERR9 -> 89
[2025-10-12 21:12:44] [info] DRC_ERR -> 67
[2025-10-12 21:12:44] [info] DRC_ERR5 -> 408
[2025-10-12 21:12:44] [info] DRC_ERR4 -> 241
[2025-10-12 21:12:44] [info] DRC_ERR1 -> 301
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Rule Violation Summary
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:12:44] [info] |-----|-----|-----|
[2025-10-12 21:12:44] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:12:44] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:12:44] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:12:44] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:12:44] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```



```
[2025-10-12 21:12:44] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:12:44] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:12:44] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:12:44] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:12:44] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:12:44] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:12:44] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:12:44] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:12:44] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:12:44] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:12:44] [info]
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Performance Metrics
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] | Stage | Time Taken |
[2025-10-12 21:12:44] [info] |-----|-----|
[2025-10-12 21:12:44] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:12:44] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 21:12:44] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Summary
[2025-10-12 21:12:44] [info] -----
[2025-10-12 21:12:44] [info] Total Errors: 1120
[2025-10-12 21:12:44] [info] Total Warnings: 321
[2025-10-12 21:12:44] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:13:41] [info] =====
[2025-10-12 21:13:41] [info] Design Rule Check Report
[2025-10-12 21:13:41] [info] =====

[2025-10-12 21:13:41] [info] Report Generated: 2025-10-12
[2025-10-12 21:13:41] [info] Run Start Time: 09:12:51 PM
[2025-10-12 21:13:41] [info] Run End Time: 09:13:41 PM
[2025-10-12 21:13:41] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:13:41] [info] Tool Version: LVR_2025.1
[2025-10-12 21:13:41] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:13:41] [info] User Name: ubuntu
[2025-10-12 21:13:41] [info] Cores Used: 8
[2025-10-12 21:13:41] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Top Cell Information
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Layout Top Cell: TOP
[2025-10-12 21:13:41] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:13:41] [info] Database Format: GDSII
[2025-10-12 21:13:41] [info] Verification Mode: Full Check

[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Type of Errors | Count
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] DB_WARN -> 1
[2025-10-12 21:13:41] [info] LRF_ERR001 -> 14
[2025-10-12 21:13:41] [info] DB_WARN002 -> 159
[2025-10-12 21:13:41] [info] DB_WARN003 -> 161
```

```
[2025-10-12 21:13:41] [info] DRC_ERR9 -> 89
[2025-10-12 21:13:41] [info] DRC_ERR -> 67
[2025-10-12 21:13:41] [info] DRC_ERR5 -> 408
[2025-10-12 21:13:41] [info] DRC_ERR4 -> 241
[2025-10-12 21:13:41] [info] DRC_ERR1 -> 301
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Rule Violation Summary
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:13:41] [info] |-----|-----|-----|
[2025-10-12 21:13:41] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:13:41] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:13:41] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:13:41] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:13:41] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:13:41] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:13:41] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:13:41] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:13:41] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:13:41] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:13:41] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:13:41] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:13:41] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:13:41] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:13:41] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Performance Metrics
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] | Stage | Time Taken |
[2025-10-12 21:13:41] [info] |-----|-----|
[2025-10-12 21:13:41] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:13:41] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 21:13:41] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Summary
[2025-10-12 21:13:41] [info] -----
[2025-10-12 21:13:41] [info] Total Errors: 1120
[2025-10-12 21:13:41] [info] Total Warnings: 321
[2025-10-12 21:13:41] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:14:40] [info] =====
[2025-10-12 21:14:40] [info] Design Rule Check Report
[2025-10-12 21:14:40] [info] =====

[2025-10-12 21:14:40] [info] Report Generated: 2025-10-12
[2025-10-12 21:14:40] [info] Run Start Time: 09:13:48 PM
[2025-10-12 21:14:40] [info] Run End Time: 09:14:40 PM
[2025-10-12 21:14:40] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:14:40] [info] Tool Version: LVR_2025.1
[2025-10-12 21:14:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:14:40] [info] User Name: ubuntu
[2025-10-12 21:14:40] [info] Cores Used: 8
[2025-10-12 21:14:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Top Cell Information
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Layout Top Cell:      TOP
[2025-10-12 21:14:40] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:14:40] [info] Database Format:      GDSII
[2025-10-12 21:14:40] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Type of Errors | Count
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] DB_WARN -> 1
[2025-10-12 21:14:40] [info] LRF_ERR001 -> 14
[2025-10-12 21:14:40] [info] DB_WARN002 -> 159
[2025-10-12 21:14:40] [info] DB_WARN003 -> 161
[2025-10-12 21:14:40] [info] DRC_ERR9 -> 89
[2025-10-12 21:14:40] [info] DRC_ERR -> 67
[2025-10-12 21:14:40] [info] DRC_ERR5 -> 408
[2025-10-12 21:14:40] [info] DRC_ERR4 -> 241
[2025-10-12 21:14:40] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Rule Violation Summary
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:14:40] [info] |-----|-----|-----|
[2025-10-12 21:14:40] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:14:40] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:14:40] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:14:40] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:14:40] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:14:40] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:14:40] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:14:40] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:14:40] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:14:40] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:14:40] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:14:40] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:14:40] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:14:40] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:14:40] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:14:40] [info] |
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Performance Metrics
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] | Stage          | Time Taken |
[2025-10-12 21:14:40] [info] |-----|-----|
[2025-10-12 21:14:40] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:14:40] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 21:14:40] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Summary
```

```
[2025-10-12 21:14:40] [info] -----
[2025-10-12 21:14:40] [info] Total Errors:      1120
[2025-10-12 21:14:40] [info] Total Warnings:    321
[2025-10-12 21:14:40] [info] DRC Status:        FAILED
```

lvr_summary_664.rpt

```
[2025-10-12 21:15:36] [info] =====
[2025-10-12 21:15:36] [info] Design Rule Check Report
[2025-10-12 21:15:36] [info] =====

[2025-10-12 21:15:36] [info] Report Generated: 2025-10-12
[2025-10-12 21:15:36] [info] Run Start Time: 09:14:44 PM
[2025-10-12 21:15:36] [info] Run End Time: 09:15:36 PM
[2025-10-12 21:15:36] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 21:15:36] [info] Tool Version: LVR_2025.1
[2025-10-12 21:15:36] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:15:36] [info] User Name: ubuntu
[2025-10-12 21:15:36] [info] Cores Used: 8
[2025-10-12 21:15:36] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] Top Cell Information
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] Layout Top Cell: TOP
[2025-10-12 21:15:36] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:15:36] [info] Database Format: GDSII
[2025-10-12 21:15:36] [info] Verification Mode: Full Check

[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] Type of Errors | Count
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] DB_WARN -> 1
[2025-10-12 21:15:36] [info] LRF_ERR001 -> 14
[2025-10-12 21:15:36] [info] DB_WARN002 -> 159
[2025-10-12 21:15:36] [info] DB_WARN003 -> 161
[2025-10-12 21:15:36] [info] DRC_ERR9 -> 89
[2025-10-12 21:15:36] [info] DRC_ERR -> 67
[2025-10-12 21:15:36] [info] DRC_ERR5 -> 408
[2025-10-12 21:15:36] [info] DRC_ERR4 -> 241
[2025-10-12 21:15:36] [info] DRC_ERR1 -> 301
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] Rule Violation Summary
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:15:36] [info] |-----|-----|-----|
[2025-10-12 21:15:36] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:15:36] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:15:36] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:15:36] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:15:36] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:15:36] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:15:36] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:15:36] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:15:36] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:15:36] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:15:36] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:15:36] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:15:36] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:15:36] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:15:36] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:15:36] [info]
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] Performance Metrics
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:15:36] [info] |-----|-----|
[2025-10-12 21:15:36] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:15:36] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 21:15:36] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info]                Summary
[2025-10-12 21:15:36] [info] -----
[2025-10-12 21:15:36] [info] Total Errors:    1120
[2025-10-12 21:15:36] [info] Total Warnings:    321
[2025-10-12 21:15:36] [info] DRC Status:      FAILED
```

lvr_summary_665.rpt

```
[2025-10-12 21:16:33] [info] =====
[2025-10-12 21:16:33] [info]                Design Rule Check Report
[2025-10-12 21:16:33] [info] =====

[2025-10-12 21:16:33] [info] Report Generated:    2025-10-12
[2025-10-12 21:16:33] [info] Run Start Time:      09:15:42 PM
[2025-10-12 21:16:33] [info] Run End Time:        09:16:33 PM
[2025-10-12 21:16:33] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 21:16:33] [info] Tool Version:        LVR_2025.1
[2025-10-12 21:16:33] [info] Host Name:           ip-172-31-37-173
[2025-10-12 21:16:33] [info] User Name:           ubuntu
[2025-10-12 21:16:33] [info] Cores Used:          8
[2025-10-12 21:16:33] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info]                Top Cell Information
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] Layout Top Cell:     TOP
[2025-10-12 21:16:33] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:16:33] [info] Database Format:       GDSII
[2025-10-12 21:16:33] [info] Verification Mode:    Full Check

[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info]                Type of Errors | Count
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] DB_WARN -> 1
[2025-10-12 21:16:33] [info] LRF_ERR001 -> 14
[2025-10-12 21:16:33] [info] DB_WARN002 -> 159
[2025-10-12 21:16:33] [info] DB_WARN003 -> 161
[2025-10-12 21:16:33] [info] DRC_ERR9 -> 89
[2025-10-12 21:16:33] [info] DRC_ERR -> 67
[2025-10-12 21:16:33] [info] DRC_ERR5 -> 408
[2025-10-12 21:16:33] [info] DRC_ERR4 -> 241
[2025-10-12 21:16:33] [info] DRC_ERR1 -> 301
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info]                Rule Violation Summary
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 21:16:33] [info] |-----|-----|-----|
[2025-10-12 21:16:33] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:16:33] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:16:33] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:16:33] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:16:33] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 21:16:33] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:16:33] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:16:33] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:16:33] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:16:33] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:16:33] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:16:33] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:16:33] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:16:33] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:16:33] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:16:33] [info]
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] Performance Metrics
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] | Stage | Time Taken |
[2025-10-12 21:16:33] [info] |-----|-----|
[2025-10-12 21:16:33] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:16:33] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 21:16:33] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] Summary
[2025-10-12 21:16:33] [info] -----
[2025-10-12 21:16:33] [info] Total Errors: 1120
[2025-10-12 21:16:33] [info] Total Warnings: 321
[2025-10-12 21:16:33] [info] DRC Status: FAILED
```

lvr_summary_666.rpt

```
[2025-10-12 21:17:30] [info] =====
[2025-10-12 21:17:30] [info] Design Rule Check Report
[2025-10-12 21:17:30] [info] =====

[2025-10-12 21:17:30] [info] Report Generated: 2025-10-12
[2025-10-12 21:17:30] [info] Run Start Time: 09:16:39 PM
[2025-10-12 21:17:30] [info] Run End Time: 09:17:30 PM
[2025-10-12 21:17:30] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:17:30] [info] Tool Version: LVR_2025.1
[2025-10-12 21:17:30] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:17:30] [info] User Name: ubuntu
[2025-10-12 21:17:30] [info] Cores Used: 8
[2025-10-12 21:17:30] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Top Cell Information
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Layout Top Cell: TOP
[2025-10-12 21:17:30] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:17:30] [info] Database Format: GDSII
[2025-10-12 21:17:30] [info] Verification Mode: Full Check

[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Type of Errors | Count
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] DB_WARN -> 1
[2025-10-12 21:17:30] [info] LRF_ERR001 -> 14
[2025-10-12 21:17:30] [info] DB_WARN002 -> 159
[2025-10-12 21:17:30] [info] DB_WARN003 -> 161
```

```

[2025-10-12 21:17:30] [info] DRC_ERR9 -> 89
[2025-10-12 21:17:30] [info] DRC_ERR -> 67
[2025-10-12 21:17:30] [info] DRC_ERR5 -> 408
[2025-10-12 21:17:30] [info] DRC_ERR4 -> 241
[2025-10-12 21:17:30] [info] DRC_ERR1 -> 301
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Rule Violation Summary
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:17:30] [info] |-----|-----|-----|
[2025-10-12 21:17:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:17:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:17:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:17:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:17:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:17:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:17:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:17:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:17:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:17:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:17:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:17:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:17:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:17:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:17:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Performance Metrics
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] | Stage | Time Taken |
[2025-10-12 21:17:30] [info] |-----|-----|
[2025-10-12 21:17:30] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:17:30] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 21:17:30] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Summary
[2025-10-12 21:17:30] [info] -----
[2025-10-12 21:17:30] [info] Total Errors: 1120
[2025-10-12 21:17:30] [info] Total Warnings: 321
[2025-10-12 21:17:30] [info] DRC Status: FAILED

```

lvr_summary_667.rpt

```

[2025-10-12 21:18:27] [info] =====
[2025-10-12 21:18:27] [info] Design Rule Check Report
[2025-10-12 21:18:27] [info] =====

[2025-10-12 21:18:27] [info] Report Generated: 2025-10-12
[2025-10-12 21:18:27] [info] Run Start Time: 09:17:36 PM
[2025-10-12 21:18:27] [info] Run End Time: 09:18:27 PM
[2025-10-12 21:18:27] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:18:27] [info] Tool Version: LVR_2025.1
[2025-10-12 21:18:27] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:18:27] [info] User Name: ubuntu
[2025-10-12 21:18:27] [info] Cores Used: 8
[2025-10-12 21:18:27] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Top Cell Information
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Layout Top Cell:      TOP
[2025-10-12 21:18:27] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:18:27] [info] Database Format:      GDSII
[2025-10-12 21:18:27] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Type of Errors | Count
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] DB_WARN -> 1
[2025-10-12 21:18:27] [info] LRF_ERR001 -> 14
[2025-10-12 21:18:27] [info] DB_WARN002 -> 159
[2025-10-12 21:18:27] [info] DB_WARN003 -> 161
[2025-10-12 21:18:27] [info] DRC_ERR9 -> 89
[2025-10-12 21:18:27] [info] DRC_ERR -> 67
[2025-10-12 21:18:27] [info] DRC_ERR5 -> 408
[2025-10-12 21:18:27] [info] DRC_ERR4 -> 241
[2025-10-12 21:18:27] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Rule Violation Summary
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:18:27] [info] |-----|-----|-----|
[2025-10-12 21:18:27] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:18:27] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:18:27] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:18:27] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:18:27] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:18:27] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:18:27] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:18:27] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:18:27] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:18:27] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:18:27] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:18:27] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:18:27] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:18:27] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:18:27] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Performance Metrics
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] | Stage          | Time Taken |
[2025-10-12 21:18:27] [info] |-----|-----|
[2025-10-12 21:18:27] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:18:27] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 21:18:27] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Summary
[2025-10-12 21:18:27] [info] -----
[2025-10-12 21:18:27] [info] Total Errors:      1120
[2025-10-12 21:18:27] [info] Total Warnings:    321
[2025-10-12 21:18:27] [info] DRC Status:        FAILED
```


lvr_summary_668.rpt

```
[2025-10-12 21:19:24] [info] =====
[2025-10-12 21:19:24] [info] Design Rule Check Report
[2025-10-12 21:19:24] [info] =====

[2025-10-12 21:19:24] [info] Report Generated: 2025-10-12
[2025-10-12 21:19:24] [info] Run Start Time: 09:18:33 PM
[2025-10-12 21:19:24] [info] Run End Time: 09:19:24 PM
[2025-10-12 21:19:24] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:19:24] [info] Tool Version: LVR_2025.1
[2025-10-12 21:19:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:19:24] [info] User Name: ubuntu
[2025-10-12 21:19:24] [info] Cores Used: 8
[2025-10-12 21:19:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Top Cell Information
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Layout Top Cell: TOP
[2025-10-12 21:19:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:19:24] [info] Database Format: GDSII
[2025-10-12 21:19:24] [info] Verification Mode: Full Check

[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Type of Errors | Count
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] DB_WARN -> 1
[2025-10-12 21:19:24] [info] LRF_ERR001 -> 14
[2025-10-12 21:19:24] [info] DB_WARN002 -> 159
[2025-10-12 21:19:24] [info] DB_WARN003 -> 161
[2025-10-12 21:19:24] [info] DRC_ERR9 -> 89
[2025-10-12 21:19:24] [info] DRC_ERR -> 67
[2025-10-12 21:19:24] [info] DRC_ERR5 -> 408
[2025-10-12 21:19:24] [info] DRC_ERR4 -> 241
[2025-10-12 21:19:24] [info] DRC_ERR1 -> 301
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Rule Violation Summary
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:19:24] [info] |-----|-----|-----|
[2025-10-12 21:19:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:19:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:19:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:19:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:19:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:19:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 21:19:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:19:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:19:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:19:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:19:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:19:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:19:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:19:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:19:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:19:24] [info]
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Performance Metrics
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:19:24] [info] |-----|-----|
[2025-10-12 21:19:24] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:19:24] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 21:19:24] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Summary
[2025-10-12 21:19:24] [info] -----
[2025-10-12 21:19:24] [info] Total Errors: 1120
[2025-10-12 21:19:24] [info] Total Warnings: 321
[2025-10-12 21:19:24] [info] DRC Status: FAILED
```

lvr_summary_669.rpt

```
[2025-10-12 21:20:21] [info] =====
[2025-10-12 21:20:21] [info] Design Rule Check Report
[2025-10-12 21:20:21] [info] =====

[2025-10-12 21:20:21] [info] Report Generated: 2025-10-12
[2025-10-12 21:20:21] [info] Run Start Time: 09:19:31 PM
[2025-10-12 21:20:21] [info] Run End Time: 09:20:21 PM
[2025-10-12 21:20:21] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:20:21] [info] Tool Version: LVR_2025.1
[2025-10-12 21:20:21] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:20:21] [info] User Name: ubuntu
[2025-10-12 21:20:21] [info] Cores Used: 8
[2025-10-12 21:20:21] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Top Cell Information
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Layout Top Cell: TOP
[2025-10-12 21:20:21] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:20:21] [info] Database Format: GDSII
[2025-10-12 21:20:21] [info] Verification Mode: Full Check

[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Type of Errors | Count
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] DB_WARN -> 1
[2025-10-12 21:20:21] [info] LRF_ERR001 -> 14
[2025-10-12 21:20:21] [info] DB_WARN002 -> 159
[2025-10-12 21:20:21] [info] DB_WARN003 -> 161
[2025-10-12 21:20:21] [info] DRC_ERR9 -> 89
[2025-10-12 21:20:21] [info] DRC_ERR -> 67
[2025-10-12 21:20:21] [info] DRC_ERR5 -> 408
[2025-10-12 21:20:21] [info] DRC_ERR4 -> 241
[2025-10-12 21:20:21] [info] DRC_ERR1 -> 301
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Rule Violation Summary
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:20:21] [info] |-----|-----|-----|
[2025-10-12 21:20:21] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:20:21] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:20:21] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:20:21] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:20:21] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 21:20:21] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:20:21] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:20:21] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:20:21] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:20:21] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:20:21] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:20:21] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:20:21] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:20:21] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:20:21] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:20:21] [info]
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Performance Metrics
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] | Stage | Time Taken |
[2025-10-12 21:20:21] [info] |-----|-----|
[2025-10-12 21:20:21] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:20:21] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 21:20:21] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Summary
[2025-10-12 21:20:21] [info] -----
[2025-10-12 21:20:21] [info] Total Errors: 1120
[2025-10-12 21:20:21] [info] Total Warnings: 321
[2025-10-12 21:20:21] [info] DRC Status: FAILED
```

lvr_summary_67.rpt

```
[2025-10-12 11:44:57] [info] =====
[2025-10-12 11:44:57] [info] Design Rule Check Report
[2025-10-12 11:44:57] [info] =====

[2025-10-12 11:44:57] [info] Report Generated: 2025-10-12
[2025-10-12 11:44:57] [info] Run Start Time: 11:44:05 AM
[2025-10-12 11:44:57] [info] Run End Time: 11:44:57 AM
[2025-10-12 11:44:57] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 11:44:57] [info] Tool Version: LVR_2025.1
[2025-10-12 11:44:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:44:57] [info] User Name: ubuntu
[2025-10-12 11:44:57] [info] Cores Used: 8
[2025-10-12 11:44:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Top Cell Information
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Layout Top Cell: TOP
[2025-10-12 11:44:57] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:44:57] [info] Database Format: GDSII
[2025-10-12 11:44:57] [info] Verification Mode: Full Check

[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Type of Errors | Count
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] DB_WARN -> 1
[2025-10-12 11:44:57] [info] LRF_ERR001 -> 14
[2025-10-12 11:44:57] [info] DB_WARN002 -> 159
[2025-10-12 11:44:57] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:44:57] [info] DRC_ERR9 -> 89
[2025-10-12 11:44:57] [info] DRC_ERR -> 67
[2025-10-12 11:44:57] [info] DRC_ERR5 -> 408
[2025-10-12 11:44:57] [info] DRC_ERR4 -> 241
[2025-10-12 11:44:57] [info] DRC_ERR1 -> 301
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Rule Violation Summary
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:44:57] [info] |-----|-----|-----|
[2025-10-12 11:44:57] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:44:57] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:44:57] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:44:57] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:44:57] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:44:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:44:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:44:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:44:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:44:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:44:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:44:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:44:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:44:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:44:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Performance Metrics
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] | Stage | Time Taken |
[2025-10-12 11:44:57] [info] |-----|-----|
[2025-10-12 11:44:57] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:44:57] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:44:57] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Summary
[2025-10-12 11:44:57] [info] -----
[2025-10-12 11:44:57] [info] Total Errors: 1120
[2025-10-12 11:44:57] [info] Total Warnings: 321
[2025-10-12 11:44:57] [info] DRC Status: FAILED
```

lvr_summary_670.rpt

```
[2025-10-12 21:21:19] [info] =====
[2025-10-12 21:21:19] [info] Design Rule Check Report
[2025-10-12 21:21:19] [info] =====

[2025-10-12 21:21:19] [info] Report Generated: 2025-10-12
[2025-10-12 21:21:19] [info] Run Start Time: 09:20:28 PM
[2025-10-12 21:21:19] [info] Run End Time: 09:21:19 PM
[2025-10-12 21:21:19] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:21:19] [info] Tool Version: LVR_2025.1
[2025-10-12 21:21:19] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:21:19] [info] User Name: ubuntu
[2025-10-12 21:21:19] [info] Cores Used: 8
[2025-10-12 21:21:19] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Top Cell Information
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Layout Top Cell:      TOP
[2025-10-12 21:21:19] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:21:19] [info] Database Format:       GDSII
[2025-10-12 21:21:19] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Type of Errors | Count
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] DB_WARN -> 1
[2025-10-12 21:21:19] [info] LRF_ERR001 -> 14
[2025-10-12 21:21:19] [info] DB_WARN002 -> 159
[2025-10-12 21:21:19] [info] DB_WARN003 -> 161
[2025-10-12 21:21:19] [info] DRC_ERR9 -> 89
[2025-10-12 21:21:19] [info] DRC_ERR -> 67
[2025-10-12 21:21:19] [info] DRC_ERR5 -> 408
[2025-10-12 21:21:19] [info] DRC_ERR4 -> 241
[2025-10-12 21:21:19] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Rule Violation Summary
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:21:19] [info] |-----|-----|-----|
[2025-10-12 21:21:19] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:21:19] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:21:19] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:21:19] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:21:19] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:21:19] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:21:19] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:21:19] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:21:19] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:21:19] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:21:19] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:21:19] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:21:19] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:21:19] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:21:19] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:21:19] [info] |
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Performance Metrics
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] | Stage          | Time Taken |
[2025-10-12 21:21:19] [info] |-----|-----|
[2025-10-12 21:21:19] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:21:19] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 21:21:19] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Summary
[2025-10-12 21:21:19] [info] -----
[2025-10-12 21:21:19] [info] Total Errors:      1120
[2025-10-12 21:21:19] [info] Total Warnings:    321
[2025-10-12 21:21:19] [info] DRC Status:        FAILED
```

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```
[2025-10-12 21:22:18] [info] =====
[2025-10-12 21:22:18] [info] Design Rule Check Report
[2025-10-12 21:22:18] [info] =====

[2025-10-12 21:22:18] [info] Report Generated: 2025-10-12
[2025-10-12 21:22:18] [info] Run Start Time: 09:21:26 PM
[2025-10-12 21:22:18] [info] Run End Time: 09:22:18 PM
[2025-10-12 21:22:18] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:22:18] [info] Tool Version: LVR_2025.1
[2025-10-12 21:22:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:22:18] [info] User Name: ubuntu
[2025-10-12 21:22:18] [info] Cores Used: 8
[2025-10-12 21:22:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Top Cell Information
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Layout Top Cell: TOP
[2025-10-12 21:22:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:22:18] [info] Database Format: GDSII
[2025-10-12 21:22:18] [info] Verification Mode: Full Check

[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Type of Errors | Count
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] DB_WARN -> 1
[2025-10-12 21:22:18] [info] LRF_ERR001 -> 14
[2025-10-12 21:22:18] [info] DB_WARN002 -> 159
[2025-10-12 21:22:18] [info] DB_WARN003 -> 161
[2025-10-12 21:22:18] [info] DRC_ERR9 -> 89
[2025-10-12 21:22:18] [info] DRC_ERR -> 67
[2025-10-12 21:22:18] [info] DRC_ERR5 -> 408
[2025-10-12 21:22:18] [info] DRC_ERR4 -> 241
[2025-10-12 21:22:18] [info] DRC_ERR1 -> 301
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Rule Violation Summary
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:22:18] [info] |-----|-----|-----|
[2025-10-12 21:22:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:22:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:22:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:22:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:22:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:22:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:22:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:22:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:22:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:22:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:22:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:22:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:22:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:22:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:22:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:22:18] [info]
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Performance Metrics
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:22:18] [info] |-----|-----|
[2025-10-12 21:22:18] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:22:18] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 21:22:18] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Summary
[2025-10-12 21:22:18] [info] -----
[2025-10-12 21:22:18] [info] Total Errors: 1120
[2025-10-12 21:22:18] [info] Total Warnings: 321
[2025-10-12 21:22:18] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:23:15] [info] =====
[2025-10-12 21:23:15] [info] Design Rule Check Report
[2025-10-12 21:23:15] [info] =====

[2025-10-12 21:23:15] [info] Report Generated: 2025-10-12
[2025-10-12 21:23:15] [info] Run Start Time: 09:22:23 PM
[2025-10-12 21:23:15] [info] Run End Time: 09:23:15 PM
[2025-10-12 21:23:15] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 21:23:15] [info] Tool Version: LVR_2025.1
[2025-10-12 21:23:15] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:23:15] [info] User Name: ubuntu
[2025-10-12 21:23:15] [info] Cores Used: 8
[2025-10-12 21:23:15] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Top Cell Information
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Layout Top Cell: TOP
[2025-10-12 21:23:15] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:23:15] [info] Database Format: GDSII
[2025-10-12 21:23:15] [info] Verification Mode: Full Check

[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Type of Errors | Count
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] DB_WARN -> 1
[2025-10-12 21:23:15] [info] LRF_ERR001 -> 14
[2025-10-12 21:23:15] [info] DB_WARN002 -> 159
[2025-10-12 21:23:15] [info] DB_WARN003 -> 161
[2025-10-12 21:23:15] [info] DRC_ERR9 -> 89
[2025-10-12 21:23:15] [info] DRC_ERR -> 67
[2025-10-12 21:23:15] [info] DRC_ERR5 -> 408
[2025-10-12 21:23:15] [info] DRC_ERR4 -> 241
[2025-10-12 21:23:15] [info] DRC_ERR1 -> 301
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Rule Violation Summary
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:23:15] [info] |-----|-----|-----|
[2025-10-12 21:23:15] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:23:15] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:23:15] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:23:15] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:23:15] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 21:23:15] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:23:15] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:23:15] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:23:15] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:23:15] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:23:15] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:23:15] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:23:15] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:23:15] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:23:15] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:23:15] [info]
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Performance Metrics
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] | Stage | Time Taken |
[2025-10-12 21:23:15] [info] |-----|-----|
[2025-10-12 21:23:15] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:23:15] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 21:23:15] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Summary
[2025-10-12 21:23:15] [info] -----
[2025-10-12 21:23:15] [info] Total Errors: 1120
[2025-10-12 21:23:15] [info] Total Warnings: 321
[2025-10-12 21:23:15] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:24:12] [info] =====
[2025-10-12 21:24:12] [info] Design Rule Check Report
[2025-10-12 21:24:12] [info] =====

[2025-10-12 21:24:12] [info] Report Generated: 2025-10-12
[2025-10-12 21:24:12] [info] Run Start Time: 09:23:21 PM
[2025-10-12 21:24:12] [info] Run End Time: 09:24:12 PM
[2025-10-12 21:24:12] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 21:24:12] [info] Tool Version: LVR_2025.1
[2025-10-12 21:24:12] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:24:12] [info] User Name: ubuntu
[2025-10-12 21:24:12] [info] Cores Used: 8
[2025-10-12 21:24:12] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Top Cell Information
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Layout Top Cell: TOP
[2025-10-12 21:24:12] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:24:12] [info] Database Format: GDSII
[2025-10-12 21:24:12] [info] Verification Mode: Full Check

[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Type of Errors | Count
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] DB_WARN -> 1
[2025-10-12 21:24:12] [info] LRF_ERR001 -> 14
[2025-10-12 21:24:12] [info] DB_WARN002 -> 159
[2025-10-12 21:24:12] [info] DB_WARN003 -> 161
```



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[2025-10-12 21:24:12] [info] DRC_ERR9 -> 89
[2025-10-12 21:24:12] [info] DRC_ERR -> 67
[2025-10-12 21:24:12] [info] DRC_ERR5 -> 408
[2025-10-12 21:24:12] [info] DRC_ERR4 -> 241
[2025-10-12 21:24:12] [info] DRC_ERR1 -> 301
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Rule Violation Summary
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:24:12] [info] |-----|-----|-----|
[2025-10-12 21:24:12] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:24:12] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:24:12] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:24:12] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:24:12] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:24:12] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:24:12] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:24:12] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:24:12] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:24:12] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:24:12] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:24:12] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:24:12] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:24:12] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:24:12] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Performance Metrics
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] | Stage | Time Taken |
[2025-10-12 21:24:12] [info] |-----|-----|
[2025-10-12 21:24:12] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:24:12] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 21:24:12] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Summary
[2025-10-12 21:24:12] [info] -----
[2025-10-12 21:24:12] [info] Total Errors: 1120
[2025-10-12 21:24:12] [info] Total Warnings: 321
[2025-10-12 21:24:12] [info] DRC Status: FAILED

```

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```

[2025-10-12 21:25:09] [info] =====
[2025-10-12 21:25:09] [info] Design Rule Check Report
[2025-10-12 21:25:09] [info] =====

[2025-10-12 21:25:09] [info] Report Generated: 2025-10-12
[2025-10-12 21:25:09] [info] Run Start Time: 09:24:18 PM
[2025-10-12 21:25:09] [info] Run End Time: 09:25:09 PM
[2025-10-12 21:25:09] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:25:09] [info] Tool Version: LVR_2025.1
[2025-10-12 21:25:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:25:09] [info] User Name: ubuntu
[2025-10-12 21:25:09] [info] Cores Used: 8
[2025-10-12 21:25:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] Top Cell Information
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] Layout Top Cell:      TOP
[2025-10-12 21:25:09] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:25:09] [info] Database Format:       GDSII
[2025-10-12 21:25:09] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] Type of Errors | Count
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] DB_WARN -> 1
[2025-10-12 21:25:09] [info] LRF_ERR001 -> 14
[2025-10-12 21:25:09] [info] DB_WARN002 -> 159
[2025-10-12 21:25:09] [info] DB_WARN003 -> 161
[2025-10-12 21:25:09] [info] DRC_ERR9 -> 89
[2025-10-12 21:25:09] [info] DRC_ERR -> 67
[2025-10-12 21:25:09] [info] DRC_ERR5 -> 408
[2025-10-12 21:25:09] [info] DRC_ERR4 -> 241
[2025-10-12 21:25:09] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] Rule Violation Summary
```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:25:09] [info] |-----|-----|-----|
[2025-10-12 21:25:09] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:25:09] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:25:09] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:25:09] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:25:09] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:25:09] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:25:09] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:25:09] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:25:09] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:25:09] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:25:09] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:25:09] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:25:09] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:25:09] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:25:09] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:25:09] [info] -----
```

```
[2025-10-12 21:25:09] [info] Performance Metrics
```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] | Stage          | Time Taken |
[2025-10-12 21:25:09] [info] |-----|-----|
[2025-10-12 21:25:09] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:25:09] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 21:25:09] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] Summary
```

```
[2025-10-12 21:25:09] [info] -----
[2025-10-12 21:25:09] [info] Total Errors:      1120
[2025-10-12 21:25:09] [info] Total Warnings:    321
[2025-10-12 21:25:09] [info] DRC Status:        FAILED
```

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```
[2025-10-12 21:26:08] [info] =====
[2025-10-12 21:26:08] [info]                      Design Rule Check Report
[2025-10-12 21:26:08] [info] =====

[2025-10-12 21:26:08] [info] Report Generated:      2025-10-12
[2025-10-12 21:26:08] [info] Run Start Time:       09:25:16 PM
[2025-10-12 21:26:08] [info] Run End Time:         09:26:08 PM
[2025-10-12 21:26:08] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 21:26:08] [info] Tool Version:          LVR_2025.1
[2025-10-12 21:26:08] [info] Host Name:             ip-172-31-37-173
[2025-10-12 21:26:08] [info] User Name:             ubuntu
[2025-10-12 21:26:08] [info] Cores Used:            8
[2025-10-12 21:26:08] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info]                      Top Cell Information
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info] Layout Top Cell:      TOP
[2025-10-12 21:26:08] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 21:26:08] [info] Database Format:        GDSII
[2025-10-12 21:26:08] [info] Verification Mode:     Full Check

[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info]                      Type of Errors | Count
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info] DB_WARN -> 1
[2025-10-12 21:26:08] [info] LRF_ERR001 -> 14
[2025-10-12 21:26:08] [info] DB_WARN002 -> 159
[2025-10-12 21:26:08] [info] DB_WARN003 -> 161
[2025-10-12 21:26:08] [info] DRC_ERR9 -> 89
[2025-10-12 21:26:08] [info] DRC_ERR -> 67
[2025-10-12 21:26:08] [info] DRC_ERR5 -> 408
[2025-10-12 21:26:08] [info] DRC_ERR4 -> 241
[2025-10-12 21:26:08] [info] DRC_ERR1 -> 301
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info]                      Rule Violation Summary
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:26:08] [info] |-----|-----|-----|
[2025-10-12 21:26:08] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:26:08] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:26:08] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:26:08] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:26:08] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 21:26:08] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 21:26:08] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:26:08] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:26:08] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:26:08] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:26:08] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:26:08] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:26:08] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:26:08] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:26:08] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:26:08] [info]
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info]                      Performance Metrics
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info] | Stage | Time Taken |
```

```
[2025-10-12 21:26:08] [info] |-----|
[2025-10-12 21:26:08] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 21:26:08] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 21:26:08] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info] Summary
[2025-10-12 21:26:08] [info] -----
[2025-10-12 21:26:08] [info] Total Errors:    1120
[2025-10-12 21:26:08] [info] Total Warnings:    321
[2025-10-12 21:26:08] [info] DRC Status:      FAILED
```

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```
[2025-10-12 21:27:05] [info] =====
[2025-10-12 21:27:05] [info] Design Rule Check Report
[2025-10-12 21:27:05] [info] =====

[2025-10-12 21:27:05] [info] Report Generated:    2025-10-12
[2025-10-12 21:27:05] [info] Run Start Time:      09:26:13 PM
[2025-10-12 21:27:05] [info] Run End Time:        09:27:05 PM
[2025-10-12 21:27:05] [info] Time Taken:          0 minutes 52 seconds

[2025-10-12 21:27:05] [info] Tool Version:        LVR_2025.1
[2025-10-12 21:27:05] [info] Host Name:           ip-172-31-37-173
[2025-10-12 21:27:05] [info] User Name:           ubuntu
[2025-10-12 21:27:05] [info] Cores Used:          8
[2025-10-12 21:27:05] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Top Cell Information
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Layout Top Cell:     TOP
[2025-10-12 21:27:05] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:27:05] [info] Database Format:      GDSII
[2025-10-12 21:27:05] [info] Verification Mode:    Full Check

[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Type of Errors | Count
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] DB_WARN -> 1
[2025-10-12 21:27:05] [info] LRF_ERR001 -> 14
[2025-10-12 21:27:05] [info] DB_WARN002 -> 159
[2025-10-12 21:27:05] [info] DB_WARN003 -> 161
[2025-10-12 21:27:05] [info] DRC_ERR9 -> 89
[2025-10-12 21:27:05] [info] DRC_ERR -> 67
[2025-10-12 21:27:05] [info] DRC_ERR5 -> 408
[2025-10-12 21:27:05] [info] DRC_ERR4 -> 241
[2025-10-12 21:27:05] [info] DRC_ERR1 -> 301
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Rule Violation Summary
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 21:27:05] [info] |-----|-----|-----|
[2025-10-12 21:27:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 21:27:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 21:27:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 21:27:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 21:27:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 21:27:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 21:27:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 21:27:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 21:27:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 21:27:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 21:27:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 21:27:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 21:27:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 21:27:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 21:27:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 21:27:05] [info]
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Performance Metrics
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] | Stage | Time Taken |
[2025-10-12 21:27:05] [info] |-----|-----|
[2025-10-12 21:27:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 21:27:05] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 21:27:05] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Summary
[2025-10-12 21:27:05] [info] -----
[2025-10-12 21:27:05] [info] Total Errors: 1120
[2025-10-12 21:27:05] [info] Total Warnings: 321
[2025-10-12 21:27:05] [info] DRC Status: FAILED
```

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```
[2025-10-12 21:30:36] [info] =====
[2025-10-12 21:30:36] [info] Design Rule Check Report
[2025-10-12 21:30:36] [info] =====

[2025-10-12 21:30:36] [info] Report Generated: 2025-10-12
[2025-10-12 21:30:36] [info] Run Start Time: 09:29:45 PM
[2025-10-12 21:30:36] [info] Run End Time: 09:30:36 PM
[2025-10-12 21:30:36] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 21:30:36] [info] Tool Version: LVR_2025.1
[2025-10-12 21:30:36] [info] Host Name: ip-172-31-37-173
[2025-10-12 21:30:36] [info] User Name: ubuntu
[2025-10-12 21:30:36] [info] Cores Used: 8
[2025-10-12 21:30:36] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Top Cell Information
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Layout Top Cell: TOP
[2025-10-12 21:30:36] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 21:30:36] [info] Database Format: GDSII
[2025-10-12 21:30:36] [info] Verification Mode: Full Check

[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Type of Errors | Count
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] DB_WARN -> 1
[2025-10-12 21:30:36] [info] LRF_ERR001 -> 14
[2025-10-12 21:30:36] [info] DB_WARN002 -> 159
[2025-10-12 21:30:36] [info] DB_WARN003 -> 161
```

```
[2025-10-12 21:30:36] [info] DRC_ERR9 -> 89
[2025-10-12 21:30:36] [info] DRC_ERR -> 67
[2025-10-12 21:30:36] [info] DRC_ERR5 -> 408
[2025-10-12 21:30:36] [info] DRC_ERR4 -> 241
[2025-10-12 21:30:36] [info] DRC_ERR1 -> 301
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Rule Violation Summary
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:30:36] [info] |-----|-----|-----|
[2025-10-12 21:30:36] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:30:36] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:30:36] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:30:36] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:30:36] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:30:36] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 21:30:36] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:30:36] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:30:36] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:30:36] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:30:36] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:30:36] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:30:36] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:30:36] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:30:36] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Performance Metrics
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] | Stage                | Time Taken |
[2025-10-12 21:30:36] [info] |-----|-----|
[2025-10-12 21:30:36] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 21:30:36] [info] | Rule Checking         | 0 minutes 39 seconds |
[2025-10-12 21:30:36] [info] | Report Generation    | 0 minutes 0 seconds |
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Summary
[2025-10-12 21:30:36] [info] -----
[2025-10-12 21:30:36] [info] Total Errors:      1120
[2025-10-12 21:30:36] [info] Total Warnings:    321
[2025-10-12 21:30:36] [info] DRC Status:        FAILED
```

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```
[2025-10-12 21:31:53] [info] =====
[2025-10-12 21:31:53] [info] Design Rule Check Report
[2025-10-12 21:31:53] [info] =====

[2025-10-12 21:31:53] [info] Report Generated:    2025-10-12
[2025-10-12 21:31:53] [info] Run Start Time:      09:31:02 PM
[2025-10-12 21:31:53] [info] Run End Time:        09:31:53 PM
[2025-10-12 21:31:53] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 21:31:53] [info] Tool Version:        LVR_2025.1
[2025-10-12 21:31:53] [info] Host Name:            ip-172-31-37-173
[2025-10-12 21:31:53] [info] User Name:            ubuntu
[2025-10-12 21:31:53] [info] Cores Used:           8
[2025-10-12 21:31:53] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] Top Cell Information
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] Layout Top Cell:      TOP
[2025-10-12 21:31:53] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 21:31:53] [info] Database Format:       GDSII
[2025-10-12 21:31:53] [info] Verification Mode:    Full Check
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] Type of Errors | Count
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] DB_WARN -> 1
[2025-10-12 21:31:53] [info] LRF_ERR001 -> 14
[2025-10-12 21:31:53] [info] DB_WARN002 -> 159
[2025-10-12 21:31:53] [info] DB_WARN003 -> 161
[2025-10-12 21:31:53] [info] DRC_ERR9 -> 89
[2025-10-12 21:31:53] [info] DRC_ERR -> 67
[2025-10-12 21:31:53] [info] DRC_ERR5 -> 408
[2025-10-12 21:31:53] [info] DRC_ERR4 -> 241
[2025-10-12 21:31:53] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] Rule Violation Summary
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 21:31:53] [info] |-----|-----|-----|
[2025-10-12 21:31:53] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 21:31:53] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 21:31:53] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 21:31:53] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 21:31:53] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 21:31:53] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 21:31:53] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 21:31:53] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 21:31:53] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 21:31:53] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 21:31:53] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 21:31:53] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 21:31:53] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 21:31:53] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 21:31:53] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 21:31:53] [info] -----
```

```
[2025-10-12 21:31:53] [info] Performance Metrics
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] | Stage          | Time Taken |
[2025-10-12 21:31:53] [info] |-----|-----|
[2025-10-12 21:31:53] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 21:31:53] [info] | Rule Checking   | 0 minutes 40 seconds |
[2025-10-12 21:31:53] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] Summary
```

```
[2025-10-12 21:31:53] [info] -----
[2025-10-12 21:31:53] [info] Total Errors:      1120
[2025-10-12 21:31:53] [info] Total Warnings:    321
[2025-10-12 21:31:53] [info] DRC Status:        FAILED
```

lvr_summary_68.rpt

```
[2025-10-12 11:45:54] [info] =====
[2025-10-12 11:45:54] [info] Design Rule Check Report
[2025-10-12 11:45:54] [info] =====

[2025-10-12 11:45:54] [info] Report Generated: 2025-10-12
[2025-10-12 11:45:54] [info] Run Start Time: 11:45:02 AM
[2025-10-12 11:45:54] [info] Run End Time: 11:45:54 AM
[2025-10-12 11:45:54] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:45:54] [info] Tool Version: LVR_2025.1
[2025-10-12 11:45:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:45:54] [info] User Name: ubuntu
[2025-10-12 11:45:54] [info] Cores Used: 8
[2025-10-12 11:45:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] Top Cell Information
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] Layout Top Cell: TOP
[2025-10-12 11:45:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:45:54] [info] Database Format: GDSII
[2025-10-12 11:45:54] [info] Verification Mode: Full Check

[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] Type of Errors | Count
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] DB_WARN -> 1
[2025-10-12 11:45:54] [info] LRF_ERR001 -> 14
[2025-10-12 11:45:54] [info] DB_WARN002 -> 159
[2025-10-12 11:45:54] [info] DB_WARN003 -> 161
[2025-10-12 11:45:54] [info] DRC_ERR9 -> 89
[2025-10-12 11:45:54] [info] DRC_ERR -> 67
[2025-10-12 11:45:54] [info] DRC_ERR5 -> 408
[2025-10-12 11:45:54] [info] DRC_ERR4 -> 241
[2025-10-12 11:45:54] [info] DRC_ERR1 -> 301
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] Rule Violation Summary
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:45:54] [info] |-----|-----|-----|
[2025-10-12 11:45:54] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:45:54] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:45:54] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:45:54] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:45:54] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:45:54] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:45:54] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:45:54] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:45:54] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:45:54] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:45:54] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:45:54] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:45:54] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:45:54] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:45:54] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:45:54] [info]
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] Performance Metrics
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] | Stage | Time Taken |
```



```
[2025-10-12 11:45:54] [info] |-----|-----|
[2025-10-12 11:45:54] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:45:54] [info] | Rule Checking          | 0 minutes 41 seconds |
[2025-10-12 11:45:54] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info]                Summary
[2025-10-12 11:45:54] [info] -----
[2025-10-12 11:45:54] [info] Total Errors:    1120
[2025-10-12 11:45:54] [info] Total Warnings:    321
[2025-10-12 11:45:54] [info] DRC Status:      FAILED
```

lvr_summary_69.rpt

```
[2025-10-12 11:46:51] [info] =====
[2025-10-12 11:46:51] [info]                Design Rule Check Report
[2025-10-12 11:46:51] [info] =====

[2025-10-12 11:46:51] [info] Report Generated:    2025-10-12
[2025-10-12 11:46:51] [info] Run Start Time:      11:45:59 AM
[2025-10-12 11:46:51] [info] Run End Time:        11:46:51 AM
[2025-10-12 11:46:51] [info] Time Taken:          0 minutes 51 seconds

[2025-10-12 11:46:51] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:46:51] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:46:51] [info] User Name:           ubuntu
[2025-10-12 11:46:51] [info] Cores Used:          8
[2025-10-12 11:46:51] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info]                Top Cell Information
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] Layout Top Cell:     TOP
[2025-10-12 11:46:51] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:46:51] [info] Database Format:       GDSII
[2025-10-12 11:46:51] [info] Verification Mode:    Full Check

[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info]                Type of Errors | Count
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] DB_WARN -> 1
[2025-10-12 11:46:51] [info] LRF_ERR001 -> 14
[2025-10-12 11:46:51] [info] DB_WARN002 -> 159
[2025-10-12 11:46:51] [info] DB_WARN003 -> 161
[2025-10-12 11:46:51] [info] DRC_ERR9 -> 89
[2025-10-12 11:46:51] [info] DRC_ERR -> 67
[2025-10-12 11:46:51] [info] DRC_ERR5 -> 408
[2025-10-12 11:46:51] [info] DRC_ERR4 -> 241
[2025-10-12 11:46:51] [info] DRC_ERR1 -> 301
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info]                Rule Violation Summary
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:46:51] [info] |-----|-----|-----|
[2025-10-12 11:46:51] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:46:51] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:46:51] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:46:51] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:46:51] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:46:51] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:46:51] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:46:51] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:46:51] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:46:51] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:46:51] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:46:51] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:46:51] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:46:51] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:46:51] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:46:51] [info]
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] Performance Metrics
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] | Stage | Time Taken |
[2025-10-12 11:46:51] [info] |-----|-----|
[2025-10-12 11:46:51] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:46:51] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:46:51] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] Summary
[2025-10-12 11:46:51] [info] -----
[2025-10-12 11:46:51] [info] Total Errors: 1120
[2025-10-12 11:46:51] [info] Total Warnings: 321
[2025-10-12 11:46:51] [info] DRC Status: FAILED
```

lvr_summary_7.rpt

```
[2025-10-12 10:48:05] [info] =====
[2025-10-12 10:48:05] [info] Design Rule Check Report
[2025-10-12 10:48:05] [info] =====

[2025-10-12 10:48:05] [info] Report Generated: 2025-10-12
[2025-10-12 10:48:05] [info] Run Start Time: 10:47:14 AM
[2025-10-12 10:48:05] [info] Run End Time: 10:48:05 AM
[2025-10-12 10:48:05] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 10:48:05] [info] Tool Version: LVR_2025.1
[2025-10-12 10:48:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:48:05] [info] User Name: ubuntu
[2025-10-12 10:48:05] [info] Cores Used: 8
[2025-10-12 10:48:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Top Cell Information
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Layout Top Cell: TOP
[2025-10-12 10:48:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:48:05] [info] Database Format: GDSII
[2025-10-12 10:48:05] [info] Verification Mode: Full Check

[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Type of Errors | Count
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] DB_WARN -> 1
[2025-10-12 10:48:05] [info] LRF_ERR001 -> 14
[2025-10-12 10:48:05] [info] DB_WARN002 -> 159
[2025-10-12 10:48:05] [info] DB_WARN003 -> 161
```

```
[2025-10-12 10:48:05] [info] DRC_ERR9 -> 89
[2025-10-12 10:48:05] [info] DRC_ERR -> 67
[2025-10-12 10:48:05] [info] DRC_ERR5 -> 408
[2025-10-12 10:48:05] [info] DRC_ERR4 -> 241
[2025-10-12 10:48:05] [info] DRC_ERR1 -> 301
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Rule Violation Summary
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:48:05] [info] |-----|-----|-----|
[2025-10-12 10:48:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:48:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:48:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:48:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:48:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:48:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:48:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:48:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:48:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:48:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:48:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:48:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:48:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:48:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:48:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Performance Metrics
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] | Stage | Time Taken |
[2025-10-12 10:48:05] [info] |-----|-----|
[2025-10-12 10:48:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:48:05] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 10:48:05] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Summary
[2025-10-12 10:48:05] [info] -----
[2025-10-12 10:48:05] [info] Total Errors: 1120
[2025-10-12 10:48:05] [info] Total Warnings: 321
[2025-10-12 10:48:05] [info] DRC Status: FAILED
```

lvr_summary_70.rpt

```
[2025-10-12 11:47:48] [info] =====
[2025-10-12 11:47:48] [info] Design Rule Check Report
[2025-10-12 11:47:48] [info] =====

[2025-10-12 11:47:48] [info] Report Generated: 2025-10-12
[2025-10-12 11:47:48] [info] Run Start Time: 11:46:56 AM
[2025-10-12 11:47:48] [info] Run End Time: 11:47:48 AM
[2025-10-12 11:47:48] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:47:48] [info] Tool Version: LVR_2025.1
[2025-10-12 11:47:48] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:47:48] [info] User Name: ubuntu
[2025-10-12 11:47:48] [info] Cores Used: 8
[2025-10-12 11:47:48] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Top Cell Information
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Layout Top Cell:      TOP
[2025-10-12 11:47:48] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:47:48] [info] Database Format:      GDSII
[2025-10-12 11:47:48] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Type of Errors | Count
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] DB_WARN -> 1
[2025-10-12 11:47:48] [info] LRF_ERR001 -> 14
[2025-10-12 11:47:48] [info] DB_WARN002 -> 159
[2025-10-12 11:47:48] [info] DB_WARN003 -> 161
[2025-10-12 11:47:48] [info] DRC_ERR9 -> 89
[2025-10-12 11:47:48] [info] DRC_ERR -> 67
[2025-10-12 11:47:48] [info] DRC_ERR5 -> 408
[2025-10-12 11:47:48] [info] DRC_ERR4 -> 241
[2025-10-12 11:47:48] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Rule Violation Summary
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:47:48] [info] |-----|-----|-----|
[2025-10-12 11:47:48] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:47:48] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:47:48] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:47:48] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:47:48] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:47:48] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:47:48] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:47:48] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:47:48] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:47:48] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:47:48] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:47:48] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:47:48] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:47:48] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:47:48] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:47:48] [info] |
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Performance Metrics
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] | Stage          | Time Taken |
[2025-10-12 11:47:48] [info] |-----|-----|
[2025-10-12 11:47:48] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:47:48] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:47:48] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Summary
```

```
[2025-10-12 11:47:48] [info] -----
[2025-10-12 11:47:48] [info] Total Errors:      1120
[2025-10-12 11:47:48] [info] Total Warnings:    321
[2025-10-12 11:47:48] [info] DRC Status:       FAILED
```

lvr_summary_71.rpt

```
[2025-10-12 11:48:43] [info] =====
[2025-10-12 11:48:43] [info]                      Design Rule Check Report
[2025-10-12 11:48:43] [info] =====

[2025-10-12 11:48:43] [info] Report Generated:      2025-10-12
[2025-10-12 11:48:43] [info] Run Start Time:       11:47:52 AM
[2025-10-12 11:48:43] [info] Run End Time:         11:48:43 AM
[2025-10-12 11:48:43] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 11:48:43] [info] Tool Version:          LVR_2025.1
[2025-10-12 11:48:43] [info] Host Name:             ip-172-31-37-173
[2025-10-12 11:48:43] [info] User Name:             ubuntu
[2025-10-12 11:48:43] [info] Cores Used:            8
[2025-10-12 11:48:43] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info]                      Top Cell Information
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info] Layout Top Cell:      TOP
[2025-10-12 11:48:43] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 11:48:43] [info] Database Format:        GDSII
[2025-10-12 11:48:43] [info] Verification Mode:     Full Check

[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info]                      Type of Errors | Count
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info] DB_WARN -> 1
[2025-10-12 11:48:43] [info] LRF_ERR001 -> 14
[2025-10-12 11:48:43] [info] DB_WARN002 -> 159
[2025-10-12 11:48:43] [info] DB_WARN003 -> 161
[2025-10-12 11:48:43] [info] DRC_ERR9 -> 89
[2025-10-12 11:48:43] [info] DRC_ERR -> 67
[2025-10-12 11:48:43] [info] DRC_ERR5 -> 408
[2025-10-12 11:48:43] [info] DRC_ERR4 -> 241
[2025-10-12 11:48:43] [info] DRC_ERR1 -> 301
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info]                      Rule Violation Summary
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:48:43] [info] |-----|-----|-----|
[2025-10-12 11:48:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:48:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:48:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:48:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:48:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:48:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:48:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:48:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:48:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:48:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:48:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:48:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:48:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:48:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:48:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:48:43] [info]
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info]                      Performance Metrics
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:48:43] [info] |-----|-----|
[2025-10-12 11:48:43] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:48:43] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:48:43] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info] Summary
[2025-10-12 11:48:43] [info] -----
[2025-10-12 11:48:43] [info] Total Errors: 1120
[2025-10-12 11:48:43] [info] Total Warnings: 321
[2025-10-12 11:48:43] [info] DRC Status: FAILED
```

lvr_summary_72.rpt

```
[2025-10-12 11:49:40] [info] =====
[2025-10-12 11:49:40] [info] Design Rule Check Report
[2025-10-12 11:49:40] [info] =====

[2025-10-12 11:49:40] [info] Report Generated: 2025-10-12
[2025-10-12 11:49:40] [info] Run Start Time: 11:48:49 AM
[2025-10-12 11:49:40] [info] Run End Time: 11:49:40 AM
[2025-10-12 11:49:40] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:49:40] [info] Tool Version: LVR_2025.1
[2025-10-12 11:49:40] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:49:40] [info] User Name: ubuntu
[2025-10-12 11:49:40] [info] Cores Used: 8
[2025-10-12 11:49:40] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Top Cell Information
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Layout Top Cell: TOP
[2025-10-12 11:49:40] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:49:40] [info] Database Format: GDSII
[2025-10-12 11:49:40] [info] Verification Mode: Full Check

[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Type of Errors | Count
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] DB_WARN -> 1
[2025-10-12 11:49:40] [info] LRF_ERR001 -> 14
[2025-10-12 11:49:40] [info] DB_WARN002 -> 159
[2025-10-12 11:49:40] [info] DB_WARN003 -> 161
[2025-10-12 11:49:40] [info] DRC_ERR9 -> 89
[2025-10-12 11:49:40] [info] DRC_ERR -> 67
[2025-10-12 11:49:40] [info] DRC_ERR5 -> 408
[2025-10-12 11:49:40] [info] DRC_ERR4 -> 241
[2025-10-12 11:49:40] [info] DRC_ERR1 -> 301
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Rule Violation Summary
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:49:40] [info] |-----|-----|-----|
[2025-10-12 11:49:40] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:49:40] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:49:40] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:49:40] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:49:40] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 11:49:40] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 11:49:40] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:49:40] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:49:40] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:49:40] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:49:40] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:49:40] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:49:40] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:49:40] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:49:40] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:49:40] [info]
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Performance Metrics
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] | Stage | Time Taken |
[2025-10-12 11:49:40] [info] |-----|-----|
[2025-10-12 11:49:40] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:49:40] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 11:49:40] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Summary
[2025-10-12 11:49:40] [info] -----
[2025-10-12 11:49:40] [info] Total Errors: 1120
[2025-10-12 11:49:40] [info] Total Warnings: 321
[2025-10-12 11:49:40] [info] DRC Status: FAILED
```

lvr_summary_73.rpt

```
[2025-10-12 11:50:37] [info] =====
[2025-10-12 11:50:37] [info] Design Rule Check Report
[2025-10-12 11:50:37] [info] =====

[2025-10-12 11:50:37] [info] Report Generated: 2025-10-12
[2025-10-12 11:50:37] [info] Run Start Time: 11:49:46 AM
[2025-10-12 11:50:37] [info] Run End Time: 11:50:37 AM
[2025-10-12 11:50:37] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:50:37] [info] Tool Version: LVR_2025.1
[2025-10-12 11:50:37] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:50:37] [info] User Name: ubuntu
[2025-10-12 11:50:37] [info] Cores Used: 8
[2025-10-12 11:50:37] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Top Cell Information
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Layout Top Cell: TOP
[2025-10-12 11:50:37] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:50:37] [info] Database Format: GDSII
[2025-10-12 11:50:37] [info] Verification Mode: Full Check

[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Type of Errors | Count
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] DB_WARN -> 1
[2025-10-12 11:50:37] [info] LRF_ERR001 -> 14
[2025-10-12 11:50:37] [info] DB_WARN002 -> 159
[2025-10-12 11:50:37] [info] DB_WARN003 -> 161
```

```

[2025-10-12 11:50:37] [info] DRC_ERR9 -> 89
[2025-10-12 11:50:37] [info] DRC_ERR -> 67
[2025-10-12 11:50:37] [info] DRC_ERR5 -> 408
[2025-10-12 11:50:37] [info] DRC_ERR4 -> 241
[2025-10-12 11:50:37] [info] DRC_ERR1 -> 301
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Rule Violation Summary
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:50:37] [info] |-----|-----|-----|
[2025-10-12 11:50:37] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:50:37] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:50:37] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:50:37] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:50:37] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:50:37] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 11:50:37] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:50:37] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:50:37] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:50:37] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:50:37] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:50:37] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:50:37] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:50:37] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:50:37] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Performance Metrics
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] | Stage          | Time Taken |
[2025-10-12 11:50:37] [info] |-----|-----|
[2025-10-12 11:50:37] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:50:37] [info] | Rule Checking   | 0 minutes 39 seconds |
[2025-10-12 11:50:37] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Summary
[2025-10-12 11:50:37] [info] -----
[2025-10-12 11:50:37] [info] Total Errors: 1120
[2025-10-12 11:50:37] [info] Total Warnings: 321
[2025-10-12 11:50:37] [info] DRC Status: FAILED

```

lvr_summary_74.rpt

```

[2025-10-12 11:51:35] [info] =====
[2025-10-12 11:51:35] [info] Design Rule Check Report
[2025-10-12 11:51:35] [info] =====

[2025-10-12 11:51:35] [info] Report Generated: 2025-10-12
[2025-10-12 11:51:35] [info] Run Start Time: 11:50:43 AM
[2025-10-12 11:51:35] [info] Run End Time: 11:51:35 AM
[2025-10-12 11:51:35] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:51:35] [info] Tool Version: LVR_2025.1
[2025-10-12 11:51:35] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:51:35] [info] User Name: ubuntu
[2025-10-12 11:51:35] [info] Cores Used: 8
[2025-10-12 11:51:35] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```



```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Top Cell Information
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Layout Top Cell:      TOP
[2025-10-12 11:51:35] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:51:35] [info] Database Format:       GDSII
[2025-10-12 11:51:35] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Type of Errors | Count
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] DB_WARN -> 1
[2025-10-12 11:51:35] [info] LRF_ERR001 -> 14
[2025-10-12 11:51:35] [info] DB_WARN002 -> 159
[2025-10-12 11:51:35] [info] DB_WARN003 -> 161
[2025-10-12 11:51:35] [info] DRC_ERR9 -> 89
[2025-10-12 11:51:35] [info] DRC_ERR -> 67
[2025-10-12 11:51:35] [info] DRC_ERR5 -> 408
[2025-10-12 11:51:35] [info] DRC_ERR4 -> 241
[2025-10-12 11:51:35] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Rule Violation Summary
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:51:35] [info] |-----|-----|-----|
[2025-10-12 11:51:35] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:51:35] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:51:35] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:51:35] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:51:35] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:51:35] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:51:35] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:51:35] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:51:35] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:51:35] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:51:35] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:51:35] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:51:35] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:51:35] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:51:35] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:51:35] [info] |
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Performance Metrics
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] | Stage          | Time Taken |
[2025-10-12 11:51:35] [info] |-----|-----|
[2025-10-12 11:51:35] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:51:35] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 11:51:35] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Summary
```

```
[2025-10-12 11:51:35] [info] -----
[2025-10-12 11:51:35] [info] Total Errors:      1120
[2025-10-12 11:51:35] [info] Total Warnings:    321
[2025-10-12 11:51:35] [info] DRC Status:        FAILED
```

lvr_summary_75.rpt

```
[2025-10-12 11:52:30] [info] =====
[2025-10-12 11:52:30] [info]                      Design Rule Check Report
[2025-10-12 11:52:30] [info] =====

[2025-10-12 11:52:30] [info] Report Generated:    2025-10-12
[2025-10-12 11:52:30] [info] Run Start Time:     11:51:39 AM
[2025-10-12 11:52:30] [info] Run End Time:       11:52:30 AM
[2025-10-12 11:52:30] [info] Time Taken:         0 minutes 50 seconds

[2025-10-12 11:52:30] [info] Tool Version:       LVR_2025.1
[2025-10-12 11:52:30] [info] Host Name:          ip-172-31-37-173
[2025-10-12 11:52:30] [info] User Name:          ubuntu
[2025-10-12 11:52:30] [info] Cores Used:         8
[2025-10-12 11:52:30] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info]                      Top Cell Information
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info] Layout Top Cell:    TOP
[2025-10-12 11:52:30] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 11:52:30] [info] Database Format:      GDSII
[2025-10-12 11:52:30] [info] Verification Mode:   Full Check

[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info]                      Type of Errors | Count
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info] DB_WARN -> 1
[2025-10-12 11:52:30] [info] LRF_ERR001 -> 14
[2025-10-12 11:52:30] [info] DB_WARN002 -> 159
[2025-10-12 11:52:30] [info] DB_WARN003 -> 161
[2025-10-12 11:52:30] [info] DRC_ERR9 -> 89
[2025-10-12 11:52:30] [info] DRC_ERR -> 67
[2025-10-12 11:52:30] [info] DRC_ERR5 -> 408
[2025-10-12 11:52:30] [info] DRC_ERR4 -> 241
[2025-10-12 11:52:30] [info] DRC_ERR1 -> 301
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info]                      Rule Violation Summary
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:52:30] [info] |-----|-----|-----|
[2025-10-12 11:52:30] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:52:30] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:52:30] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:52:30] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:52:30] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:52:30] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:52:30] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:52:30] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:52:30] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:52:30] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:52:30] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:52:30] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:52:30] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:52:30] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:52:30] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:52:30] [info]
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info]                      Performance Metrics
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:52:30] [info] |-----|-----|
[2025-10-12 11:52:30] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:52:30] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:52:30] [info] | Report Generation      | 0 minutes 0 seconds  |

[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info]                Summary
[2025-10-12 11:52:30] [info] -----
[2025-10-12 11:52:30] [info] Total Errors:    1120
[2025-10-12 11:52:30] [info] Total Warnings:    321
[2025-10-12 11:52:30] [info] DRC Status:      FAILED
```

lvr_summary_76.rpt

```
[2025-10-12 11:53:28] [info] =====
[2025-10-12 11:53:28] [info]                Design Rule Check Report
[2025-10-12 11:53:28] [info] =====

[2025-10-12 11:53:28] [info] Report Generated:    2025-10-12
[2025-10-12 11:53:28] [info] Run Start Time:      11:52:37 AM
[2025-10-12 11:53:28] [info] Run End Time:        11:53:28 AM
[2025-10-12 11:53:28] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 11:53:28] [info] Tool Version:        LVR_2025.1
[2025-10-12 11:53:28] [info] Host Name:           ip-172-31-37-173
[2025-10-12 11:53:28] [info] User Name:           ubuntu
[2025-10-12 11:53:28] [info] Cores Used:          8
[2025-10-12 11:53:28] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info]                Top Cell Information
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] Layout Top Cell:     TOP
[2025-10-12 11:53:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:53:28] [info] Database Format:       GDSII
[2025-10-12 11:53:28] [info] Verification Mode:    Full Check

[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info]                Type of Errors | Count
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] DB_WARN -> 1
[2025-10-12 11:53:28] [info] LRF_ERR001 -> 14
[2025-10-12 11:53:28] [info] DB_WARN002 -> 159
[2025-10-12 11:53:28] [info] DB_WARN003 -> 161
[2025-10-12 11:53:28] [info] DRC_ERR9 -> 89
[2025-10-12 11:53:28] [info] DRC_ERR -> 67
[2025-10-12 11:53:28] [info] DRC_ERR5 -> 408
[2025-10-12 11:53:28] [info] DRC_ERR4 -> 241
[2025-10-12 11:53:28] [info] DRC_ERR1 -> 301
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info]                Rule Violation Summary
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 11:53:28] [info] |-----|-----|-----|
[2025-10-12 11:53:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:53:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:53:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:53:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:53:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 11:53:28] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2 | 59 |
[2025-10-12 11:53:28] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:53:28] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:53:28] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:53:28] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:53:28] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:53:28] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:53:28] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:53:28] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:53:28] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:53:28] [info]
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] Performance Metrics
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] | Stage | Time Taken |
[2025-10-12 11:53:28] [info] |-----|-----|
[2025-10-12 11:53:28] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:53:28] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 11:53:28] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] Summary
[2025-10-12 11:53:28] [info] -----
[2025-10-12 11:53:28] [info] Total Errors: 1120
[2025-10-12 11:53:28] [info] Total Warnings: 321
[2025-10-12 11:53:28] [info] DRC Status: FAILED
```

lvr_summary_77.rpt

```
[2025-10-12 11:54:24] [info] =====
[2025-10-12 11:54:24] [info] Design Rule Check Report
[2025-10-12 11:54:24] [info] =====

[2025-10-12 11:54:24] [info] Report Generated: 2025-10-12
[2025-10-12 11:54:24] [info] Run Start Time: 11:53:33 AM
[2025-10-12 11:54:24] [info] Run End Time: 11:54:24 AM
[2025-10-12 11:54:24] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:54:24] [info] Tool Version: LVR_2025.1
[2025-10-12 11:54:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:54:24] [info] User Name: ubuntu
[2025-10-12 11:54:24] [info] Cores Used: 8
[2025-10-12 11:54:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Top Cell Information
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Layout Top Cell: TOP
[2025-10-12 11:54:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:54:24] [info] Database Format: GDSII
[2025-10-12 11:54:24] [info] Verification Mode: Full Check

[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Type of Errors | Count
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] DB_WARN -> 1
[2025-10-12 11:54:24] [info] LRF_ERR001 -> 14
[2025-10-12 11:54:24] [info] DB_WARN002 -> 159
[2025-10-12 11:54:24] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:54:24] [info] DRC_ERR9 -> 89
[2025-10-12 11:54:24] [info] DRC_ERR -> 67
[2025-10-12 11:54:24] [info] DRC_ERR5 -> 408
[2025-10-12 11:54:24] [info] DRC_ERR4 -> 241
[2025-10-12 11:54:24] [info] DRC_ERR1 -> 301
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Rule Violation Summary
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:54:24] [info] |-----|-----|-----|
[2025-10-12 11:54:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:54:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:54:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:54:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:54:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:54:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:54:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:54:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:54:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:54:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:54:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:54:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:54:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:54:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:54:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Performance Metrics
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] | Stage | Time Taken |
[2025-10-12 11:54:24] [info] |-----|-----|
[2025-10-12 11:54:24] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:54:24] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 11:54:24] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Summary
[2025-10-12 11:54:24] [info] -----
[2025-10-12 11:54:24] [info] Total Errors: 1120
[2025-10-12 11:54:24] [info] Total Warnings: 321
[2025-10-12 11:54:24] [info] DRC Status: FAILED
```

lvr_summary_78.rpt

```
[2025-10-12 11:55:22] [info] =====
[2025-10-12 11:55:22] [info] Design Rule Check Report
[2025-10-12 11:55:22] [info] =====

[2025-10-12 11:55:22] [info] Report Generated: 2025-10-12
[2025-10-12 11:55:22] [info] Run Start Time: 11:54:31 AM
[2025-10-12 11:55:22] [info] Run End Time: 11:55:22 AM
[2025-10-12 11:55:22] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:55:22] [info] Tool Version: LVR_2025.1
[2025-10-12 11:55:22] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:55:22] [info] User Name: ubuntu
[2025-10-12 11:55:22] [info] Cores Used: 8
[2025-10-12 11:55:22] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Top Cell Information
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Layout Top Cell:      TOP
[2025-10-12 11:55:22] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:55:22] [info] Database Format:      GDSII
[2025-10-12 11:55:22] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Type of Errors | Count
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] DB_WARN -> 1
[2025-10-12 11:55:22] [info] LRF_ERR001 -> 14
[2025-10-12 11:55:22] [info] DB_WARN002 -> 159
[2025-10-12 11:55:22] [info] DB_WARN003 -> 161
[2025-10-12 11:55:22] [info] DRC_ERR9 -> 89
[2025-10-12 11:55:22] [info] DRC_ERR -> 67
[2025-10-12 11:55:22] [info] DRC_ERR5 -> 408
[2025-10-12 11:55:22] [info] DRC_ERR4 -> 241
[2025-10-12 11:55:22] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Rule Violation Summary
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:55:22] [info] |-----|-----|-----|
[2025-10-12 11:55:22] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:55:22] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:55:22] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:55:22] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:55:22] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:55:22] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:55:22] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:55:22] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:55:22] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:55:22] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:55:22] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:55:22] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:55:22] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:55:22] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:55:22] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Performance Metrics
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] | Stage          | Time Taken |
[2025-10-12 11:55:22] [info] |-----|-----|
[2025-10-12 11:55:22] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:55:22] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 11:55:22] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Summary
[2025-10-12 11:55:22] [info] -----
[2025-10-12 11:55:22] [info] Total Errors:      1120
[2025-10-12 11:55:22] [info] Total Warnings:    321
[2025-10-12 11:55:22] [info] DRC Status:        FAILED
```

lvr_summary_79.rpt

```
[2025-10-12 11:56:19] [info] =====
[2025-10-12 11:56:19] [info]                      Design Rule Check Report
[2025-10-12 11:56:19] [info] =====

[2025-10-12 11:56:19] [info] Report Generated:      2025-10-12
[2025-10-12 11:56:19] [info] Run Start Time:       11:55:27 AM
[2025-10-12 11:56:19] [info] Run End Time:         11:56:19 AM
[2025-10-12 11:56:19] [info] Time Taken:           0 minutes 51 seconds

[2025-10-12 11:56:19] [info] Tool Version:         LVR_2025.1
[2025-10-12 11:56:19] [info] Host Name:            ip-172-31-37-173
[2025-10-12 11:56:19] [info] User Name:            ubuntu
[2025-10-12 11:56:19] [info] Cores Used:           8
[2025-10-12 11:56:19] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info]                      Top Cell Information
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info] Layout Top Cell:      TOP
[2025-10-12 11:56:19] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 11:56:19] [info] Database Format:        GDSII
[2025-10-12 11:56:19] [info] Verification Mode:     Full Check

[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info]                      Type of Errors | Count
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info] DB_WARN -> 1
[2025-10-12 11:56:19] [info] LRF_ERR001 -> 14
[2025-10-12 11:56:19] [info] DB_WARN002 -> 159
[2025-10-12 11:56:19] [info] DB_WARN003 -> 161
[2025-10-12 11:56:19] [info] DRC_ERR9 -> 89
[2025-10-12 11:56:19] [info] DRC_ERR -> 67
[2025-10-12 11:56:19] [info] DRC_ERR5 -> 408
[2025-10-12 11:56:19] [info] DRC_ERR4 -> 241
[2025-10-12 11:56:19] [info] DRC_ERR1 -> 301
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info]                      Rule Violation Summary
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:56:19] [info] |-----|-----|-----|
[2025-10-12 11:56:19] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:56:19] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:56:19] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:56:19] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:56:19] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:56:19] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:56:19] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:56:19] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:56:19] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:56:19] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:56:19] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:56:19] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:56:19] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:56:19] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:56:19] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:56:19] [info]
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info]                      Performance Metrics
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:56:19] [info] |-----|-----|
[2025-10-12 11:56:19] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:56:19] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 11:56:19] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info]                Summary
[2025-10-12 11:56:19] [info] -----
[2025-10-12 11:56:19] [info] Total Errors:    1120
[2025-10-12 11:56:19] [info] Total Warnings:    321
[2025-10-12 11:56:19] [info] DRC Status:      FAILED
```

lvr_summary_8.rpt

```
[2025-10-12 10:49:01] [info] =====
[2025-10-12 10:49:01] [info]                Design Rule Check Report
[2025-10-12 10:49:01] [info] =====

[2025-10-12 10:49:01] [info] Report Generated:    2025-10-12
[2025-10-12 10:49:01] [info] Run Start Time:      10:48:10 AM
[2025-10-12 10:49:01] [info] Run End Time:        10:49:01 AM
[2025-10-12 10:49:01] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 10:49:01] [info] Tool Version:        LVR_2025.1
[2025-10-12 10:49:01] [info] Host Name:           ip-172-31-37-173
[2025-10-12 10:49:01] [info] User Name:           ubuntu
[2025-10-12 10:49:01] [info] Cores Used:          8
[2025-10-12 10:49:01] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info]                Top Cell Information
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] Layout Top Cell:     TOP
[2025-10-12 10:49:01] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 10:49:01] [info] Database Format:       GDSII
[2025-10-12 10:49:01] [info] Verification Mode:    Full Check

[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info]                Type of Errors | Count
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] DB_WARN -> 1
[2025-10-12 10:49:01] [info] LRF_ERR001 -> 14
[2025-10-12 10:49:01] [info] DB_WARN002 -> 159
[2025-10-12 10:49:01] [info] DB_WARN003 -> 161
[2025-10-12 10:49:01] [info] DRC_ERR9 -> 89
[2025-10-12 10:49:01] [info] DRC_ERR -> 67
[2025-10-12 10:49:01] [info] DRC_ERR5 -> 408
[2025-10-12 10:49:01] [info] DRC_ERR4 -> 241
[2025-10-12 10:49:01] [info] DRC_ERR1 -> 301
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info]                Rule Violation Summary
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 10:49:01] [info] |-----|-----|-----|
[2025-10-12 10:49:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 10:49:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 10:49:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 10:49:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 10:49:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```



```
[2025-10-12 10:49:01] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 10:49:01] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:49:01] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:49:01] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:49:01] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:49:01] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:49:01] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:49:01] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:49:01] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:49:01] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:49:01] [info]
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] Performance Metrics
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] | Stage | Time Taken |
[2025-10-12 10:49:01] [info] |-----|-----|
[2025-10-12 10:49:01] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 10:49:01] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 10:49:01] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] Summary
[2025-10-12 10:49:01] [info] -----
[2025-10-12 10:49:01] [info] Total Errors: 1120
[2025-10-12 10:49:01] [info] Total Warnings: 321
[2025-10-12 10:49:01] [info] DRC Status: FAILED
```

lvr_summary_80.rpt

```
[2025-10-12 11:57:17] [info] =====
[2025-10-12 11:57:17] [info] Design Rule Check Report
[2025-10-12 11:57:17] [info] =====

[2025-10-12 11:57:17] [info] Report Generated: 2025-10-12
[2025-10-12 11:57:17] [info] Run Start Time: 11:56:25 AM
[2025-10-12 11:57:17] [info] Run End Time: 11:57:17 AM
[2025-10-12 11:57:17] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:57:17] [info] Tool Version: LVR_2025.1
[2025-10-12 11:57:17] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:57:17] [info] User Name: ubuntu
[2025-10-12 11:57:17] [info] Cores Used: 8
[2025-10-12 11:57:17] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Top Cell Information
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Layout Top Cell: TOP
[2025-10-12 11:57:17] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:57:17] [info] Database Format: GDSII
[2025-10-12 11:57:17] [info] Verification Mode: Full Check

[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Type of Errors | Count
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] DB_WARN -> 1
[2025-10-12 11:57:17] [info] LRF_ERR001 -> 14
[2025-10-12 11:57:17] [info] DB_WARN002 -> 159
[2025-10-12 11:57:17] [info] DB_WARN003 -> 161
```

```
[2025-10-12 11:57:17] [info] DRC_ERR9 -> 89
[2025-10-12 11:57:17] [info] DRC_ERR -> 67
[2025-10-12 11:57:17] [info] DRC_ERR5 -> 408
[2025-10-12 11:57:17] [info] DRC_ERR4 -> 241
[2025-10-12 11:57:17] [info] DRC_ERR1 -> 301
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Rule Violation Summary
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:57:17] [info] |-----|-----|-----|
[2025-10-12 11:57:17] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:57:17] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:57:17] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:57:17] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:57:17] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:57:17] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 11:57:17] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:57:17] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:57:17] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:57:17] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:57:17] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:57:17] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:57:17] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:57:17] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:57:17] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Performance Metrics
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] | Stage | Time Taken |
[2025-10-12 11:57:17] [info] |-----|-----|
[2025-10-12 11:57:17] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 11:57:17] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 11:57:17] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Summary
[2025-10-12 11:57:17] [info] -----
[2025-10-12 11:57:17] [info] Total Errors: 1120
[2025-10-12 11:57:17] [info] Total Warnings: 321
[2025-10-12 11:57:17] [info] DRC Status: FAILED
```

lvr_summary_81.rpt

```
[2025-10-12 11:58:13] [info] =====
[2025-10-12 11:58:13] [info] Design Rule Check Report
[2025-10-12 11:58:13] [info] =====

[2025-10-12 11:58:13] [info] Report Generated: 2025-10-12
[2025-10-12 11:58:13] [info] Run Start Time: 11:57:22 AM
[2025-10-12 11:58:13] [info] Run End Time: 11:58:13 AM
[2025-10-12 11:58:13] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 11:58:13] [info] Tool Version: LVR_2025.1
[2025-10-12 11:58:13] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:58:13] [info] User Name: ubuntu
[2025-10-12 11:58:13] [info] Cores Used: 8
[2025-10-12 11:58:13] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Top Cell Information
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Layout Top Cell:      TOP
[2025-10-12 11:58:13] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 11:58:13] [info] Database Format:       GDSII
[2025-10-12 11:58:13] [info] Verification Mode:    Full Check
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Type of Errors | Count
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] DB_WARN -> 1
[2025-10-12 11:58:13] [info] LRF_ERR001 -> 14
[2025-10-12 11:58:13] [info] DB_WARN002 -> 159
[2025-10-12 11:58:13] [info] DB_WARN003 -> 161
[2025-10-12 11:58:13] [info] DRC_ERR9 -> 89
[2025-10-12 11:58:13] [info] DRC_ERR -> 67
[2025-10-12 11:58:13] [info] DRC_ERR5 -> 408
[2025-10-12 11:58:13] [info] DRC_ERR4 -> 241
[2025-10-12 11:58:13] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Rule Violation Summary
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 11:58:13] [info] |-----|-----|-----|
[2025-10-12 11:58:13] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 11:58:13] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 11:58:13] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 11:58:13] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 11:58:13] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 11:58:13] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 11:58:13] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 11:58:13] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 11:58:13] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 11:58:13] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 11:58:13] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 11:58:13] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 11:58:13] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 11:58:13] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 11:58:13] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 11:58:13] [info] |
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Performance Metrics
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] | Stage          | Time Taken |
[2025-10-12 11:58:13] [info] |-----|-----|
[2025-10-12 11:58:13] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 11:58:13] [info] | Rule Checking   | 0 minutes 41 seconds |
[2025-10-12 11:58:13] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Summary
```

```
[2025-10-12 11:58:13] [info] -----
[2025-10-12 11:58:13] [info] Total Errors:      1120
[2025-10-12 11:58:13] [info] Total Warnings:    321
[2025-10-12 11:58:13] [info] DRC Status:        FAILED
```

lvr_summary_82.rpt

```
[2025-10-12 11:59:09] [info] =====
[2025-10-12 11:59:09] [info] Design Rule Check Report
[2025-10-12 11:59:09] [info] =====

[2025-10-12 11:59:09] [info] Report Generated: 2025-10-12
[2025-10-12 11:59:09] [info] Run Start Time: 11:58:19 AM
[2025-10-12 11:59:09] [info] Run End Time: 11:59:09 AM
[2025-10-12 11:59:09] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 11:59:09] [info] Tool Version: LVR_2025.1
[2025-10-12 11:59:09] [info] Host Name: ip-172-31-37-173
[2025-10-12 11:59:09] [info] User Name: ubuntu
[2025-10-12 11:59:09] [info] Cores Used: 8
[2025-10-12 11:59:09] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Top Cell Information
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Layout Top Cell: TOP
[2025-10-12 11:59:09] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 11:59:09] [info] Database Format: GDSII
[2025-10-12 11:59:09] [info] Verification Mode: Full Check

[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Type of Errors | Count
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] DB_WARN -> 1
[2025-10-12 11:59:09] [info] LRF_ERR001 -> 14
[2025-10-12 11:59:09] [info] DB_WARN002 -> 159
[2025-10-12 11:59:09] [info] DB_WARN003 -> 161
[2025-10-12 11:59:09] [info] DRC_ERR9 -> 89
[2025-10-12 11:59:09] [info] DRC_ERR -> 67
[2025-10-12 11:59:09] [info] DRC_ERR5 -> 408
[2025-10-12 11:59:09] [info] DRC_ERR4 -> 241
[2025-10-12 11:59:09] [info] DRC_ERR1 -> 301
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Rule Violation Summary
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] | Rule ID | Description | Error Count |
[2025-10-12 11:59:09] [info] |-----|-----|-----|
[2025-10-12 11:59:09] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 11:59:09] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 11:59:09] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 11:59:09] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 11:59:09] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 11:59:09] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 11:59:09] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 11:59:09] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 11:59:09] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 11:59:09] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 11:59:09] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 11:59:09] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 11:59:09] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 11:59:09] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 11:59:09] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 11:59:09] [info]
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Performance Metrics
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] | Stage | Time Taken |
```

```
[2025-10-12 11:59:09] [info] |-----|-----|
[2025-10-12 11:59:09] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 11:59:09] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 11:59:09] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Summary
[2025-10-12 11:59:09] [info] -----
[2025-10-12 11:59:09] [info] Total Errors: 1120
[2025-10-12 11:59:09] [info] Total Warnings: 321
[2025-10-12 11:59:09] [info] DRC Status: FAILED
```

lvr_summary_83.rpt

```
[2025-10-12 12:00:06] [info] =====
[2025-10-12 12:00:06] [info] Design Rule Check Report
[2025-10-12 12:00:06] [info] =====

[2025-10-12 12:00:06] [info] Report Generated: 2025-10-12
[2025-10-12 12:00:06] [info] Run Start Time: 11:59:15 AM
[2025-10-12 12:00:06] [info] Run End Time: 12:00:06 PM
[2025-10-12 12:00:06] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:00:06] [info] Tool Version: LVR_2025.1
[2025-10-12 12:00:06] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:00:06] [info] User Name: ubuntu
[2025-10-12 12:00:06] [info] Cores Used: 8
[2025-10-12 12:00:06] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Top Cell Information
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Layout Top Cell: TOP
[2025-10-12 12:00:06] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:00:06] [info] Database Format: GDSII
[2025-10-12 12:00:06] [info] Verification Mode: Full Check

[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Type of Errors | Count
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] DB_WARN -> 1
[2025-10-12 12:00:06] [info] LRF_ERR001 -> 14
[2025-10-12 12:00:06] [info] DB_WARN002 -> 159
[2025-10-12 12:00:06] [info] DB_WARN003 -> 161
[2025-10-12 12:00:06] [info] DRC_ERR9 -> 89
[2025-10-12 12:00:06] [info] DRC_ERR -> 67
[2025-10-12 12:00:06] [info] DRC_ERR5 -> 408
[2025-10-12 12:00:06] [info] DRC_ERR4 -> 241
[2025-10-12 12:00:06] [info] DRC_ERR1 -> 301
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Rule Violation Summary
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:00:06] [info] |-----|-----|-----|
[2025-10-12 12:00:06] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:00:06] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:00:06] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:00:06] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:00:06] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 12:00:06] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:00:06] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:00:06] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:00:06] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:00:06] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:00:06] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:00:06] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:00:06] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:00:06] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:00:06] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:00:06] [info]
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Performance Metrics
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] | Stage | Time Taken |
[2025-10-12 12:00:06] [info] |-----|-----|
[2025-10-12 12:00:06] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:00:06] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:00:06] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Summary
[2025-10-12 12:00:06] [info] -----
[2025-10-12 12:00:06] [info] Total Errors: 1120
[2025-10-12 12:00:06] [info] Total Warnings: 321
[2025-10-12 12:00:06] [info] DRC Status: FAILED
```

lvr_summary_84.rpt

```
[2025-10-12 12:01:05] [info] =====
[2025-10-12 12:01:05] [info] Design Rule Check Report
[2025-10-12 12:01:05] [info] =====

[2025-10-12 12:01:05] [info] Report Generated: 2025-10-12
[2025-10-12 12:01:05] [info] Run Start Time: 12:00:13 PM
[2025-10-12 12:01:05] [info] Run End Time: 12:01:05 PM
[2025-10-12 12:01:05] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 12:01:05] [info] Tool Version: LVR_2025.1
[2025-10-12 12:01:05] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:01:05] [info] User Name: ubuntu
[2025-10-12 12:01:05] [info] Cores Used: 8
[2025-10-12 12:01:05] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Top Cell Information
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Layout Top Cell: TOP
[2025-10-12 12:01:05] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:01:05] [info] Database Format: GDSII
[2025-10-12 12:01:05] [info] Verification Mode: Full Check

[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Type of Errors | Count
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] DB_WARN -> 1
[2025-10-12 12:01:05] [info] LRF_ERR001 -> 14
[2025-10-12 12:01:05] [info] DB_WARN002 -> 159
[2025-10-12 12:01:05] [info] DB_WARN003 -> 161
```

```
[2025-10-12 12:01:05] [info] DRC_ERR9 -> 89
[2025-10-12 12:01:05] [info] DRC_ERR -> 67
[2025-10-12 12:01:05] [info] DRC_ERR5 -> 408
[2025-10-12 12:01:05] [info] DRC_ERR4 -> 241
[2025-10-12 12:01:05] [info] DRC_ERR1 -> 301
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Rule Violation Summary
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:01:05] [info] |-----|-----|-----|
[2025-10-12 12:01:05] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:01:05] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:01:05] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:01:05] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:01:05] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:01:05] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:01:05] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:01:05] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:01:05] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:01:05] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:01:05] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:01:05] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:01:05] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:01:05] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:01:05] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Performance Metrics
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] | Stage | Time Taken |
[2025-10-12 12:01:05] [info] |-----|-----|
[2025-10-12 12:01:05] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:01:05] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:01:05] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Summary
[2025-10-12 12:01:05] [info] -----
[2025-10-12 12:01:05] [info] Total Errors: 1120
[2025-10-12 12:01:05] [info] Total Warnings: 321
[2025-10-12 12:01:05] [info] DRC Status: FAILED
```

lvr_summary_85.rpt

```
[2025-10-12 12:02:01] [info] =====
[2025-10-12 12:02:01] [info] Design Rule Check Report
[2025-10-12 12:02:01] [info] =====

[2025-10-12 12:02:01] [info] Report Generated: 2025-10-12
[2025-10-12 12:02:01] [info] Run Start Time: 12:01:09 PM
[2025-10-12 12:02:01] [info] Run End Time: 12:02:01 PM
[2025-10-12 12:02:01] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 12:02:01] [info] Tool Version: LVR_2025.1
[2025-10-12 12:02:01] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:02:01] [info] User Name: ubuntu
[2025-10-12 12:02:01] [info] Cores Used: 8
[2025-10-12 12:02:01] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Top Cell Information
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Layout Top Cell:      TOP
[2025-10-12 12:02:01] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:02:01] [info] Database Format:      GDSII
[2025-10-12 12:02:01] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Type of Errors | Count
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] DB_WARN -> 1
[2025-10-12 12:02:01] [info] LRF_ERR001 -> 14
[2025-10-12 12:02:01] [info] DB_WARN002 -> 159
[2025-10-12 12:02:01] [info] DB_WARN003 -> 161
[2025-10-12 12:02:01] [info] DRC_ERR9 -> 89
[2025-10-12 12:02:01] [info] DRC_ERR -> 67
[2025-10-12 12:02:01] [info] DRC_ERR5 -> 408
[2025-10-12 12:02:01] [info] DRC_ERR4 -> 241
[2025-10-12 12:02:01] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Rule Violation Summary
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:02:01] [info] |-----|-----|-----|
[2025-10-12 12:02:01] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:02:01] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:02:01] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:02:01] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:02:01] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:02:01] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:02:01] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:02:01] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:02:01] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:02:01] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:02:01] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:02:01] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvvp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:02:01] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:02:01] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:02:01] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:02:01] [info] |
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Performance Metrics
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] | Stage          | Time Taken |
[2025-10-12 12:02:01] [info] |-----|-----|
[2025-10-12 12:02:01] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:02:01] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:02:01] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Summary
```

```
[2025-10-12 12:02:01] [info] -----
[2025-10-12 12:02:01] [info] Total Errors:      1120
[2025-10-12 12:02:01] [info] Total Warnings:    321
[2025-10-12 12:02:01] [info] DRC Status:        FAILED
```


lvr_summary_86.rpt

```
[2025-10-12 12:02:59] [info] =====
[2025-10-12 12:02:59] [info]                      Design Rule Check Report
[2025-10-12 12:02:59] [info] =====

[2025-10-12 12:02:59] [info] Report Generated:      2025-10-12
[2025-10-12 12:02:59] [info] Run Start Time:       12:02:08 PM
[2025-10-12 12:02:59] [info] Run End Time:         12:02:59 PM
[2025-10-12 12:02:59] [info] Time Taken:           0 minutes 50 seconds

[2025-10-12 12:02:59] [info] Tool Version:         LVR_2025.1
[2025-10-12 12:02:59] [info] Host Name:            ip-172-31-37-173
[2025-10-12 12:02:59] [info] User Name:            ubuntu
[2025-10-12 12:02:59] [info] Cores Used:           8
[2025-10-12 12:02:59] [info] Memory Utilized:      670.22 MB / 61.77812957763672 GB

[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info]                      Top Cell Information
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info] Layout Top Cell:      TOP
[2025-10-12 12:02:59] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 12:02:59] [info] Database Format:       GDSII
[2025-10-12 12:02:59] [info] Verification Mode:     Full Check

[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info]                      Type of Errors | Count
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info] DB_WARN -> 1
[2025-10-12 12:02:59] [info] LRF_ERR001 -> 14
[2025-10-12 12:02:59] [info] DB_WARN002 -> 159
[2025-10-12 12:02:59] [info] DB_WARN003 -> 161
[2025-10-12 12:02:59] [info] DRC_ERR9 -> 89
[2025-10-12 12:02:59] [info] DRC_ERR -> 67
[2025-10-12 12:02:59] [info] DRC_ERR5 -> 408
[2025-10-12 12:02:59] [info] DRC_ERR4 -> 241
[2025-10-12 12:02:59] [info] DRC_ERR1 -> 301
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info]                      Rule Violation Summary
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:02:59] [info] |-----|-----|-----|
[2025-10-12 12:02:59] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:02:59] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:02:59] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:02:59] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:02:59] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:02:59] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:02:59] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:02:59] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:02:59] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:02:59] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:02:59] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:02:59] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:02:59] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:02:59] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:02:59] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:02:59] [info]
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info]                      Performance Metrics
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:02:59] [info] |-----|-----|
[2025-10-12 12:02:59] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 12:02:59] [info] | Rule Checking       | 0 minutes 40 seconds |
[2025-10-12 12:02:59] [info] | Report Generation   | 0 minutes 0 seconds |

[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info]                Summary
[2025-10-12 12:02:59] [info] -----
[2025-10-12 12:02:59] [info] Total Errors:      1120
[2025-10-12 12:02:59] [info] Total Warnings:    321
[2025-10-12 12:02:59] [info] DRC Status:        FAILED
```

lvr_summary_87.rpt

```
[2025-10-12 12:03:56] [info] =====
[2025-10-12 12:03:56] [info]                Design Rule Check Report
[2025-10-12 12:03:56] [info] =====

[2025-10-12 12:03:56] [info] Report Generated:   2025-10-12
[2025-10-12 12:03:56] [info] Run Start Time:     12:03:04 PM
[2025-10-12 12:03:56] [info] Run End Time:       12:03:56 PM
[2025-10-12 12:03:56] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 12:03:56] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:03:56] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:03:56] [info] User Name:          ubuntu
[2025-10-12 12:03:56] [info] Cores Used:         8
[2025-10-12 12:03:56] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info]                Top Cell Information
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] Layout Top Cell:    TOP
[2025-10-12 12:03:56] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:03:56] [info] Database Format:      GDSII
[2025-10-12 12:03:56] [info] Verification Mode:   Full Check

[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info]                Type of Errors | Count
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] DB_WARN -> 1
[2025-10-12 12:03:56] [info] LRF_ERR001 -> 14
[2025-10-12 12:03:56] [info] DB_WARN002 -> 159
[2025-10-12 12:03:56] [info] DB_WARN003 -> 161
[2025-10-12 12:03:56] [info] DRC_ERR9 -> 89
[2025-10-12 12:03:56] [info] DRC_ERR -> 67
[2025-10-12 12:03:56] [info] DRC_ERR5 -> 408
[2025-10-12 12:03:56] [info] DRC_ERR4 -> 241
[2025-10-12 12:03:56] [info] DRC_ERR1 -> 301
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info]                Rule Violation Summary
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:03:56] [info] |-----|-----|-----|
[2025-10-12 12:03:56] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:03:56] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:03:56] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:03:56] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:03:56] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 12:03:56] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:03:56] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:03:56] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:03:56] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:03:56] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:03:56] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:03:56] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:03:56] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:03:56] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:03:56] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:03:56] [info]
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] Performance Metrics
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] | Stage | Time Taken |
[2025-10-12 12:03:56] [info] |-----|-----|
[2025-10-12 12:03:56] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:03:56] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:03:56] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] Summary
[2025-10-12 12:03:56] [info] -----
[2025-10-12 12:03:56] [info] Total Errors: 1120
[2025-10-12 12:03:56] [info] Total Warnings: 321
[2025-10-12 12:03:56] [info] DRC Status: FAILED
```

lvr_summary_88.rpt

```
[2025-10-12 12:04:54] [info] =====
[2025-10-12 12:04:54] [info] Design Rule Check Report
[2025-10-12 12:04:54] [info] =====

[2025-10-12 12:04:54] [info] Report Generated: 2025-10-12
[2025-10-12 12:04:54] [info] Run Start Time: 12:04:02 PM
[2025-10-12 12:04:54] [info] Run End Time: 12:04:54 PM
[2025-10-12 12:04:54] [info] Time Taken: 0 minutes 52 seconds

[2025-10-12 12:04:54] [info] Tool Version: LVR_2025.1
[2025-10-12 12:04:54] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:04:54] [info] User Name: ubuntu
[2025-10-12 12:04:54] [info] Cores Used: 8
[2025-10-12 12:04:54] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Top Cell Information
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Layout Top Cell: TOP
[2025-10-12 12:04:54] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:04:54] [info] Database Format: GDSII
[2025-10-12 12:04:54] [info] Verification Mode: Full Check

[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Type of Errors | Count
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] DB_WARN -> 1
[2025-10-12 12:04:54] [info] LRF_ERR001 -> 14
[2025-10-12 12:04:54] [info] DB_WARN002 -> 159
[2025-10-12 12:04:54] [info] DB_WARN003 -> 161
```

```
[2025-10-12 12:04:54] [info] DRC_ERR9 -> 89
[2025-10-12 12:04:54] [info] DRC_ERR -> 67
[2025-10-12 12:04:54] [info] DRC_ERR5 -> 408
[2025-10-12 12:04:54] [info] DRC_ERR4 -> 241
[2025-10-12 12:04:54] [info] DRC_ERR1 -> 301
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Rule Violation Summary
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:04:54] [info] |-----|-----|-----|
[2025-10-12 12:04:54] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:04:54] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:04:54] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:04:54] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:04:54] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:04:54] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um2" | 59          |
[2025-10-12 12:04:54] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:04:54] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:04:54] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:04:54] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:04:54] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:04:54] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:04:54] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:04:54] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:04:54] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Performance Metrics
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] | Stage          | Time Taken |
[2025-10-12 12:04:54] [info] |-----|-----|
[2025-10-12 12:04:54] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:04:54] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:04:54] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Summary
[2025-10-12 12:04:54] [info] -----
[2025-10-12 12:04:54] [info] Total Errors: 1120
[2025-10-12 12:04:54] [info] Total Warnings: 321
[2025-10-12 12:04:54] [info] DRC Status: FAILED
```

lvr_summary_89.rpt

```
[2025-10-12 12:05:49] [info] =====
[2025-10-12 12:05:49] [info] Design Rule Check Report
[2025-10-12 12:05:49] [info] =====

[2025-10-12 12:05:49] [info] Report Generated: 2025-10-12
[2025-10-12 12:05:49] [info] Run Start Time: 12:04:58 PM
[2025-10-12 12:05:49] [info] Run End Time: 12:05:49 PM
[2025-10-12 12:05:49] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:05:49] [info] Tool Version: LVR_2025.1
[2025-10-12 12:05:49] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:05:49] [info] User Name: ubuntu
[2025-10-12 12:05:49] [info] Cores Used: 8
[2025-10-12 12:05:49] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Top Cell Information
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Layout Top Cell:      TOP
[2025-10-12 12:05:49] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:05:49] [info] Database Format:      GDSII
[2025-10-12 12:05:49] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Type of Errors | Count
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] DB_WARN -> 1
[2025-10-12 12:05:49] [info] LRF_ERR001 -> 14
[2025-10-12 12:05:49] [info] DB_WARN002 -> 159
[2025-10-12 12:05:49] [info] DB_WARN003 -> 161
[2025-10-12 12:05:49] [info] DRC_ERR9 -> 89
[2025-10-12 12:05:49] [info] DRC_ERR -> 67
[2025-10-12 12:05:49] [info] DRC_ERR5 -> 408
[2025-10-12 12:05:49] [info] DRC_ERR4 -> 241
[2025-10-12 12:05:49] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Rule Violation Summary
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:05:49] [info] |-----|-----|-----|
[2025-10-12 12:05:49] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:05:49] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:05:49] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:05:49] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:05:49] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:05:49] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:05:49] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:05:49] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:05:49] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:05:49] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:05:49] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:05:49] [info] | li.1a        | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:05:49] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:05:49] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:05:49] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:05:49] [info] |
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Performance Metrics
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] | Stage          | Time Taken |
[2025-10-12 12:05:49] [info] |-----|-----|
[2025-10-12 12:05:49] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:05:49] [info] | Rule Checking  | 0 minutes 40 seconds |
[2025-10-12 12:05:49] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Summary
```

```
[2025-10-12 12:05:49] [info] -----
[2025-10-12 12:05:49] [info] Total Errors:      1120
[2025-10-12 12:05:49] [info] Total Warnings:    321
[2025-10-12 12:05:49] [info] DRC Status:        FAILED
```

lvr_summary_9.rpt

```
[2025-10-12 10:49:57] [info] =====
[2025-10-12 10:49:57] [info] Design Rule Check Report
[2025-10-12 10:49:57] [info] =====

[2025-10-12 10:49:57] [info] Report Generated: 2025-10-12
[2025-10-12 10:49:57] [info] Run Start Time: 10:49:07 AM
[2025-10-12 10:49:57] [info] Run End Time: 10:49:57 AM
[2025-10-12 10:49:57] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 10:49:57] [info] Tool Version: LVR_2025.1
[2025-10-12 10:49:57] [info] Host Name: ip-172-31-37-173
[2025-10-12 10:49:57] [info] User Name: ubuntu
[2025-10-12 10:49:57] [info] Cores Used: 8
[2025-10-12 10:49:57] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] Top Cell Information
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] Layout Top Cell: TOP
[2025-10-12 10:49:57] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 10:49:57] [info] Database Format: GDSII
[2025-10-12 10:49:57] [info] Verification Mode: Full Check

[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] Type of Errors | Count
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] DB_WARN -> 1
[2025-10-12 10:49:57] [info] LRF_ERR001 -> 14
[2025-10-12 10:49:57] [info] DB_WARN002 -> 159
[2025-10-12 10:49:57] [info] DB_WARN003 -> 161
[2025-10-12 10:49:57] [info] DRC_ERR9 -> 89
[2025-10-12 10:49:57] [info] DRC_ERR -> 67
[2025-10-12 10:49:57] [info] DRC_ERR5 -> 408
[2025-10-12 10:49:57] [info] DRC_ERR4 -> 241
[2025-10-12 10:49:57] [info] DRC_ERR1 -> 301
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] Rule Violation Summary
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] | Rule ID | Description | Error Count |
[2025-10-12 10:49:57] [info] |-----|-----|-----|
[2025-10-12 10:49:57] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 10:49:57] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 10:49:57] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 10:49:57] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 10:49:57] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 10:49:57] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 10:49:57] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 10:49:57] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 10:49:57] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 10:49:57] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 10:49:57] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 10:49:57] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 10:49:57] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 10:49:57] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 10:49:57] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 10:49:57] [info]
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] Performance Metrics
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] | Stage | Time Taken |
```

```
[2025-10-12 10:49:57] [info] |-----|-----|
[2025-10-12 10:49:57] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 10:49:57] [info] | Rule Checking          | 0 minutes 39 seconds |
[2025-10-12 10:49:57] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info]                Summary
[2025-10-12 10:49:57] [info] -----
[2025-10-12 10:49:57] [info] Total Errors:    1120
[2025-10-12 10:49:57] [info] Total Warnings:    321
[2025-10-12 10:49:57] [info] DRC Status:      FAILED
```

lvr_summary_90.rpt

```
[2025-10-12 12:06:45] [info] =====
[2025-10-12 12:06:45] [info]                Design Rule Check Report
[2025-10-12 12:06:45] [info] =====

[2025-10-12 12:06:45] [info] Report Generated:    2025-10-12
[2025-10-12 12:06:45] [info] Run Start Time:      12:05:55 PM
[2025-10-12 12:06:45] [info] Run End Time:        12:06:45 PM
[2025-10-12 12:06:45] [info] Time Taken:          0 minutes 50 seconds

[2025-10-12 12:06:45] [info] Tool Version:        LVR_2025.1
[2025-10-12 12:06:45] [info] Host Name:           ip-172-31-37-173
[2025-10-12 12:06:45] [info] User Name:           ubuntu
[2025-10-12 12:06:45] [info] Cores Used:          8
[2025-10-12 12:06:45] [info] Memory Utilized:     670.22 MB / 61.77812957763672 GB

[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info]                Top Cell Information
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] Layout Top Cell:     TOP
[2025-10-12 12:06:45] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:06:45] [info] Database Format:       GDSII
[2025-10-12 12:06:45] [info] Verification Mode:    Full Check

[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info]                Type of Errors | Count
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] DB_WARN -> 1
[2025-10-12 12:06:45] [info] LRF_ERR001 -> 14
[2025-10-12 12:06:45] [info] DB_WARN002 -> 159
[2025-10-12 12:06:45] [info] DB_WARN003 -> 161
[2025-10-12 12:06:45] [info] DRC_ERR9 -> 89
[2025-10-12 12:06:45] [info] DRC_ERR -> 67
[2025-10-12 12:06:45] [info] DRC_ERR5 -> 408
[2025-10-12 12:06:45] [info] DRC_ERR4 -> 241
[2025-10-12 12:06:45] [info] DRC_ERR1 -> 301
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info]                Rule Violation Summary
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:06:45] [info] |-----|-----|-----|
[2025-10-12 12:06:45] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:06:45] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:06:45] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:06:45] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:06:45] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 12:06:45] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:06:45] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:06:45] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:06:45] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:06:45] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:06:45] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:06:45] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:06:45] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:06:45] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:06:45] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:06:45] [info]
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] Performance Metrics
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] | Stage | Time Taken |
[2025-10-12 12:06:45] [info] |-----|-----|
[2025-10-12 12:06:45] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:06:45] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:06:45] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] Summary
[2025-10-12 12:06:45] [info] -----
[2025-10-12 12:06:45] [info] Total Errors: 1120
[2025-10-12 12:06:45] [info] Total Warnings: 321
[2025-10-12 12:06:45] [info] DRC Status: FAILED
```

lvr_summary_91.rpt

```
[2025-10-12 12:07:43] [info] =====
[2025-10-12 12:07:43] [info] Design Rule Check Report
[2025-10-12 12:07:43] [info] =====

[2025-10-12 12:07:43] [info] Report Generated: 2025-10-12
[2025-10-12 12:07:43] [info] Run Start Time: 12:06:52 PM
[2025-10-12 12:07:43] [info] Run End Time: 12:07:43 PM
[2025-10-12 12:07:43] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:07:43] [info] Tool Version: LVR_2025.1
[2025-10-12 12:07:43] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:07:43] [info] User Name: ubuntu
[2025-10-12 12:07:43] [info] Cores Used: 8
[2025-10-12 12:07:43] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Top Cell Information
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Layout Top Cell: TOP
[2025-10-12 12:07:43] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:07:43] [info] Database Format: GDSII
[2025-10-12 12:07:43] [info] Verification Mode: Full Check

[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Type of Errors | Count
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] DB_WARN -> 1
[2025-10-12 12:07:43] [info] LRF_ERR001 -> 14
[2025-10-12 12:07:43] [info] DB_WARN002 -> 159
[2025-10-12 12:07:43] [info] DB_WARN003 -> 161
```



```
[2025-10-12 12:07:43] [info] DRC_ERR9 -> 89
[2025-10-12 12:07:43] [info] DRC_ERR -> 67
[2025-10-12 12:07:43] [info] DRC_ERR5 -> 408
[2025-10-12 12:07:43] [info] DRC_ERR4 -> 241
[2025-10-12 12:07:43] [info] DRC_ERR1 -> 301
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Rule Violation Summary
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:07:43] [info] |-----|-----|-----|
[2025-10-12 12:07:43] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:07:43] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:07:43] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:07:43] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:07:43] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:07:43] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:07:43] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:07:43] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:07:43] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:07:43] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:07:43] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:07:43] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:07:43] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:07:43] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:07:43] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Performance Metrics
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] | Stage | Time Taken |
[2025-10-12 12:07:43] [info] |-----|-----|
[2025-10-12 12:07:43] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:07:43] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:07:43] [info] | Report Generation | 0 minutes 0 seconds |
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Summary
[2025-10-12 12:07:43] [info] -----
[2025-10-12 12:07:43] [info] Total Errors: 1120
[2025-10-12 12:07:43] [info] Total Warnings: 321
[2025-10-12 12:07:43] [info] DRC Status: FAILED
```

lvr_summary_92.rpt

```
[2025-10-12 12:08:39] [info] =====
[2025-10-12 12:08:39] [info] Design Rule Check Report
[2025-10-12 12:08:39] [info] =====

[2025-10-12 12:08:39] [info] Report Generated: 2025-10-12
[2025-10-12 12:08:39] [info] Run Start Time: 12:07:49 PM
[2025-10-12 12:08:39] [info] Run End Time: 12:08:39 PM
[2025-10-12 12:08:39] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:08:39] [info] Tool Version: LVR_2025.1
[2025-10-12 12:08:39] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:08:39] [info] User Name: ubuntu
[2025-10-12 12:08:39] [info] Cores Used: 8
[2025-10-12 12:08:39] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB
```

```
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Top Cell Information
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Layout Top Cell:      TOP
[2025-10-12 12:08:39] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:08:39] [info] Database Format:      GDSII
[2025-10-12 12:08:39] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Type of Errors | Count
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] DB_WARN -> 1
[2025-10-12 12:08:39] [info] LRF_ERR001 -> 14
[2025-10-12 12:08:39] [info] DB_WARN002 -> 159
[2025-10-12 12:08:39] [info] DB_WARN003 -> 161
[2025-10-12 12:08:39] [info] DRC_ERR9 -> 89
[2025-10-12 12:08:39] [info] DRC_ERR -> 67
[2025-10-12 12:08:39] [info] DRC_ERR5 -> 408
[2025-10-12 12:08:39] [info] DRC_ERR4 -> 241
[2025-10-12 12:08:39] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Rule Violation Summary
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:08:39] [info] |-----|-----|-----|
[2025-10-12 12:08:39] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:08:39] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:08:39] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:08:39] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:08:39] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:08:39] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:08:39] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:08:39] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:08:39] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:08:39] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:08:39] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:08:39] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:08:39] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:08:39] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:08:39] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
```

```
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Performance Metrics
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] | Stage          | Time Taken |
[2025-10-12 12:08:39] [info] |-----|-----|
[2025-10-12 12:08:39] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:08:39] [info] | Rule Checking  | 0 minutes 39 seconds |
[2025-10-12 12:08:39] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Summary
[2025-10-12 12:08:39] [info] -----
[2025-10-12 12:08:39] [info] Total Errors:      1120
[2025-10-12 12:08:39] [info] Total Warnings:    321
[2025-10-12 12:08:39] [info] DRC Status:        FAILED
```

lvr_summary_93.rpt

```
[2025-10-12 12:09:38] [info] =====
[2025-10-12 12:09:38] [info]                      Design Rule Check Report
[2025-10-12 12:09:38] [info] =====

[2025-10-12 12:09:38] [info] Report Generated:      2025-10-12
[2025-10-12 12:09:38] [info] Run Start Time:       12:08:45 PM
[2025-10-12 12:09:38] [info] Run End Time:         12:09:38 PM
[2025-10-12 12:09:38] [info] Time Taken:           0 minutes 52 seconds

[2025-10-12 12:09:38] [info] Tool Version:          LVR_2025.1
[2025-10-12 12:09:38] [info] Host Name:             ip-172-31-37-173
[2025-10-12 12:09:38] [info] User Name:             ubuntu
[2025-10-12 12:09:38] [info] Cores Used:            8
[2025-10-12 12:09:38] [info] Memory Utilized:       670.22 MB / 61.77812957763672 GB

[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info]                      Top Cell Information
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info] Layout Top Cell:      TOP
[2025-10-12 12:09:38] [info] Technology Node:       SKY130HD (130nm)
[2025-10-12 12:09:38] [info] Database Format:       GDSII
[2025-10-12 12:09:38] [info] Verification Mode:     Full Check

[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info]                      Type of Errors | Count
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info] DB_WARN -> 1
[2025-10-12 12:09:38] [info] LRF_ERR001 -> 14
[2025-10-12 12:09:38] [info] DB_WARN002 -> 159
[2025-10-12 12:09:38] [info] DB_WARN003 -> 161
[2025-10-12 12:09:38] [info] DRC_ERR9 -> 89
[2025-10-12 12:09:38] [info] DRC_ERR -> 67
[2025-10-12 12:09:38] [info] DRC_ERR5 -> 408
[2025-10-12 12:09:38] [info] DRC_ERR4 -> 241
[2025-10-12 12:09:38] [info] DRC_ERR1 -> 301
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info]                      Rule Violation Summary
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:09:38] [info] |-----|-----|-----|
[2025-10-12 12:09:38] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:09:38] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:09:38] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:09:38] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:09:38] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:09:38] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:09:38] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:09:38] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:09:38] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:09:38] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:09:38] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:09:38] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:09:38] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:09:38] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:09:38] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:09:38] [info]
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info]                      Performance Metrics
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info] | Stage | Time Taken |
```

```
[2025-10-12 12:09:38] [info] |-----|-----|
[2025-10-12 12:09:38] [info] | GDS Parsing          | 0 minutes 9 seconds |
[2025-10-12 12:09:38] [info] | Rule Checking       | 0 minutes 41 seconds |
[2025-10-12 12:09:38] [info] | Report Generation   | 0 minutes 0 seconds |

[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info]                Summary
[2025-10-12 12:09:38] [info] -----
[2025-10-12 12:09:38] [info] Total Errors:      1120
[2025-10-12 12:09:38] [info] Total Warnings:    321
[2025-10-12 12:09:38] [info] DRC Status:        FAILED
```

lvr_summary_94.rpt

```
[2025-10-12 12:10:34] [info] =====
[2025-10-12 12:10:34] [info]                Design Rule Check Report
[2025-10-12 12:10:34] [info] =====

[2025-10-12 12:10:34] [info] Report Generated:   2025-10-12
[2025-10-12 12:10:34] [info] Run Start Time:     12:09:42 PM
[2025-10-12 12:10:34] [info] Run End Time:       12:10:34 PM
[2025-10-12 12:10:34] [info] Time Taken:         0 minutes 51 seconds

[2025-10-12 12:10:34] [info] Tool Version:       LVR_2025.1
[2025-10-12 12:10:34] [info] Host Name:          ip-172-31-37-173
[2025-10-12 12:10:34] [info] User Name:          ubuntu
[2025-10-12 12:10:34] [info] Cores Used:         8
[2025-10-12 12:10:34] [info] Memory Utilized:    670.22 MB / 61.77812957763672 GB

[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info]                Top Cell Information
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] Layout Top Cell:    TOP
[2025-10-12 12:10:34] [info] Technology Node:     SKY130HD (130nm)
[2025-10-12 12:10:34] [info] Database Format:      GDSII
[2025-10-12 12:10:34] [info] Verification Mode:   Full Check

[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info]                Type of Errors | Count
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] DB_WARN -> 1
[2025-10-12 12:10:34] [info] LRF_ERR001 -> 14
[2025-10-12 12:10:34] [info] DB_WARN002 -> 159
[2025-10-12 12:10:34] [info] DB_WARN003 -> 161
[2025-10-12 12:10:34] [info] DRC_ERR9 -> 89
[2025-10-12 12:10:34] [info] DRC_ERR -> 67
[2025-10-12 12:10:34] [info] DRC_ERR5 -> 408
[2025-10-12 12:10:34] [info] DRC_ERR4 -> 241
[2025-10-12 12:10:34] [info] DRC_ERR1 -> 301
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info]                Rule Violation Summary
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] | Rule ID      | Description                                | Error Count |
[2025-10-12 12:10:34] [info] |-----|-----|-----|
[2025-10-12 12:10:34] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:10:34] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:10:34] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:10:34] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:10:34] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
```

```
[2025-10-12 12:10:34] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:10:34] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:10:34] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:10:34] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:10:34] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:10:34] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:10:34] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:10:34] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:10:34] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:10:34] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:10:34] [info]
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] Performance Metrics
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] | Stage | Time Taken |
[2025-10-12 12:10:34] [info] |-----|-----|
[2025-10-12 12:10:34] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:10:34] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:10:34] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] Summary
[2025-10-12 12:10:34] [info] -----
[2025-10-12 12:10:34] [info] Total Errors: 1120
[2025-10-12 12:10:34] [info] Total Warnings: 321
[2025-10-12 12:10:34] [info] DRC Status: FAILED
```

lvr_summary_95.rpt

```
[2025-10-12 12:11:31] [info] =====
[2025-10-12 12:11:31] [info] Design Rule Check Report
[2025-10-12 12:11:31] [info] =====

[2025-10-12 12:11:31] [info] Report Generated: 2025-10-12
[2025-10-12 12:11:31] [info] Run Start Time: 12:10:39 PM
[2025-10-12 12:11:31] [info] Run End Time: 12:11:31 PM
[2025-10-12 12:11:31] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:11:31] [info] Tool Version: LVR_2025.1
[2025-10-12 12:11:31] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:11:31] [info] User Name: ubuntu
[2025-10-12 12:11:31] [info] Cores Used: 8
[2025-10-12 12:11:31] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Top Cell Information
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Layout Top Cell: TOP
[2025-10-12 12:11:31] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:11:31] [info] Database Format: GDSII
[2025-10-12 12:11:31] [info] Verification Mode: Full Check

[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Type of Errors | Count
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] DB_WARN -> 1
[2025-10-12 12:11:31] [info] LRF_ERR001 -> 14
[2025-10-12 12:11:31] [info] DB_WARN002 -> 159
[2025-10-12 12:11:31] [info] DB_WARN003 -> 161
```

```

[2025-10-12 12:11:31] [info] DRC_ERR9 -> 89
[2025-10-12 12:11:31] [info] DRC_ERR -> 67
[2025-10-12 12:11:31] [info] DRC_ERR5 -> 408
[2025-10-12 12:11:31] [info] DRC_ERR4 -> 241
[2025-10-12 12:11:31] [info] DRC_ERR1 -> 301
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Rule Violation Summary
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:11:31] [info] |-----|-----|-----|
[2025-10-12 12:11:31] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:11:31] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:11:31] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:11:31] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:11:31] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:11:31] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:11:31] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:11:31] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:11:31] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:11:31] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:11:31] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:11:31] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:11:31] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:11:31] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:11:31] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Performance Metrics
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] | Stage | Time Taken |
[2025-10-12 12:11:31] [info] |-----|-----|
[2025-10-12 12:11:31] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:11:31] [info] | Rule Checking | 0 minutes 41 seconds |
[2025-10-12 12:11:31] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Summary
[2025-10-12 12:11:31] [info] -----
[2025-10-12 12:11:31] [info] Total Errors: 1120
[2025-10-12 12:11:31] [info] Total Warnings: 321
[2025-10-12 12:11:31] [info] DRC Status: FAILED

```

lvr_summary_96.rpt

```

[2025-10-12 12:12:28] [info] =====
[2025-10-12 12:12:28] [info] Design Rule Check Report
[2025-10-12 12:12:28] [info] =====

[2025-10-12 12:12:28] [info] Report Generated: 2025-10-12
[2025-10-12 12:12:28] [info] Run Start Time: 12:11:36 PM
[2025-10-12 12:12:28] [info] Run End Time: 12:12:28 PM
[2025-10-12 12:12:28] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:12:28] [info] Tool Version: LVR_2025.1
[2025-10-12 12:12:28] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:12:28] [info] User Name: ubuntu
[2025-10-12 12:12:28] [info] Cores Used: 8
[2025-10-12 12:12:28] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

```

```
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] Top Cell Information
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] Layout Top Cell:      TOP
[2025-10-12 12:12:28] [info] Technology Node:      SKY130HD (130nm)
[2025-10-12 12:12:28] [info] Database Format:      GDSII
[2025-10-12 12:12:28] [info] Verification Mode:    Full Check
```

```
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] Type of Errors | Count
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] DB_WARN -> 1
[2025-10-12 12:12:28] [info] LRF_ERR001 -> 14
[2025-10-12 12:12:28] [info] DB_WARN002 -> 159
[2025-10-12 12:12:28] [info] DB_WARN003 -> 161
[2025-10-12 12:12:28] [info] DRC_ERR9 -> 89
[2025-10-12 12:12:28] [info] DRC_ERR -> 67
[2025-10-12 12:12:28] [info] DRC_ERR5 -> 408
[2025-10-12 12:12:28] [info] DRC_ERR4 -> 241
[2025-10-12 12:12:28] [info] DRC_ERR1 -> 301
```

```
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] Rule Violation Summary
```

```
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] | Rule ID      | Description                                     | Error Count |
[2025-10-12 12:12:28] [info] |-----|-----|-----|
[2025-10-12 12:12:28] [info] | m5.1         | "m5.1 : min. m5 width : 1.6um" | 4           |
[2025-10-12 12:12:28] [info] | li.5         | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5           |
[2025-10-12 12:12:28] [info] | poly.1a      | "poly.1a : min. poly width : 0.15um" | 19          |
[2025-10-12 12:12:28] [info] | poly.7       | "poly.7 : min. source/drain length : 0.25um" | 8           |
[2025-10-12 12:12:28] [info] | licon.15     | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110         |
[2025-10-12 12:12:28] [info] | m1.6         | "m1.6 : min. m1 area : 0.083um^2" | 59          |
[2025-10-12 12:12:28] [info] | m4.5         | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391         |
[2025-10-12 12:12:28] [info] | m4.5ab       | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301         |
[2025-10-12 12:12:28] [info] | psdm.1       | "psdm.1 : min. psdm width : 0.38um" | 14          |
[2025-10-12 12:12:28] [info] | licon.5      | "licon.5 : min. diff enclosure of licon : 0.04um" | 131         |
[2025-10-12 12:12:28] [info] | li.1         | "li.1 : min. li width : 0.17um" | 17          |
[2025-10-12 12:12:28] [info] | li.1a       | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15          |
[2025-10-12 12:12:28] [info] | m1.1         | "m1.1 : min. m1 width : 0.14um" | 3           |
[2025-10-12 12:12:28] [info] | licon.8a     | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12          |
[2025-10-12 12:12:28] [info] | npc.1        | "npc.1 : min. npc width : 0.27um" | 17          |
[2025-10-12 12:12:28] [info] -----
```

```
[2025-10-12 12:12:28] [info] Performance Metrics
```

```
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] | Stage          | Time Taken |
[2025-10-12 12:12:28] [info] |-----|-----|
[2025-10-12 12:12:28] [info] | GDS Parsing    | 0 minutes 9 seconds |
[2025-10-12 12:12:28] [info] | Rule Checking  | 0 minutes 41 seconds |
[2025-10-12 12:12:28] [info] | Report Generation | 0 minutes 0 seconds |
```

```
[2025-10-12 12:12:28] [info] -----
```

```
[2025-10-12 12:12:28] [info] Summary
```

```
[2025-10-12 12:12:28] [info] -----
[2025-10-12 12:12:28] [info] Total Errors:      1120
[2025-10-12 12:12:28] [info] Total Warnings:    321
[2025-10-12 12:12:28] [info] DRC Status:        FAILED
```

lvr_summary_97.rpt

```
[2025-10-12 12:13:24] [info] =====
[2025-10-12 12:13:24] [info] Design Rule Check Report
[2025-10-12 12:13:24] [info] =====

[2025-10-12 12:13:24] [info] Report Generated: 2025-10-12
[2025-10-12 12:13:24] [info] Run Start Time: 12:12:33 PM
[2025-10-12 12:13:24] [info] Run End Time: 12:13:24 PM
[2025-10-12 12:13:24] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:13:24] [info] Tool Version: LVR_2025.1
[2025-10-12 12:13:24] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:13:24] [info] User Name: ubuntu
[2025-10-12 12:13:24] [info] Cores Used: 8
[2025-10-12 12:13:24] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Top Cell Information
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Layout Top Cell: TOP
[2025-10-12 12:13:24] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:13:24] [info] Database Format: GDSII
[2025-10-12 12:13:24] [info] Verification Mode: Full Check

[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Type of Errors | Count
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] DB_WARN -> 1
[2025-10-12 12:13:24] [info] LRF_ERR001 -> 14
[2025-10-12 12:13:24] [info] DB_WARN002 -> 159
[2025-10-12 12:13:24] [info] DB_WARN003 -> 161
[2025-10-12 12:13:24] [info] DRC_ERR9 -> 89
[2025-10-12 12:13:24] [info] DRC_ERR -> 67
[2025-10-12 12:13:24] [info] DRC_ERR5 -> 408
[2025-10-12 12:13:24] [info] DRC_ERR4 -> 241
[2025-10-12 12:13:24] [info] DRC_ERR1 -> 301
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Rule Violation Summary
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:13:24] [info] |-----|-----|-----|
[2025-10-12 12:13:24] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:13:24] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:13:24] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:13:24] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:13:24] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:13:24] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um^2" | 59 |
[2025-10-12 12:13:24] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:13:24] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:13:24] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:13:24] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:13:24] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:13:24] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:13:24] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:13:24] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:13:24] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:13:24] [info]
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Performance Metrics
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] | Stage | Time Taken |
```



```
[2025-10-12 12:13:24] [info] |-----|-----|
[2025-10-12 12:13:24] [info] | GDS Parsing           | 0 minutes 9 seconds |
[2025-10-12 12:13:24] [info] | Rule Checking          | 0 minutes 40 seconds |
[2025-10-12 12:13:24] [info] | Report Generation      | 0 minutes 0 seconds |

[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Summary
[2025-10-12 12:13:24] [info] -----
[2025-10-12 12:13:24] [info] Total Errors: 1120
[2025-10-12 12:13:24] [info] Total Warnings: 321
[2025-10-12 12:13:24] [info] DRC Status: FAILED
```

lvr_summary_98.rpt

```
[2025-10-12 12:14:20] [info] =====
[2025-10-12 12:14:20] [info] Design Rule Check Report
[2025-10-12 12:14:20] [info] =====

[2025-10-12 12:14:20] [info] Report Generated: 2025-10-12
[2025-10-12 12:14:20] [info] Run Start Time: 12:13:29 PM
[2025-10-12 12:14:20] [info] Run End Time: 12:14:20 PM
[2025-10-12 12:14:20] [info] Time Taken: 0 minutes 50 seconds

[2025-10-12 12:14:20] [info] Tool Version: LVR_2025.1
[2025-10-12 12:14:20] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:14:20] [info] User Name: ubuntu
[2025-10-12 12:14:20] [info] Cores Used: 8
[2025-10-12 12:14:20] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Top Cell Information
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Layout Top Cell: TOP
[2025-10-12 12:14:20] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:14:20] [info] Database Format: GDSII
[2025-10-12 12:14:20] [info] Verification Mode: Full Check

[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Type of Errors | Count
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] DB_WARN -> 1
[2025-10-12 12:14:20] [info] LRF_ERR001 -> 14
[2025-10-12 12:14:20] [info] DB_WARN002 -> 159
[2025-10-12 12:14:20] [info] DB_WARN003 -> 161
[2025-10-12 12:14:20] [info] DRC_ERR9 -> 89
[2025-10-12 12:14:20] [info] DRC_ERR -> 67
[2025-10-12 12:14:20] [info] DRC_ERR5 -> 408
[2025-10-12 12:14:20] [info] DRC_ERR4 -> 241
[2025-10-12 12:14:20] [info] DRC_ERR1 -> 301
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Rule Violation Summary
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:14:20] [info] |-----|-----|-----|
[2025-10-12 12:14:20] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:14:20] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:14:20] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:14:20] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:14:20] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
```

```
[2025-10-12 12:14:20] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:14:20] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:14:20] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:14:20] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:14:20] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:14:20] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:14:20] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:14:20] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:14:20] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:14:20] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:14:20] [info]
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Performance Metrics
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] | Stage | Time Taken |
[2025-10-12 12:14:20] [info] |-----|-----|
[2025-10-12 12:14:20] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:14:20] [info] | Rule Checking | 0 minutes 39 seconds |
[2025-10-12 12:14:20] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Summary
[2025-10-12 12:14:20] [info] -----
[2025-10-12 12:14:20] [info] Total Errors: 1120
[2025-10-12 12:14:20] [info] Total Warnings: 321
[2025-10-12 12:14:20] [info] DRC Status: FAILED
```

lvr_summary_99.rpt

```
[2025-10-12 12:15:18] [info] =====
[2025-10-12 12:15:18] [info] Design Rule Check Report
[2025-10-12 12:15:18] [info] =====

[2025-10-12 12:15:18] [info] Report Generated: 2025-10-12
[2025-10-12 12:15:18] [info] Run Start Time: 12:14:27 PM
[2025-10-12 12:15:18] [info] Run End Time: 12:15:18 PM
[2025-10-12 12:15:18] [info] Time Taken: 0 minutes 51 seconds

[2025-10-12 12:15:18] [info] Tool Version: LVR_2025.1
[2025-10-12 12:15:18] [info] Host Name: ip-172-31-37-173
[2025-10-12 12:15:18] [info] User Name: ubuntu
[2025-10-12 12:15:18] [info] Cores Used: 8
[2025-10-12 12:15:18] [info] Memory Utilized: 670.22 MB / 61.77812957763672 GB

[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Top Cell Information
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Layout Top Cell: TOP
[2025-10-12 12:15:18] [info] Technology Node: SKY130HD (130nm)
[2025-10-12 12:15:18] [info] Database Format: GDSII
[2025-10-12 12:15:18] [info] Verification Mode: Full Check

[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Type of Errors | Count
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] DB_WARN -> 1
[2025-10-12 12:15:18] [info] LRF_ERR001 -> 14
[2025-10-12 12:15:18] [info] DB_WARN002 -> 159
[2025-10-12 12:15:18] [info] DB_WARN003 -> 161
```

```

[2025-10-12 12:15:18] [info] DRC_ERR9 -> 89
[2025-10-12 12:15:18] [info] DRC_ERR -> 67
[2025-10-12 12:15:18] [info] DRC_ERR5 -> 408
[2025-10-12 12:15:18] [info] DRC_ERR4 -> 241
[2025-10-12 12:15:18] [info] DRC_ERR1 -> 301
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Rule Violation Summary
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] | Rule ID | Description | Error Count |
[2025-10-12 12:15:18] [info] |-----|-----|-----|
[2025-10-12 12:15:18] [info] | m5.1 | "m5.1 : min. m5 width : 1.6um" | 4 |
[2025-10-12 12:15:18] [info] | li.5 | "li.5 : min. li enclosure of licon of 2 opposite edges : 0.08um" | 5 |
[2025-10-12 12:15:18] [info] | poly.1a | "poly.1a : min. poly width : 0.15um" | 19 |
[2025-10-12 12:15:18] [info] | poly.7 | "poly.7 : min. source/drain length : 0.25um" | 8 |
[2025-10-12 12:15:18] [info] | licon.15 | "licon.15 : min. npc enclosure of poly-licon : 0.1um" | 110 |
[2025-10-12 12:15:18] [info] | m1.6 | "m1.6 : min. m1 area : 0.083um2" | 59 |
[2025-10-12 12:15:18] [info] | m4.5 | "m4.5 : min. m4 enclosure of via3 of 2 opposite edges : 0.085um" | 391 |
[2025-10-12 12:15:18] [info] | m4.5ab | "m4.5ab : min. 3um.m4 spacing m4 : 0.4um" | 301 |
[2025-10-12 12:15:18] [info] | psdm.1 | "psdm.1 : min. psdm width : 0.38um" | 14 |
[2025-10-12 12:15:18] [info] | licon.5 | "licon.5 : min. diff enclosure of licon : 0.04um" | 131 |
[2025-10-12 12:15:18] [info] | li.1 | "li.1 : min. li width : 0.17um" | 17 |
[2025-10-12 12:15:18] [info] | li.1a | "li.1a : min. li width for the cells s8rf2_xcmvpp_hd5_* : 0.14um" | 15 |
[2025-10-12 12:15:18] [info] | m1.1 | "m1.1 : min. m1 width : 0.14um" | 3 |
[2025-10-12 12:15:18] [info] | licon.8a | "licon.8a : min. poly enclosure of licon by one of 2 opposite edges : 0.08um" | 12 |
[2025-10-12 12:15:18] [info] | npc.1 | "npc.1 : min. npc width : 0.27um" | 17 |
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Performance Metrics
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] | Stage | Time Taken |
[2025-10-12 12:15:18] [info] |-----|-----|
[2025-10-12 12:15:18] [info] | GDS Parsing | 0 minutes 9 seconds |
[2025-10-12 12:15:18] [info] | Rule Checking | 0 minutes 40 seconds |
[2025-10-12 12:15:18] [info] | Report Generation | 0 minutes 0 seconds |

[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Summary
[2025-10-12 12:15:18] [info] -----
[2025-10-12 12:15:18] [info] Total Errors: 1120
[2025-10-12 12:15:18] [info] Total Warnings: 321
[2025-10-12 12:15:18] [info] DRC Status: FAILED

```